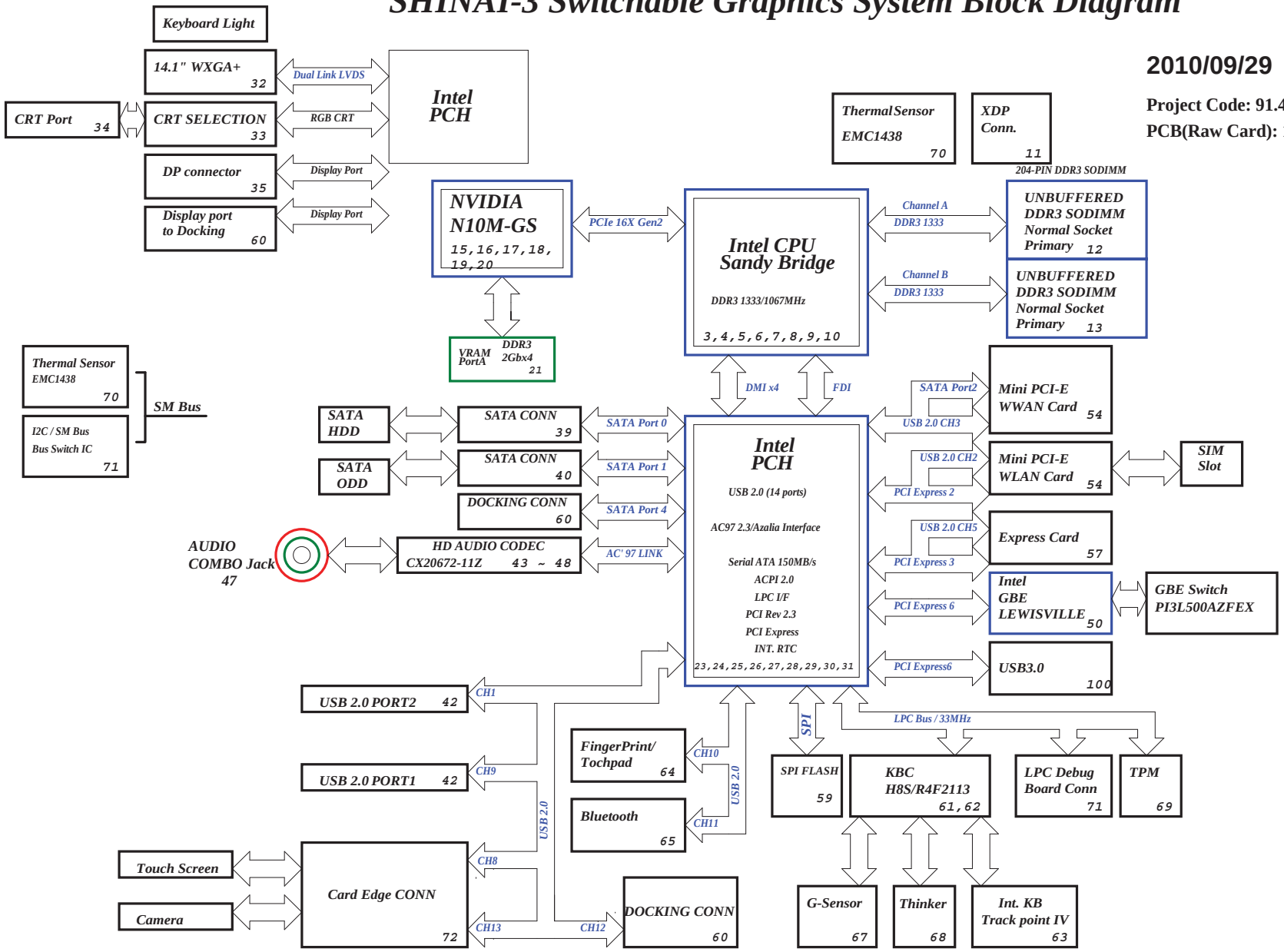
SHINAI-3 Switchable Graphics System Block Diagram

2010/09/29

Project Code: 91.4KF01.001

PCB(Raw Card): 10226-SB



PCB Layer Stackup

L1: TOP
L2: SIGNAL
L3: G/P
L4: SIGNAL
L5: G/P
L6: G/P
L7: SIGNAL
L8: G/P
L9: SIGNAL
L10: BOTTOM

Battery Charger/Selector BQ24742 74	
INPUTS	OUTPUTS
DCIN_PWR20_F	M-BAT-PWR S-BAT-PWR

System DC/DC TPS51222 78	
VINT20	VCC5M VCC3M

CPU DC/DC VT1316/VT1317 79	
VCC5M_OUT	VCCCPUCORE

GFX CORE_D ADP3211 83	
VINT20	VCCGFXCORE_D

GFX CORE_I VT1317 81	
VCC5M_OUT	VCCGFXCORE_I

VCC1R5A VT358FCX 86	
VCC5M_OUT	VCC1R5A

VCC0R75B MAX1510 87	
VCC1R5A	VCC0R75B

VCC1R8B TPS62290 89	
VCC5M	VCC1R8B

VCC1R05AMT VT356FCX 85	
VCC5M_OUT	VCC1R05LAN

VCC1R05B_VTT VT357FCX 84	
VCC5M_OUT	VCC1R05B_VTT

<Variant Name>

緯創資通 **Wistron Corporation**
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title: **Block Diagram**

Size	Document Number	Rev
Custom	SHINAI-3 SWG	SB

Date: Wednesday, September 29, 2010 Sheet 1 of 102

RESISTOR

Symbol name	Value	Tolerance (J: 5%, F: 1%, D: 0.5%, B: 0.1 %)	Rating 0402=> 1/16W, 25V 0603 => 1/16W, 75V 0805 => 1/10W, 100V	Size 2=>0402, 3=>0603, 5=>0805, 6=>1206, 0=>1210
10KR3	10K Ohm	If no letter, it means J: 5%	1/16W, 75V	0603
33D3R5	33.3 Ohm	If no letter, it means J: 5%	1/10W, 100V	0805
1KR3F	1K Ohm	F: 1%	1/16W, 75V	0603

The naming rule is value + R + size + tolerance
For the value, it can be read by the number before R. (R means resistor)
For the tolerance, it can be read from the last letter.
For the rating, we don't show on the symbol name.
For the size, R2=>0402, R3=>0603, R5=>0805,....

CAPACITOR

Symbol name	Value	Tolerance (M: +/-20, K: +/-10, Z: +80/-20)	Rating	Size 2=>0402, 3=>0603, 5=>0805, 6=>1206, 0=>1210
SCD1U10V2MX-1	0.1uF	M/X5R	10V	0402
SC10U6D3V5MX	10uF	M/X5R	6.3V	0805
SC2D2U16V5ZY	2.2uF	Z/Y5V	16V	0805

The naming rule is
Capacitor type + value + rating + size + tolerance + material
SCD1U10V2MX-1
SC=> SMT Ceremic, TC=> POS cap or SP cap
D1U => 0.1uF
10V => the voltage rating is 10V
2=> 0402, 3=>0603, 5=>0805
M=>tolerance M, K, Z
X=> X7R/X5R, Y=> Y5V
-1 => symbol version, nonsense to EE characteristic

PLANAR_ID[3..0]

ICH9-M GPIOn	39	38	37	36	Planar ID Version	Planar PCB Version
PLANAR_IDn	3	2	1	0		
	0	0	0	0	SHINAI-3 SWG PDV	SA
	0	0	0	1		
	0	0	1	0		
	0	0	1	1		
	0	1	0	0		
	0	1	0	1		
	0	1	1	0		
	0	1	1	1		

EC HISTORY

Stage	Date	EC No.	Page	Note

PCI TABLE

DEVICE	IDSEL	IRQ (Default)	REQ# / GNT#
MINIPCI SLOT	AD18	F, G	REQ# 3/ GNT#3
CARDBUS R5C811	AD16	SERIRQ	REQ#0 / GNT#0
USB UHCI	AD29	A, C, D	
USB 2.0 EHCI	AD29	H	
DMI-to-PCI/ AC97 Modem/ AC97 Audio	AD30	B B	
LPC Bridge IDE SATA SMBus	AD31	C C B	
PCI Express	AD28	A, B, C, D	

<Variant Name>

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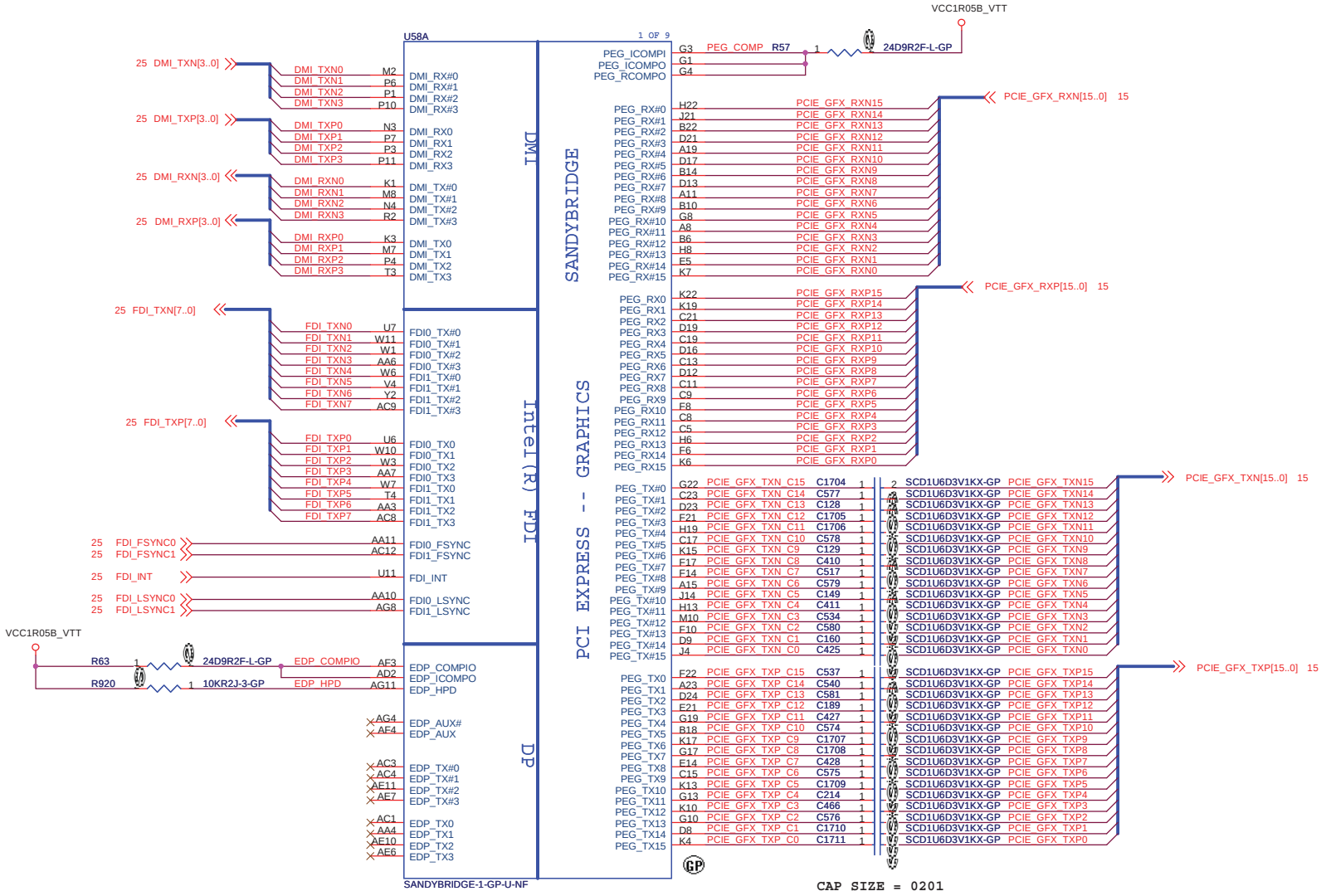
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title: EC HISTORY

Size: A3 | Document Number: SHINAI-3 SWG | Rev: SB

Date: Tuesday, September 07, 2010 | Sheet 2 of 102



<Variant Name>

緯創資通 Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title: CPU(1/8): DMI/EDP/PEG/FDI

Size	Document Number	Rev
A3	SHINAI-3 SWG	SB

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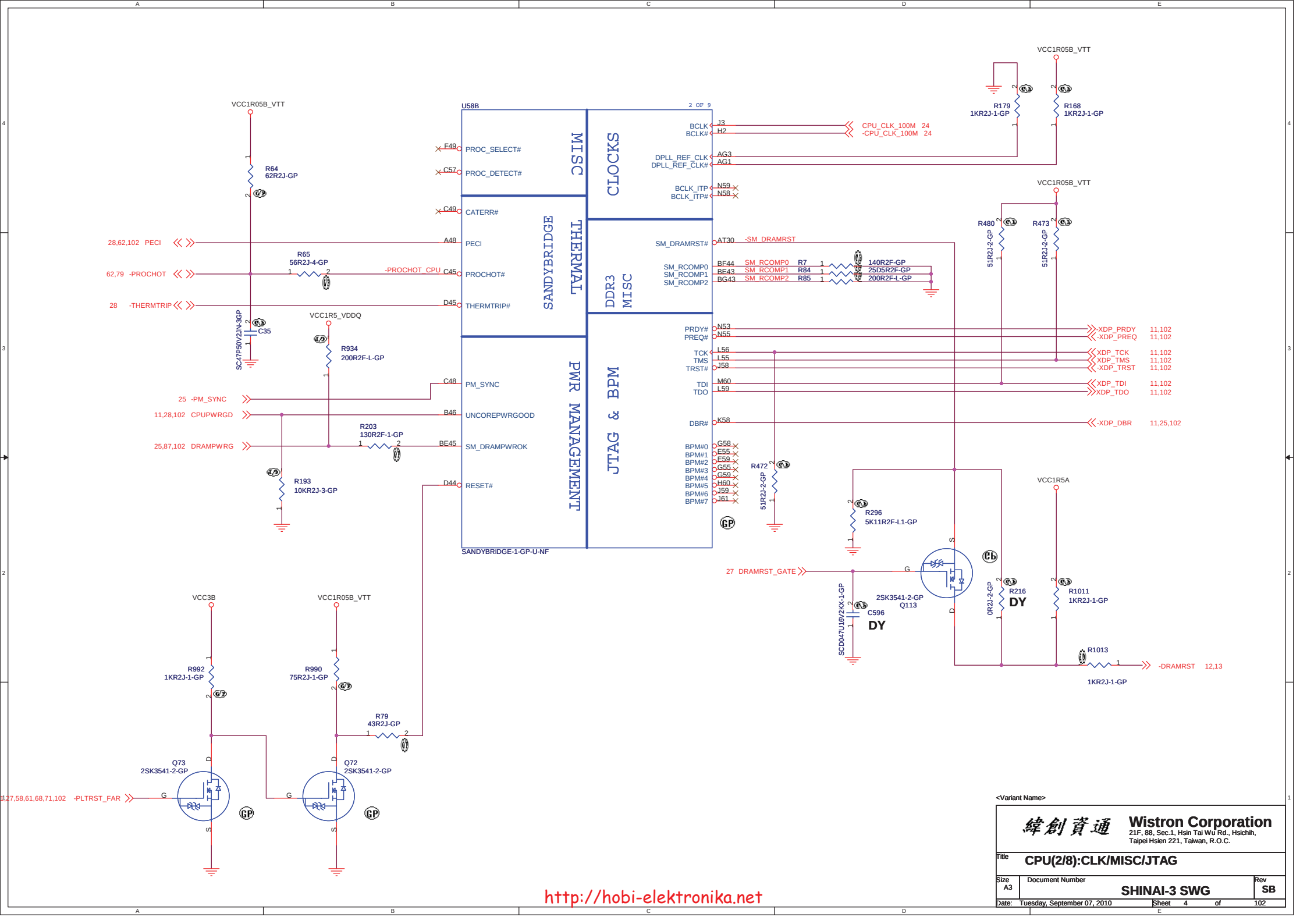
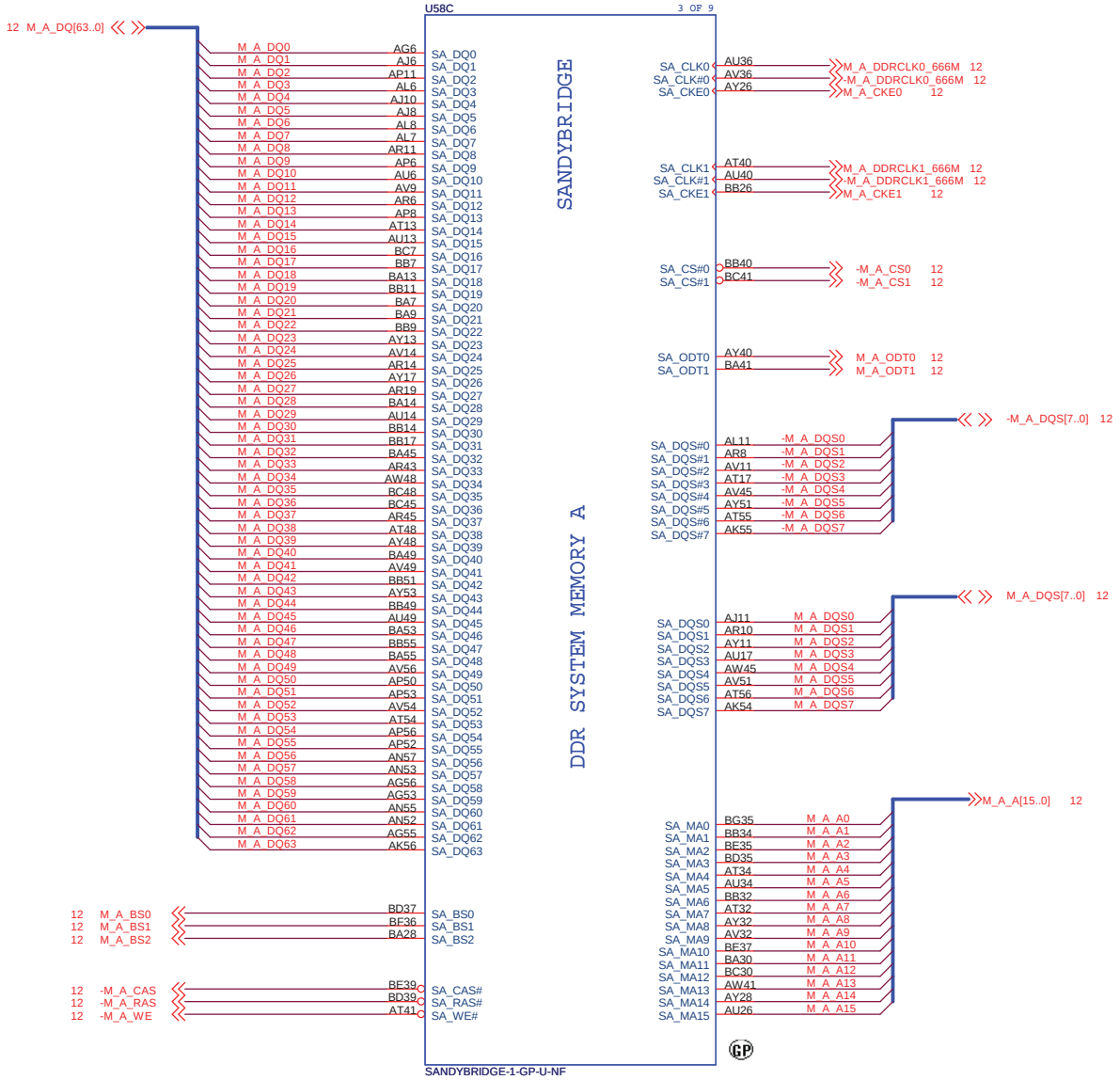


<Variant Name>

緯創資通 Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

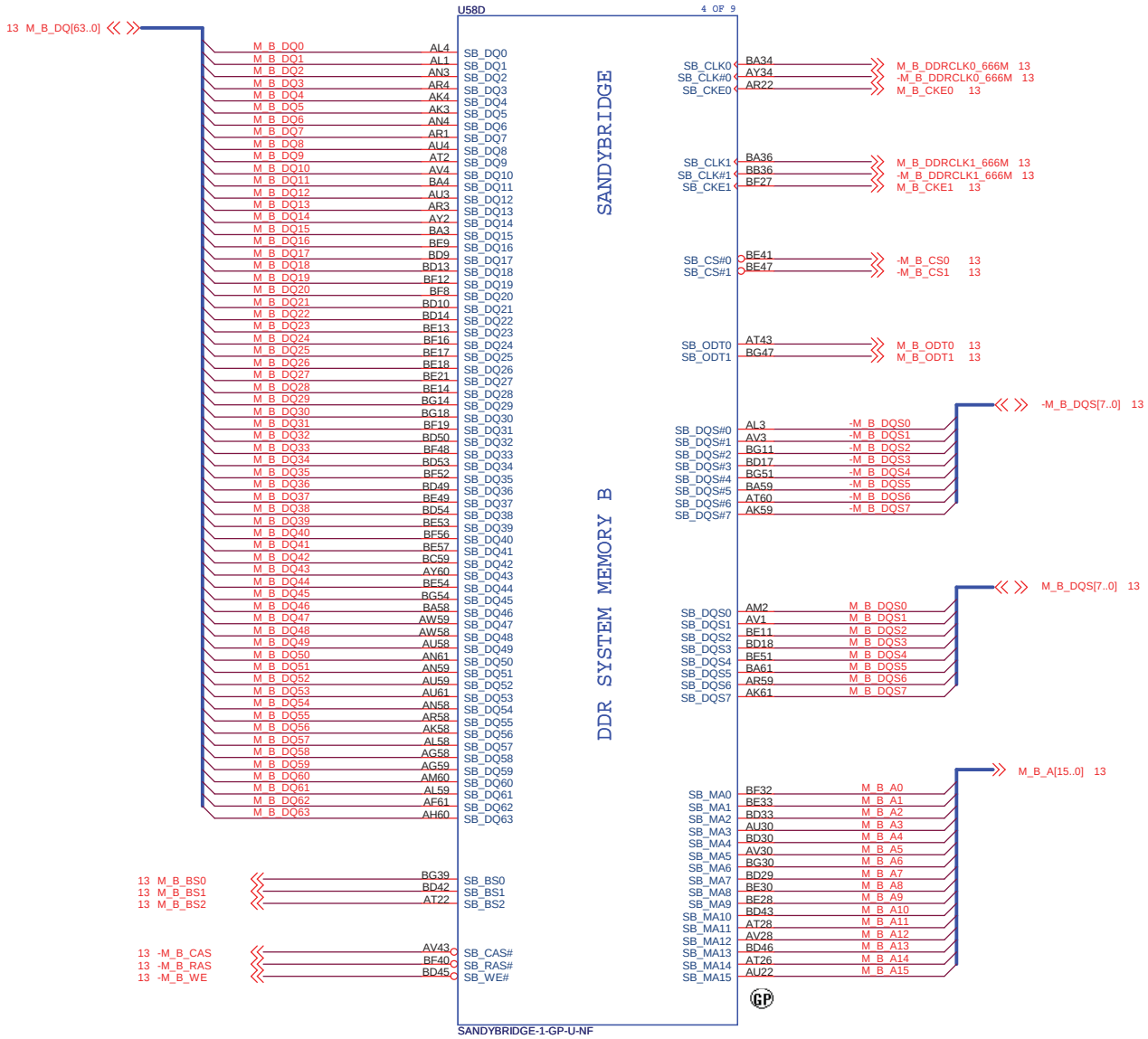
Title	CPU(2/8):CLK/MISC/JTAG	
Size A3	Document Number SHINAI-3 SWG	Rev SB
Date: Tuesday, September 07, 2010	Sheet 4 of 102	



<Variant Name>

緯創資通 Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

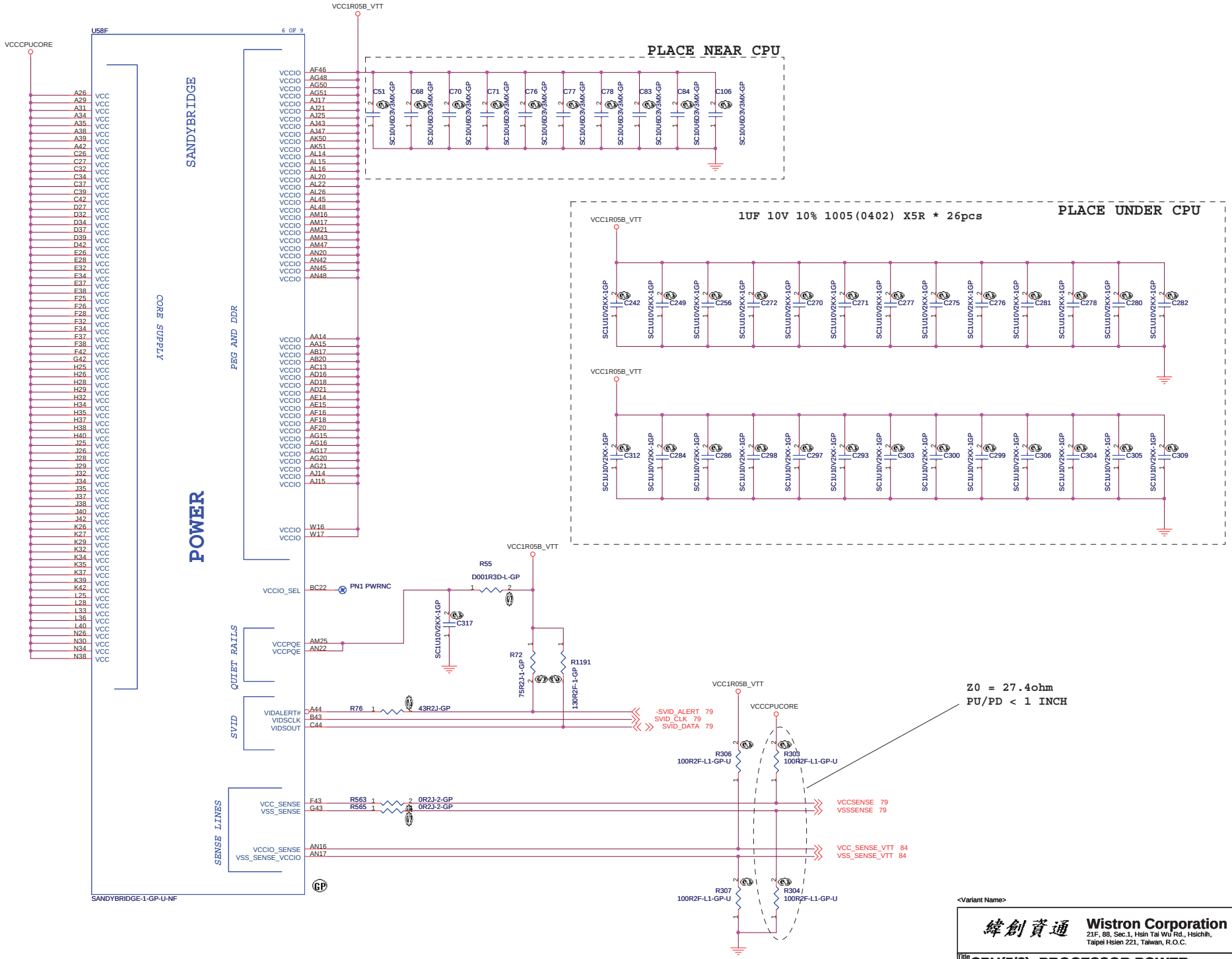
Title	CPU(3/8):DDR3 CH-A	
Size A3	Document Number SHINAI-3 SWG	Rev SB
Date: Tuesday, September 07, 2010	Sheet 5 of 102	



<Variant Name>

緯創資通 Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title	CPU(4/8):DDR3 CH-B	
Size A3	Document Number SHINAI-3 SWG	Rev SB
Date: Tuesday, September 07, 2010	Sheet 6 of 102	



<Variant Name>

緯創資通 Wistron Corporation
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Taipei Hsien 221, Taiwan, R.O.C.

Title: CPU(5/8): PROCESSOR POWER

Size	Document Number	Rev
Custom	SHINAI-3 SWG	SB

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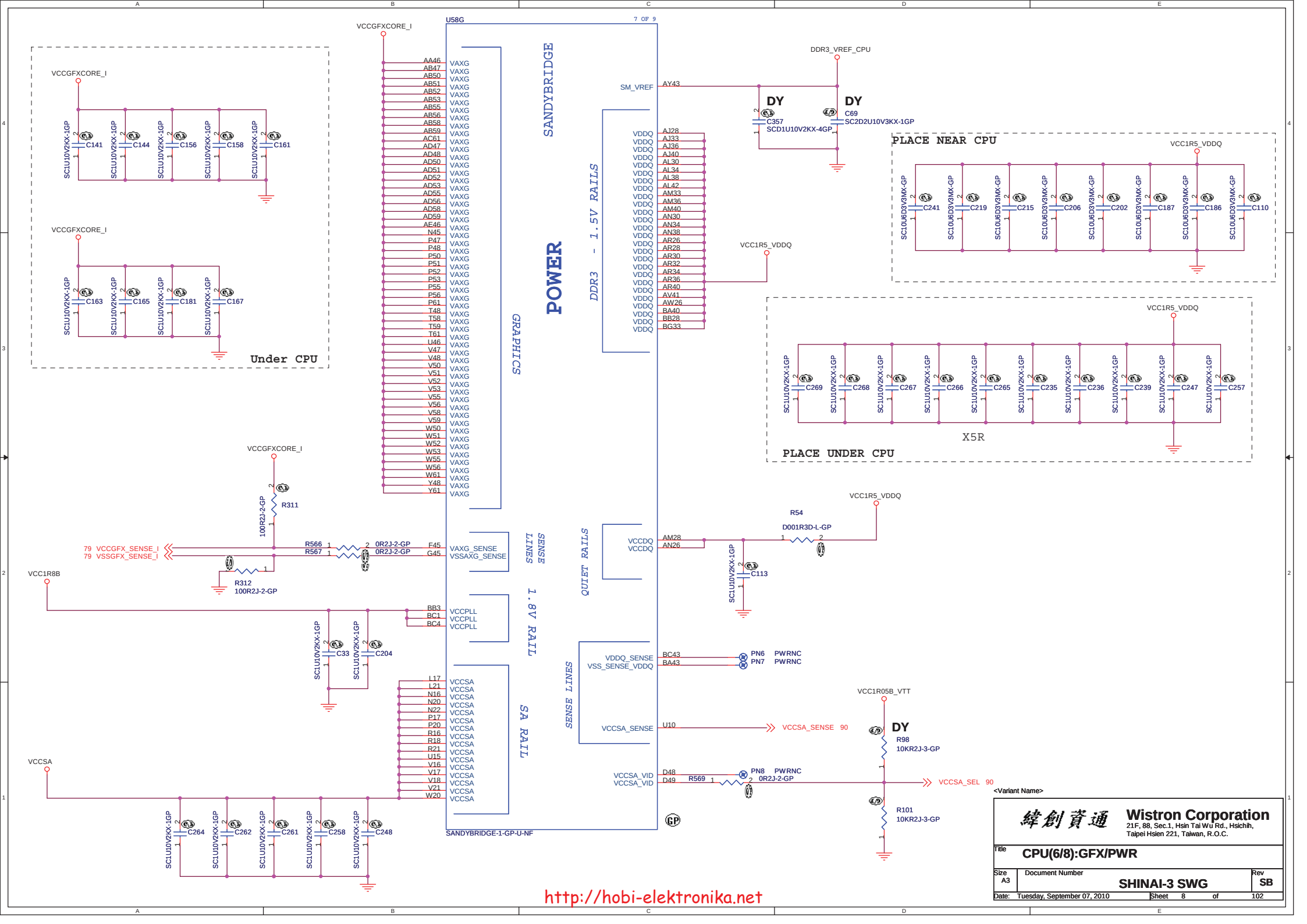
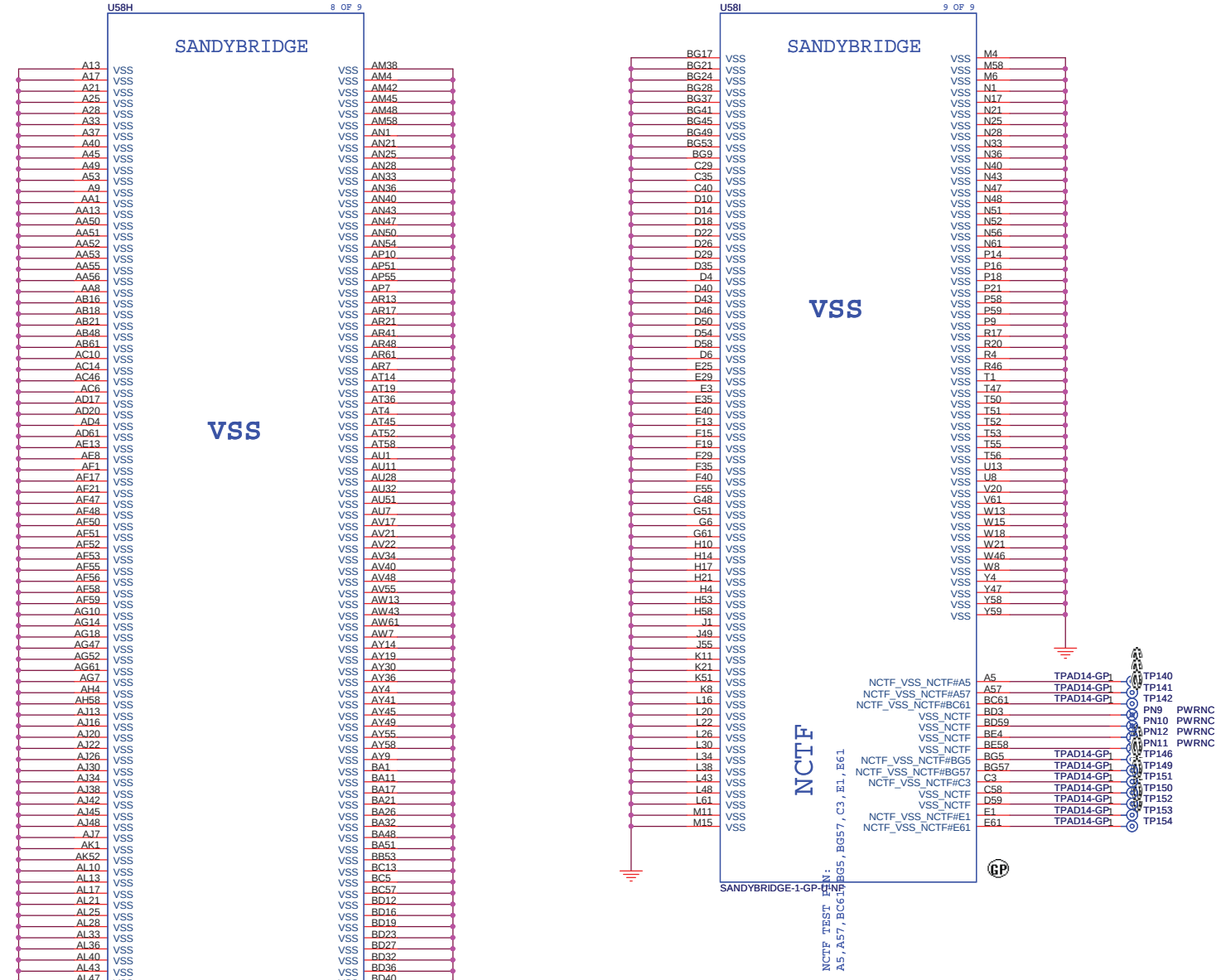
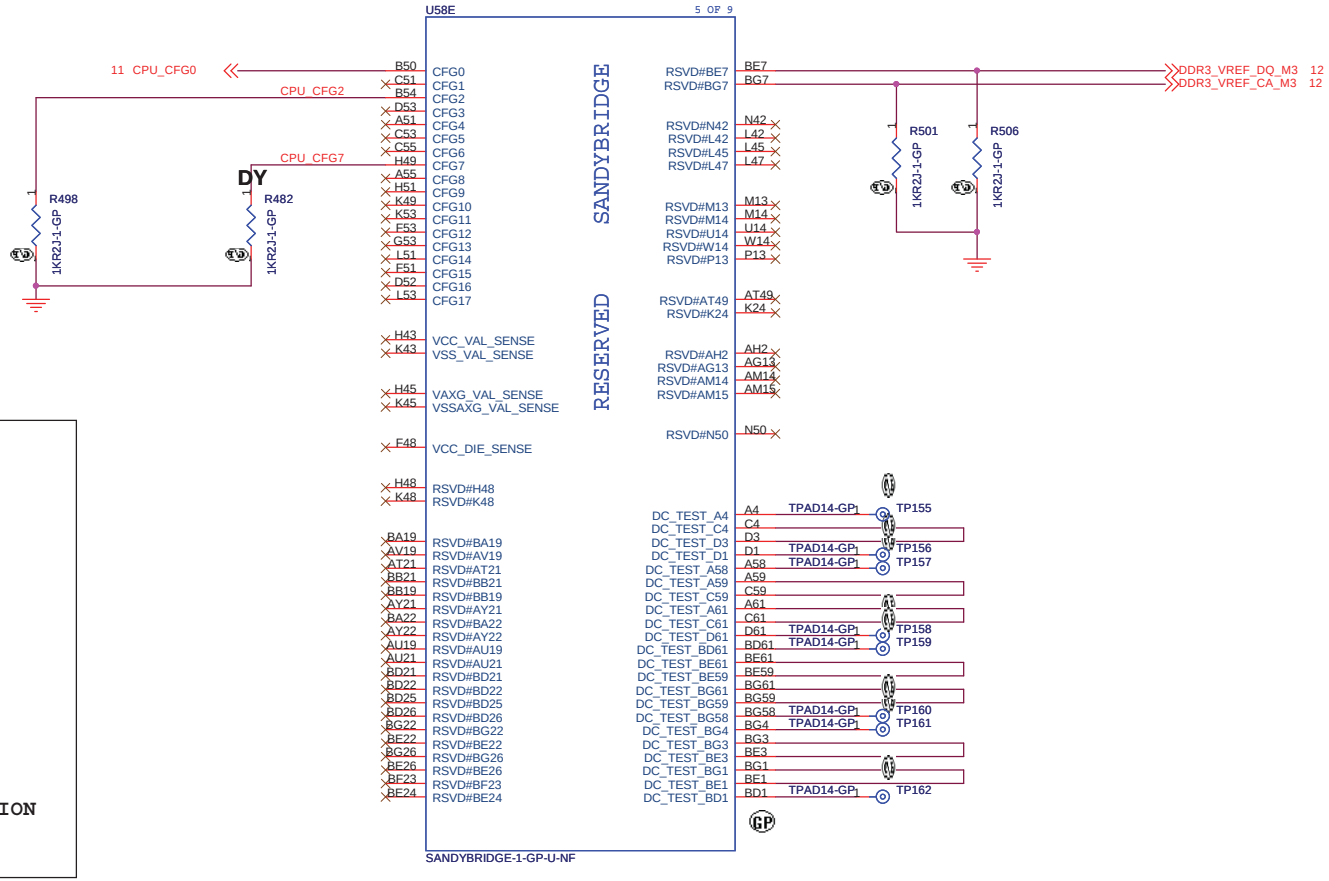




<Variant Name>

緯創資通 Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title	CPU(7/8): GND	
Size A3	Document Number SHINAI-3 SWG	Rev SB
Date: Tuesday, September 07, 2010	Sheet 9 of 102	



TABLE

```
CFG2    PEG LANE REVERSAL
 1 -NO_ASM: NORMAL
 0 -ASM: RESVERSE

CFG4  DISPLAY PORT PRESENCE
 1 -NO_ASM: DISABLE
 0 -ASM: ENABLE

CFG[6:5]  PEG BIFURCATION CONFIG
 11 -SINGLE 16 LANES (D01:F1 AND D01:F2 DISABLED)
 10 -2X8 LANES (D01:F1 ENABLED: D01:F2 DISABLED)
 01 -RESERVED
 00 -8 LANES AND 2X4 LANES (D01:F1 AND D01:F2 ENABLED)

CFG7 PEG DEFER TRAINING
 1 -NO_ASM: PEG TRAIN IMMEDIATELY FOLLOWING XXRESETB DEASSERTION
 0 -ASM: PEG WAIT FOR BIOS FOR TRAINING
```


<Variant Name>

緯創資通 Wistron Corporation
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Title: CPU(8/8): CFG/RESERVED

Size: A3 | Document Number: SHINAI-3 SWG | Rev: SB

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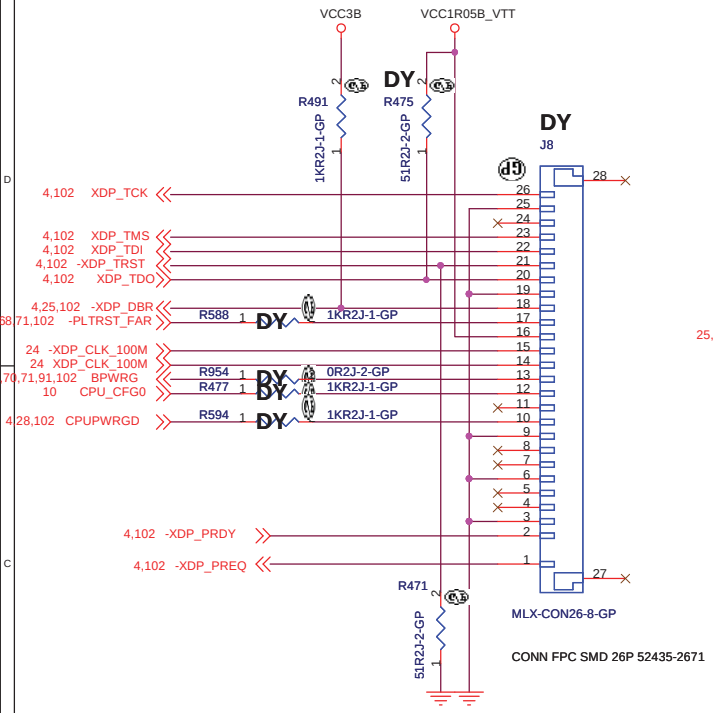
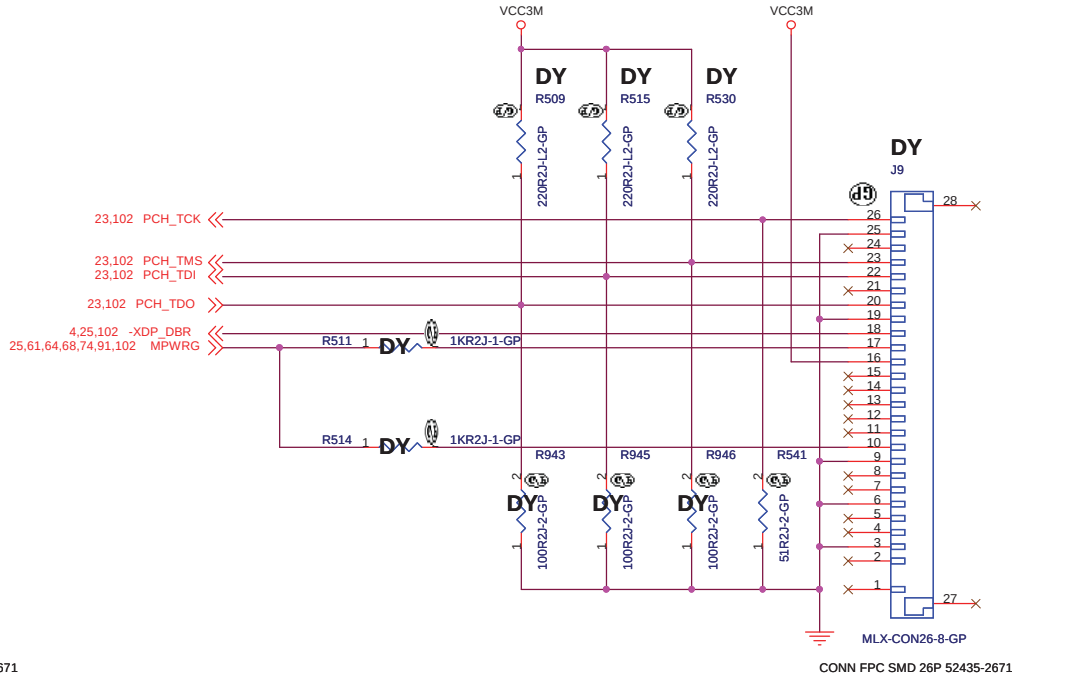


DEBUG Interface for Processor.



DEBUG Interface for PCH.

TABLE NOTE: J8 "ASM" FOR PRE-DV ONLY

SIGNAL	REF DES	ENABLE	DISABLE
TDO	R475	ASM	NO_ASM
TRST#	R471	ASM	ASM
DBRST#	R491	ASM	ASM
RESET#	R588	ASM	NO_ASM
CFG0	R477	ASM	NO_ASM
PWRGD	R594	ASM	NO_ASM
BPWRG	R954	ASM	NO_ASM
	J8	ASM	NO_ASM

LOGIC

TABLE NOTE: J9 "ASM" FOR PRE-DV ONLY

SIGNAL	REF DES	ENABLE	DISABLE
TDO	R509	220	NO_ASM
	R943	100	NO_ASM
TMS	R530	220	NO_ASM
	R946	100	NO_ASM
TDI	R515	220	NO_ASM
	R945	100	NO_ASM
TCK	R541	51	51
MPWRG	R511	ASM	NO_ASM
	R514	ASM	NO_ASM
	J9	ASM	NO_ASM

LOGIC

<Variant Name>

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Title: XDP CONNECTOR

Size	Document Number	Rev
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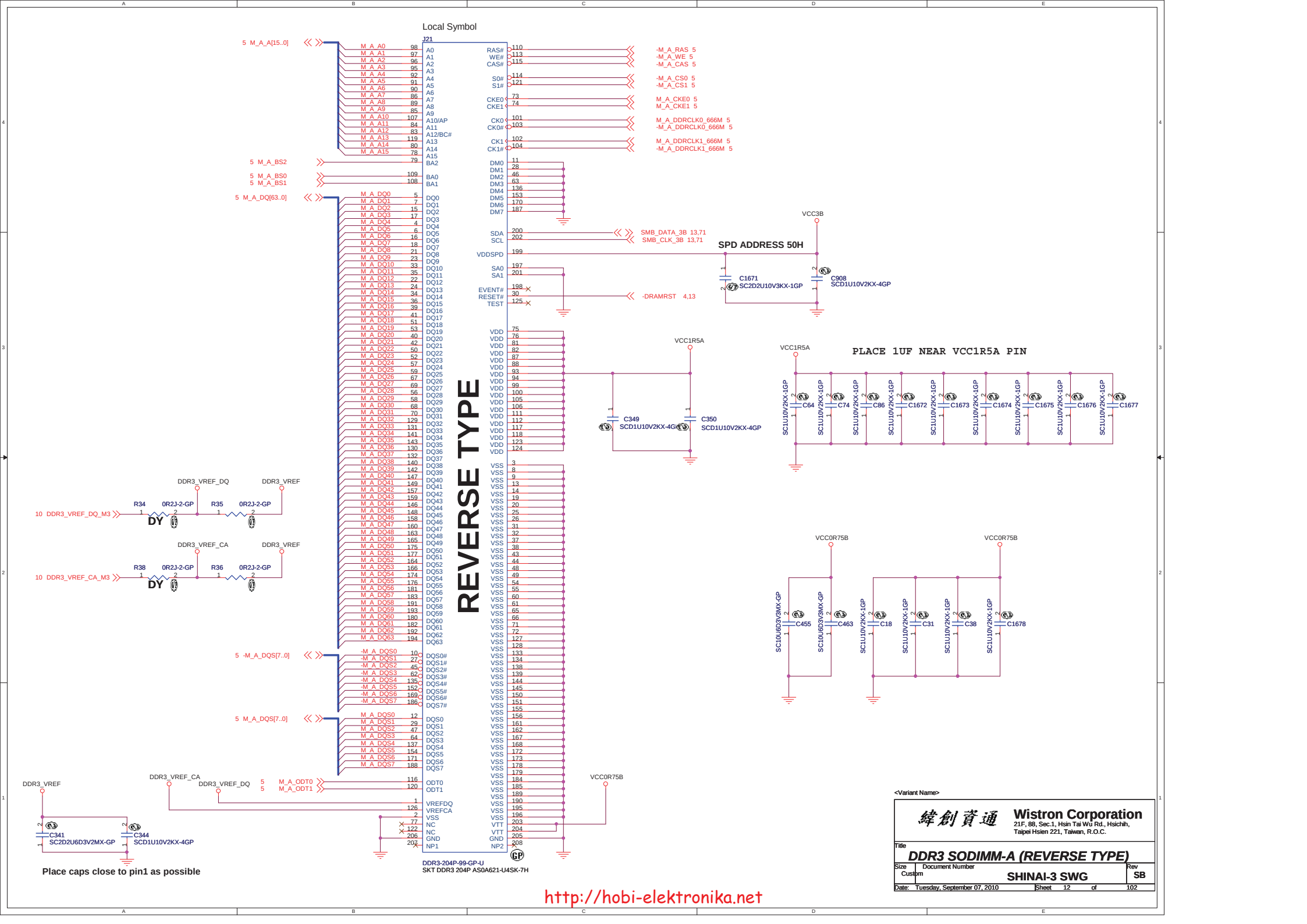
Local Symbol
J21
REVERSE TYPE
5 M_A_A[15..0]
M_A_A0 98
M_A_A1 97
M_A_A2 96
M_A_A3 95
M_A_A4 92
M_A_A5 91
M_A_A6 90
M_A_A7 86
M_A_A8 89
M_A_A9 85
M_A_A10 107
M_A_A11 84
M_A_A12 83
M_A_A13 119
M_A_A14 80
M_A_A15 78
A0 A1 A2 A3 A4 A5 A6 A7 A8 A9 A10/AP A11 A12/BC# A13 A14 A15
5 M_A_BS2
79 BA2
5 M_A_BS0
109 BA0
5 M_A_BS1
108 BA1
5 M_A_DQ[63..0]
M_A_DQ0 5
M_A_DQ1 7
M_A_DQ2 15
M_A_DQ3 17
M_A_DQ4 4
M_A_DQ5 6
M_A_DQ6 16
M_A_DQ7 18
M_A_DQ8 21
M_A_DQ9 23
M_A_DQ10 33
M_A_DQ11 35
M_A_DQ12 22
M_A_DQ13 24
M_A_DQ14 34
M_A_DQ15 36
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M_A_DQ19 53
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M_A_DQ21 42
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M_A_DQ24 57
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M_A_DQ26 67
M_A_DQ27 69
M_A_DQ28 56
M_A_DQ29 58
M_A_DQ30 68
M_A_DQ31 70
M_A_DQ32 129
M_A_DQ33 131
M_A_DQ34 141
M_A_DQ35 143
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M_A_DQ57 183
M_A_DQ58 191
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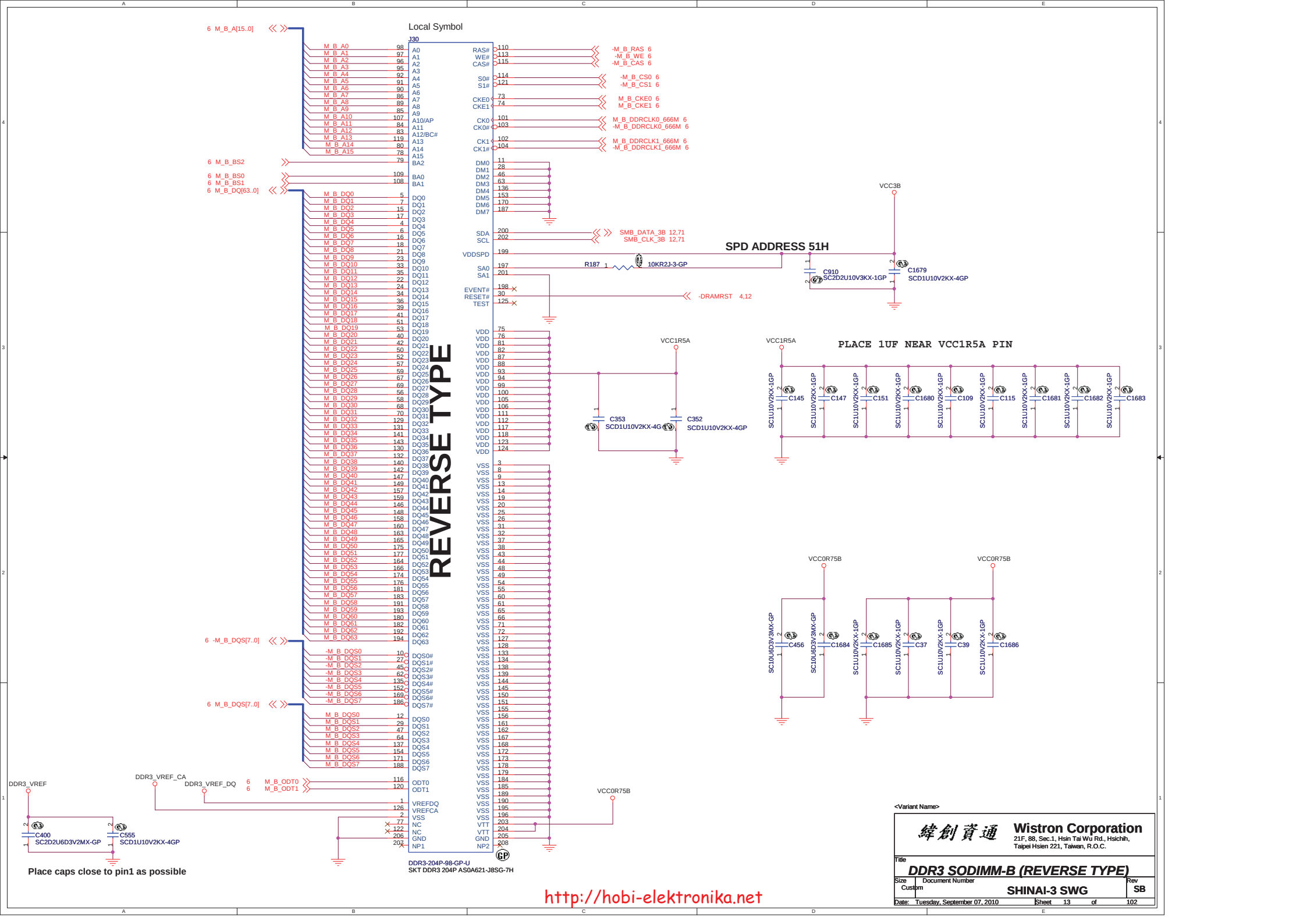
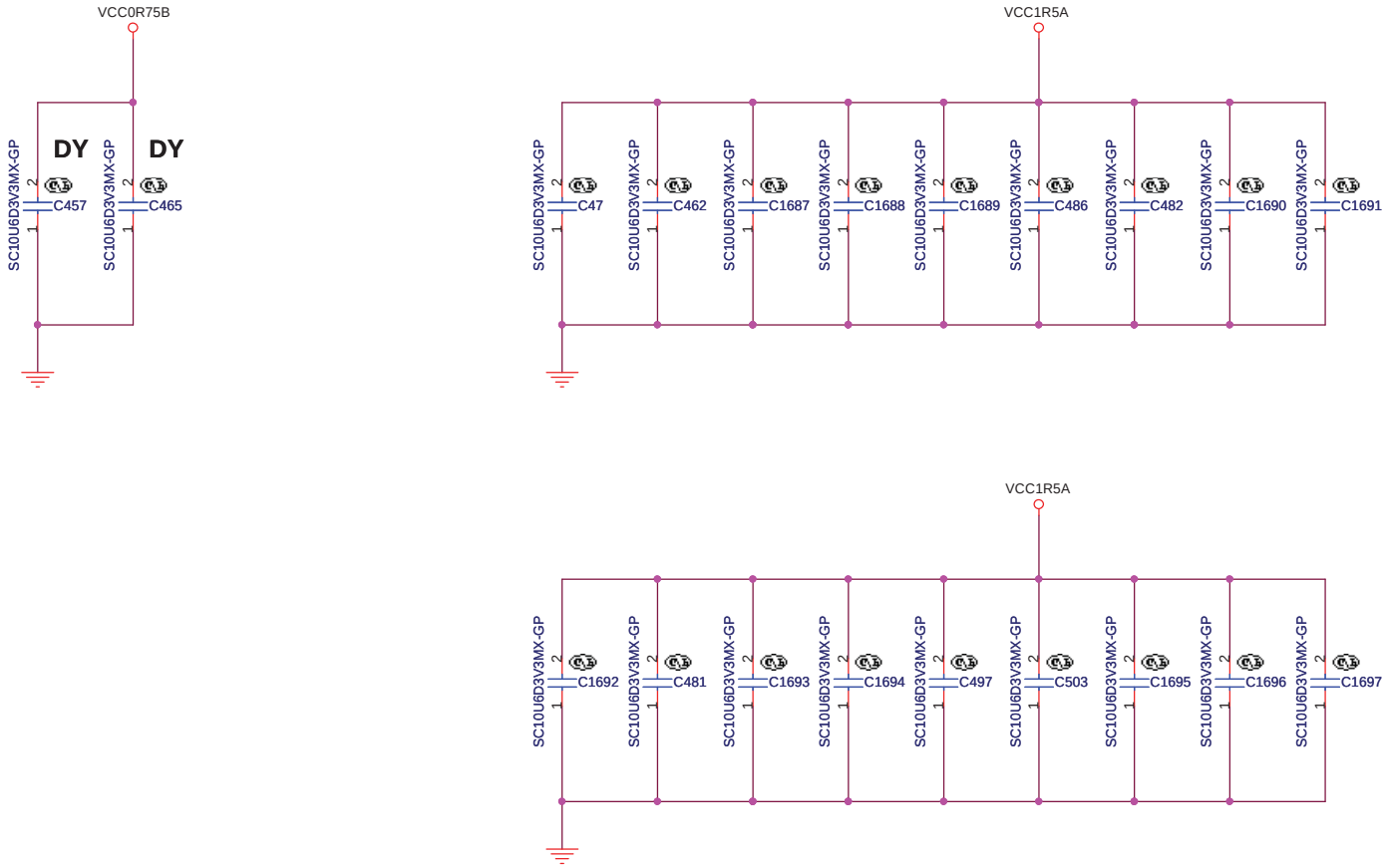
M_A_DQ60 180
M_A_DQ61 182
M_A_DQ62 192
M_A_DQ63 194
DQ0 DQ1 DQ2 DQ3 DQ4 DQ5 DQ6 DQ7 DQ8 DQ9 DQ10 DQ11 DQ12 DQ13 DQ14 DQ15 DQ16 DQ17 DQ18 DQ19 DQ20 DQ21 DQ22 DQ23 DQ24 DQ25 DQ26 DQ27 DQ28 DQ29 DQ30 DQ31 DQ32 DQ33 DQ34 DQ35 DQ36 DQ37 DQ38 DQ39 DQ40 DQ41 DQ42 DQ43 DQ44 DQ45 DQ46 DQ47 DQ48 DQ49 DQ50 DQ51 DQ52 DQ53 DQ54 DQ55 DQ56 DQ57 DQ58 DQ59 DQ60 DQ61 DQ62 DQ63
5 -M_A_DQS[7..0]
-M_A_DQS0 10
-M_A_DQS1 27
-M_A_DQS2 45
-M_A_DQS3 62
-M_A_DQS4 135
-M_A_DQS5 152
-M_A_DQS6 169
-M_A_DQS7 186
DQS0# DQS1# DQS2# DQS3# DQS4# DQS5# DQS6# DQS7#
5 M_A_DQS[7..0]
M_A_DQS0 12
M_A_DQS1 29
M_A_DQS2 47
M_A_DQS3 64
M_A_DQS4 137
M_A_DQS5 154
M_A_DQS6 171
M_A_DQS7 188
DQS0 DQS1 DQS2 DQS3 DQS4 DQS5 DQS6 DQS7
5 M_A_ODT0
5 M_A_ODT1
116 ODT0
120 ODT1
1 VREFDQ
126 VREFCA
2 VSS
77 NC
122 NC
206 GND
207 NP1
RAS# 110
WE# 113
CAS# 115
-M_A_RAS 5
-M_A_WE 5
-M_A_CAS 5
S0# 114
S1# 121
-M_A_CS0 5
-M_A_CS1 5
CKE0 73
CKE1 74
M_A_CKE0 5
M_A_CKE1 5
CK0 101
CK0# 103
M_A_DDRCLK0_666M 5
-M_A_DDRCLK0_666M 5
CK1 102
CK1# 104
M_A_DDRCLK1_666M 5
-M_A_DDRCLK1_666M 5
DM0 11
DM1 28
DM2 46
DM3 63
DM4 136
DM5 153
DM6 170
DM7 187
SDA 200
SCL 202
SMB_DATA_3B 13,71
SMB_CLK_3B 13,71
SPD ADDRESS 50H
VDDSPD 199
SA0 197
SA1 201
EVENT# 198
RESET# 30
TEST 125
-DRAMRST 4,13
VDD 75 76 81 82 87 88 93 94 99 100 105 106 111 112 117 118 123 124
VSS 3 8 9 13 14 19 20 25 26 31 32 37 38 43 44 48 49 54 55 60 61 66 71 72 127 128 133 134 138 139 144 145 150 151 155 156 161 162 167 168 172 173 178 179 184 185 189 190 195 196 203 204 205 208
VTT 187
VTT 205
GND 208
NP2
GP
DDR3-204P-99-GP-U
SKT DDR3 204P AS0A621-U4SK-7H
VCC3B
C1671 SC2D2U10V3KX-1GP
C908 SCD1U10V2KX-4GP
VCC1R5A
C349 SCD1U10V2KX-4GP
C350 SCD1U10V2KX-4GP
PLACE 1UF NEAR VCC1R5A PIN
C64 C74 C86 C1672 C1673 C1674 C1675 C1676 C1677
SC1U10V2KX-1GP
VCC0R75B
C455 C463
SC10U6D3V3MX-GP
C18 C31 C38 C1678
SC1U10V2KX-1GP
DDR3_VREF_DQ
DDR3_VREF
10 DDR3_VREF_DQ_M3
R34 0R2J-2-GP
R35 0R2J-2-GP
DY
DDR3_VREF_CA
10 DDR3_VREF_CA_M3
R38 0R2J-2-GP
R36 0R2J-2-GP
C341 SC2D2U6D3V2MX-GP
C344 SCD1U10V2KX-4GP
Place caps close to pin1 as possible
<Variant Name>
緯創資通 Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.
Title
DDR3 SODIMM-A (REVERSE TYPE)
Size Custom
Document Number SHINAI-3 SWG
Rev SB
Date: Tuesday, September 07, 2010
Sheet 12 of 102
http://hobi-elektronika.net





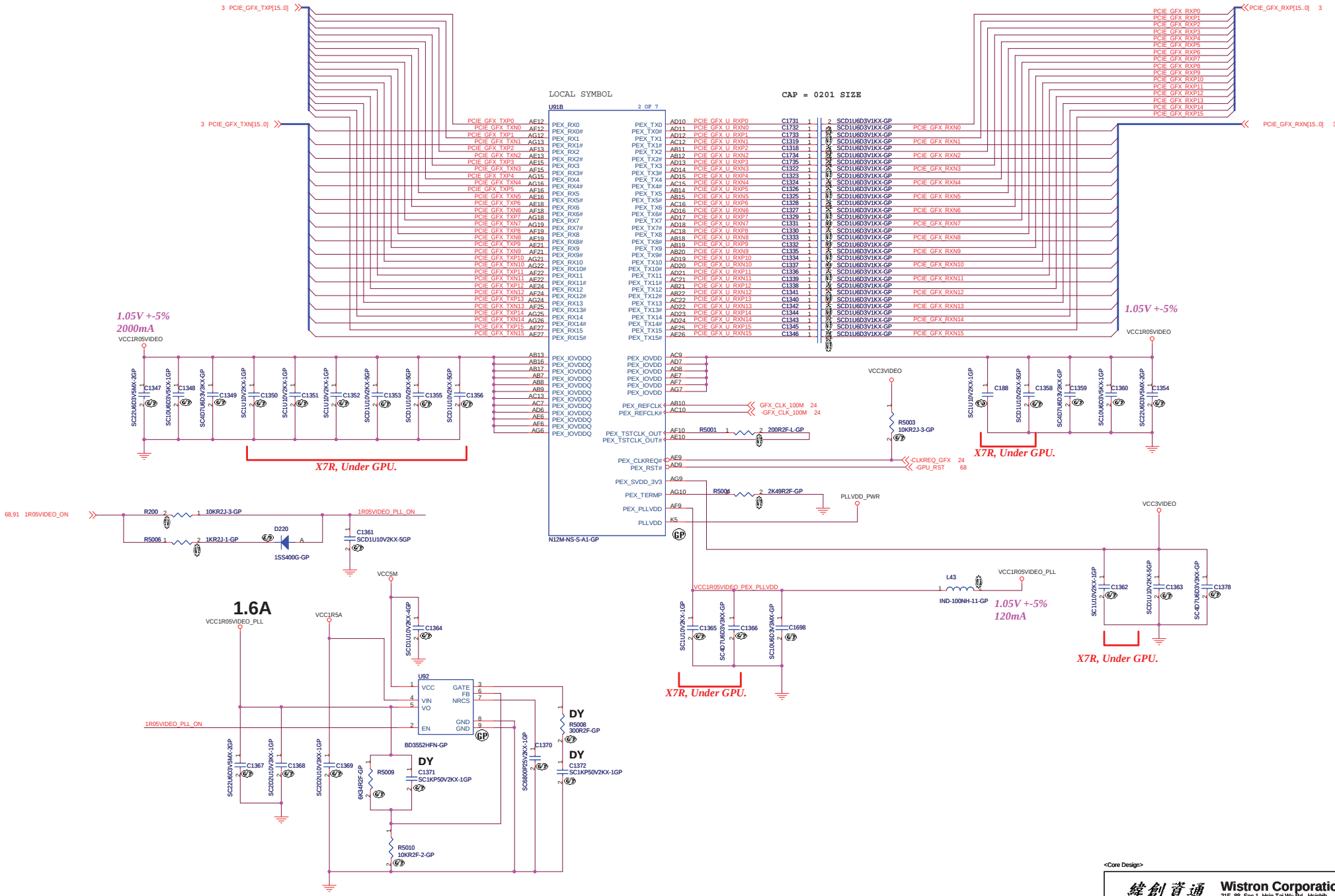
<Variant Name>

緯創資通 **Wistron Corporation**
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title: **DDR3 DECOUPLING**

Size	Document Number	Rev
A4	SHINAI-3 SWG	SB

Date: Tuesday, September 07, 2010 Sheet 14 of 102


LOCAL SYMBOL
CAP = 0201 SIZE
U91B
2 OF 7
N12M-NS-S-A1-GP
1.05V +-5%
2000mA
X7R, Under GPU.
1.05V +-5%
120mA
1.6A
VCC1R05VIDEO
VCC3VIDEO
VCC5M
VCC1R5A
VCC1R05VIDEO_PLL
VCC1R05VIDEO_PEX_PLLVDD
PLLVDD_PWR
1R05VIDEO_PLL_ON
68,91 1R05VIDEO_ON
GFX_CLK_100M 24
-GFX_CLK_100M 24
CLKREQ_GFX 24
-GPU_RST 68
BD3552HFN-GP
IND-100NH-11-GP
1SS400G-GP
DY
http://hobi-elektronika.net
Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.
N12M-NS(1/6): PEG I/F
SHINAI-3 SWG
Tuesday, September 07, 2010
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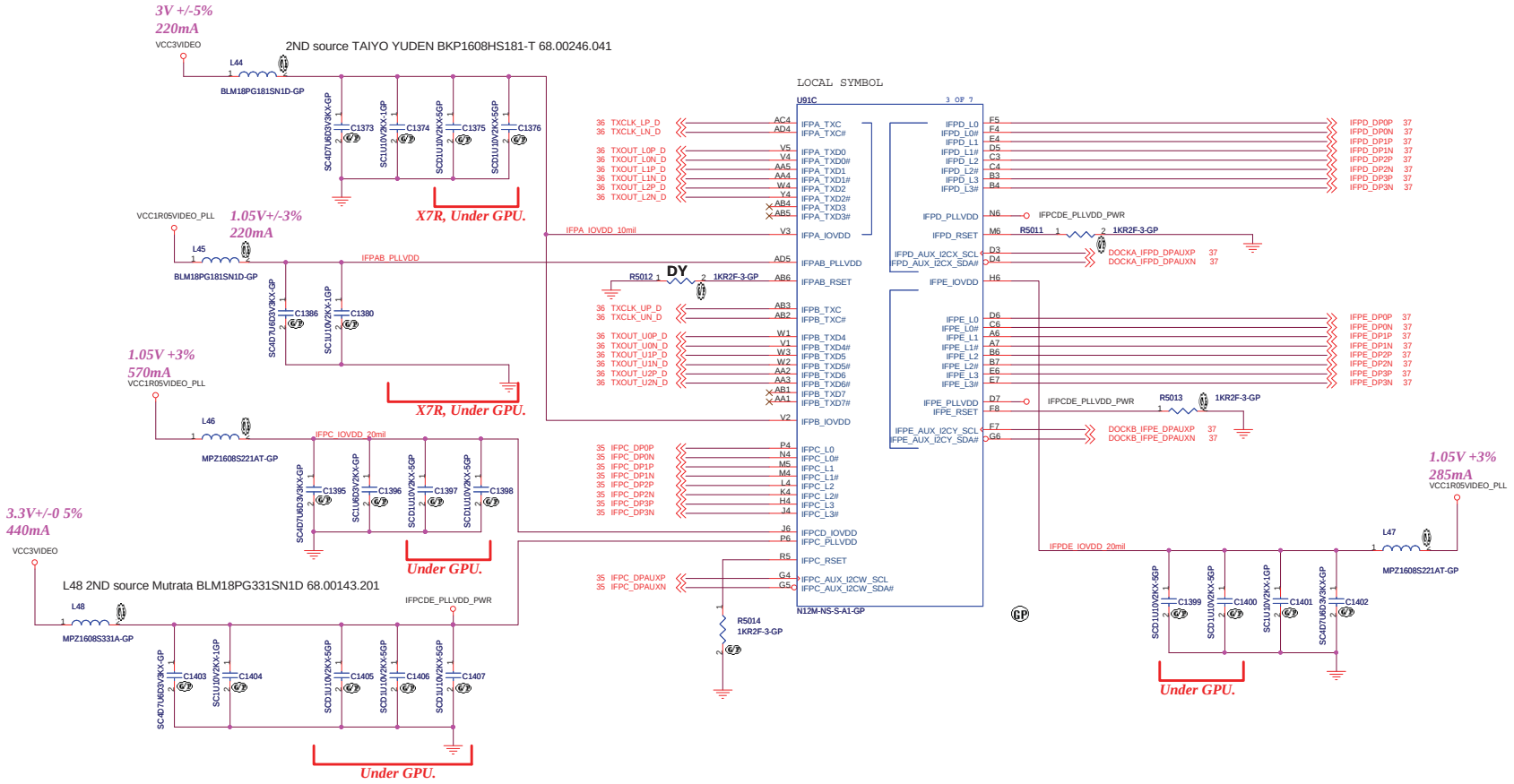
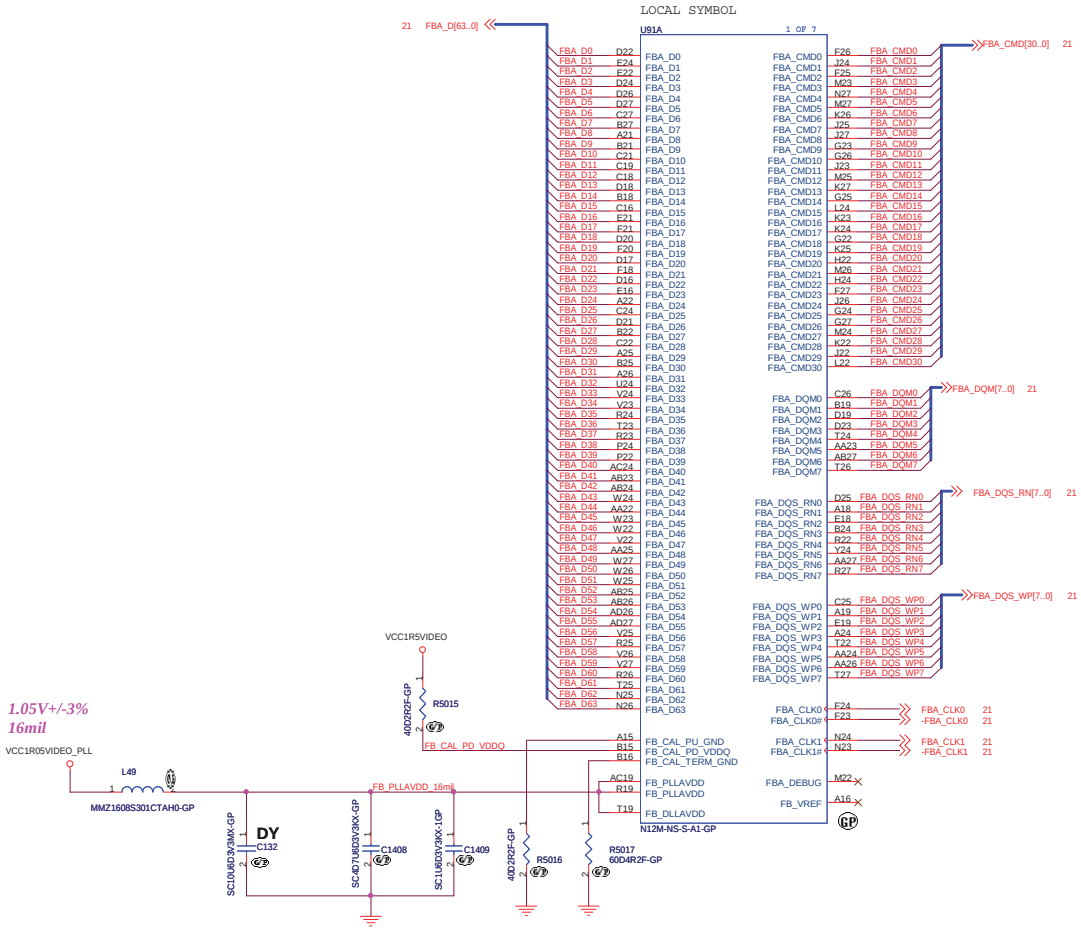
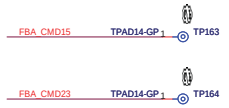
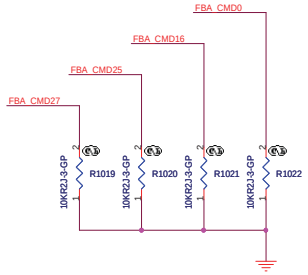


<Core Design>

緯創資通	Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.
Title	N12M-NS(2/6): DIGITAL OUT I/F
Size: C	Document Number: SHINAI-3 SWG
Rev: SB	
Date: Tuesday, September 07, 2010	Sheet 16 of 102







CKE0: FBA_CMD0
ODT0: FBA_CMD25
CKE1: FBA_CMD27
ODT1: FBA_CMD16

Need to confirm on FB_VREF w/ NVIDIA

<Core Design>

緯創資通 Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.		
Title: N12M-NS(3/6): VRAM I/F		
Size: C	Document Number: SHINAI-3 SWG	Rev: SB
Date: Tuesday, September 07, 2010	Sheet 17 of 102	

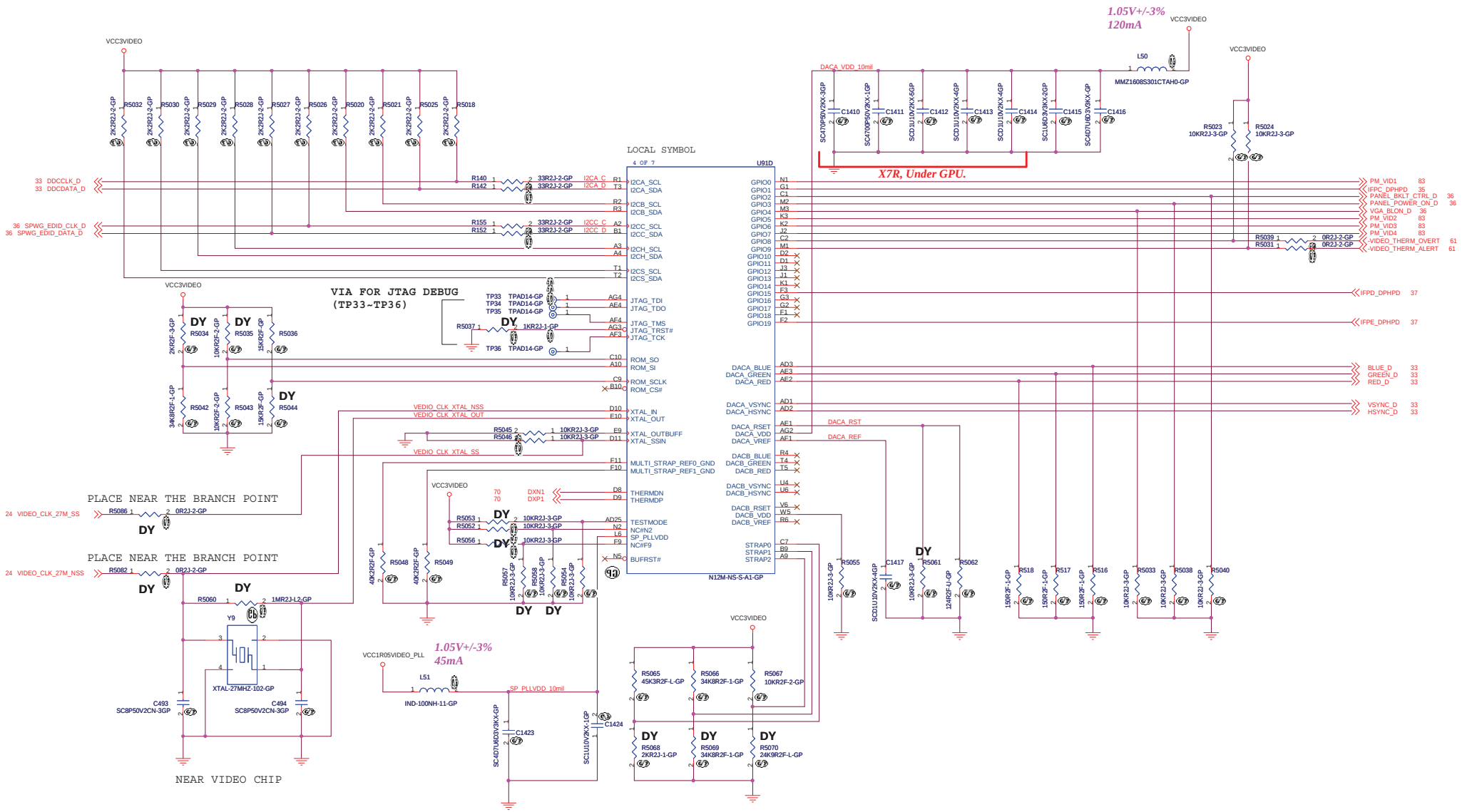


TABLE P/N 61Y9503

VENDOR	PART	WISTRON P/N
TXC	7M27000149	61Y9503AA
RIVER	FCX-04-27.0000M-8.5PF-50PPM	61Y9503BA

TABLE

	N10M-NS	N12M-NS
STRAP0	45K PU	45K PU
STRAP1	35K PU	35K PU
STRAP2	25K PU for DEV ID 0xA6C	10K PU for DEV ID 0xA79

TABLE

	N10M-NS	N12M-NS
ROM_SCLK R5044	15K PD for DEV ID 0x0A6C	15K PD for DEV ID 0x0A79

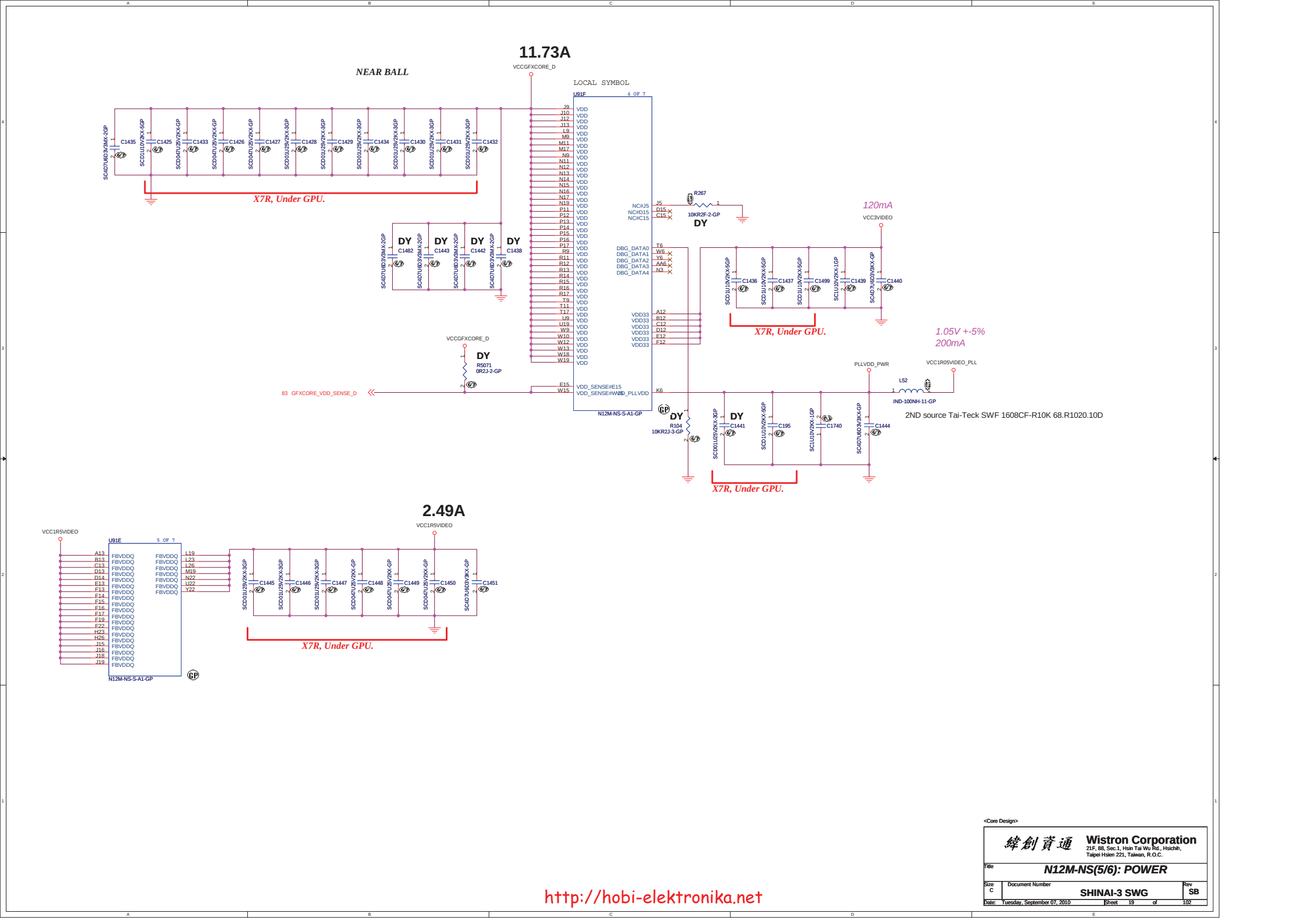
<Core Design>

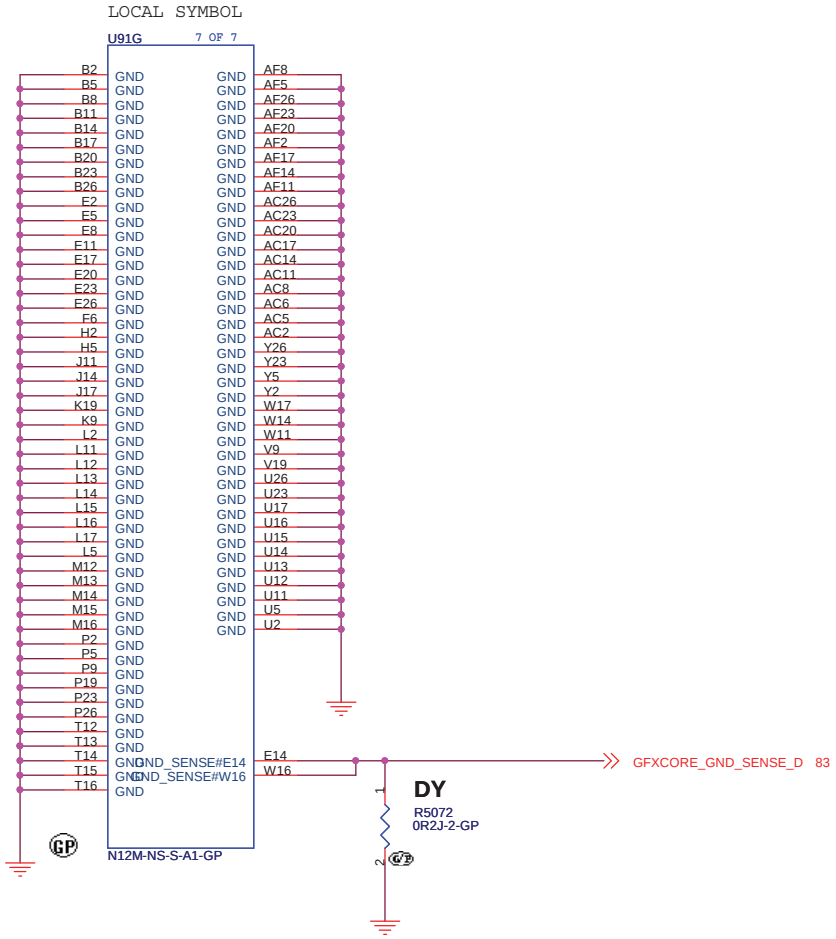
Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title: N12M-NS(4/6): GPIO

Size: C | Document Number: SHINAI-3 SWG | Rev: SB

Date: Tuesday, September 07, 2010 | Sheet 18 of 102

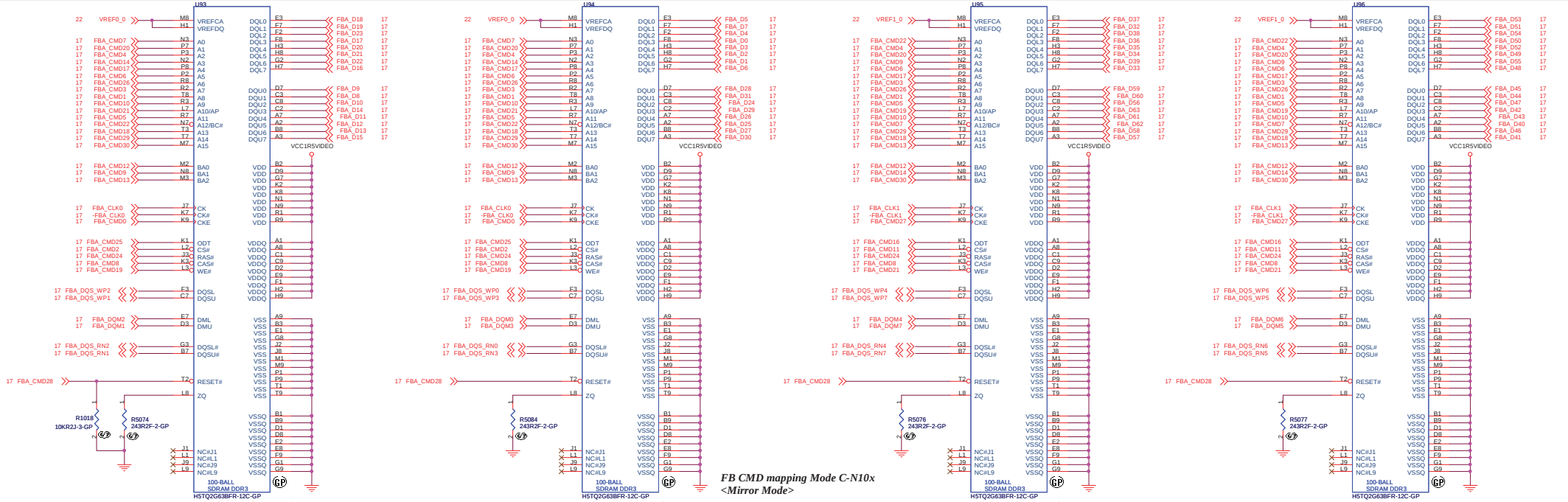

11.73A
NEAR BALL
VCCGFXCORE_D
LOCAL SYMBOL
U91F
6 OF 7
X7R, Under GPU.
120mA
VCC3VIDEO
1.05V +-5%
200mA
PLLVDD_PWR
VCC1R05VIDEO_PLL
2ND source Tai-Teck SWF 1608CF-R10K 68.R1020.10D
N12M-NS-S-A1-GP
2.49A
VCC1R5VIDEO
U91E
5 OF 7
http://hobi-elektronika.net
Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.
N12M-NS(5/6): POWER
SHINAI-3 SWG
Tuesday, September 07, 2010



<Core Design>

緯創資通 Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title	N12M-NS(6/6): GND	
Size B	Document Number SHINAI-3 SWG	Rev SB
Date: Tuesday, September 07, 2010	Sheet 20 of 102	



TOP/LEFT SIDE M0

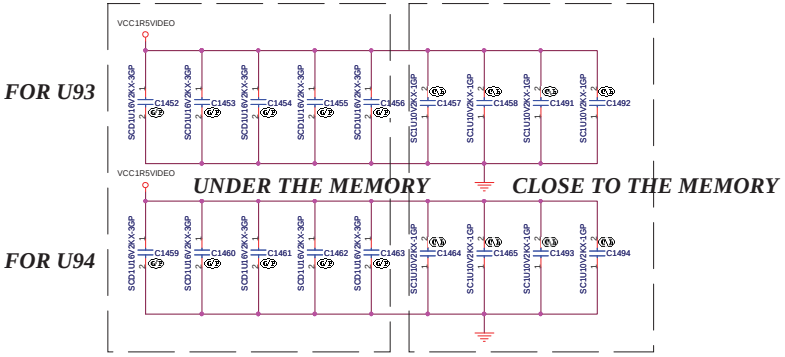
BOTTIM/LEFT SIDE M1

BOTTIM/RIGHT SIDE M3

FB CMD mapping Mode C-N10x
<Mirror Mode>

DDR3 Power consumption 1.5V@800MHz
Hynix :IDD4W=220.4mA
Samsung :IDD7=150.4mA (calcurated)

Need to Check the DDR3 VDD Current rating.



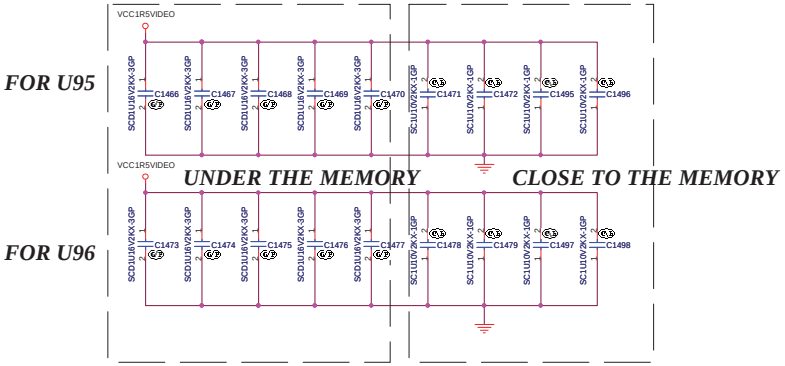


TABLE
DDR3 VIDEO MEMORY

	SAMSUNG	HYNIX
U93 U94 U95 U96	K4W1G1646E-HC12	H5TQ1G63BFR-12C

LOGIC

MEMORY CHANNEL A

<Core Design>

緯創資通 Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title: *VRAM CHANNEL-A*

Size: A2 | Document Number: SHINAI-3 SWG | Rev: SB

Date: Tuesday, September 07, 2010 | Sheet 21 of 102

<Core Design>

緯創資通 **Wistron Corporation**
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

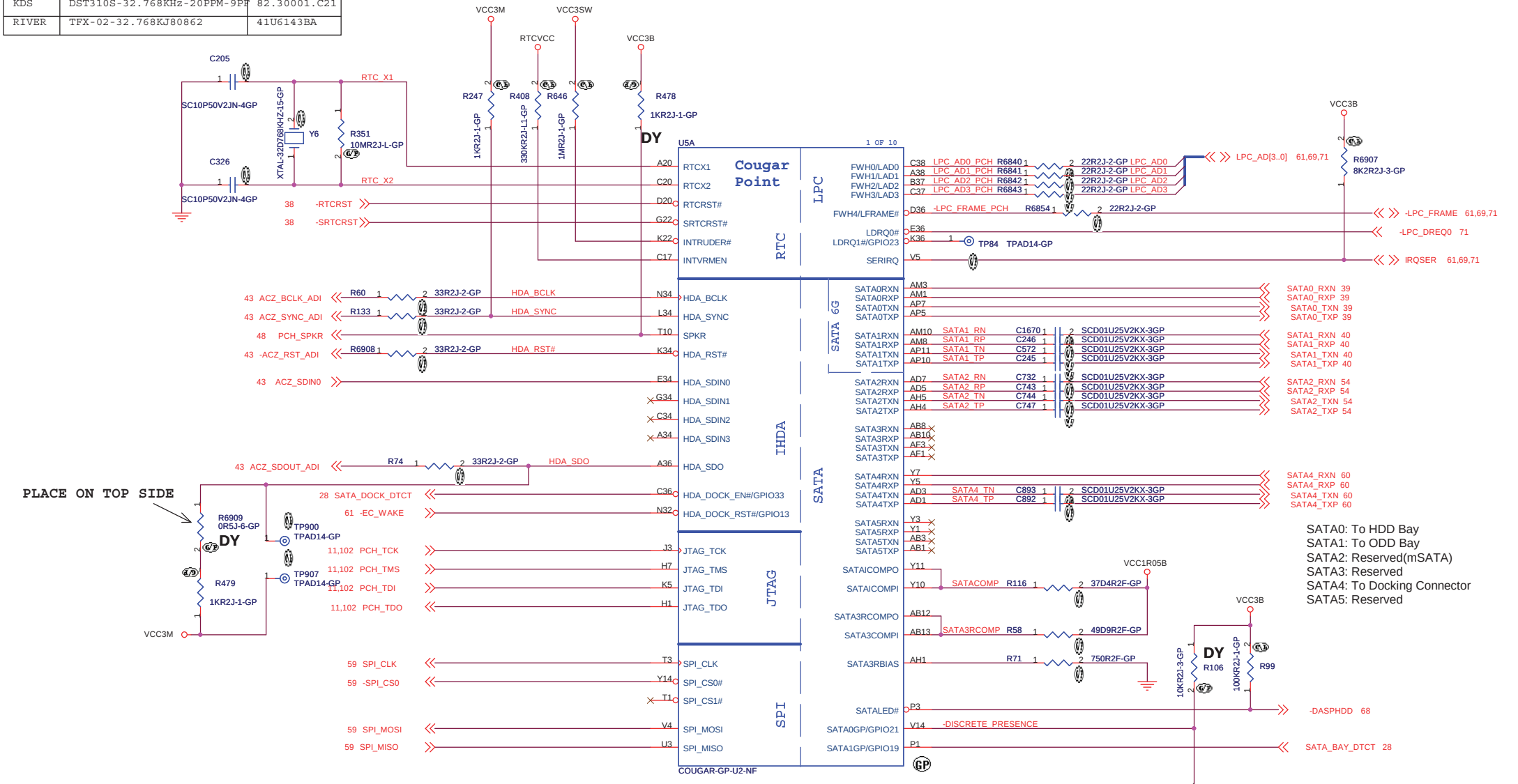
Title: ***DYNAMIC MEMORY TERMINATION***

Size	Document Number	Rev
A3	SHINAI-3 SWG	SB

Date: Tuesday, September 07, 2010 Sheet 22 of 102

TABLE P/N 41U6143

VENDOR	PART	WISTRON P/N
KDS	DST310S-32.768KHz-20PPM-9PF	82.30001.C21
RIVER	TFX-02-32.768KJ80862	41U6143BA



TABLE

AMT RPAT	YES YES	NO YES	NO NO
U5	QM67	HM67	HM65

LOGIC

TABLE

SPKR TCO TIMER SYSTEM REBOOT
HIGHL: DISABLED (NO REBOOT)
LOW: ENABLED

TABLE

MSATA	YES	NO
C732	ASM	NO_ASM
C743	ASM	NO_ASM
C744	ASM	NO_ASM
C747	ASM	NO_ASM

LOGIC

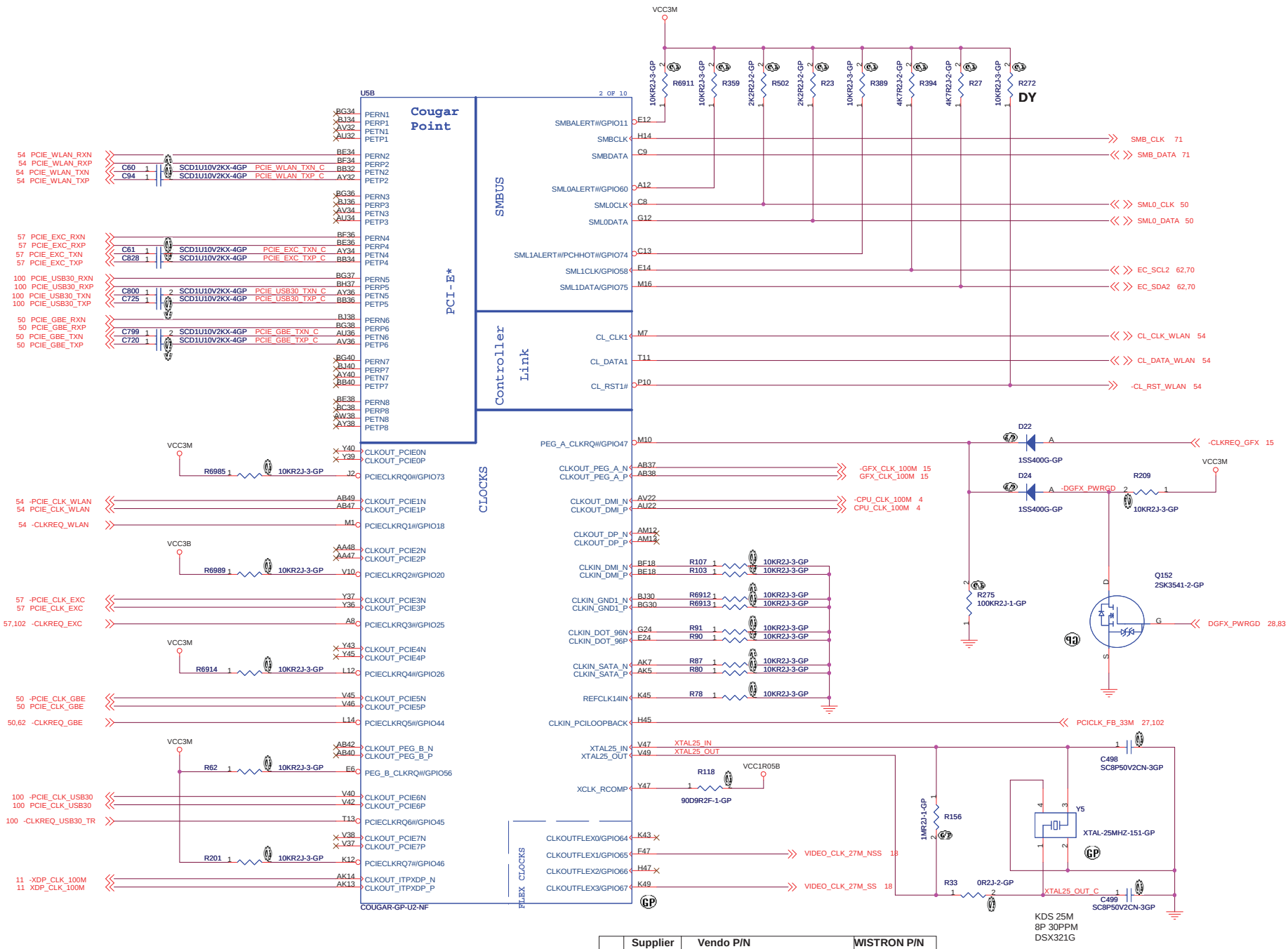
<Variant Name>

緯創資通 Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title: PCH (1/9):HDA/JTAG/SATA

Size: A3 | Document Number: SHINAI-3 SWG | Rev: SB

Date: Tuesday, September 07, 2010 | Sheet 23 of 102



	Supplier	Vendo P/N	WISTRON P/N
1	KDS	DSX321G 25M 8PF 30PPM	61Y9504AA
2	TXC	7V25080004	82.30020.E71
3	RIVER	FCX-04-25MJ90730	82.30020.E51

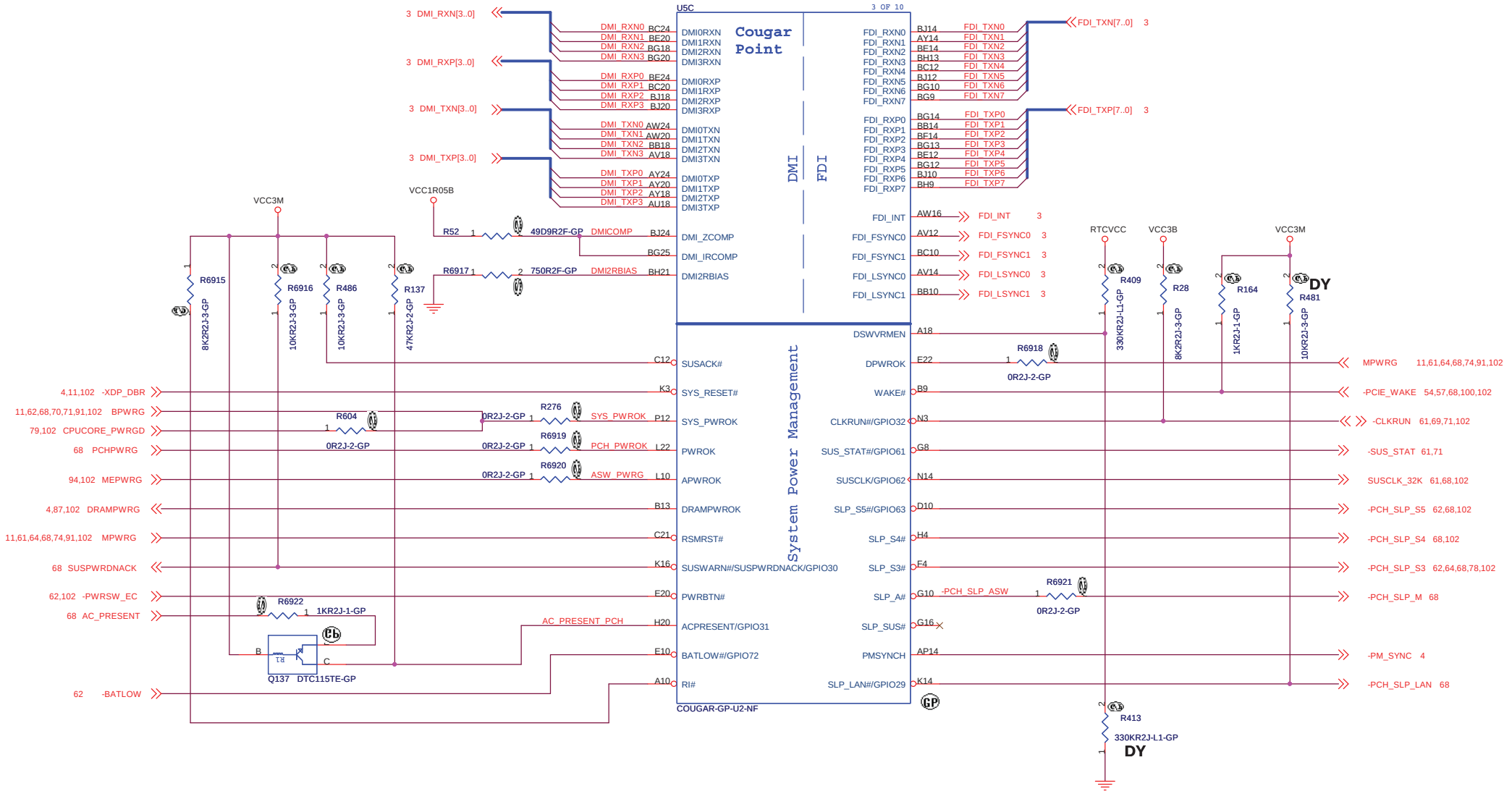
<Variant Name>

Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title: PCH(2/9):PCIE/SMBUS/CLK

Size: Custom | Document Number: SHINAI-3 SWG | Rev: SB

Date: Tuesday, September 07, 2010 | Sheet 24 of 102



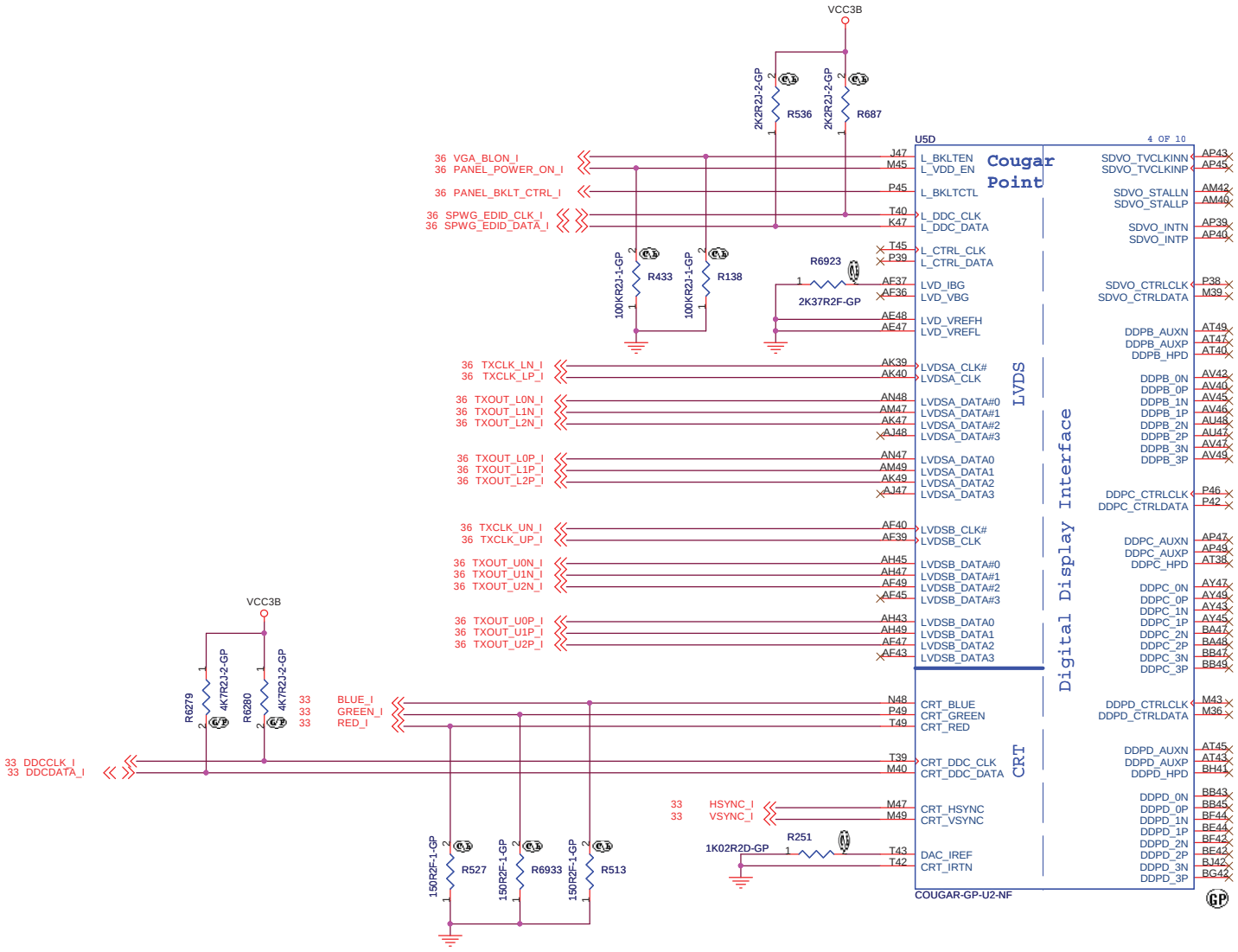
<Variant Name>

緯創資通 Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

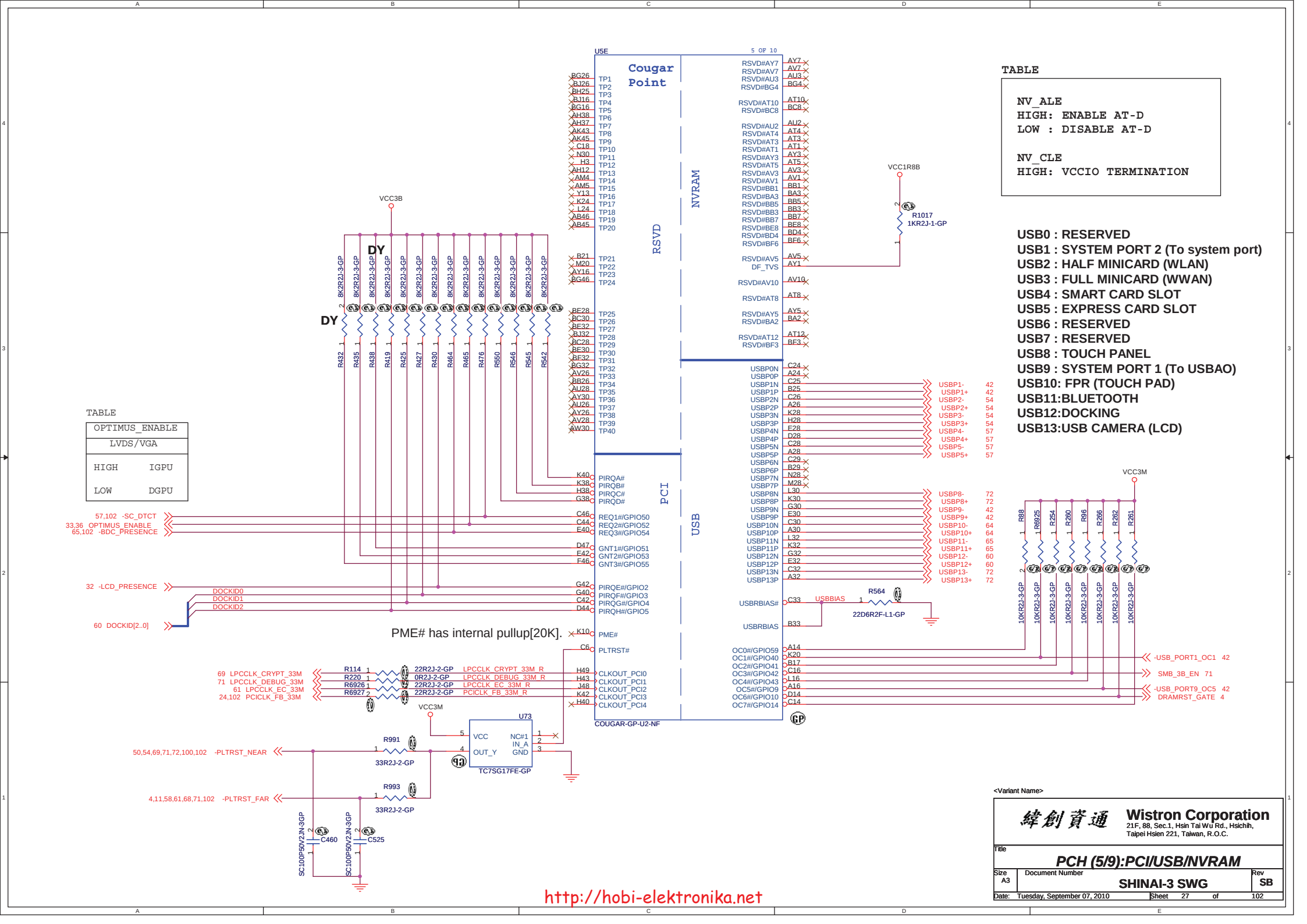
Title: PCH (3/9):DMI/FDI/PM

Size: A3 | Document Number: SHINAI-3 SWG | Rev: SB

Date: Tuesday, September 07, 2010 | Sheet 25 of 102



緯創資通	**Wistron Corporation** 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.
Title	***PCH (4/9):LVDS/CRT/DDI***
Size: A3	Document Number: **SHINAI-3 SWG**
Rev: SB	
Date: Tuesday, September 07, 2010	Sheet 26 of 102


U5E
Cougar Point
5 OF 10
RSVD
NVRAM
PCI
USB
COUGAR-GP-U2-NF
VCC3B
VCC1R8B
VCC3M
DY
R1017
1KR2J-1-GP
R564
22D6R2F-L1-GP
USBBIAS
PME# has internal pullup[20K].
U73
TC7SG17FE-GP
R991
33R2J-2-GP
R993
33R2J-2-GP
C460
C525
SC100P50V2JN-3GP
SC100P50V2JN-3GP
TABLE
OPTIMUS_ENABLE
LVDS/VGA
HIGH IGPU
LOW DGPU
TABLE
NV_ALE
HIGH: ENABLE AT-D
LOW : DISABLE AT-D
NV_CLE
HIGH: VCCIO TERMINATION
USB0 : RESERVED
USB1 : SYSTEM PORT 2 (To system port)
USB2 : HALF MINICARD (WLAN)
USB3 : FULL MINICARD (WWAN)
USB4 : SMART CARD SLOT
USB5 : EXPRESS CARD SLOT
USB6 : RESERVED
USB7 : RESERVED
USB8 : TOUCH PANEL
USB9 : SYSTEM PORT 1 (To USBAO)
USB10: FPR (TOUCH PAD)
USB11:BLUETOOTH
USB12:DOCKING
USB13:USB CAMERA (LCD)
<Variant Name>
緯創資通
Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.
Title
PCH (5/9):PCI/USB/NVRAM
Size A3
Document Number
SHINAI-3 SWG
Rev SB
Date: Tuesday, September 07, 2010
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http://hobi-elektronika.net

TABLE

GPIO15: ME CRYPTO STRAP HIGH: WITH CONFIDENTIALITY LOW: NO CONFIDENTIALITY

TABLE

GPIO8: INTEGRATED HIGH: DISABLED(BTM) LOW: ENABLED(FCIM)

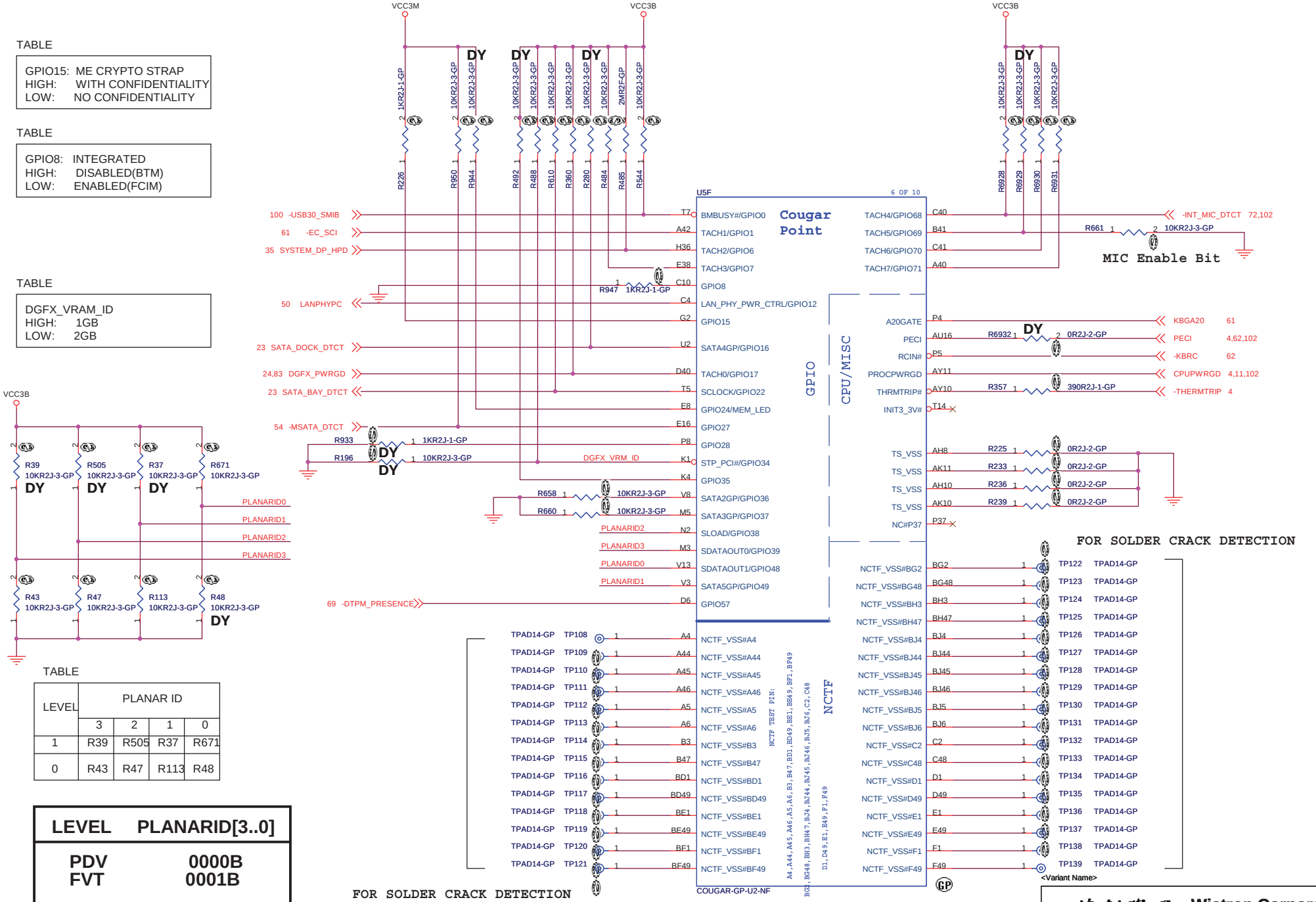
TABLE

DGFX_VRAM_ID HIGH: 1GB LOW: 2GB

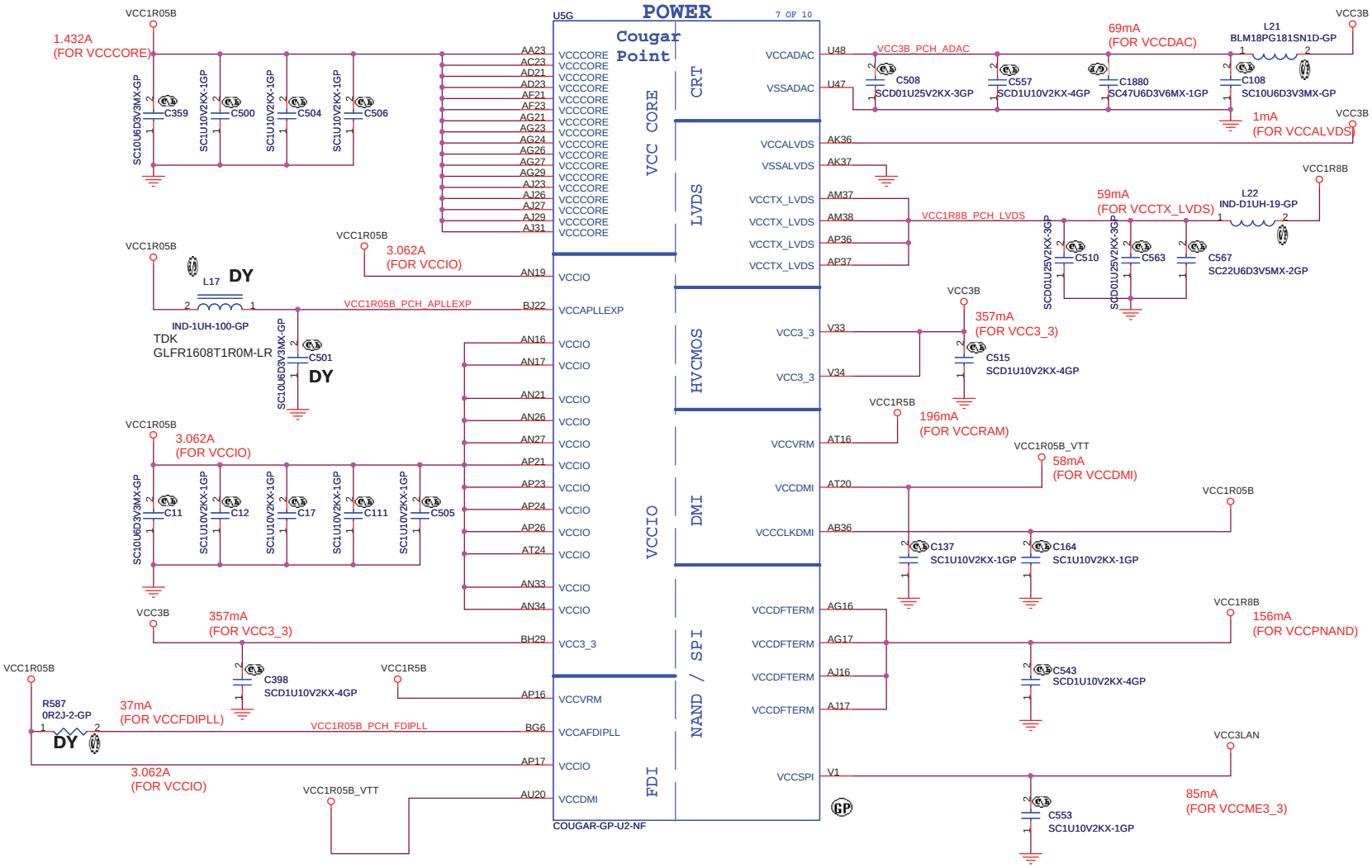
TABLE

LEVEL	PLANAR ID			
	3	2	1	0
1	R39	R505	R37	R671
0	R43	R47	R113	R48

LEVEL	PLANARID[3..0]
PDV	0000B
FVT	0001B



緯創資通	Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.
Title	PCH (6/9):GPIO/NTCF/RSVD
Size A3	Document Number SHINAI-3 SWG Rev SB
Date: Tuesday, September 07, 2010	Sheet 28 of 102



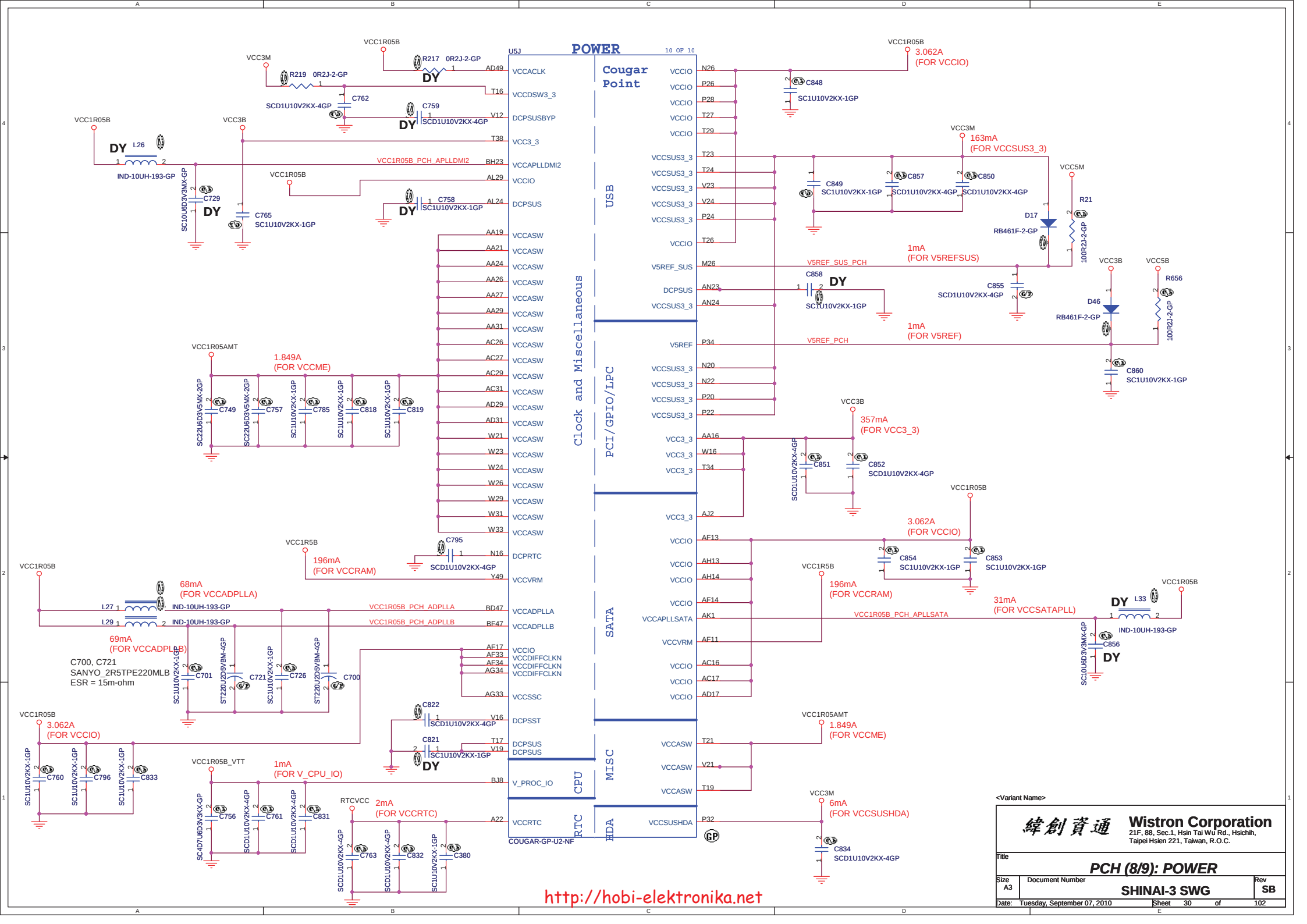
<Variant Name>

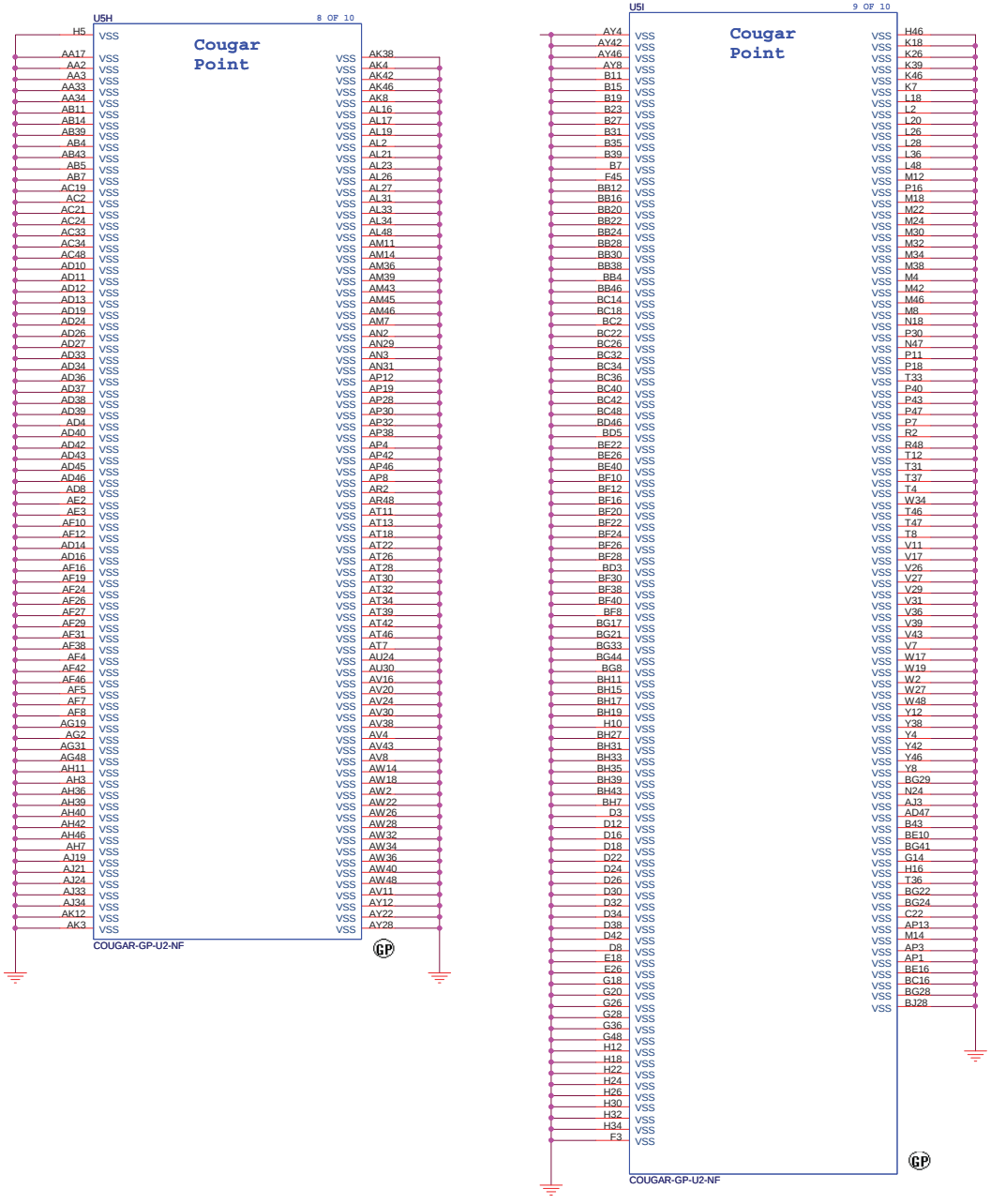
緯創資通 Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title: PCH (7/9): POWER

Size	Document Number	Rev
A3	SHINAI-3 SWG	SB

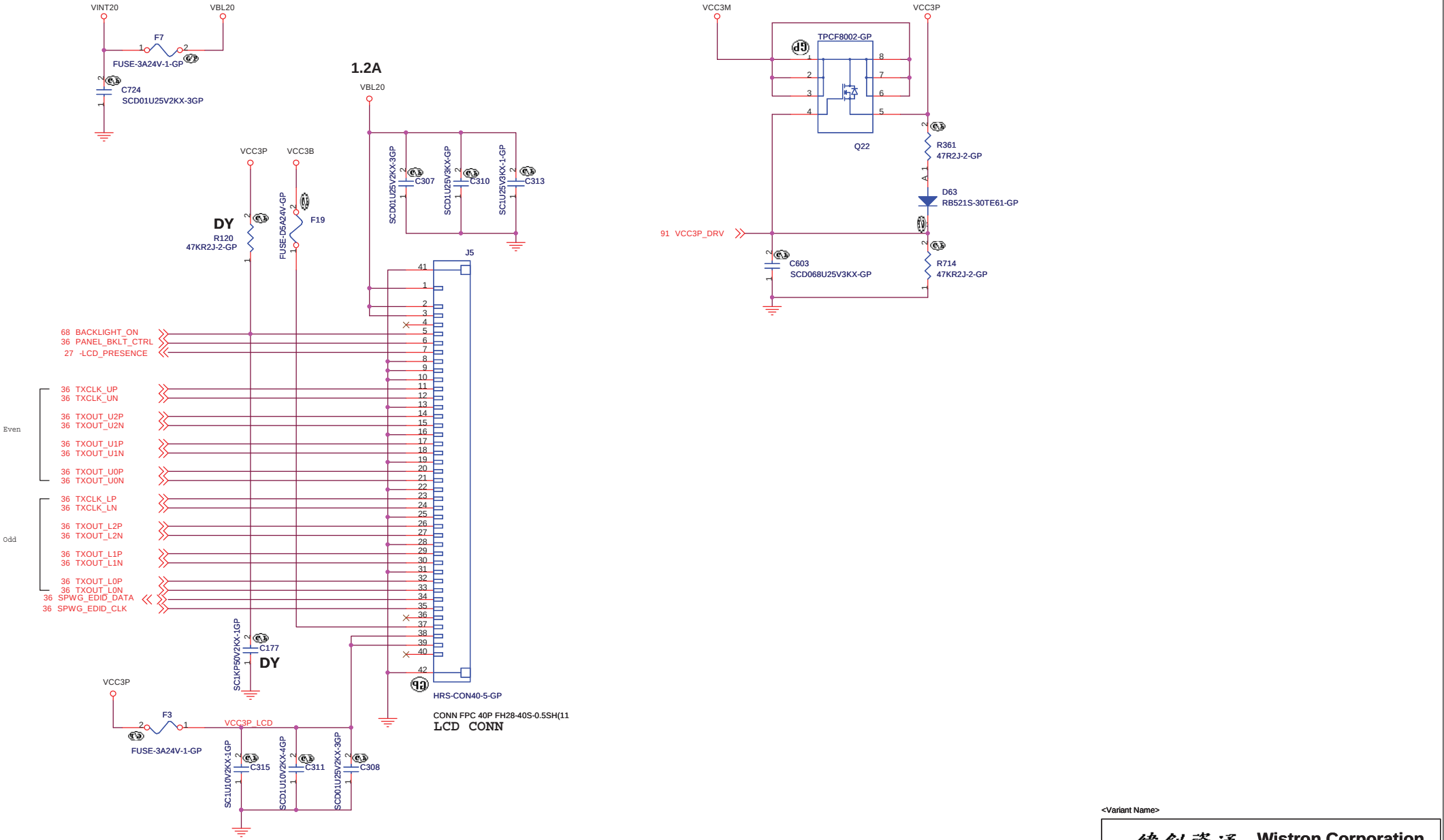
Date: Tuesday, September 07, 2010 Sheet 29 of 102


POWER
10 OF 10
U5J
Cougar Point
Clock and Miscellaneous
USB
PCI/GPIO/LPC
SATA
MISC
CPU
RTC
HDA
COUGAR-GP-U2-NF
VCCACLK
VCCDSW3_3
DCPSUSBYP
VCC3_3
VCCAPLLDMI2
VCCIO
DCPSUS
VCCASW
DCPRTC
VCCVRM
VCCADPLLA
VCCADPLLB
VCCDIFFCLKN
VCCSSC
DCPSST
V_PROC_IO
VCCRTC
VCCSUS3_3
V5REF_SUS
V5REF
VCCAPLLSATA
VCCSUSHDA
VCC1R05B
VCC3M
VCC3B
VCC1R05AMT
VCC1R5B
VCC1R05B_VTT
RTCVCC
VCC5M
VCC5B
3.062A
(FOR VCCIO)
163mA
(FOR VCCSUS3_3)
1mA
(FOR V5REFSUS)
1mA
(FOR V5REF)
357mA
(FOR VCC3_3)
1.849A
(FOR VCCME)
196mA
(FOR VCCRAM)
68mA
(FOR VCCADPLLA)
69mA
(FOR VCCADPLLB)
31mA
(FOR VCCSATAPLL)
1mA
(FOR V_CPU_IO)
2mA
(FOR VCCRTC)
6mA
(FOR VCCSUSHDA)
C700, C721
SANYO_2R5TPE220MLB
ESR = 15m-ohm
VCC1R05B_PCH_APLLDMI2
VCC1R05B_PCH_ADPLLA
VCC1R05B_PCH_ADPLLB
V5REF_SUS_PCH
V5REF_PCH
VCC1R05B_PCH_APLLSATA
IND-10UH-193-GP
SCD1U10V2KX-4GP
SC1U10V2KX-1GP
SC22U6D3V5MX-2GP
SC10U6D3V3MX-GP
SC4D7U6D3V3KX-GP
ST220U2D5VBM-4GP
RB461F-2-GP
100R2J-2-GP
<Variant Name>
Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.
Title
PCH (8/9): POWER
Size A3
Document Number
SHINAI-3 SWG
Rev SB
Date: Tuesday, September 07, 2010
Sheet 30 of 102
http://hobi-elektronika.net



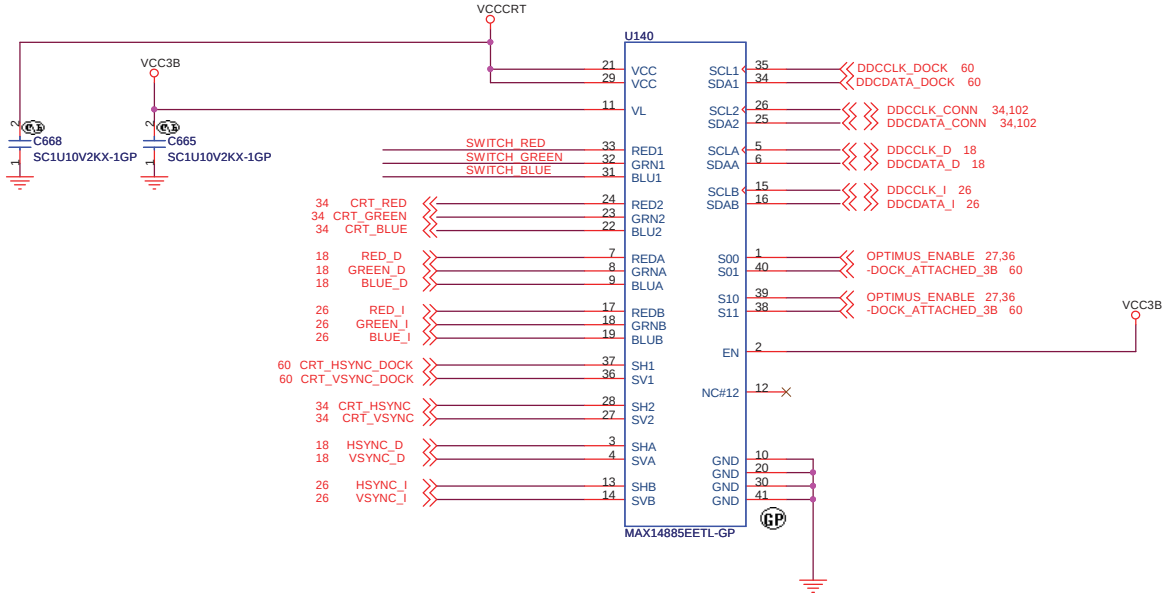
緯創資通	**Wistron Corporation** 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title	***PCH (9/9):GND***	
Size: Custom	Document Number: SHINAI-3 SWG	Rev: SB
Date: Tuesday, September 07, 2010	Sheet 31 of 102	

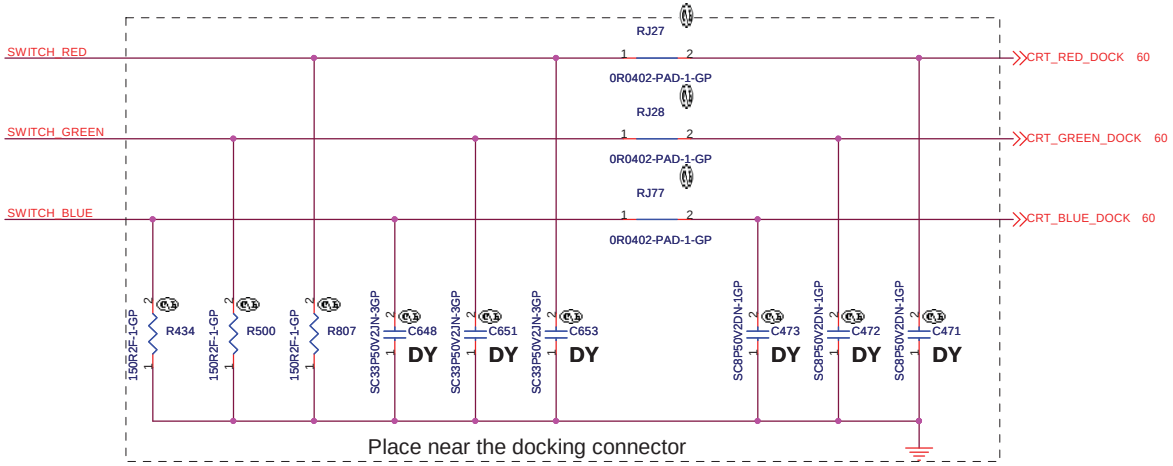
LCD / Inverter Connector



<Variant Name>

緯創資通 Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.		
Title: LCD CONNECTOR		
Size: A3	Document Number: SHINAI-3 SWG	Rev: SB
Date: Tuesday, September 07, 2010	Sheet 32 of 102	





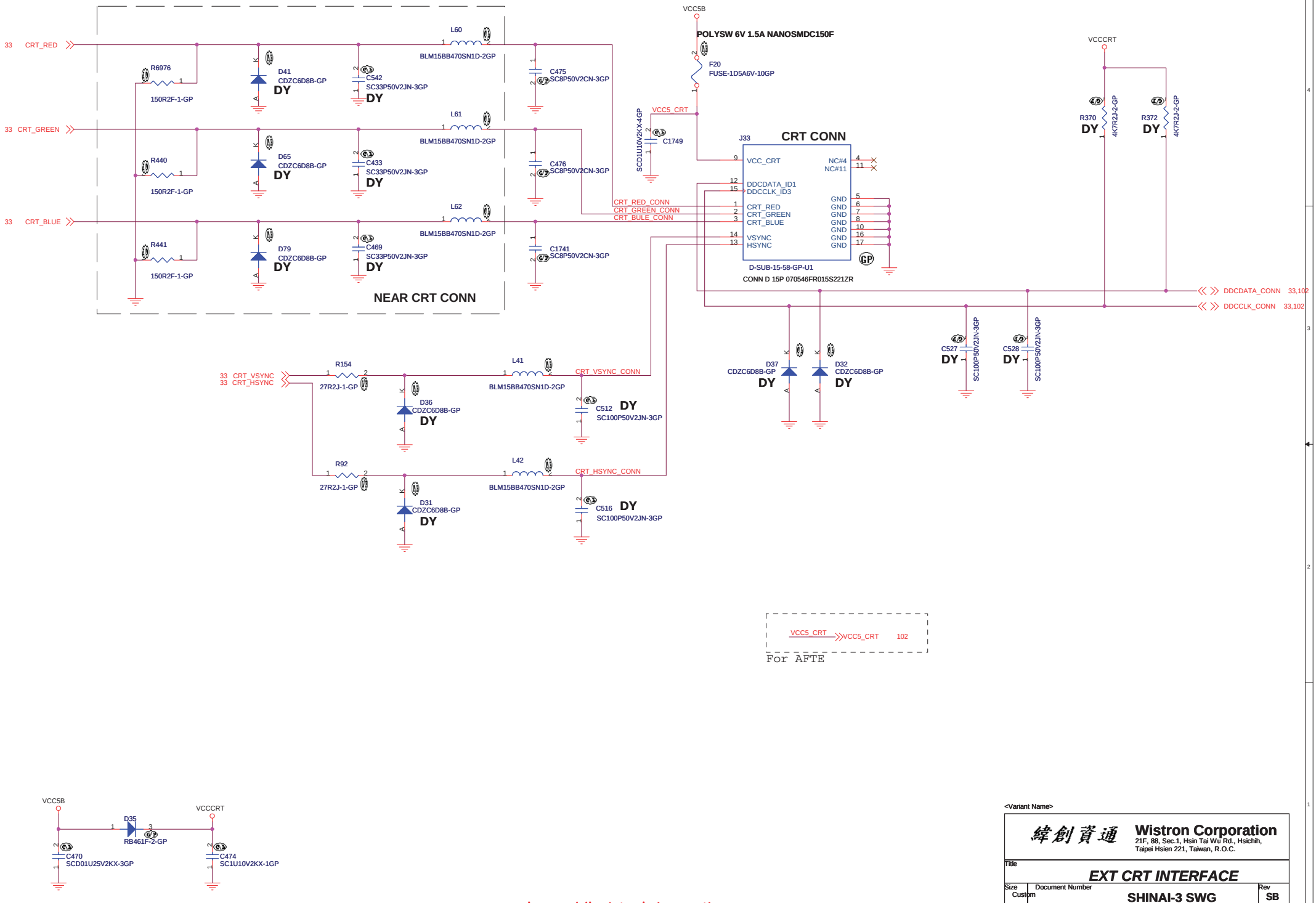
<Variant Name>

Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title: RGB SWITCH

Size: A3 | Document Number: SHINAI-3 SWG | Rev: SB

Date: Tuesday, September 07, 2010 | Sheet 33 of 102



<Variant Name>

緯創資通 Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title: EXT CRT INTERFACE

Size	Document Number	Rev
Custom	SHINAI-3 SWG	SB

Date: Tuesday, September 07, 2010 Sheet 34 of 102

To prevent leakage current from DP monitor

VCC3B
VCC3VIDEO_DP
POLYSW 6V 1.5A NANOSMDC150F
Typ. 500mA
Max. 3A
F18
FUSE-1D5A6V-10GP
Q147
TPCF8002-GP
C1133
SC4D7U6D3V3KX-GP
C1589
SC100U6D3V6MX-GP
DY
91,95 VCC3B_DRV
R421
470KR2J-2-GP
C1113
SCD01U25V2KX-3GP

VCC5B
C1744
SCD1U10V2KX-5GP
U111
TC7WB126FK-1GP
16 IFPC_DPAUXN
16 IFPC_DPAUXP
C1742
C1743
SCD1U10V2KX-5GP
100KR2J-1-GP
R282
R6977

VCC3B
VCC3VIDEO
R157
1MR2J-1-GP
18 IFPC_DPHPD
Q131
BSS138-7-F-GP
D120
CDZC6D8B-GP
28 SYSTEM_DP_HPD
Q134
BSS138-7-F-GP

VCC3B
R237
100KR2J-1-GP
VCC3VIDEO_DP
C178
SCD1U10V2KX-4GP

DVI_DONGLE_DTCT
DP_14P
DP_HPD_CONN
DVI_DONGLE_DTCT 102
DP_14P 102
DP_HPD_CONN 102
For AFTE

SKT-USB-353-GP
J34

16 IFPC_DP3N
16 IFPC_DP3P
16 IFPC_DP2N
16 IFPC_DP2P
16 IFPC_DP1N
16 IFPC_DP1P
16 IFPC_DP0N
16 IFPC_DP0P
C1536
C1537
C1535
C1534
C1532
C1745
C1531
C1746
SCD1U10V2KX-5GP

DP_HPD_CONN
DP_AUXN_CONN
DP_AUXP_CONN
DVI_DONGLE_DTCT
DP3N
DP3P
DP2N
DP2P
DP1N
DP1P
DP0N
DP0P
DP_14P

R6978
100KR2J-1-GP
SCD01U25V2KX-3GP
C1120
DY
100KR2J-1-GP
R234
1MR2J-L2-GP
R93
SCD1U25V2KX-GP
C139
5M1R2J-GP
R102

D86
D230
L1#1L1#8
L2#2L2#7
GNDGND
L3#3L3#6
L4#4L4#5
DY
RCLAMP0524P-GP
D121
D122
CDZC6D8B-GP
DY

placed near DP connector

http://hobi-elektronika.net

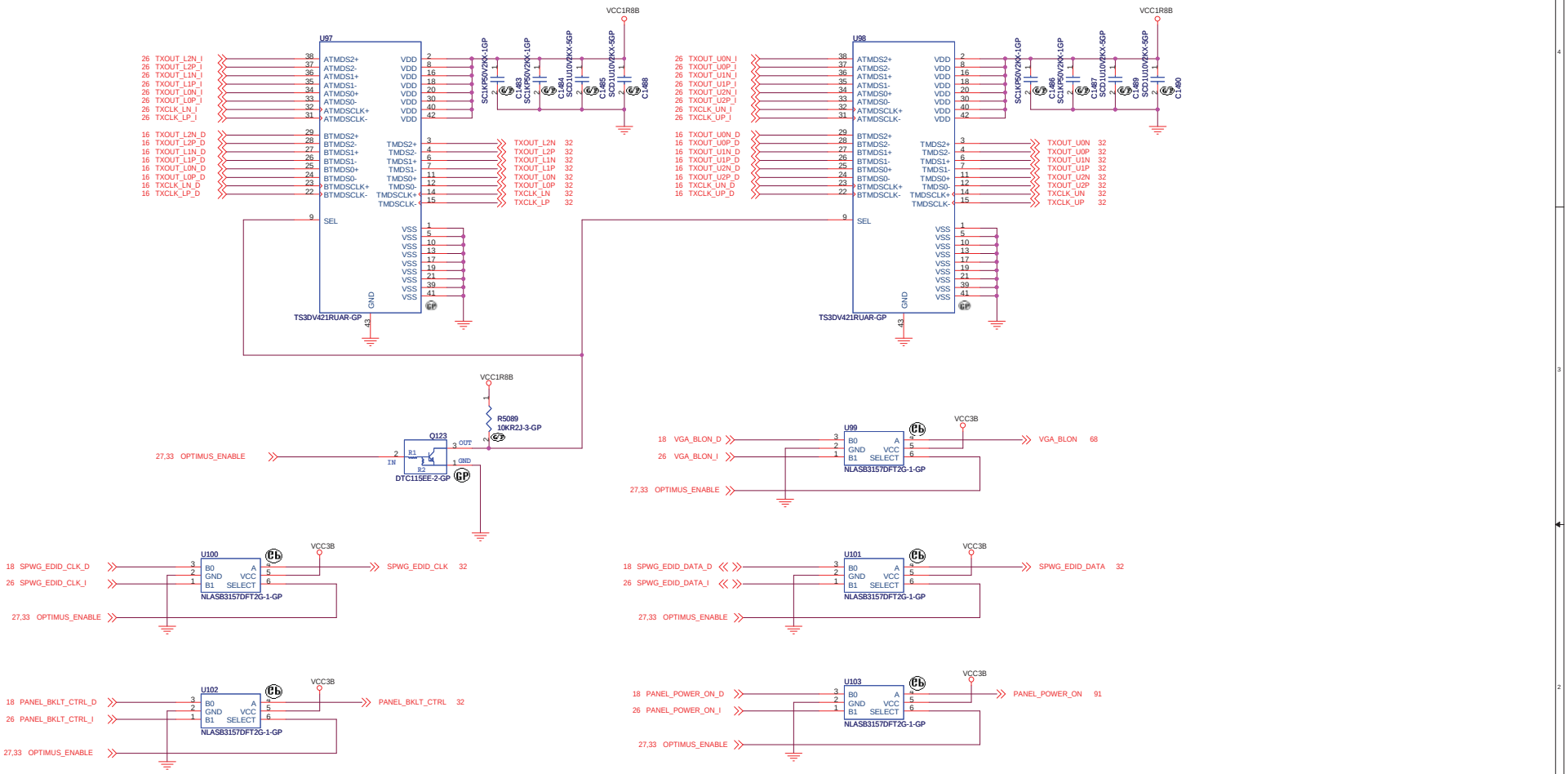
<Variant Name>

緯創資通 Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title: DISPLAY PORT CONNECTOR

Size	Document Number	Rev
A3	SHINAI-3 SWG	SB

Date: Tuesday, September 07, 2010 Sheet 35 of 102



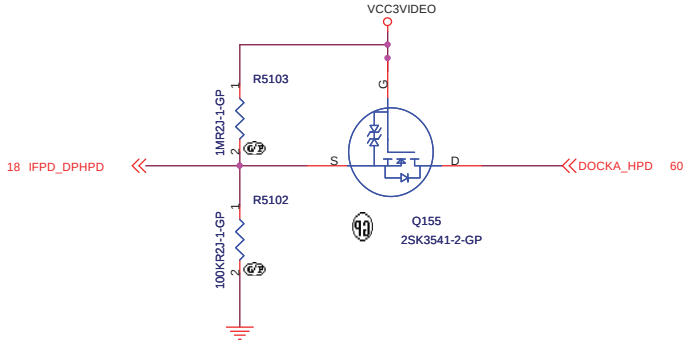
<Core Design>

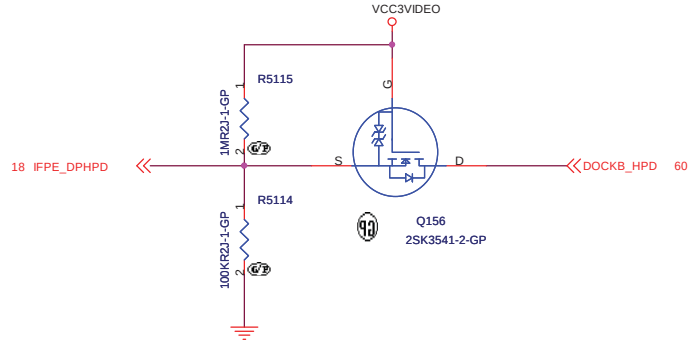
緯創資通 **Wistron Corporation**
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

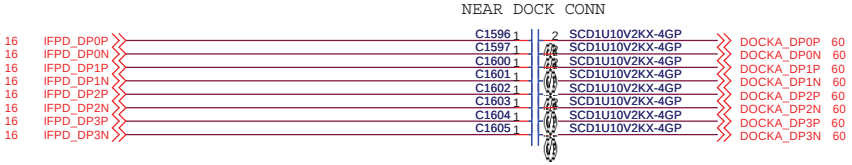
Title: **LVDS SWITCH**

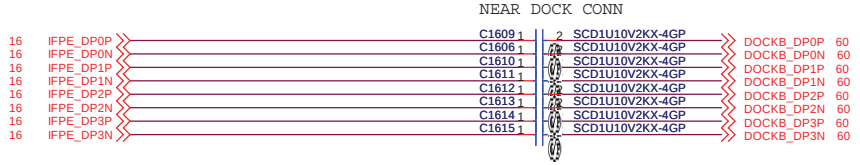
Size	Document Number	Rev
C	SHINAI-3 SWG	SB

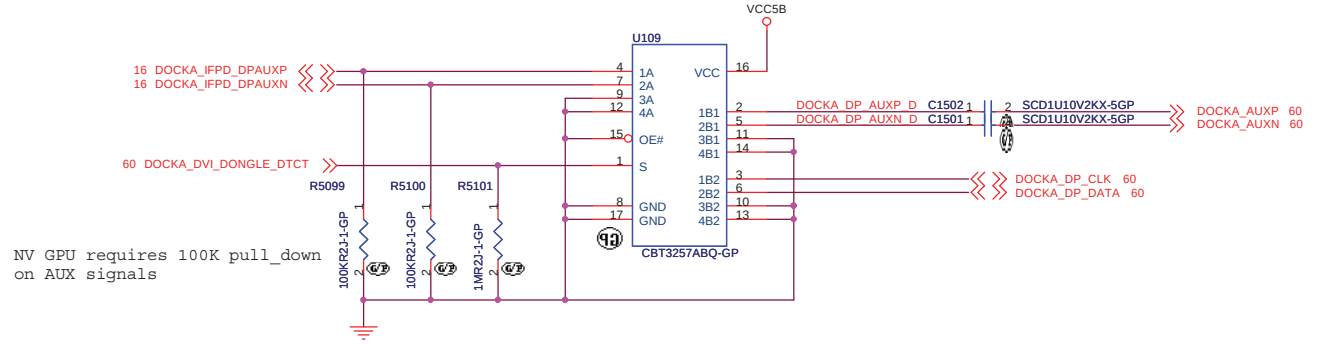
Date: Tuesday, September 07, 2010 Sheet 36 of 102

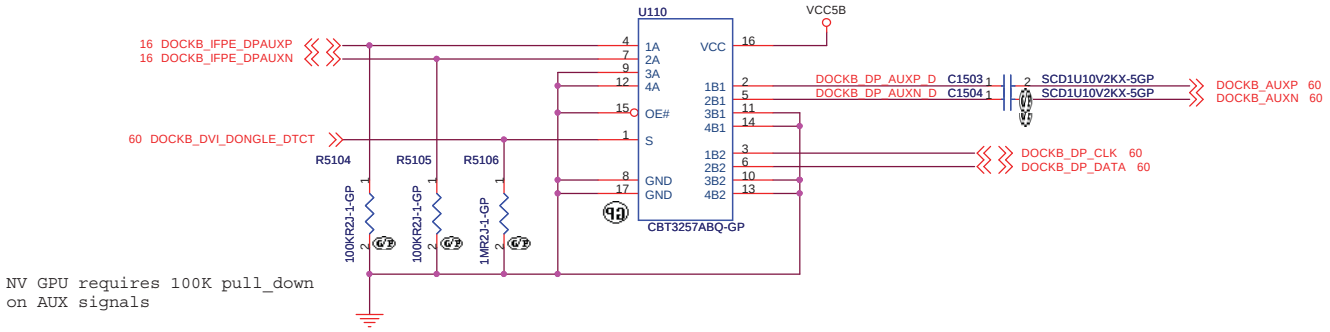












	Vendor P/N	WISTRON P/N
NXP	CBT3257ABQ-GP	45K0213AA
ONSEMI	74FST3257MNTWG-GP	45K0213BA

INPUTS		INPUT/OUTPUT	FUNCTION
OE#	S	A	
L	L	B1	A port = B1 port
L	H	B2	A port = B2 port
H	X	Z	Disconnect

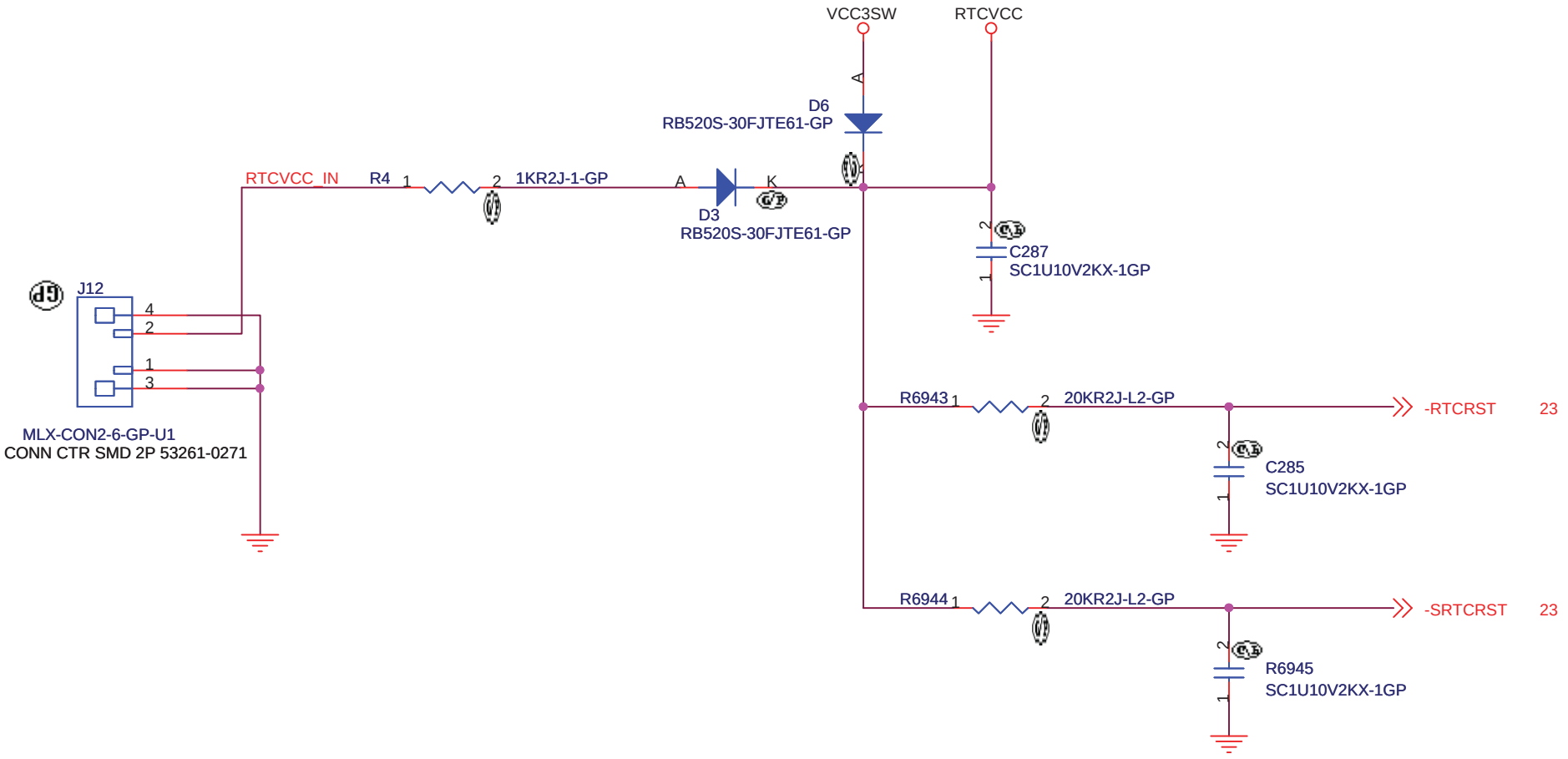
<Variant Name>

緯創資通 Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title: DISPLAY PORT MUX

Size	Document Number	Rev
A3	SHINAI-3 SWG	SB

Date: Tuesday, September 07, 2010 Sheet 37 of 102



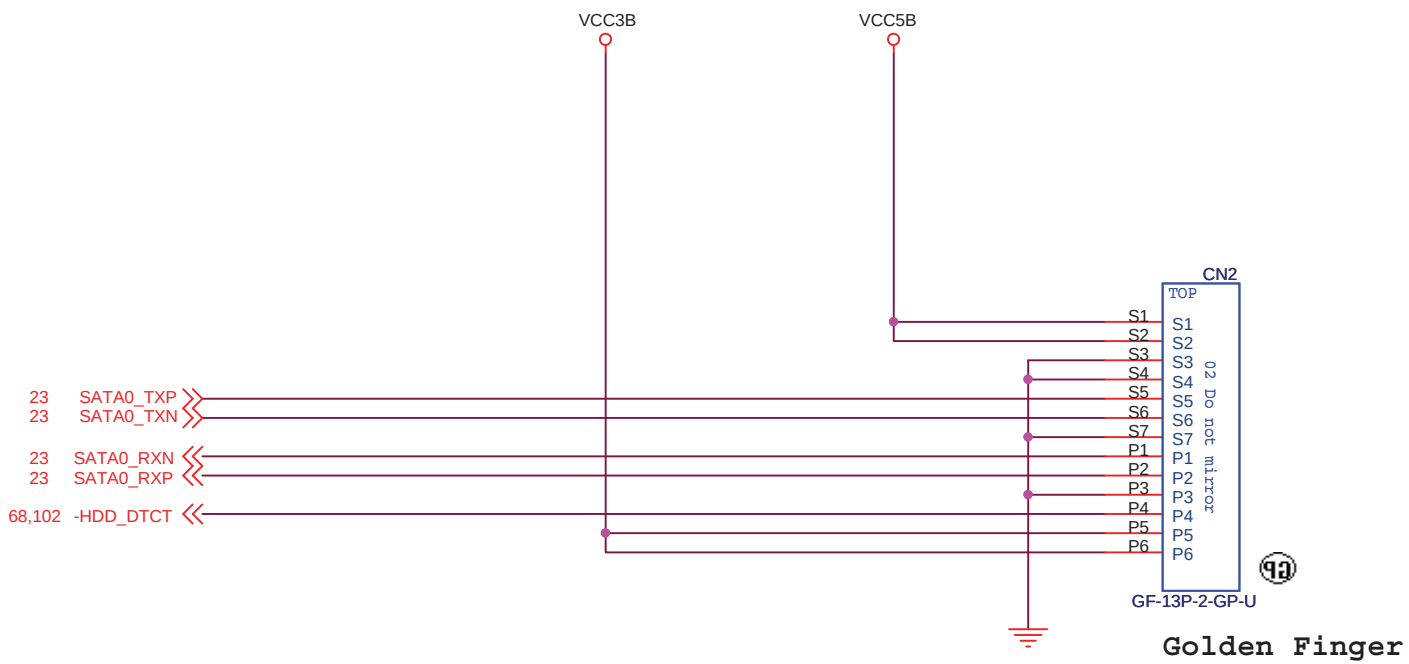
<Variant Name>

緯創資通 **Wistron Corporation**
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title: ***RTC BATTERY***

Size	Document Number	Rev
A	SHINAI-3 SWG	SB

Date: Tuesday, September 07, 2010 Sheet 38 of 102



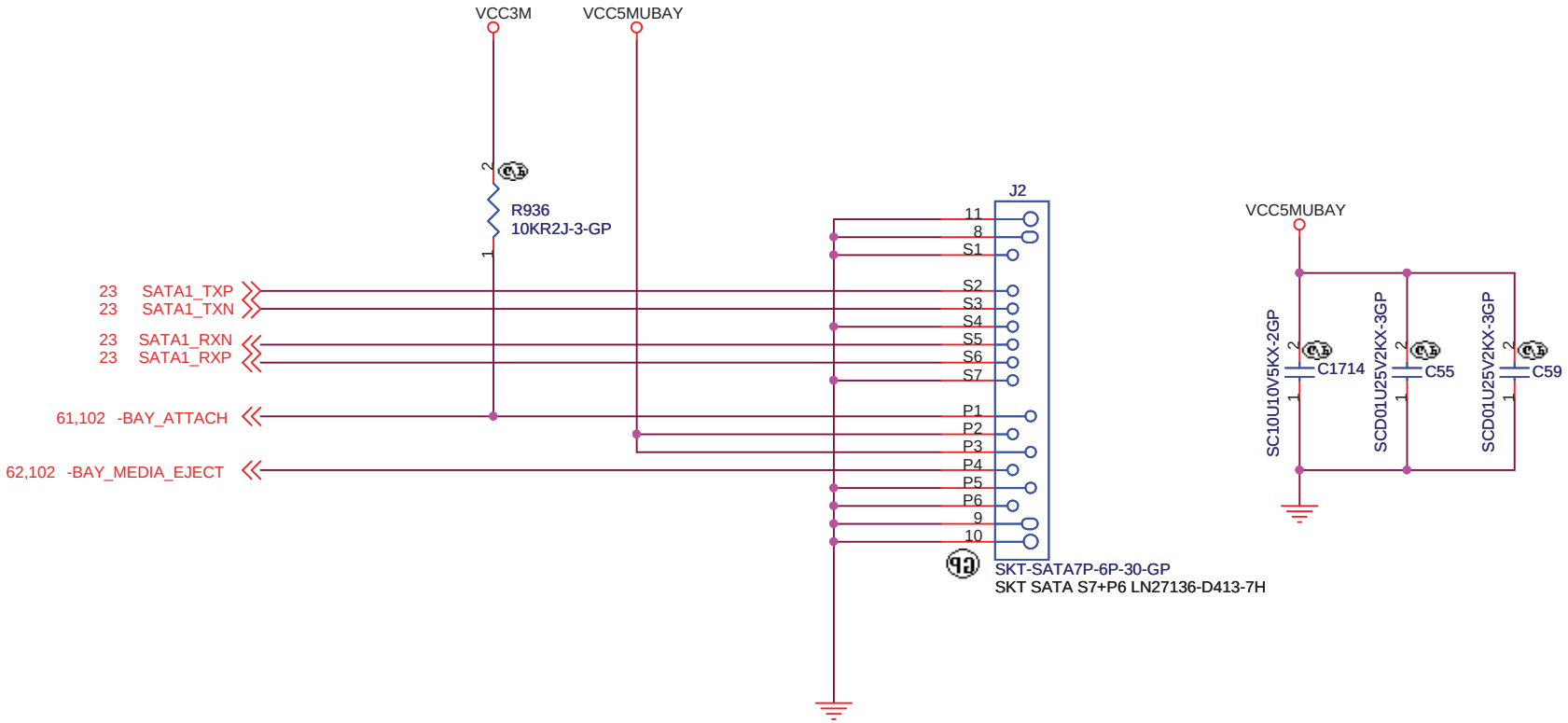
<Variant Name>

緯創資通 **Wistron Corporation**
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title: ***SATA HDD CONN***

Size	Document Number	Rev
A4	SHINAI-3 SWG	SB

Date: Tuesday, September 07, 2010 Sheet 39 of 102



<Variant Name>

緯創資通 Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title

SATA ODD BAY

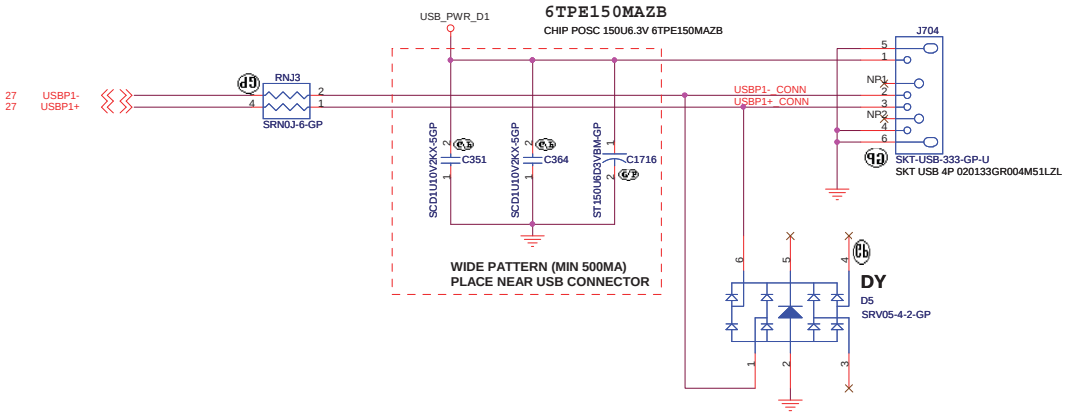
Size	Document Number	Rev
A4	SHINAI-3 SWG	SB

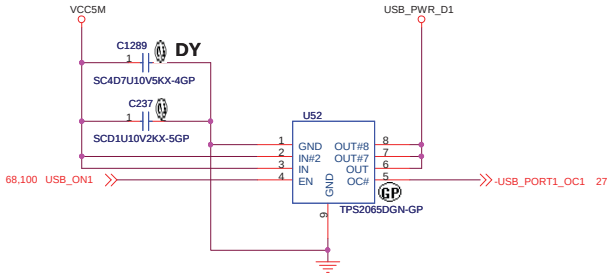
Date: Tuesday, September 07, 2010 Sheet 40 of 102

ESATA + USB CONN is not used on SN3

<Core Design>

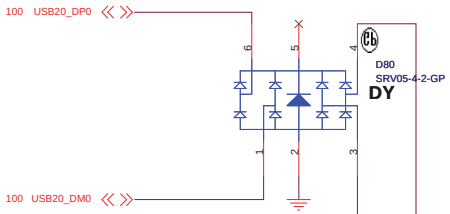
緯創資通 Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title: *ESATA CONNECTOR*	
Size: A3	Document Number: SHINAI-3 SWG Rev: SB
Date: Tuesday, September 07, 2010	Sheet 41 of 102

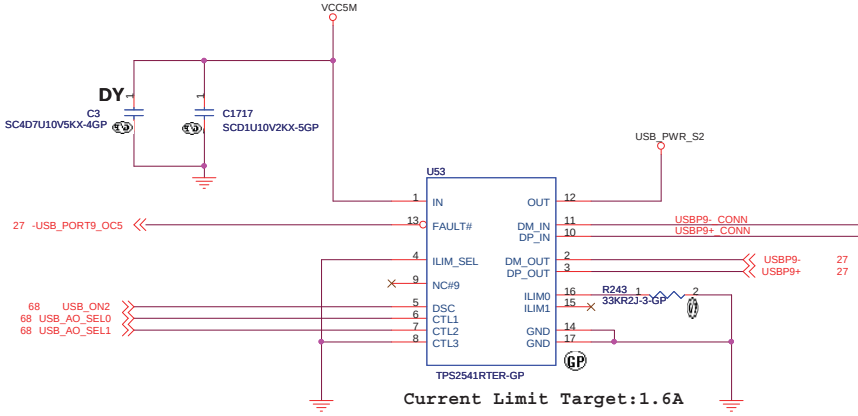


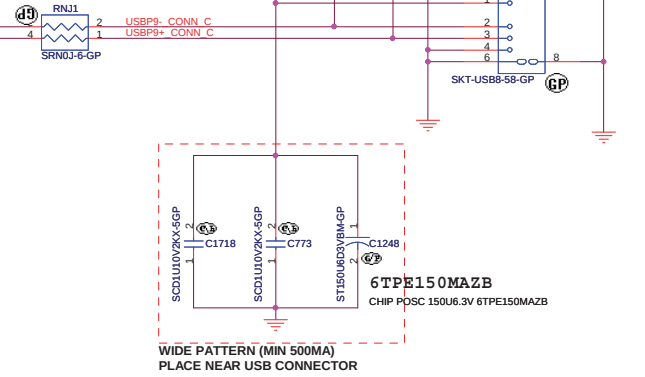


1ch USB Power Switch w/o Discharge Function

	Supplier	Vendor P/N	WISTRON P/N
1	TI	TPS2065DGN-GP	74.02065.079







2ch USB Power Switch

	Supplier	Vendor P/N	WISTRON P/N
1	TI	TPS2541RTE	74.02541.073

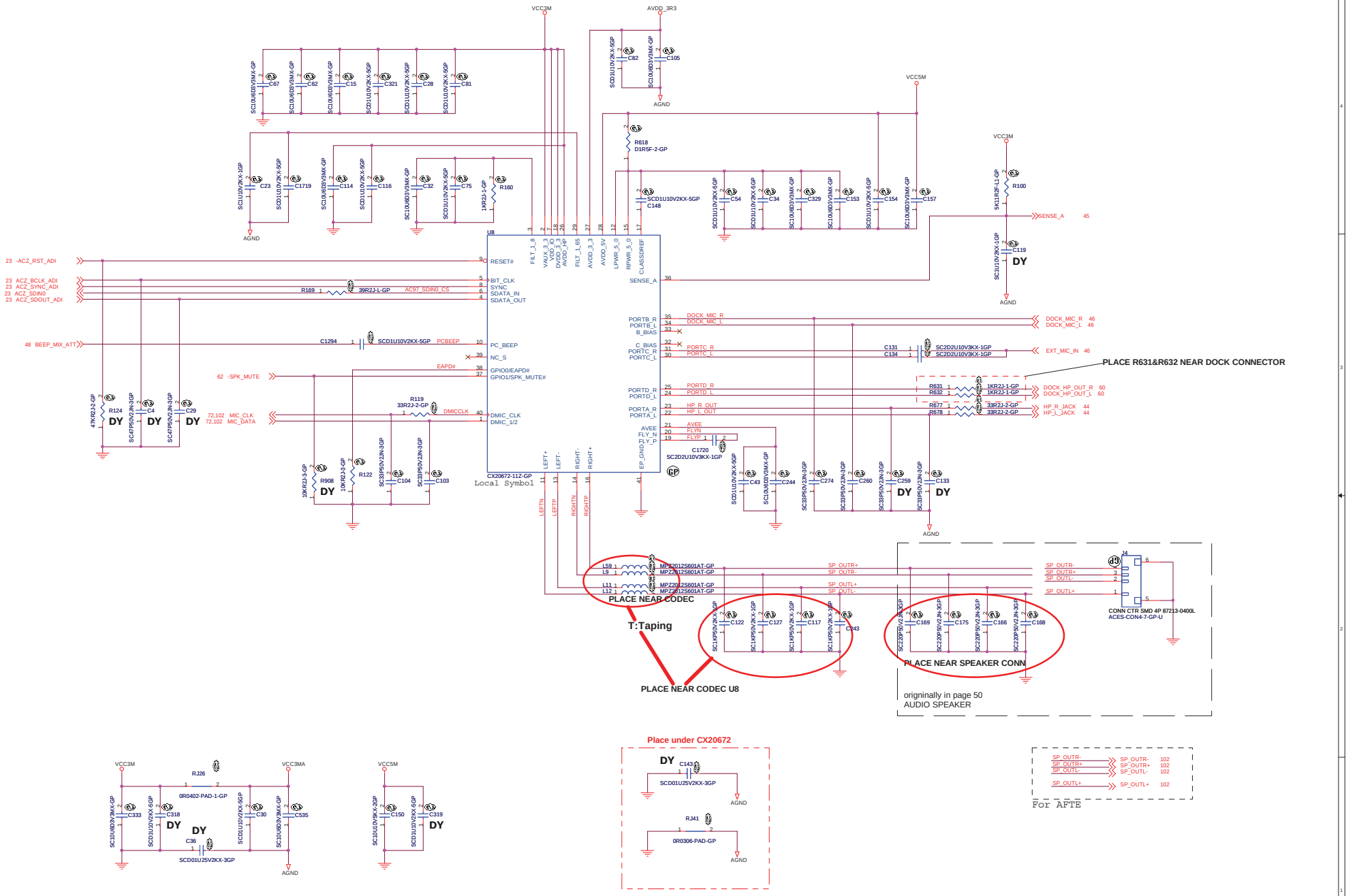
<Variant Name>

緯創資通 Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title: USB2.0 CONN&PWR

Size: Custom | Document Number: SHINAI-3 SWG | Rev: SB

Date: Tuesday, September 07, 2010 | Sheet 42 of 102



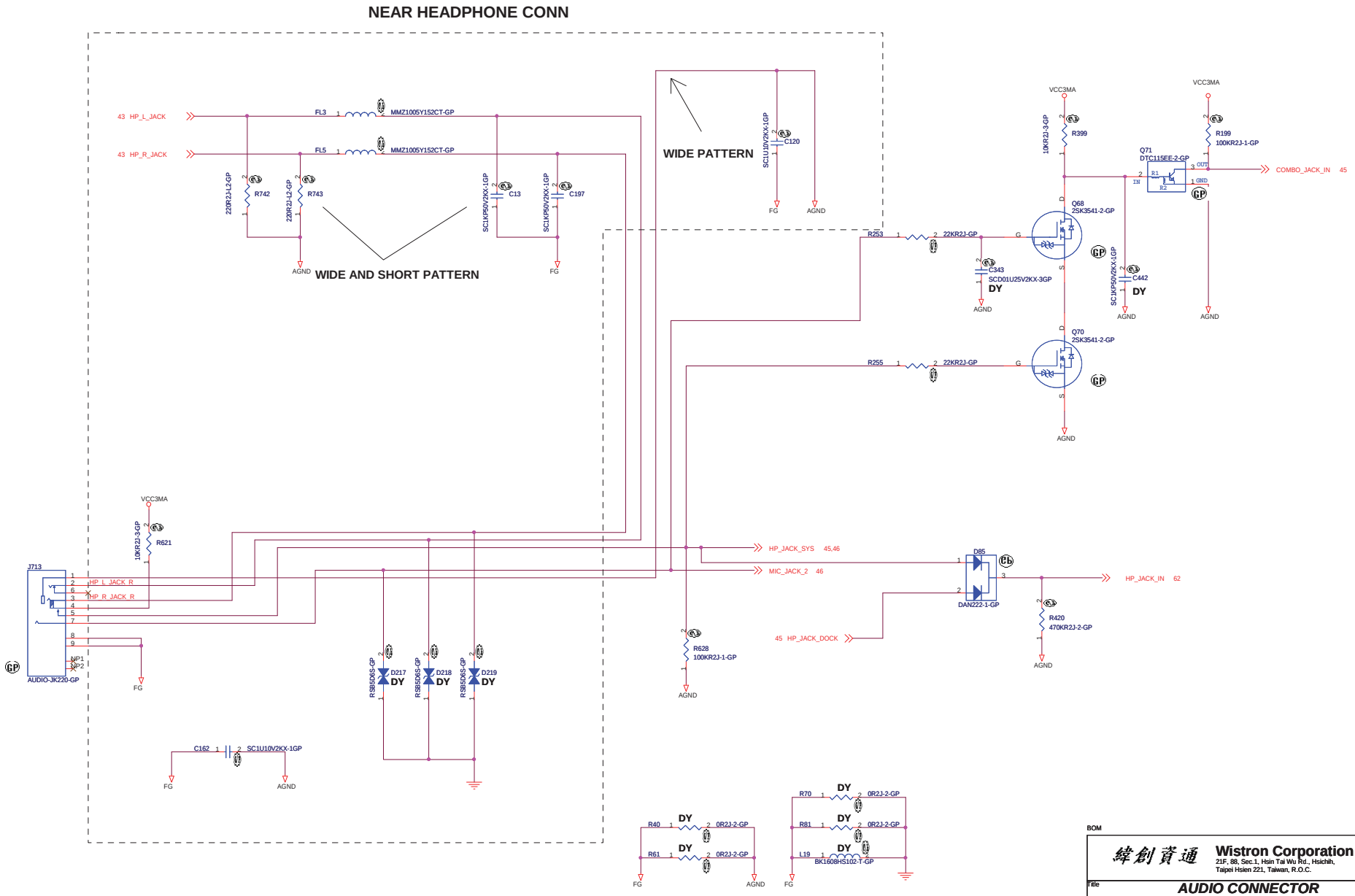
BOM

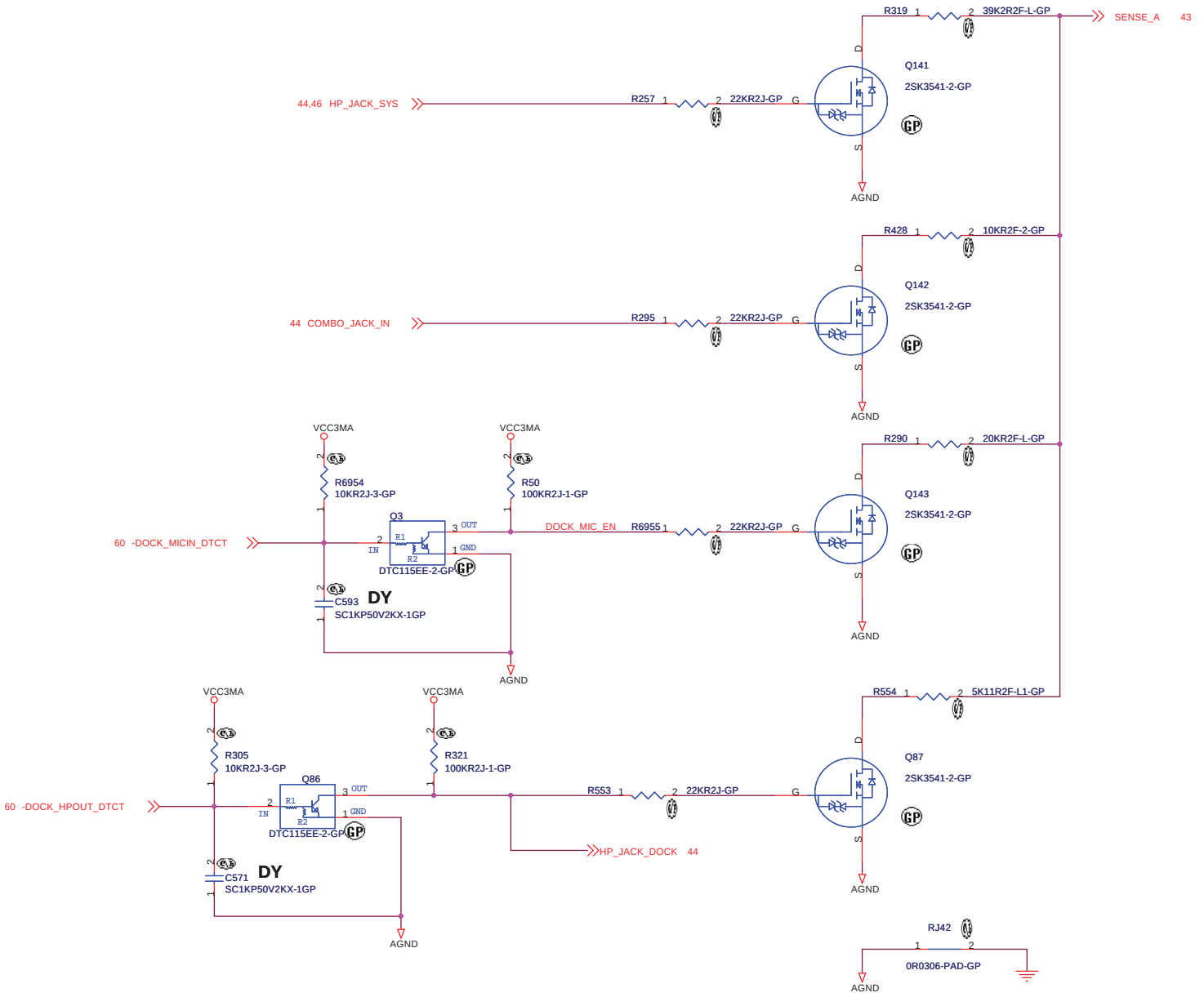
Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title: ***AUDIO CODEC CX20672***

Size	Document Number	Rev
A2	SHINAI-3 SWG	SB

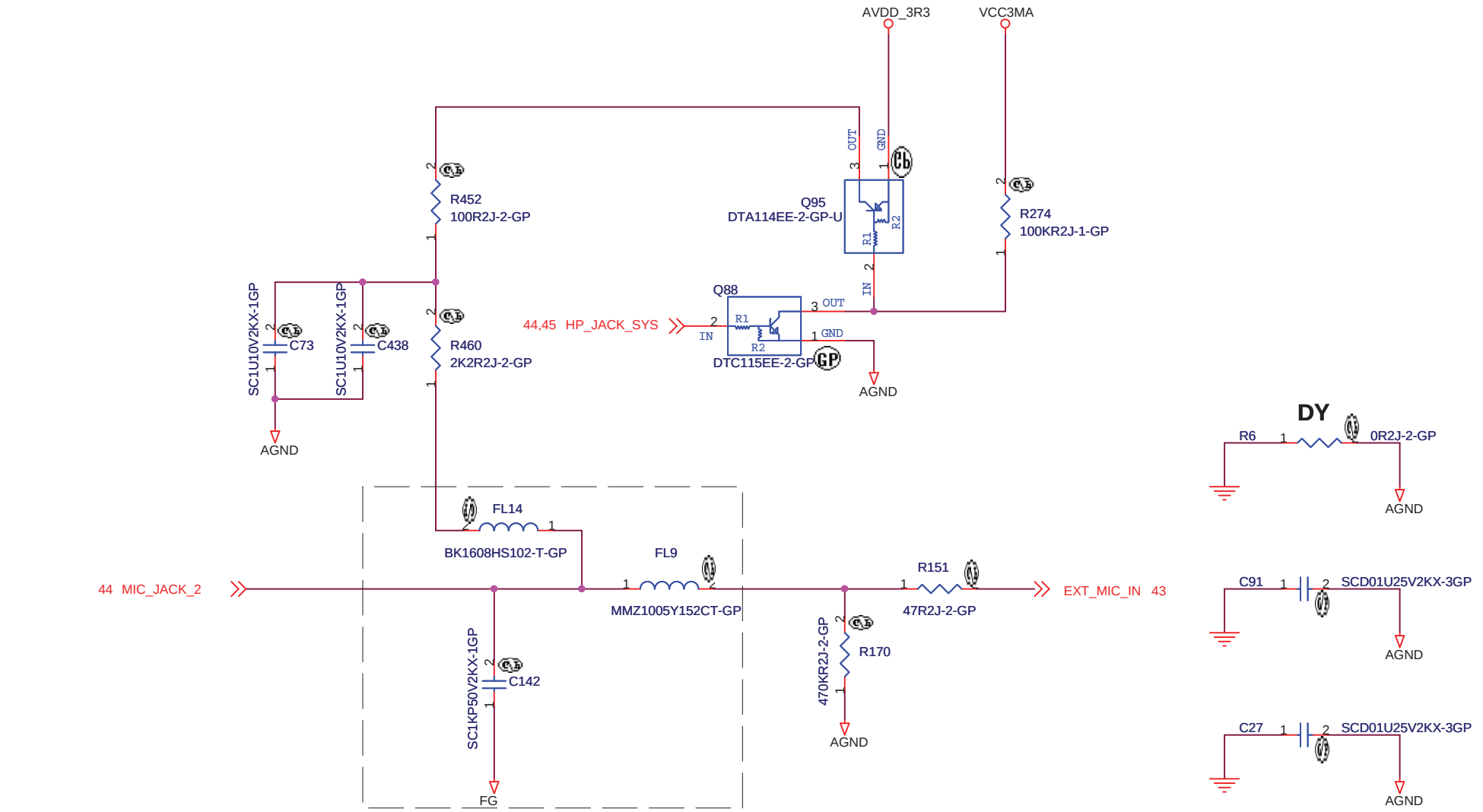
Date: Tuesday, September 07, 2010 Sheet 43 of 102


NEAR HEADPHONE CONN
43 HP_L_JACK
43 HP_R_JACK
FL3
FL5
MMZ1005Y152CT-GP
R742
R743
220KR2J-1-2-GP
C13
C197
SC1KP50V2KX-1GP
WIDE AND SHORT PATTERN
WIDE PATTERN
C120
SC1U10V2KX-1GP
AGND
FG
VCC3MA
R399
10KR2J-3-GP
Q68
2SK3541-2-GP
Q70
R253
R255
22KR2J-GP
C343
SCD01U25V2KX-3GP
DY
C442
Q71
DTC115EE-2-GP
R199
100KR2J-1-GP
COMBO_JACK_IN 45
HP_JACK_SYS 45,46
MIC_JACK_2 46
HP_JACK_IN 62
45 HP_JACK_DOCK
D85
DAN222-1-GP
R420
470KR2J-2-GP
R621
R628
100KR2J-1-GP
J713
HP_L_JACK_R
HP_R_JACK_R
AUDIO-JK220-GP
D217
D218
D219
RB505S-GP
C162
R40
R61
R70
R81
L19
0R2J-2-GP
BK1608HS102-T-GP
http://hobi-elektronika.net
BOM
緯創資通
Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.
Title
AUDIO CONNECTOR
Size C
Document Number
SHINAI-3 SWG
Rev SB
Date: Thursday, September 23, 2010
Sheet 44 of 102

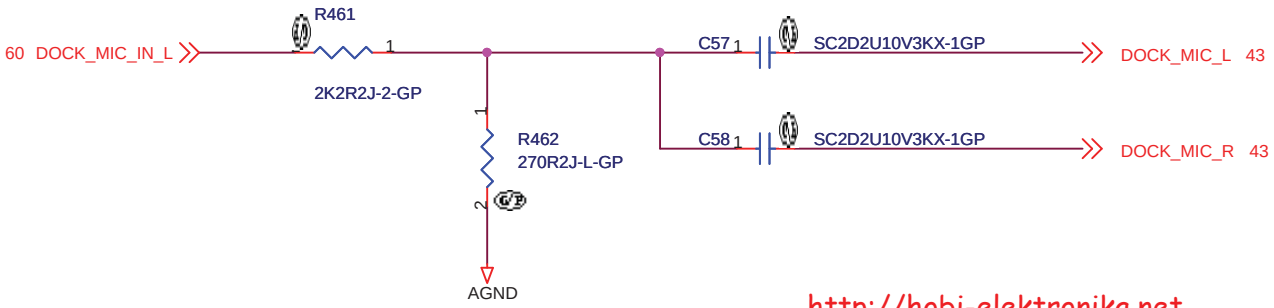


BOM

緯創資通 Wistron Corporation	21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.
Title	AUDIO JACK SENSE
Size A3 / Document Number	SHINAI-3 SWG / Rev SB
Date: Tuesday, September 07, 2010	Sheet 45 of 102



NEAR EXT MIC CONN



BOM

緯創資通 Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title: **AUDIO EXT MIC I/F**

Size	Document Number	Rev
A4	SHINAI-3 SWG	SB

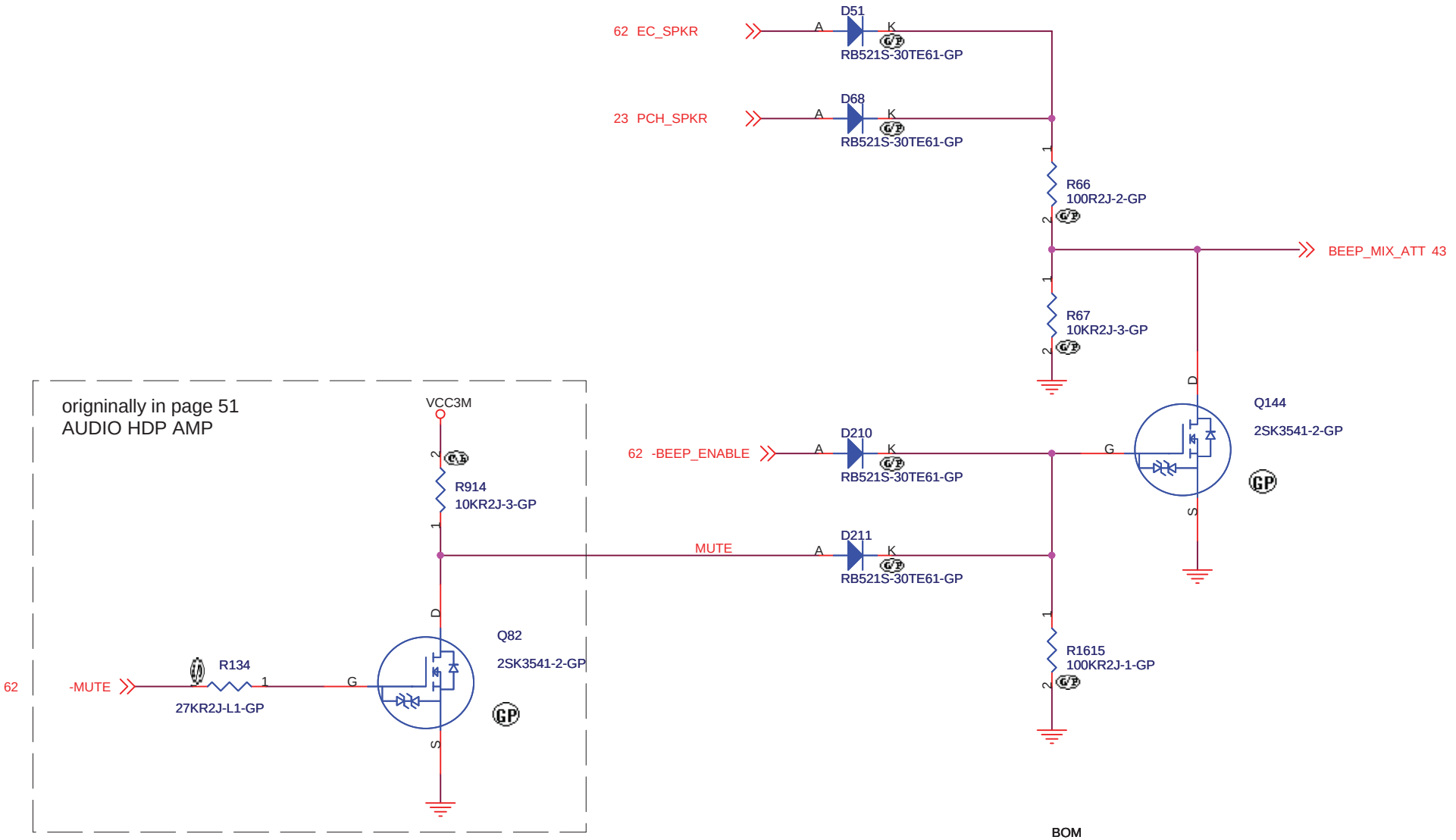
Date: Tuesday, September 07, 2010 Sheet 46 of 102

AUDIO SPEAKER
MOVE TO CODEC PAGE

<Variant Name>

緯創資通 Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title	***BLANK***	
Size A4	Document Number **SHINAI-3 SWG**	Rev **SB**
Date: Tuesday, September 07, 2010	Sheet 47 of 102	



BOM

緯創資通 Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title: **AUDIO BEEP**

Size	Document Number	Rev
A4	SHINAI-3 SWG	SB

Date: Tuesday, September 07, 2010 Sheet 48 of 102

BLANK

<Variant Name>

緯創資通 Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

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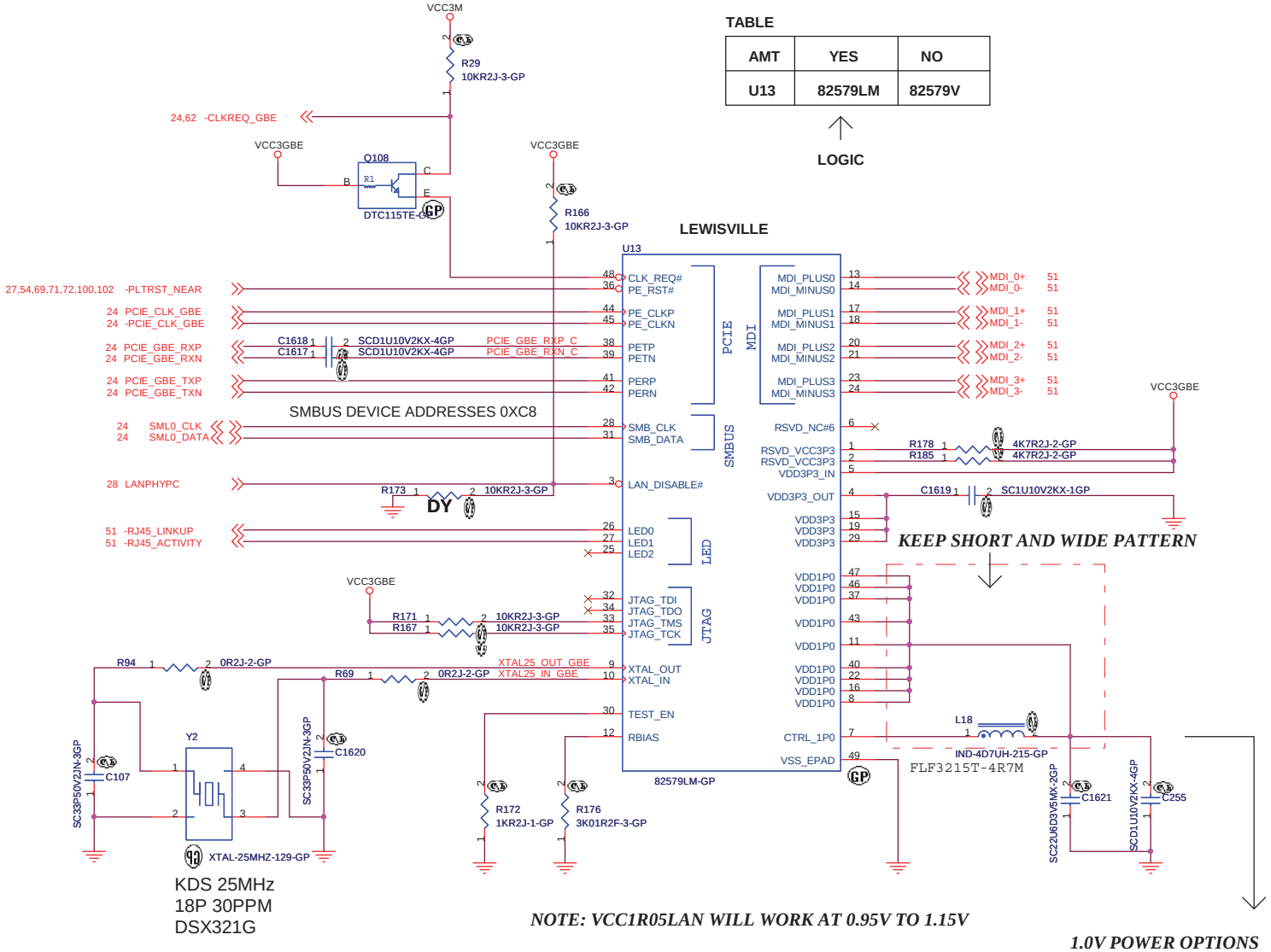
Size A4	Document Number SHINAI-3 SWG	Rev SB
Date: Tuesday, September 07, 2010	Sheet 49 of 102	



KDS 25MHz
18P 30PPM
DSX321G

TABLE

AMT	YES	NO
U13	82579LM	82579V

LOGIC

NOTE: VCC1R05LAN WILL WORK AT 0.95V TO 1.15V

1.0V POWER OPTIONS

	L18	R918
SHARED WITH PCH VCC1R05LAN SVR	NO ASM	ASM
ISVR	ASM	NO ASM

	Supplier	Vendor P/N	WISTRON P/N
1	KDS	DSX321G 25M 18P 30PPM	82.30020.B11
2	TXC	7V25020001 25M 18PF 30PPM	41U6141AA
3	RIVER	FCX-04-25MJ90141	82.30020.E51

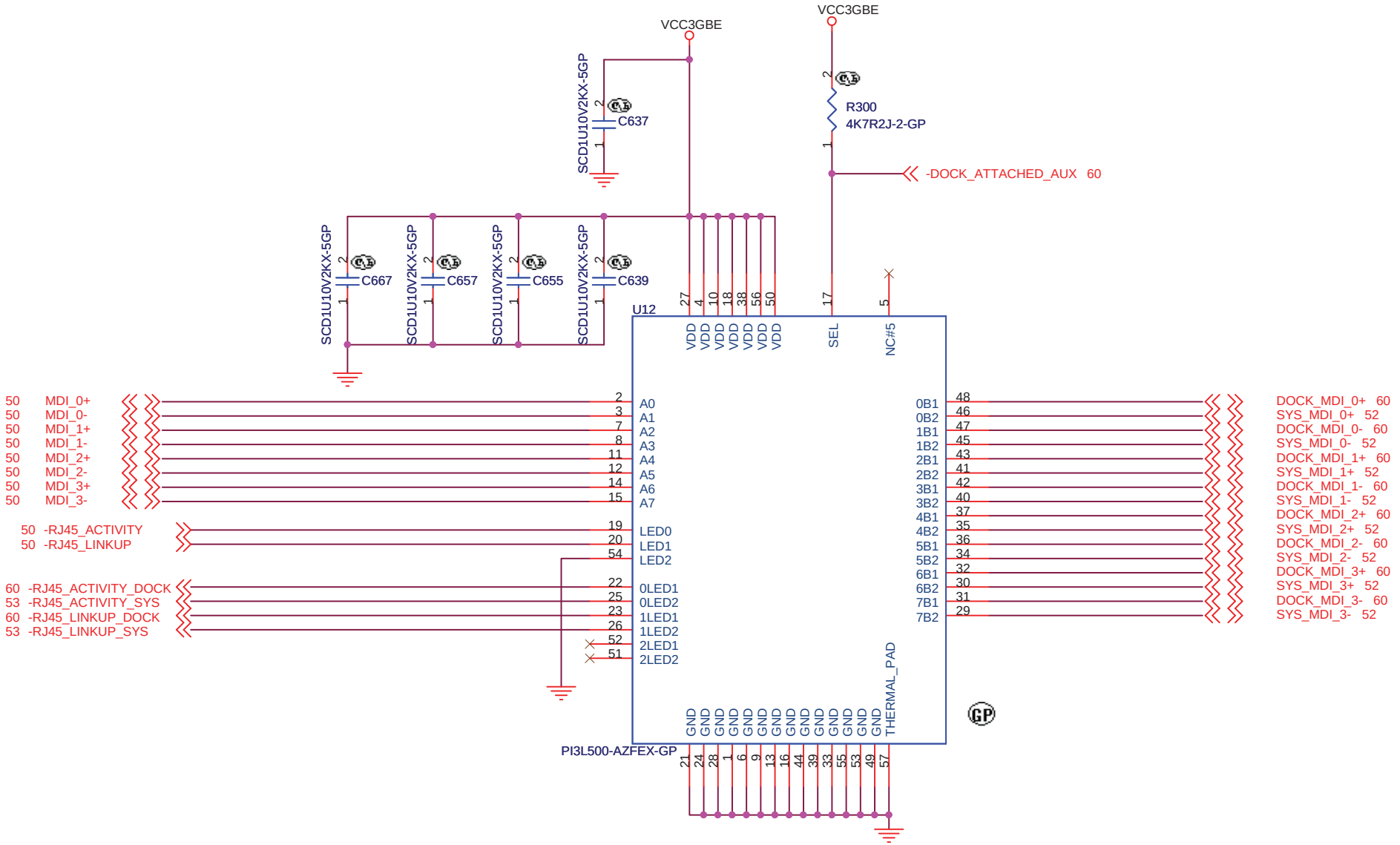
<Variant Name>

緯創資通 Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title: GBE LEWISVILLE

Size: A3 | Document Number: SHINAI-3 SWG | Rev: SB

Date: Tuesday, September 07, 2010 | Sheet 50 of 102



[Source Cadidate]			
1st	Pericom	PI3L500-AZFEX	41R0539AA
2nd	TI	TS3L500AERHUR	41R0539BA

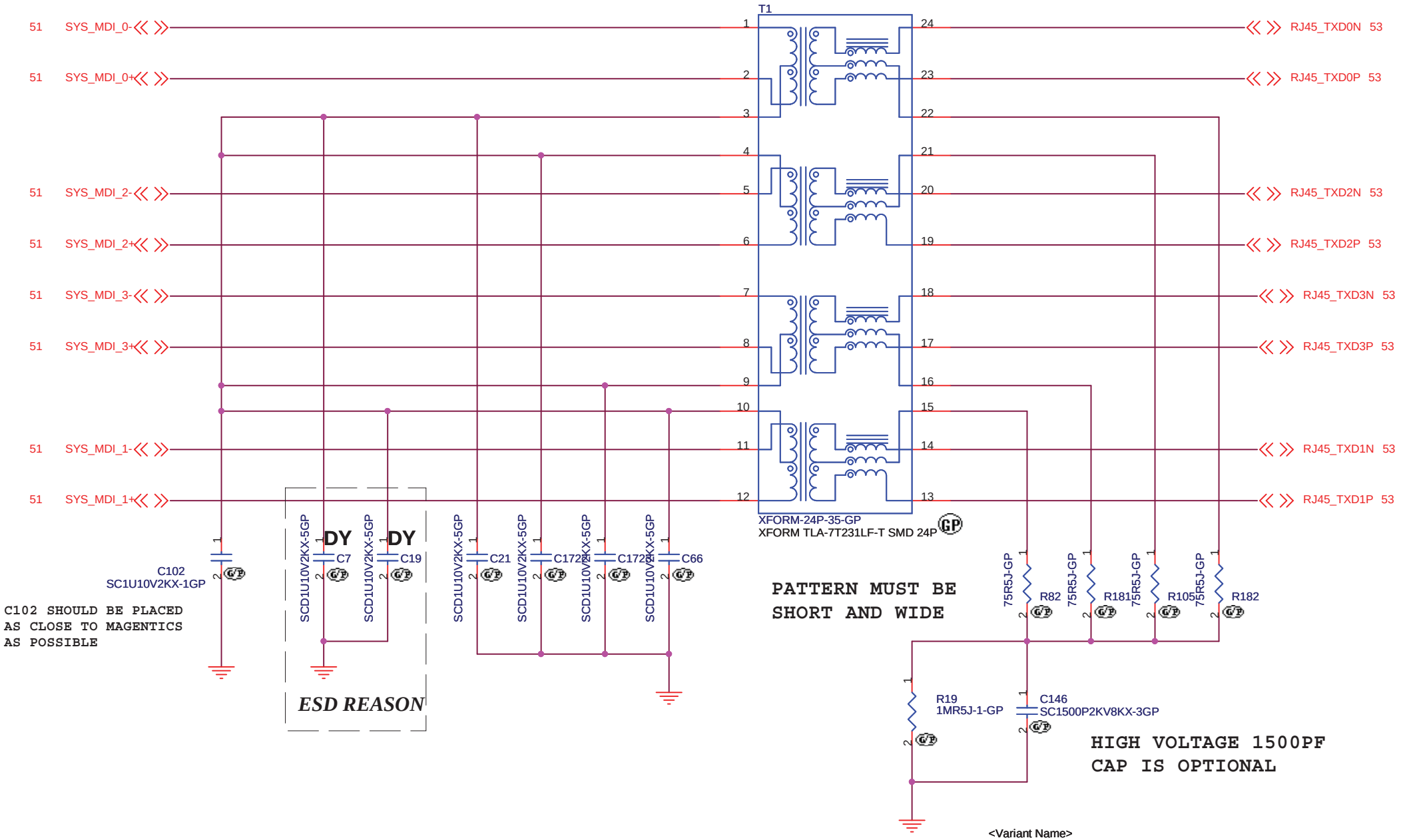
<Variant Name>

緯創資通 Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title: **GBE LAN SW**

Size	Document Number	Rev
A4	SHINAI-3 SWG	SB

Date: Tuesday, September 07, 2010 Sheet 51 of 102



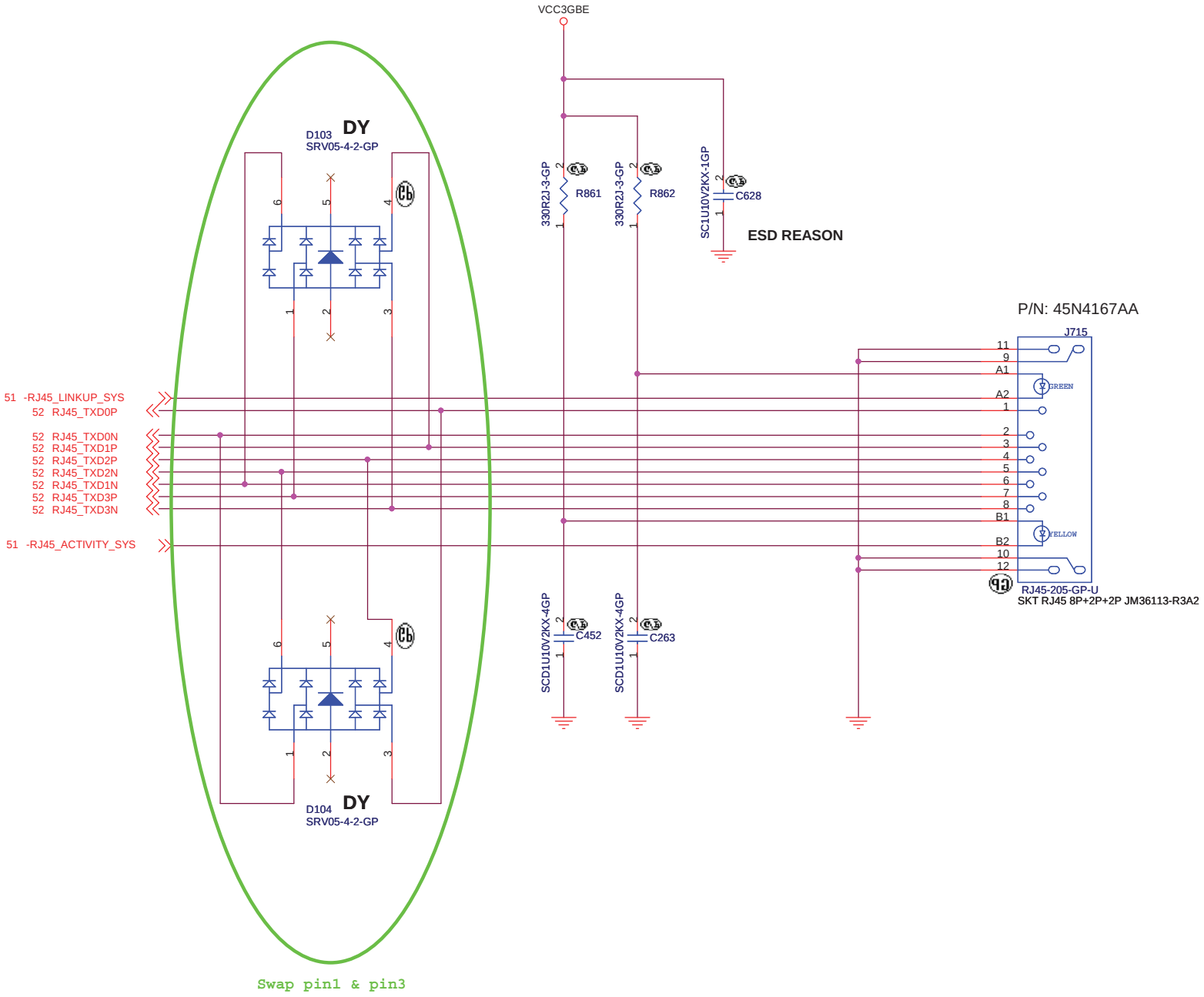
<Variant Name>

緯創資通 **Wistron Corporation**
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title: **GBE MAGNETICS**

Size	Document Number	Rev
A4	SHINAI-3 SWG	SB

Date: Tuesday, September 07, 2010 Sheet 52 of 102

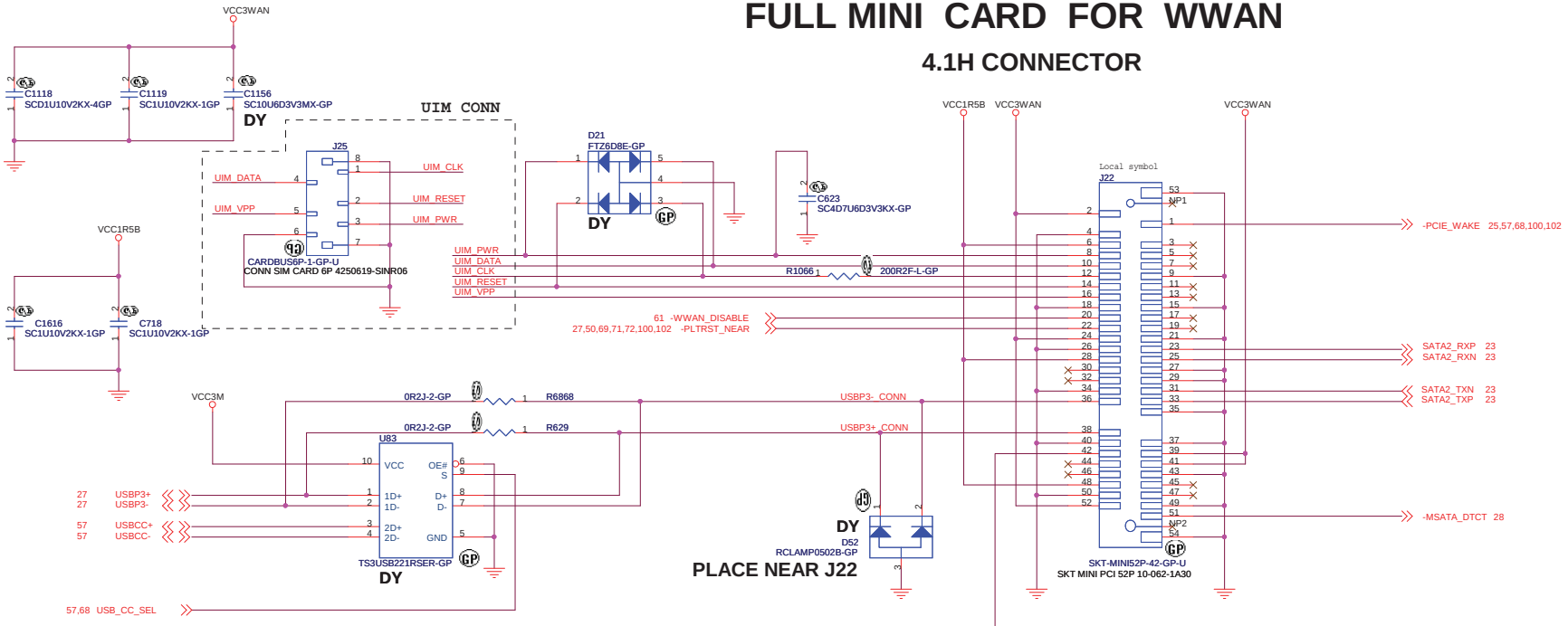


緯創資通 **Wistron Corporation**
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title	***RJ45 CONN***	
Size A4	Document Number **SHINAI-3 SWG**	Rev **SB**
Date: Tuesday, September 07, 2010	Sheet 53 of 102	

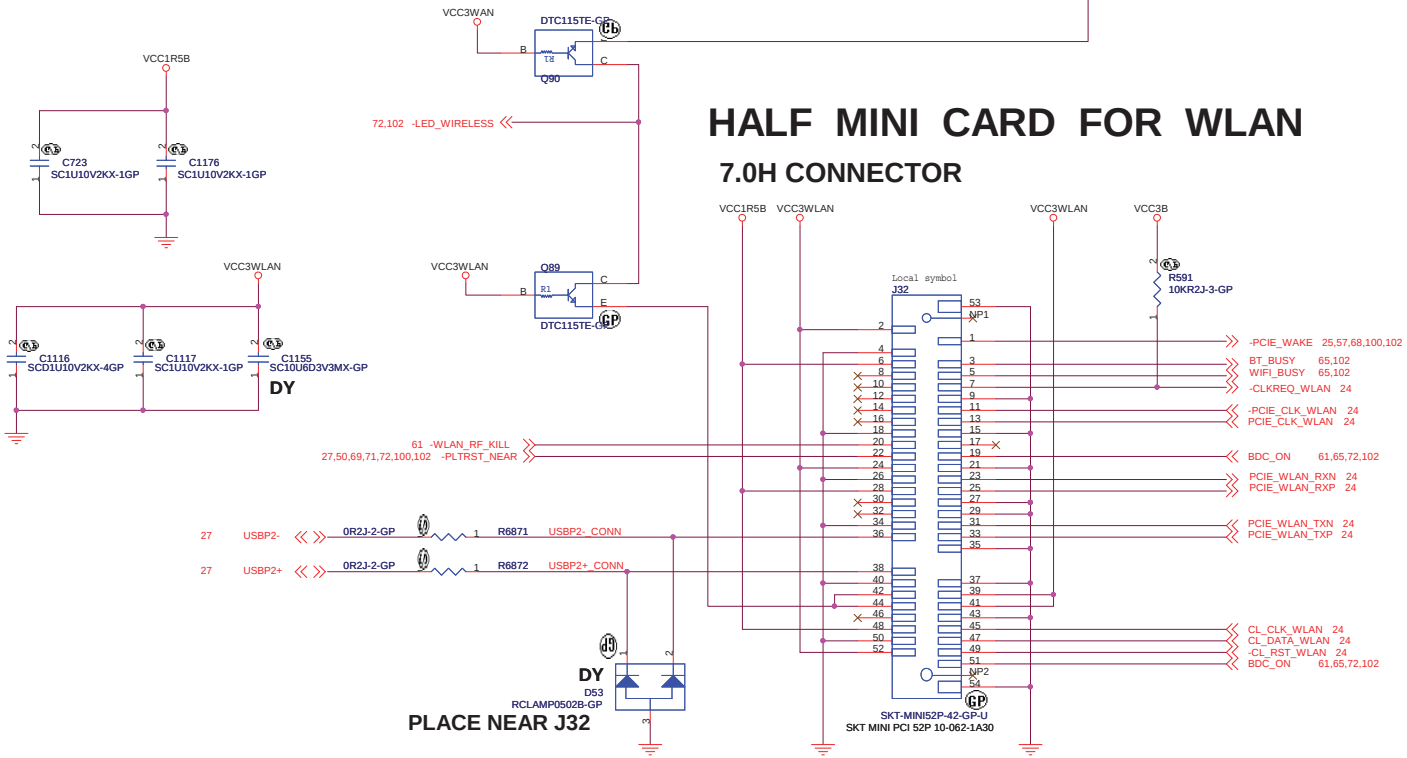
FULL MINI CARD FOR WWAN

4.1H CONNECTOR



HALF MINI CARD FOR WLAN

7.0H CONNECTOR



TABLE

WWAN CONSTANT CONNECT	W/WWAN VER.2	W/WWAN VER.1/NO	W/O WWAN NO
J22	ASM	ASM	NO_ASM
J25	ASM	ASM	NO_ASM
C1118	ASM	ASM	NO_ASM
C1119	ASM	ASM	NO_ASM
R1066	ASM	ASM	NO_ASM
C623	ASM	ASM	NO_ASM
D21	ASM	ASM	NO_ASM
U83	ASM	NO_ASM	NO_ASM
R629	NO_ASM	ASM	NO_ASM
R651	NO_ASM	ASM	NO_ASM

LOGIC

<Variant Name>

緯創資通 Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title: PCIE MINI CARD SLOT

Size: Custom | Document Number: SHINAI-3 SWG | Rev: SB

Date: Tuesday, September 07, 2010 | Sheet 54 of 102

BLANK

<Variant Name>

緯創資通 **Wistron Corporation**
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title: ***BLANK***		
Size: B	Document Number: **SHINAI-3 SWG**	Rev: SB
Date: Tuesday, September 07, 2010	Sheet 55 of 102	

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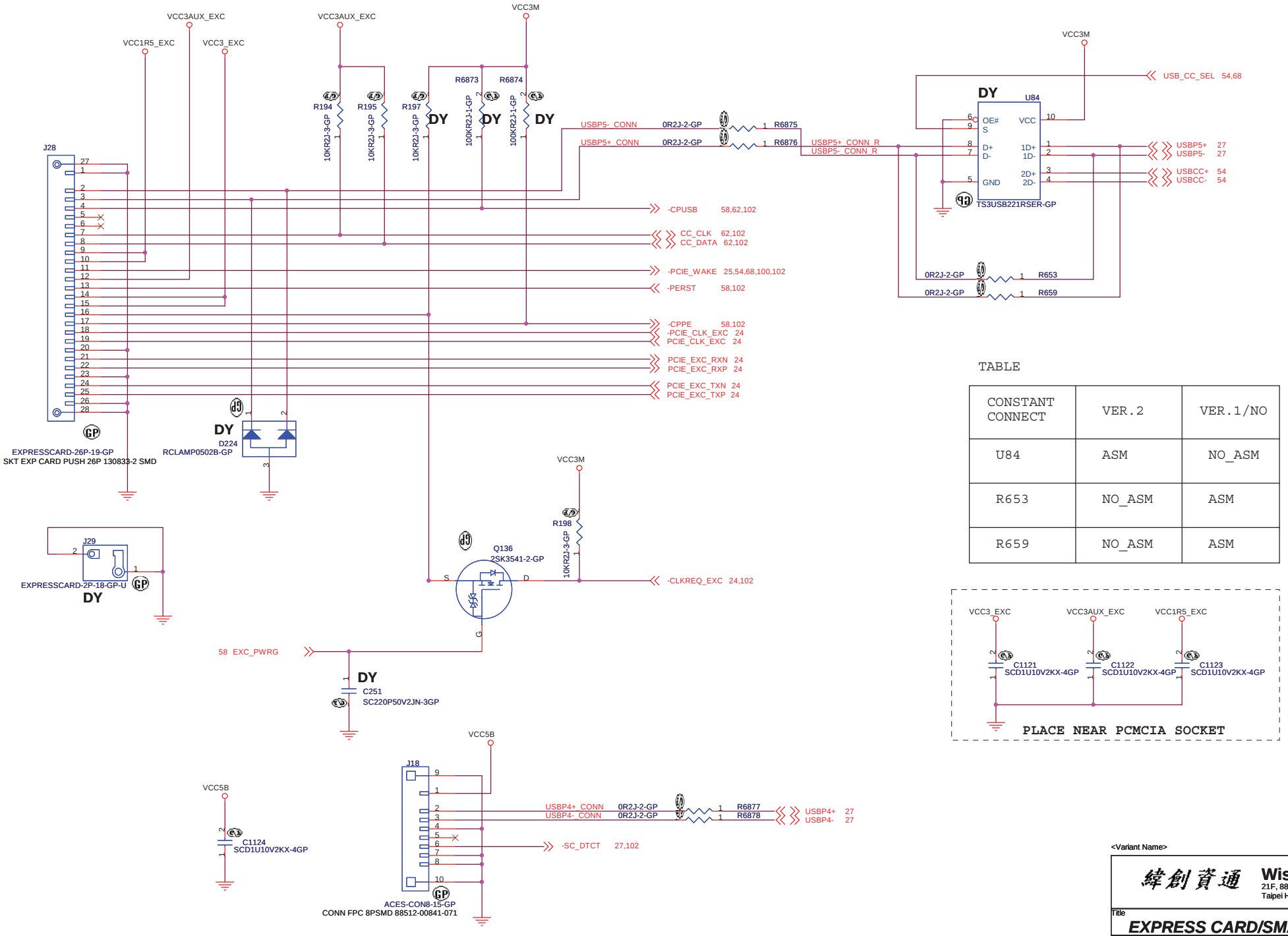
<Variant Name>

緯創資通 **Wistron Corporation**
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title: *BLANK*		
Size: A3	Document Number: SHINAI-3 SWG	Rev: SB
Date: Tuesday, September 07, 2010	Sheet 56 of 102	



TABLE

CONSTANT CONNECT	VER.2	VER.1/NO
U84	ASM	NO_ASM
R653	NO_ASM	ASM
R659	NO_ASM	ASM

<Variant Name>

Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

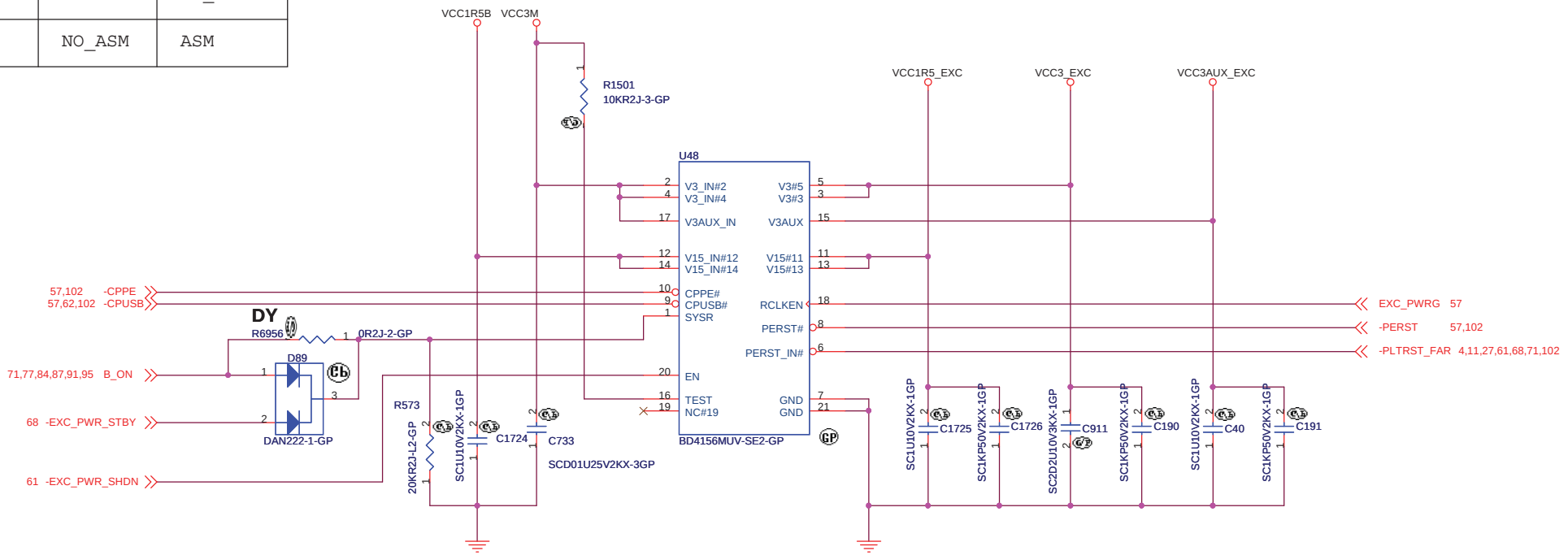
Title: EXPRESS CARD/SMART CARD I/F

Size: A3 | Document Number: SHINAI-3 SWG | Rev: SB

Date: Tuesday, September 07, 2010 | Sheet 57 of 102

TABLE

CONSTANT CONNECT	YES	NO
D89	ASM	NO_ASM
R573	ASM	NO_ASM
R6956	NO_ASM	ASM



TABLE

CONSTANT CONNECT	VER.2/VER.1	NO
U48	BD4156MUV TPS2231MRGPR-3	TPS2231MRGPR-3 BD4156MUV W83L351YG
R1501	ASM	ASM

LOGIC

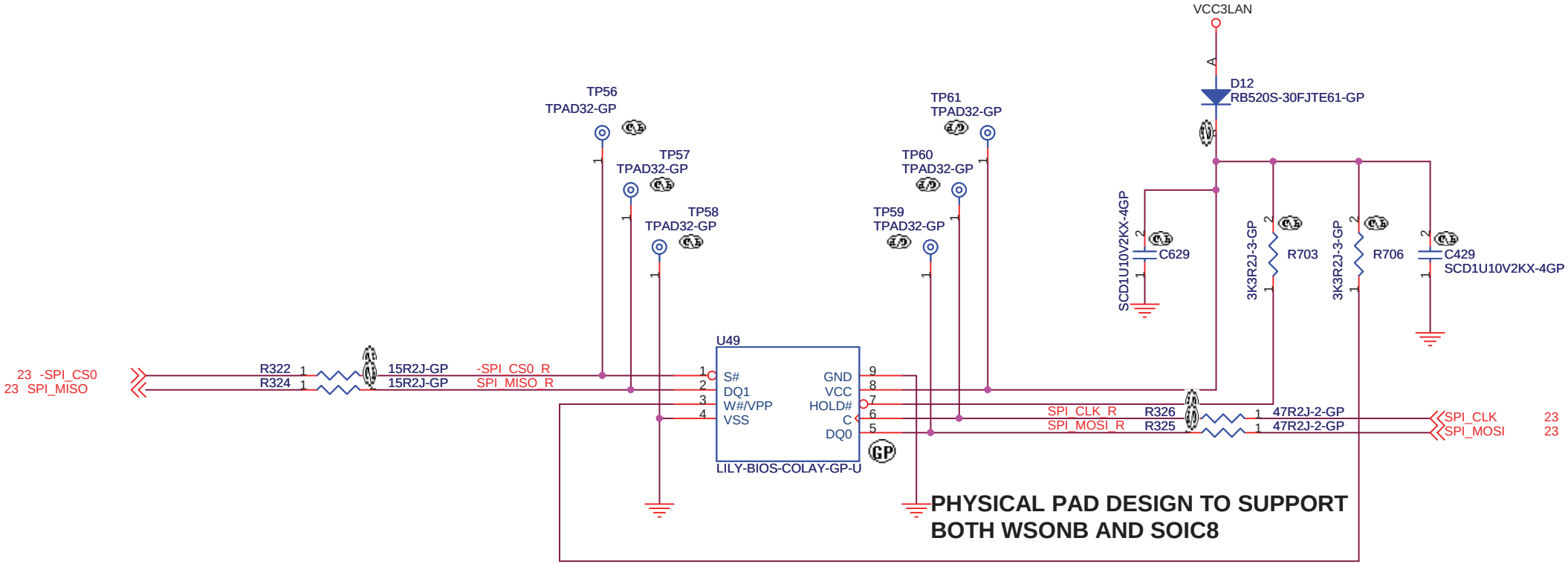
<Variant Name>

緯創資通 **Wistron Corporation**
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title: **SLOT POWER CONTRL**

Size	Document Number	Rev
B	SHINAI-3 SWG	SB

Date: Tuesday, September 07, 2010 Sheet 58 of 102



TABLE

	WISTRON P/N
8MB SOIC8	
MACRONIX MX25L6436EM2I-10G	72.25643.001
MACRONIX MX25L6406EM2I-10G	Wait Wistron PN
WINBAOND W25Q64CVSSIG	72.25Q64.B01
8MB WSON8	
NUMONYX M25PX64-VDM6TG	72.25P64.E01

TABLE

SF100 PIN HEADER IN TERFACE (TOP VIEW)	
1 VCC D12.1	GND GND 2
3 CS# R322.2	R326.2 CLK 4
5 MISO R324.2	R325.2 MOSI 6
7 (KEY) N/A	N/A (RESET) 8

<Variant Name>

緯創資通 **Wistron Corporation**
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

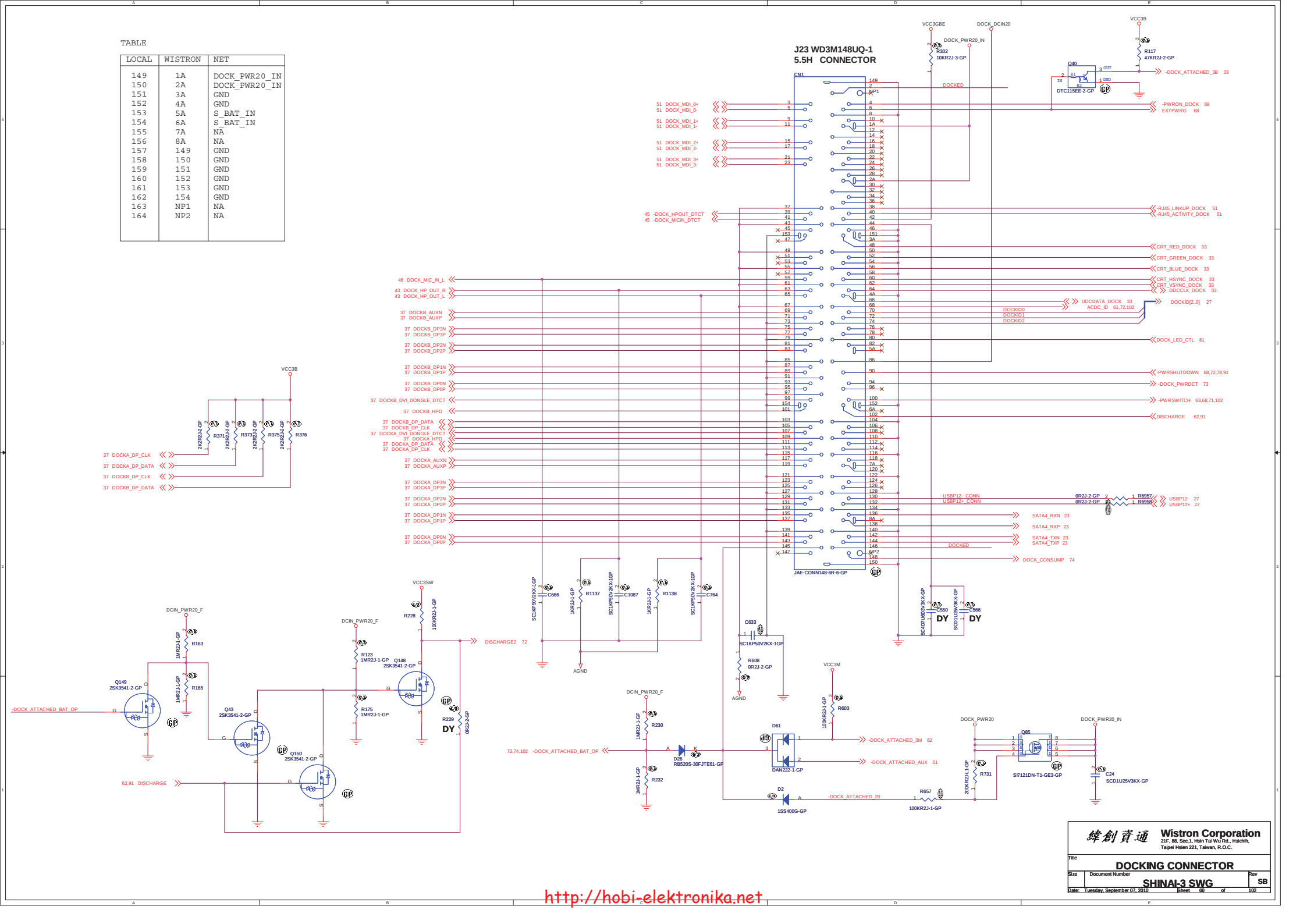
Title: ***SPI FLASH***

Size	Document Number	Rev
A4	SHINAI-3 SWG	SB

Date: Tuesday, September 07, 2010 Sheet 59 of 102

TABLE

LOCAL	WISTRON	NET
149	1A	DOCK_PWR20_IN
150	2A	DOCK_PWR20_IN
151	3A	GND
152	4A	GND
153	5A	S_BAT_IN
154	6A	S_BAT_IN
155	7A	NA
156	8A	NA
157	149	GND
158	150	GND
159	151	GND
160	152	GND
161	153	GND
162	154	GND
163	NP1	NA
164	NP2	NA



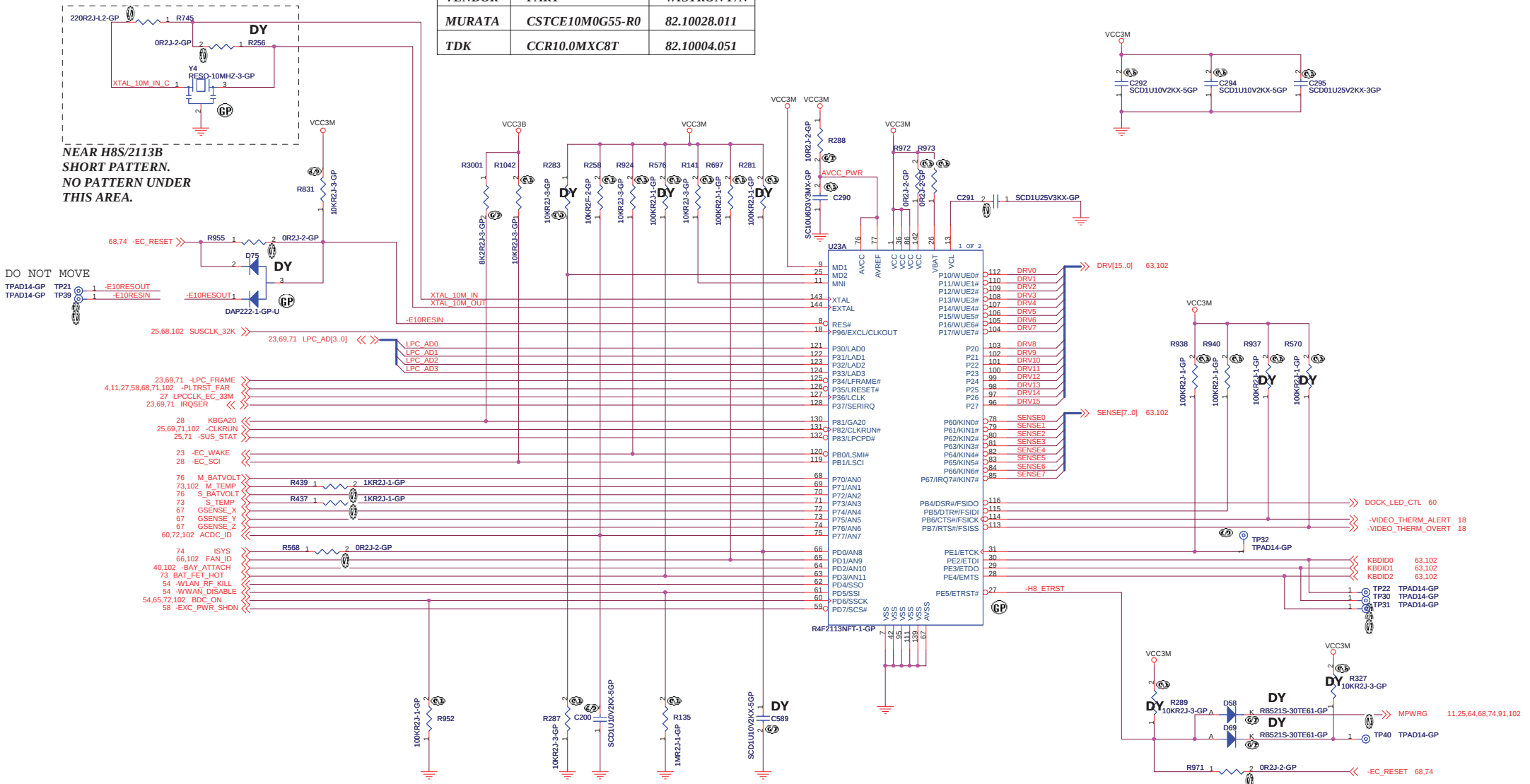
緯創資通 Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title: DOCKING CONNECTOR

Size	Document Number	Rev
	SHINAI-3 SWG	SB

Date: Tuesday, September 07, 2010 Sheet 60 of 102

TABLE	P/N 41A1205	
VENDOR	PART	WISTRON P/N
MURATA	CSTCE10M0G55-R0	82.10028.011
TDK	CCR10.0MXC8T	82.10004.051



TABLE

E10A DEBUG I/F	ENABLE	DISABLE(FINAL LOGIC)
R955 D75 R831	NO_ASM ASM ASM	ASM NO_ASM ASM
R971 D58 D69 R289 R327	NO_ASM ASM ASM ASM ASM	ASM NO_ASM NO_ASM NO_ASM NO_ASM
R283 R287	ASM NO_ASM	NO_ASM ASM
R881 R218	ASM NO_ASM	NO_ASM ASM
RN25	NO_ASM	ASM

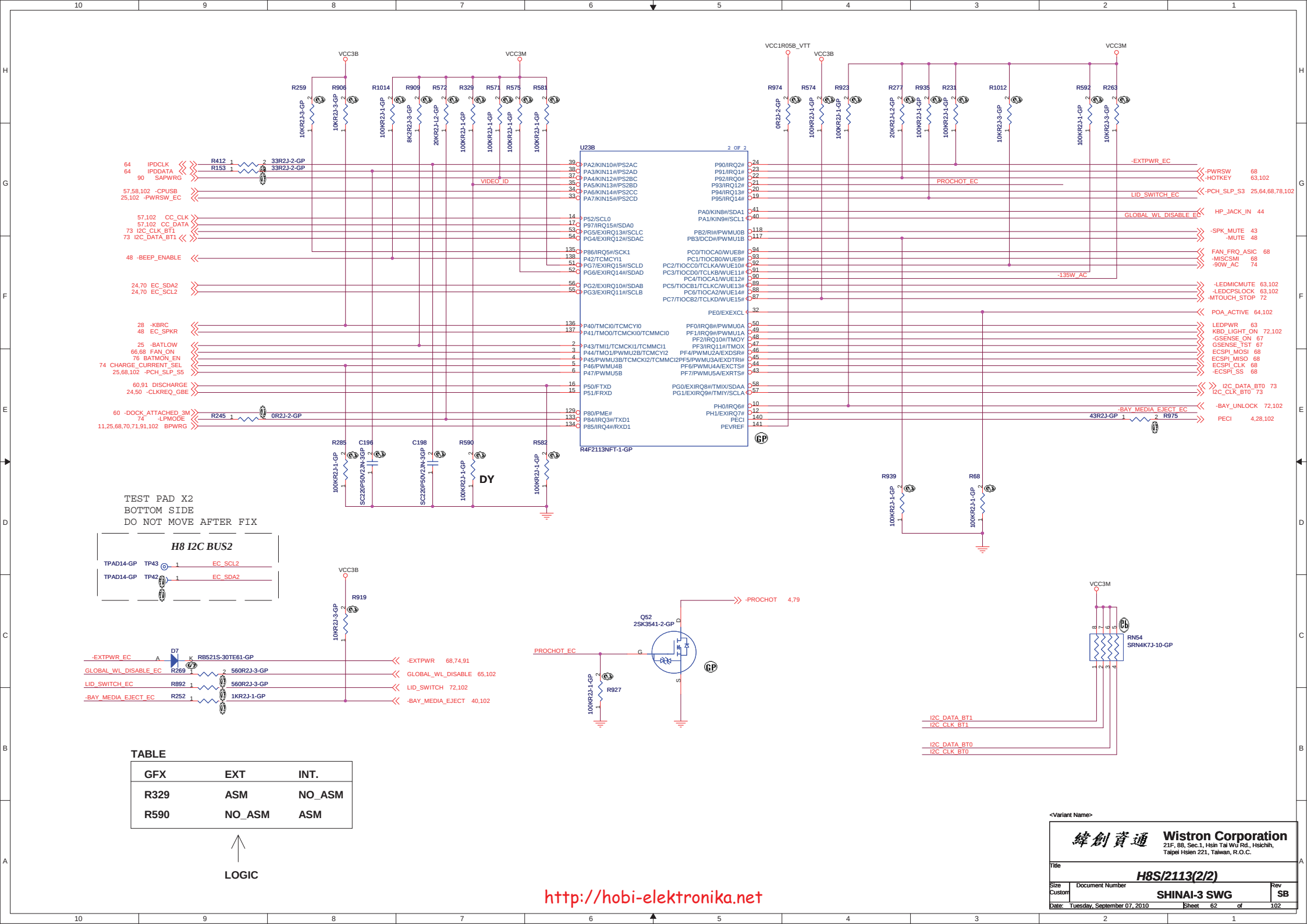
<Variant Name>

緯創資通 Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

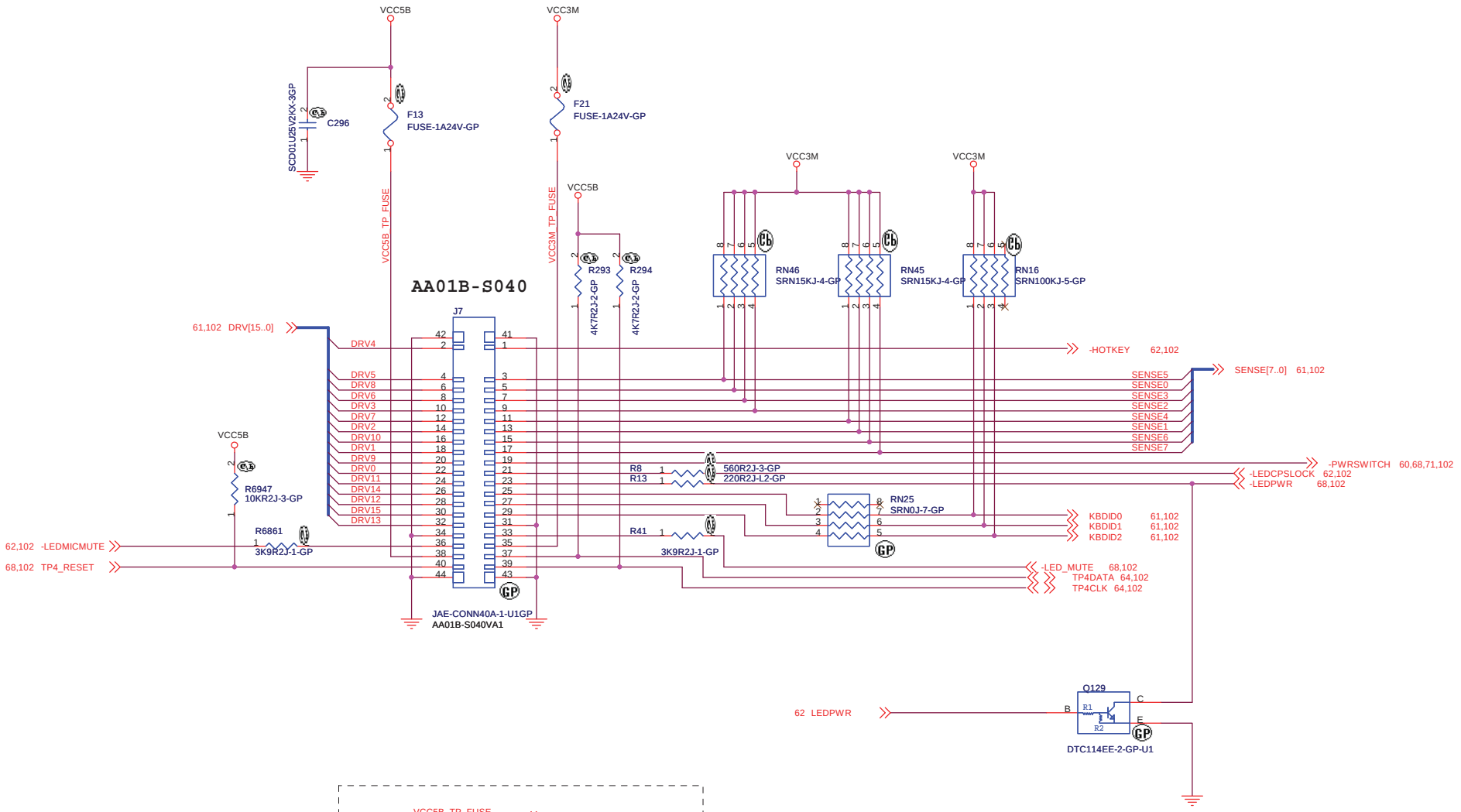
Title: H8S/2113 (1/2)

Size: Custom | Document Number: SHINAI-3 SWG | Rev: SB

Date: Tuesday, September 07, 2010 | Sheet 61 of 102


VCC3B
VCC3M
VCC1R05B_VTT
R259 10K/R2J-3-GP
R906 10K/R2J-3-GP
R1014 100K/R2J-1-GP
R909 8K2/R2J-3-GP
R572 20K/R2J-L2-GP
R329 100K/R2J-1-GP
R571 100K/R2J-1-GP
R575 100K/R2J-1-GP
R581 100K/R2J-1-GP
R974 0R2J-2-GP
R574 100K/R2J-1-GP
R923 100K/R2J-1-GP
R277 20K/R2J-L2-GP
R935 100K/R2J-1-GP
R231 100K/R2J-1-GP
R1012 10K/R2J-3-GP
R592 100K/R2J-1-GP
R263 10K/R2J-3-GP
U23B
2 OF 2
R4F2113NFT-1-GP
R412 33R2J-2-GP
R153 33R2J-2-GP
VIDEO_ID
PROCHOT_EC
-EXTPWR_EC
LID_SWITCH_EC
GLOBAL_WL_DISABLE_EC
-135W_AC
R285 100K/R2J-1-GP
C196 SC220P50V2JN-3GP
C198 SC220P50V2JN-3GP
R590 100K/R2J-1-GP
DY
R582 100K/R2J-1-GP
R939 100K/R2J-1-GP
R68 100K/R2J-1-GP
R245 0R2J-2-GP
R975 43R2J-GP
TEST PAD X2
BOTTOM SIDE
DO NOT MOVE AFTER FIX
H8 I2C BUS2
TPAD14-GP TP43
TPAD14-GP TP42
EC_SCL2
EC_SDA2
R919 10K/R2J-3-GP
D7 RB521S-30TE61-GP
R269 560R2J-3-GP
R892 560R2J-3-GP
R252 1K/R2J-1-GP
-EXTPWR_EC
GLOBAL_WL_DISABLE_EC
LID_SWITCH_EC
-BAY_MEDIA_EJECT_EC
Q52 2SK3541-2-GP
R927 100K/R2J-1-GP
PROCHOT_EC
-PROCHOT 4,79
RN54 SRN4K7J-10-GP
I2C_DATA_BT1
I2C_CLK_BT1
I2C_DATA_BT0
I2C_CLK_BT0
TABLE
GFX EXT INT.
R329 ASM NO_ASM
R590 NO_ASM ASM
LOGIC
http://hobi-elektronika.net
<Variant Name>
Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.
Title
H8S/2113(2/2)
Size Custom
Document Number
SHINAI-3 SWG
Rev SB
Date: Tuesday, September 07, 2010
Sheet 62 of 102

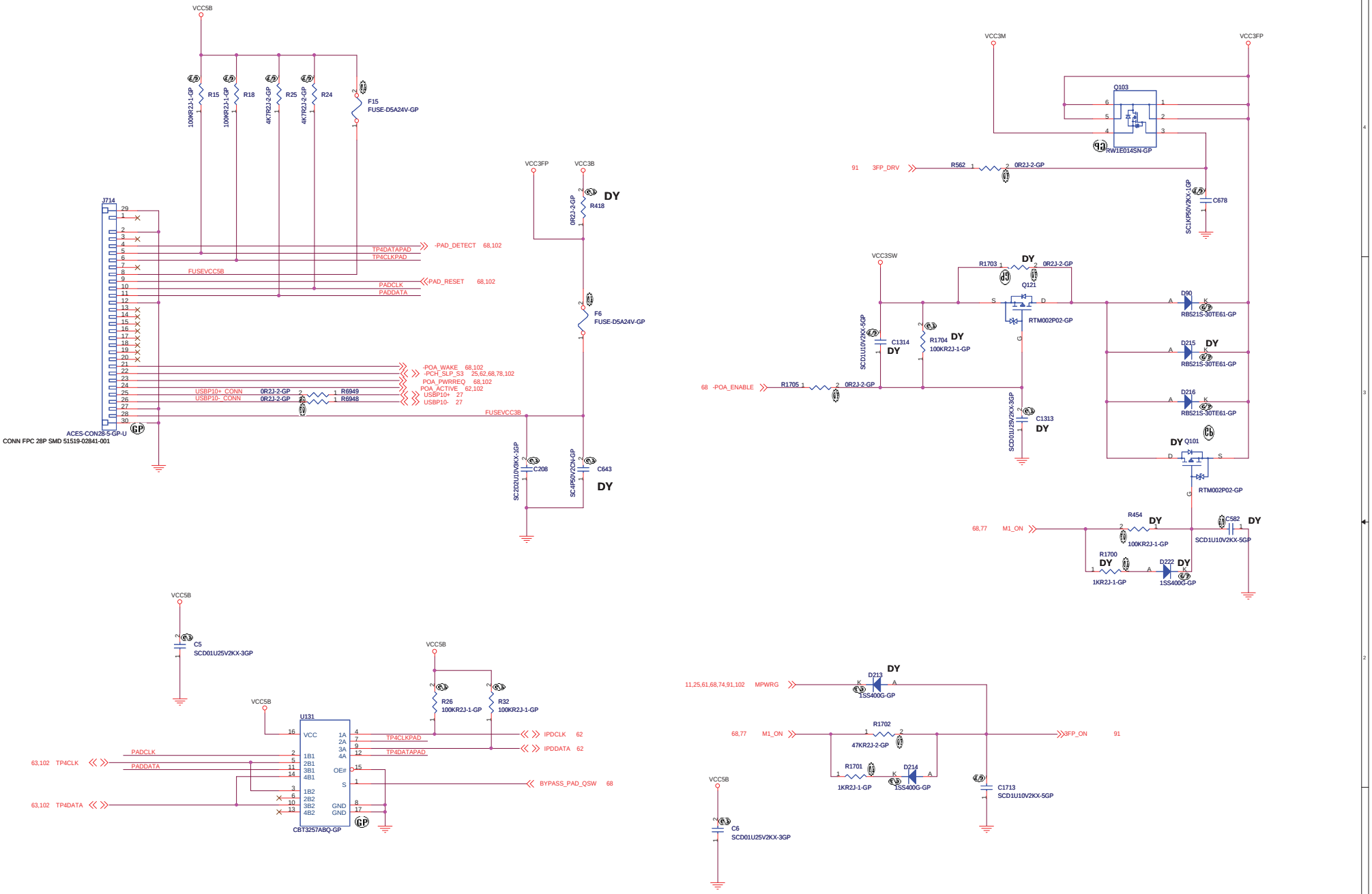
Keyboard Connector



<Variant Name>

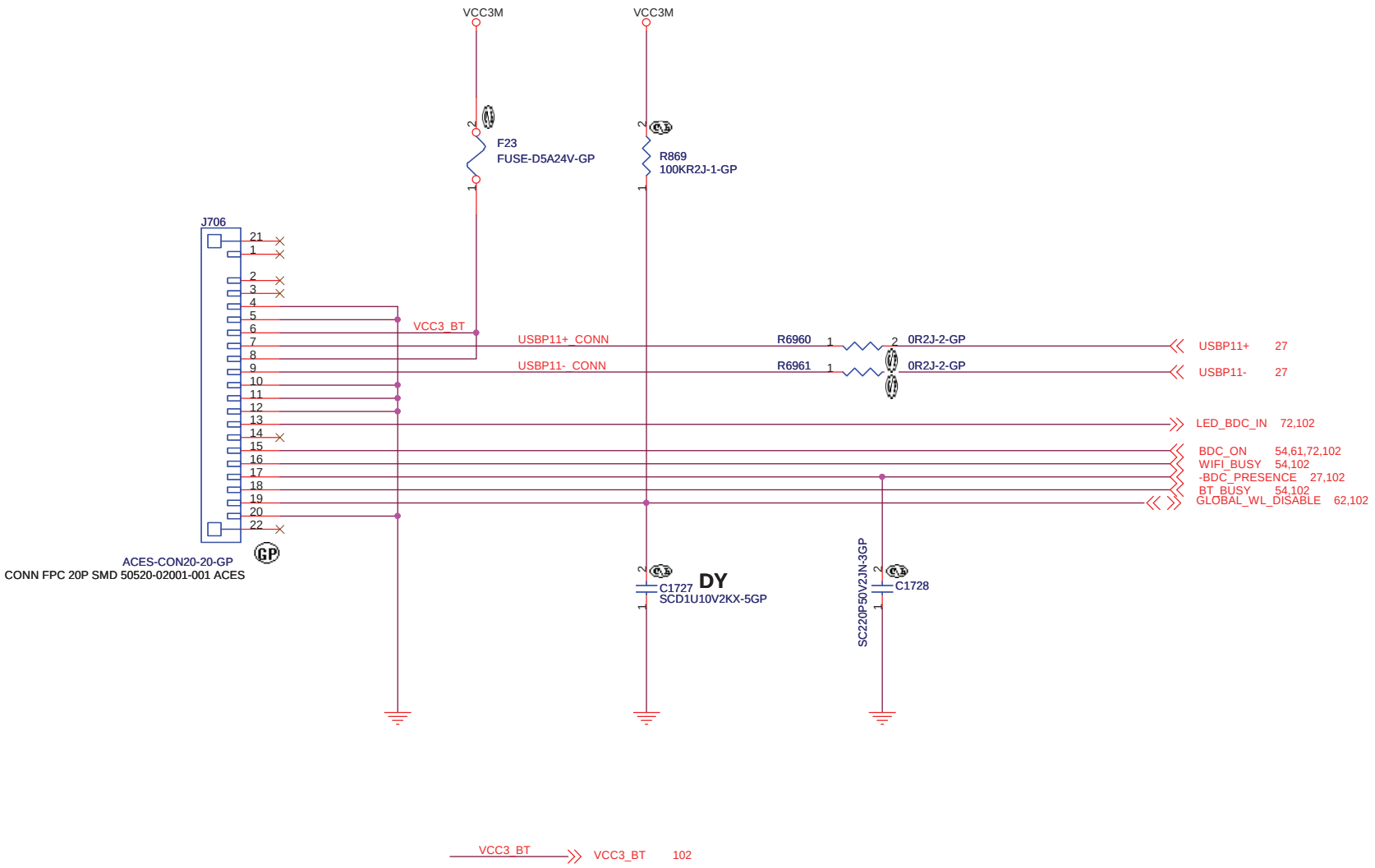
緯創資通 Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title	KEYBOARD CONNECTOR	
Size A3	Document Number SHINAI-3 SWG	Rev SB
Date: Thursday, September 23, 2010	Sheet 63 of 102	



<Variant Name>

緯創資通	Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.
Title	TOUCH PAD CONNECTOR
Size: C	Document Number: SHINAI-3 SWG
Rev	SB
Date: Tuesday, September 07, 2010	Sheet 64 of 102



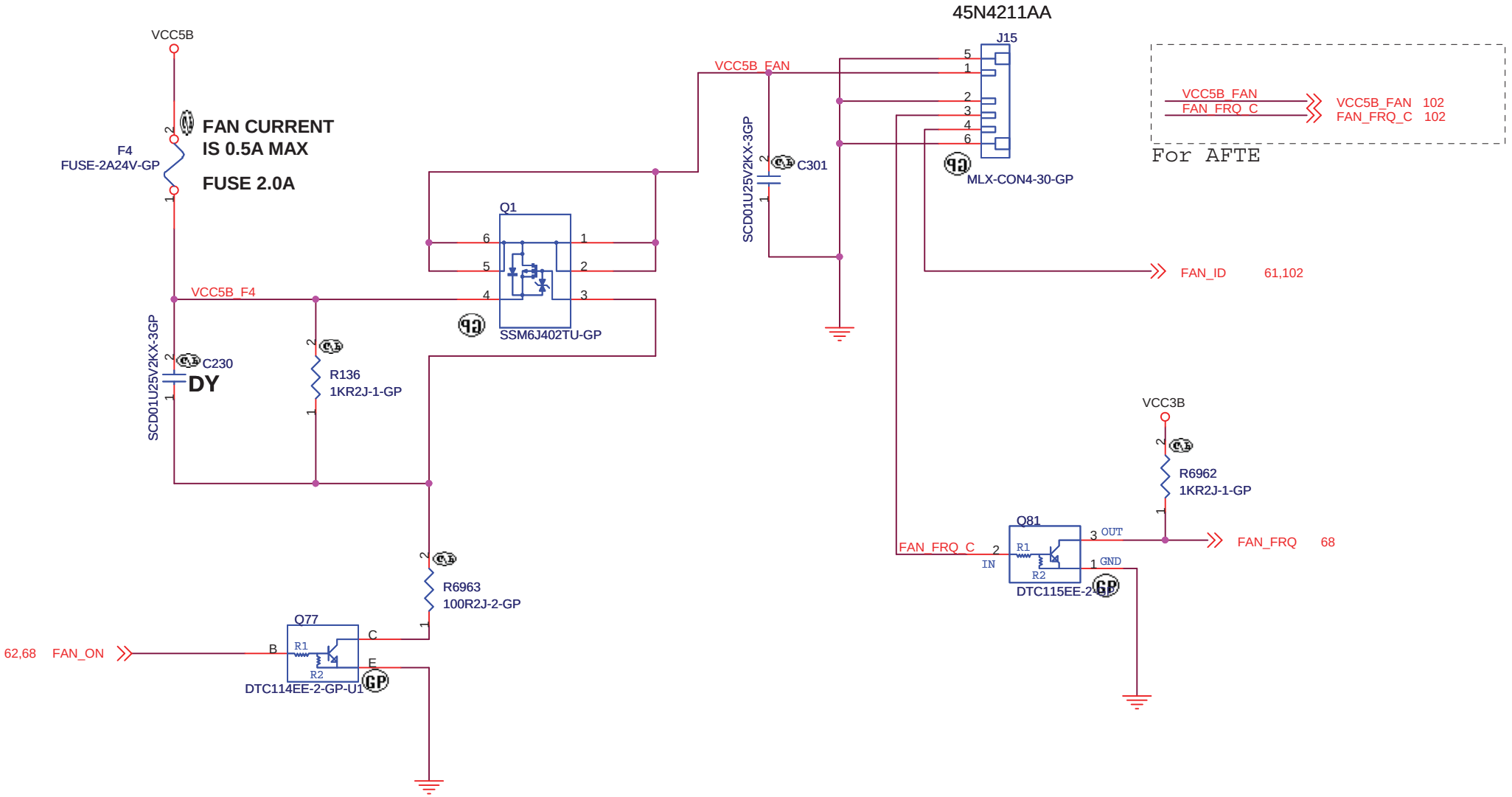
<Core Design>

緯創資通 **Wistron Corporation**
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title: ***WIRELESS DISABLE SW***

Size	Document Number	Rev
A4	SHINAI-3 SWG	SB

Date: Tuesday, September 07, 2010 Sheet 65 of 102



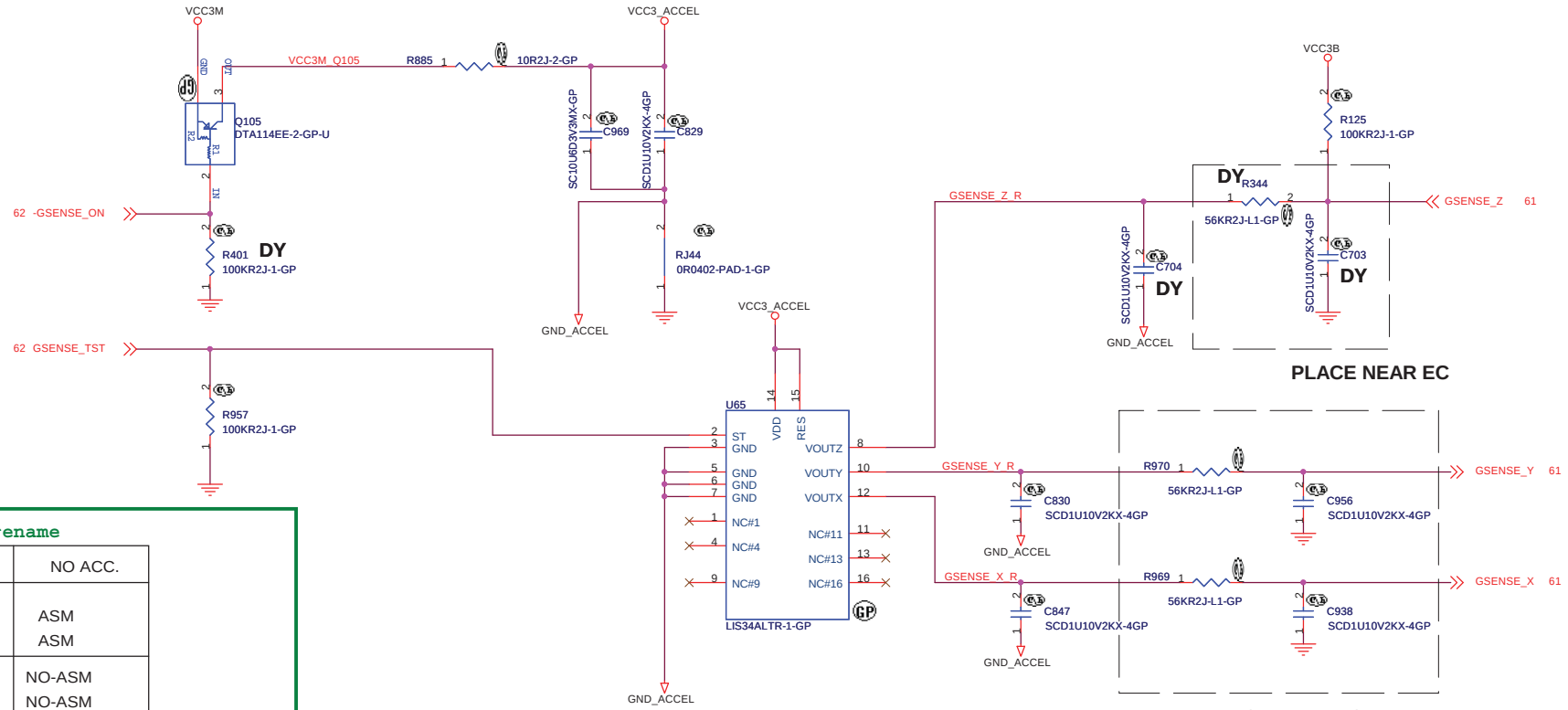
<Core Design>

緯創資通 **Wistron Corporation**
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title: **FAN CONNECTOR**

Size	Document Number	Rev
A4	SHINAI-3 SWG	SB

Date: Tuesday, September 07, 2010 Sheet 66 of 102



Will confirm after rename

	LIS244AL/LIS34AL	NO ACC.
R401	NO-ASM	ASM
R957	ASM	ASM
U65	ASM	NO-ASM
Q105	ASM	NO-ASM
D97	ASM	NO-ASM
R62	ASM	NO-ASM
R885	10-OHM	NO-ASM
C829	ASM	NO-ASM
C969	ASM	NO-ASM
C830	ASM	NO-ASM
C847	ASM	NO-ASM
R969	56K	NO-ASM
C938	ASM	NO-ASM
R970	56K	NO-ASM
C956	ASM	NO-ASM
C704	NO-ASM	NO-ASM
R344	NO-ASM	NO-ASM
C703	NO-ASM	NO-ASM
R125	ASM	ASM

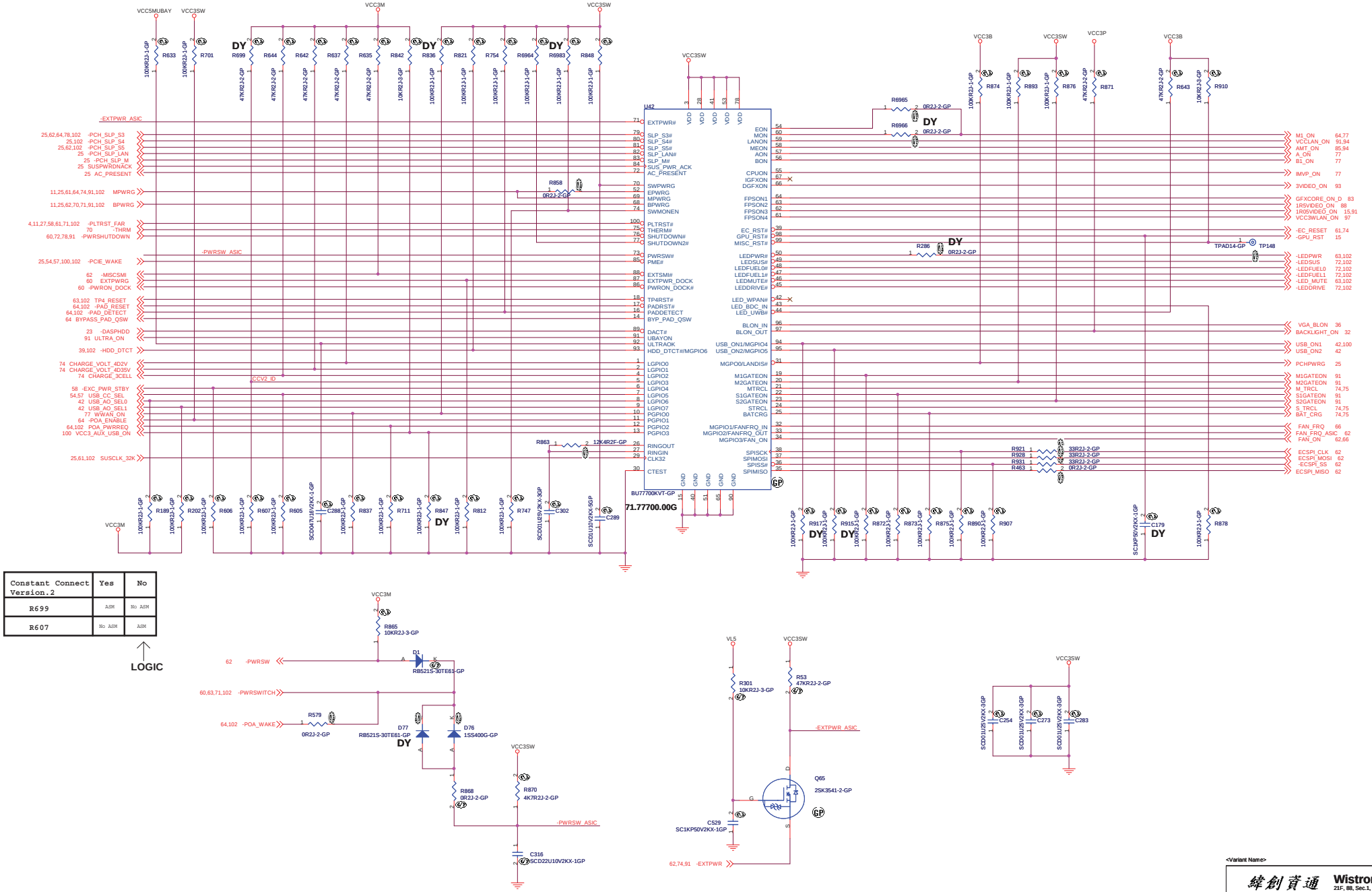
<Core Design>

緯創資通 Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title: G-SENSOR

Size: A3 | Document Number: SHINAI-3 SWG | Rev: SB

Date: Tuesday, September 07, 2010 | Sheet 67 of 102



<Variant Name>

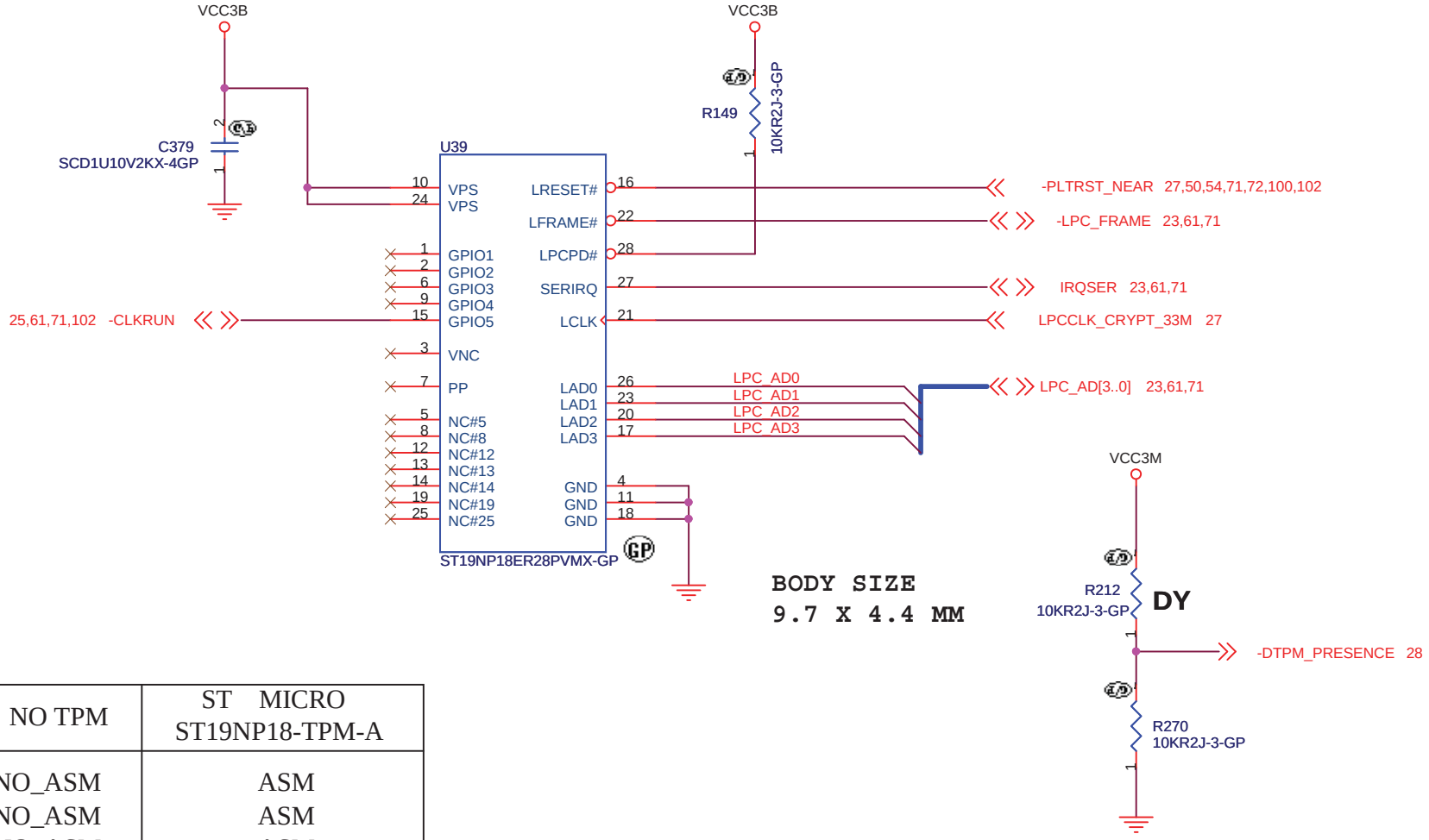
緯創資通 Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title: THINKER-1

Size: A2 | Document Number: SHINAI-3 SWG | Rev: SB

Date: Tuesday, September 07, 2010 | Sheet 68 of 102

TPM



TABLE

REF DES	NO TPM	ST MICRO ST19NP18-TPM-A
U39	NO_ASM	ASM
C379	NO_ASM	ASM
R149	NO_ASM	ASM
R212	ASM	NO_ASM
R270	NO_ASM	ASM

LOGIC

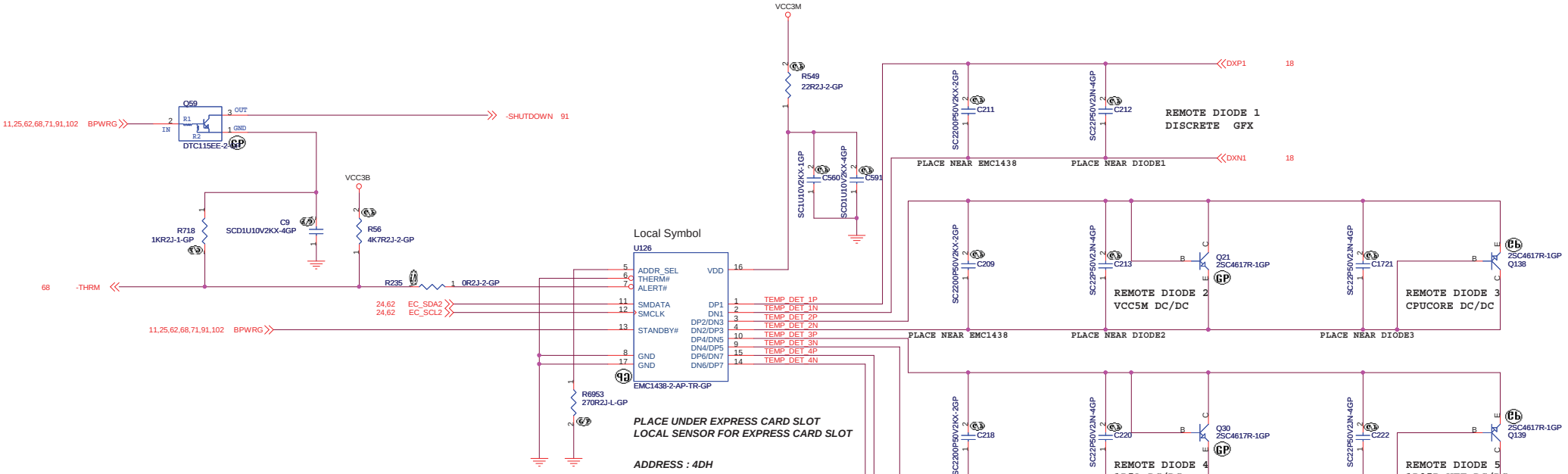
<Variant Name>

緯創資通 Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title: TPM

Size: A4 | Document Number: SHINAI-3 SWG | Rev: SB

Date: Tuesday, September 07, 2010 | Sheet 69 of 102



PLACE UNDER EXPRESS CARD SLOT
LOCAL SENSOR FOR EXPRESS CARD SLOT

ADDRESS : 4DH

TABLE

Resistor(+/-10%)	SMBus Address
GND	1001 100, 4Ch
270	1001 101, 4Dh
560	1001 110, 4Eh
1K	1001 111, 4Fh
1.50K	1001 001, 49h
2.7K	1001 010, 4Ah
5.6K	1001 110, 4Bh
>=18K	0001 000, 18h

LOGIC

<Variant Name>

緯創資通 **Wistron Corporation**
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title: ***THERMAL SENSOR***

Size: Custom | Document Number: **SHINAI-3 SWG** | Rev: **SB**

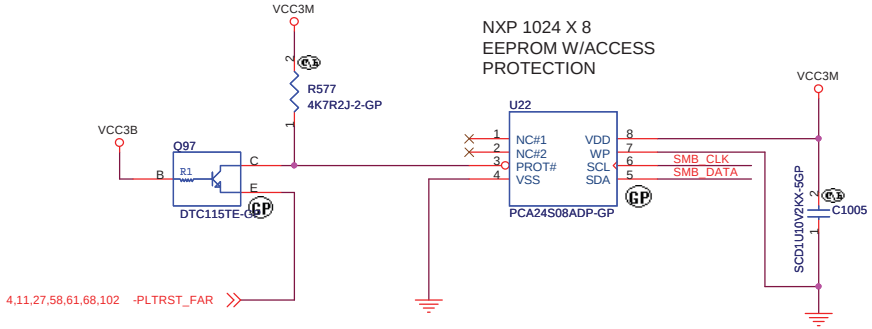
Date: Tuesday, September 07, 2010 | Sheet 70 of 102

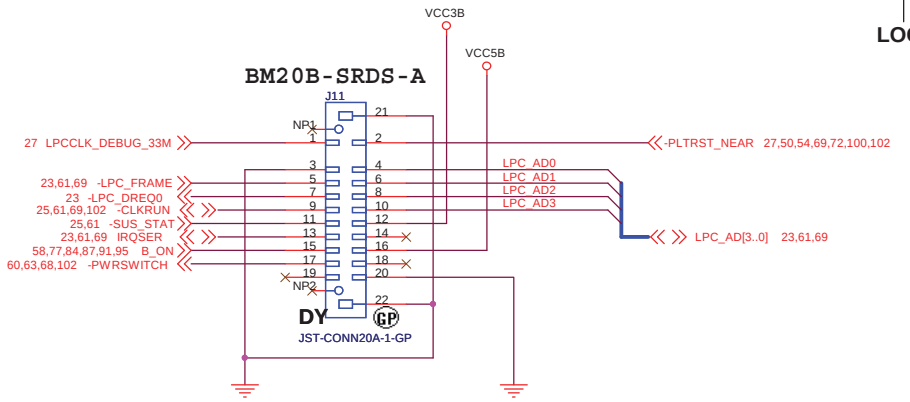
	Supplier	Vendor P/N	WISTRON P/N
1	NXP	PCA24S08ADP	72.24S08.A0Q
2	ROHM	BUL08-1FVJ-W	72.BUL08.00Q
3	SANYO	LE26CAP08TT-TLM-H	72.26C08.00R

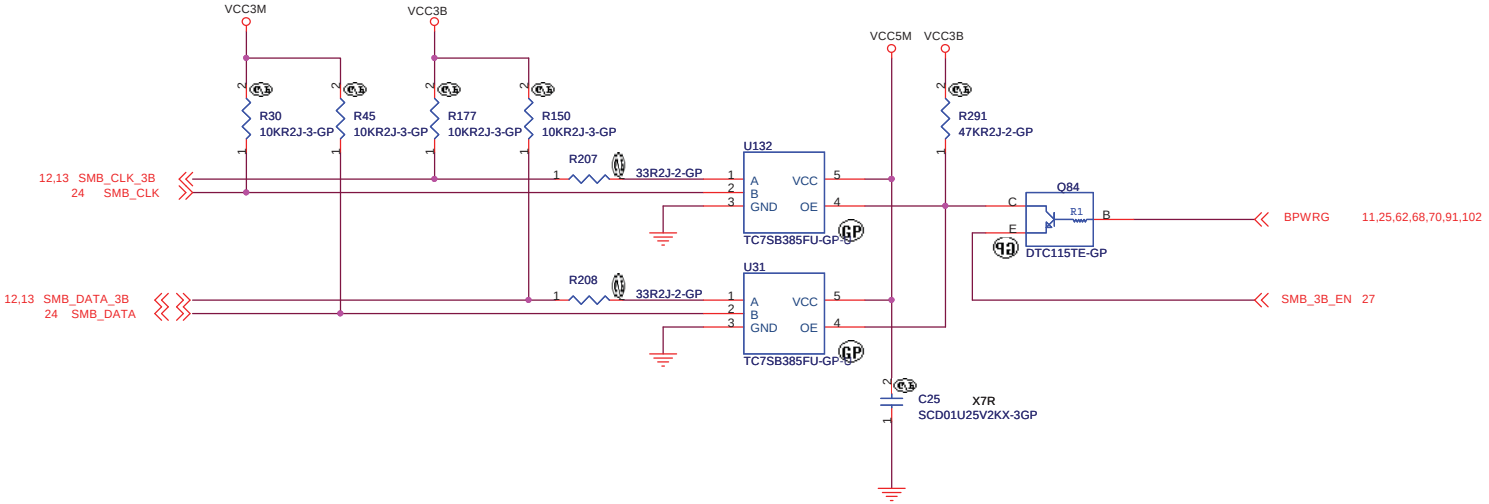
TABLE

REF DES	ENABLE	DISABLE
J11	ASM	NO_ASM

LOGIC







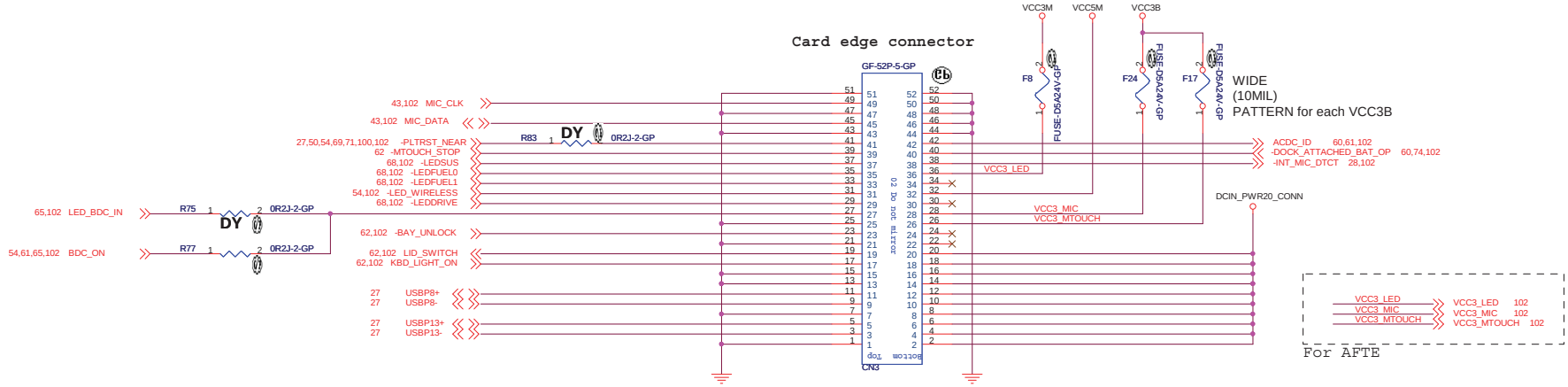
<Core Design>

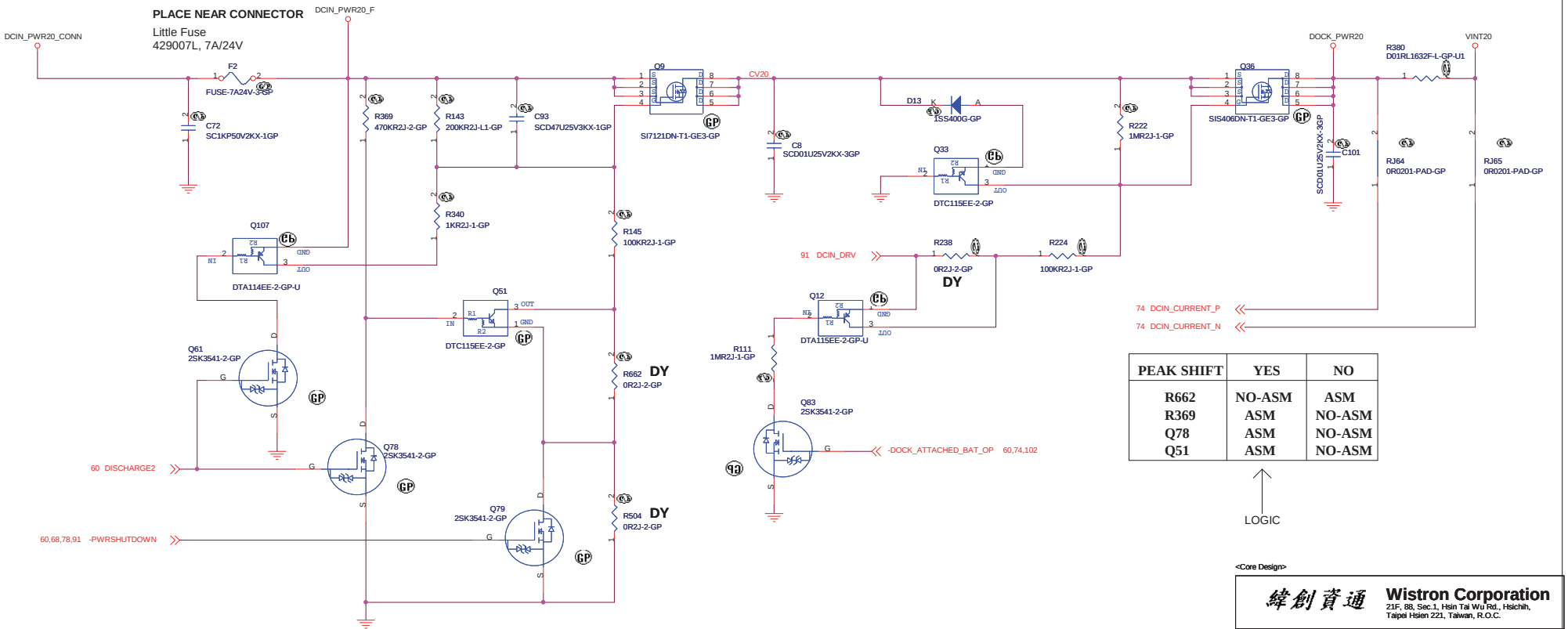
緯創資通 Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title: EEPROM/SMBUS SW

Size: A3 | Document Number: SHINAI-3 SWG | Rev: SB

Date: Tuesday, September 07, 2010 | Sheet 71 of 102





PEAK SHIFT	YES	NO
R662	NO-ASM	ASM
R369	ASM	NO-ASM
Q78	ASM	NO-ASM
Q51	ASM	NO-ASM

LOGIC

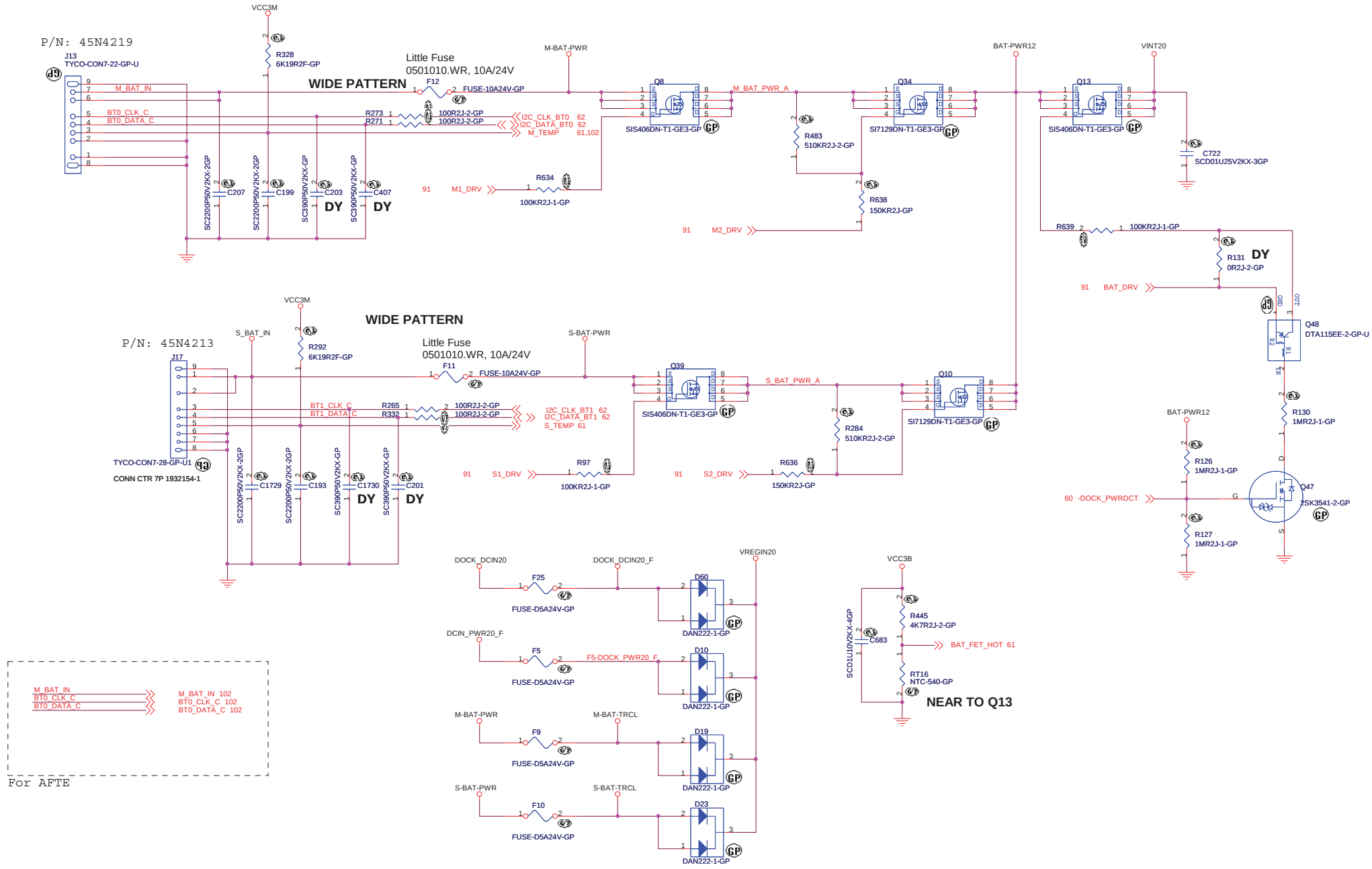
<Core Design>

Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title: DC-IN

Size: Custom | Document Number: SHINAI-3 SWG | Rev: SB

Date: Tuesday, September 07, 2010 | Sheet 72 of 102



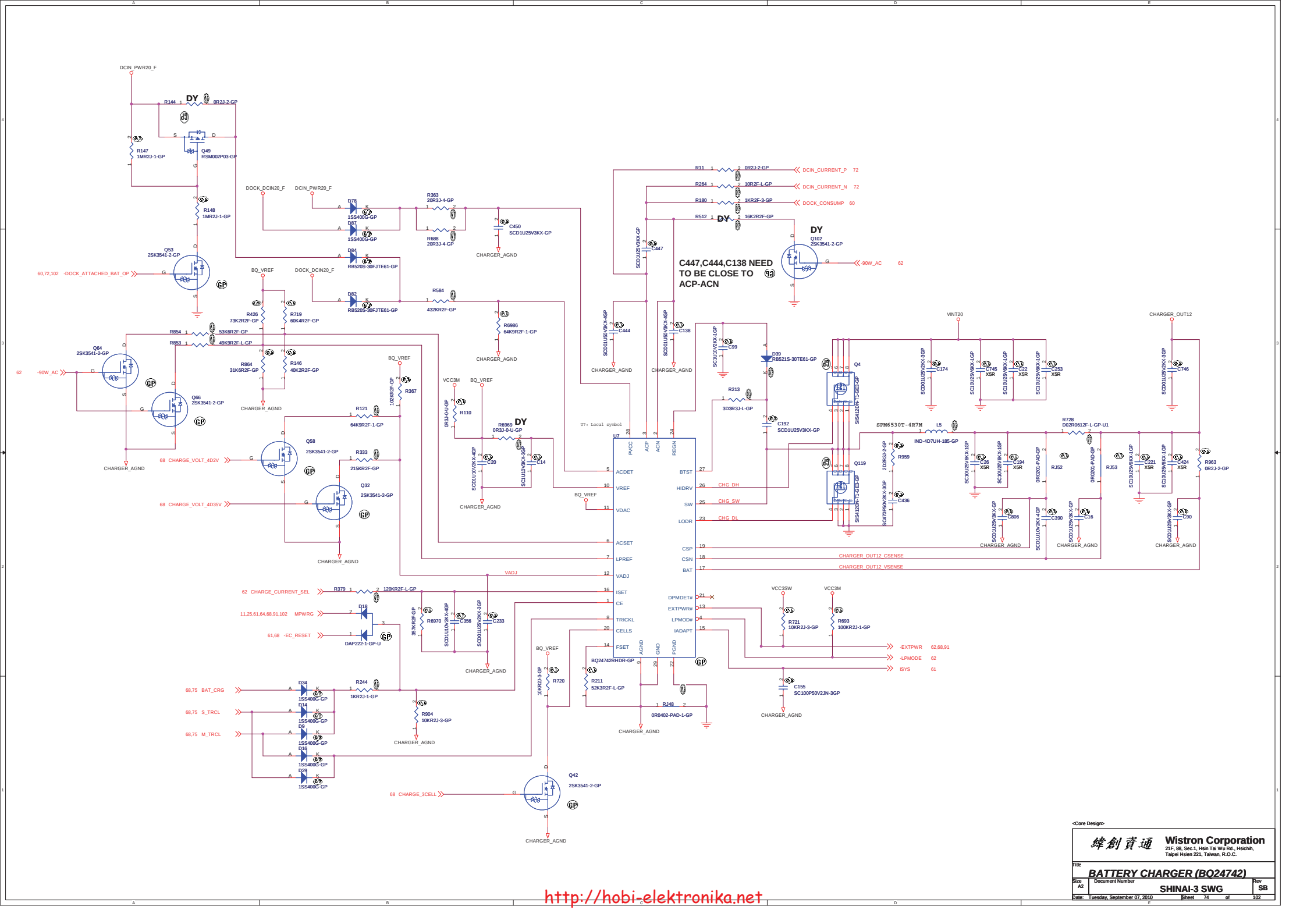
<Core Design>

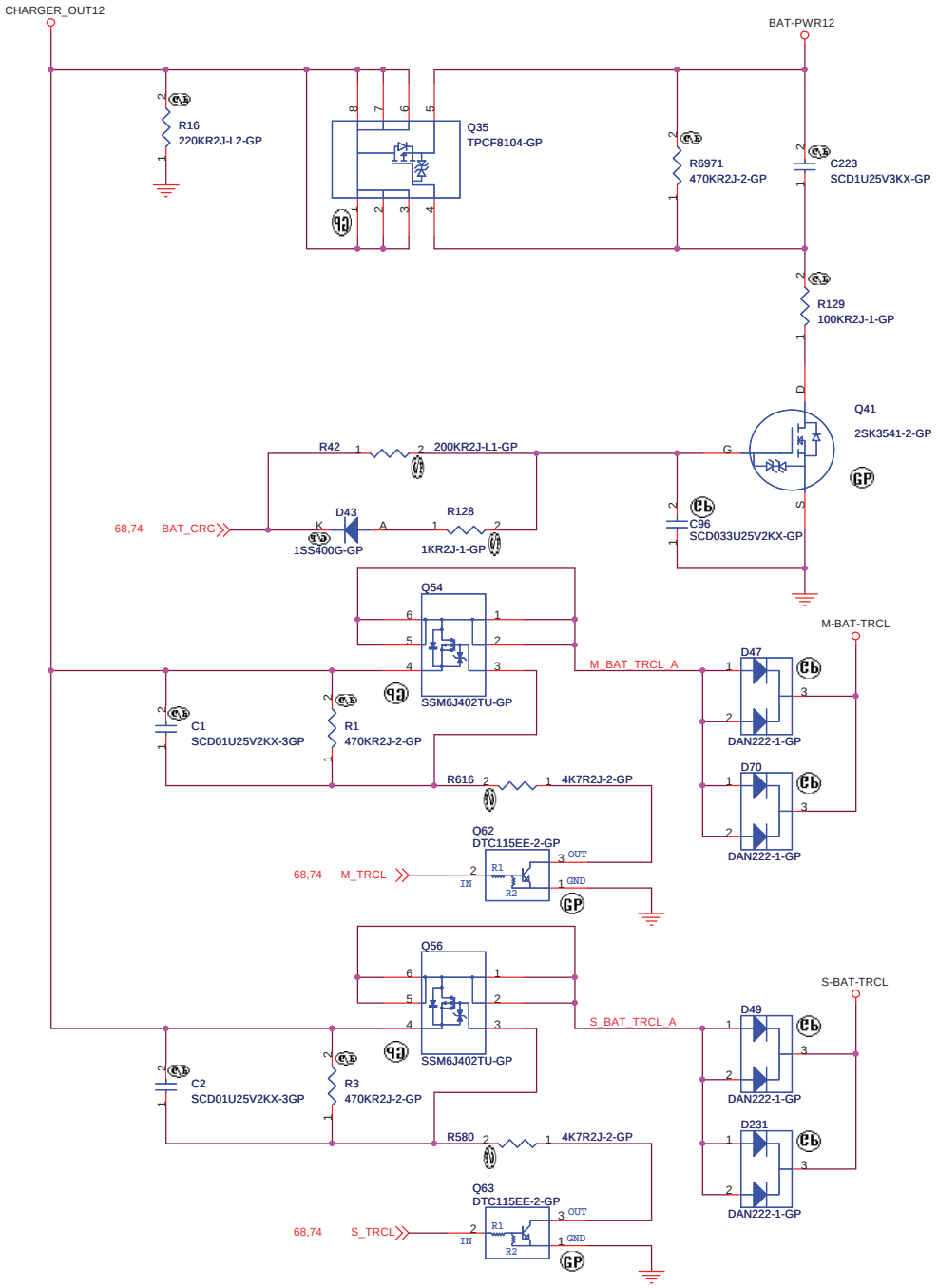
Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title: BATTERY INPUT

Size: Custom | Document Number: SHINAI-3 SWG | Rev: SB

Date: Tuesday, September 07, 2010 | Sheet 73 of 102


DCIN_PWR20_F
DOCK_DCIN20_F
BQ_VREF
VCC3M
VINT20
CHARGER_OUT12
U7
BQ24742RHDR-GP
C447,C444,C138 NEED TO BE CLOSE TO ACP-ACN
CHARGER_AGND
CHARGER_OUT12_CSENSE
CHARGER_OUT12_VSENSE
-EXTPWR
-LPMODE
ISYS
-90W_AC
-DOCK_ATTACHED_BAT_OP
CHARGE_VOLT_4D2V
CHARGE_VOLT_4D35V
CHARGE_CURRENT_SEL
MPWRG
-EC_RESET
BAT_CRG
S_TRCL
M_TRCL
CHARGE_3CELL
DCIN_CURRENT_P
DCIN_CURRENT_N
DOCK_CONSUMP
CHG_DH
CHG_SW
CHG_DL
VADJ
ACDET
VREF
VDAC
ACSET
LPREF
VADJ
ISET
CE
TRICKL
CELLS
FSET
PVCC
ACP
ACN
REGN
BTST
HIDRV
SW
LODR
CSP
CSN
BAT
DPMDET#
EXTPWR#
LPMOD#
IADAPT
AGND
GND
PGND
SPM6530T-4R7M
IND-4D7UH-185-GP
Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.
BATTERY CHARGER (BQ24742)
SHINAI-3 SWG
Size A2
Rev SB
Date: Tuesday, September 07, 2010
Sheet 74 of 102
http://hobi-elektronika.net



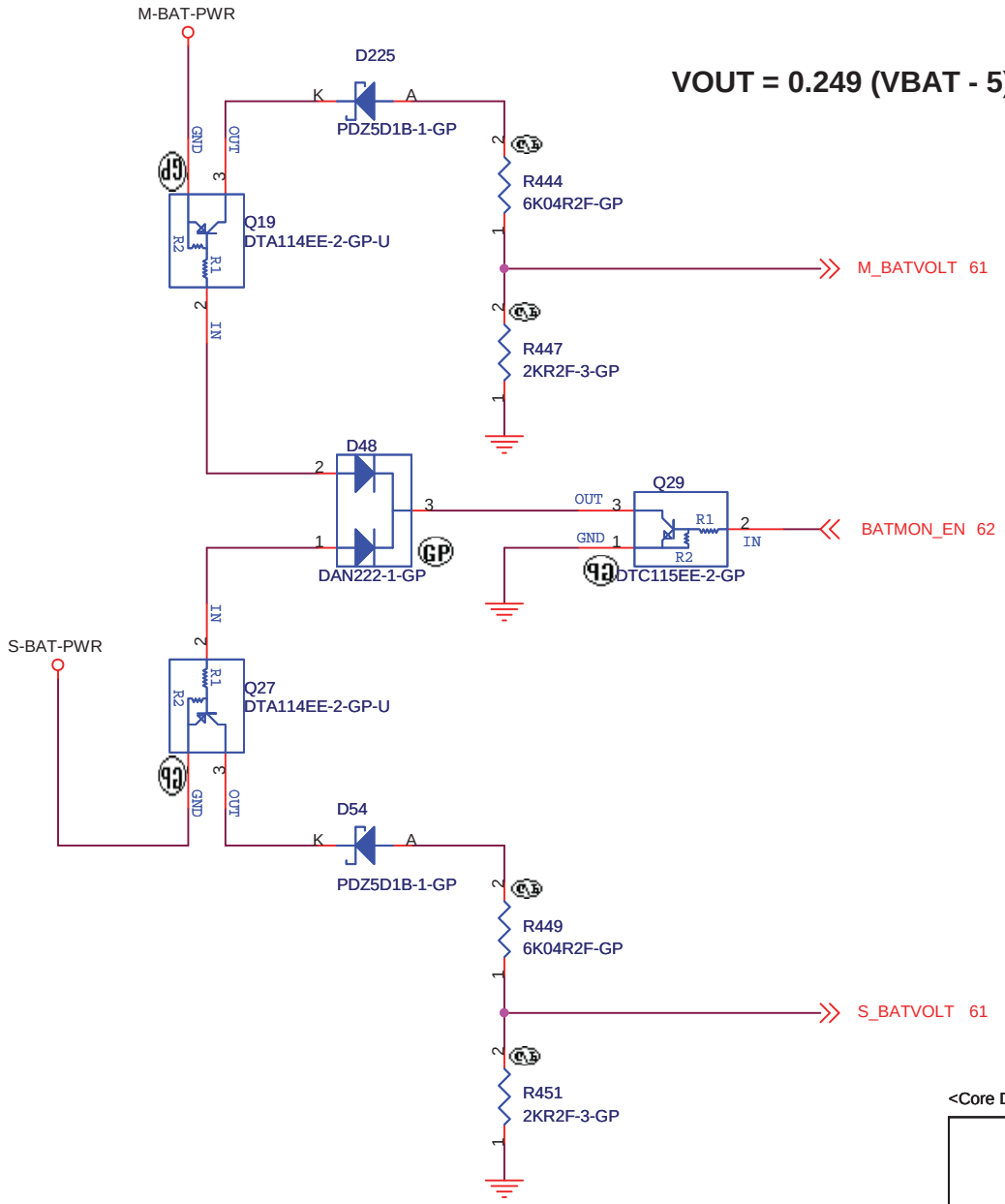
<Core Design>

緯創資通 Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title: CHARGER SELECT

Size: A3 | Document Number: SHINAI-3 SWG | Rev: SB

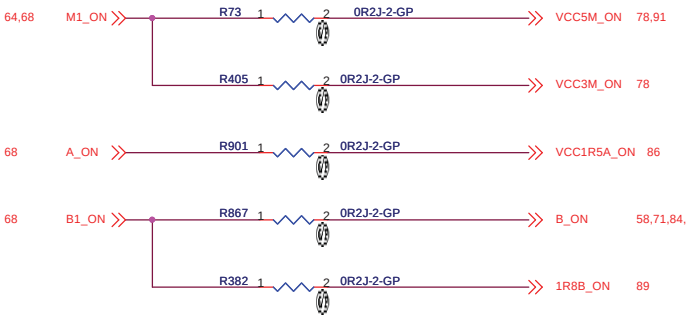
Date: Tuesday, September 07, 2010 | Sheet 75 of 102

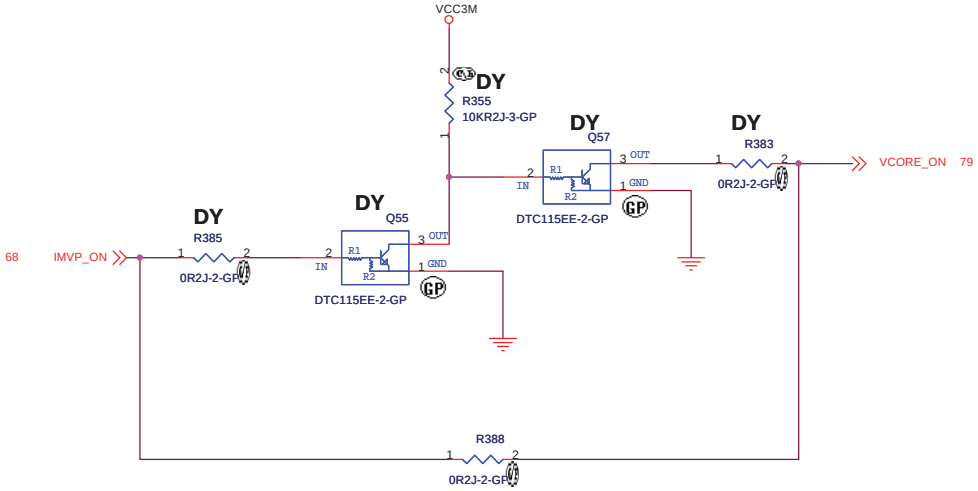


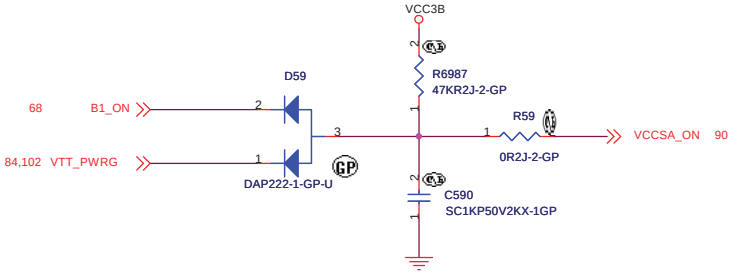
VOUT = 0.249 (VBAT - 5)

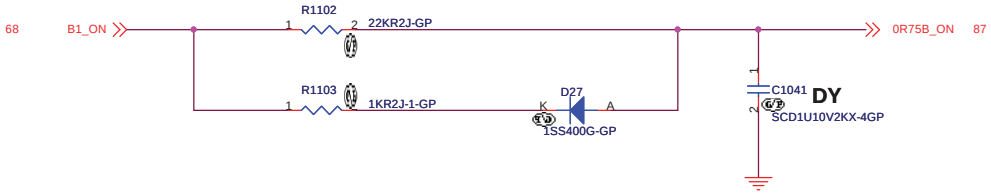
<Core Design>

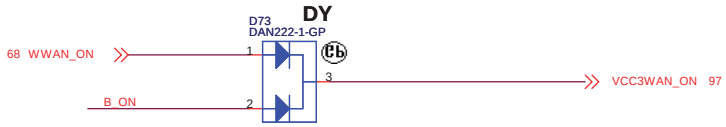
緯創資通 Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.		
Title: **BATTERY MONITOR**		
Size: A4	Document Number: **SHINAI-3 SWG**	Rev: SB
Date: Tuesday, September 07, 2010	Sheet 76 of 102	











Table

CONSTANT CONNECT	VER.2	VER.1/NO
D73	ASM	NO_ASM

LOGIC

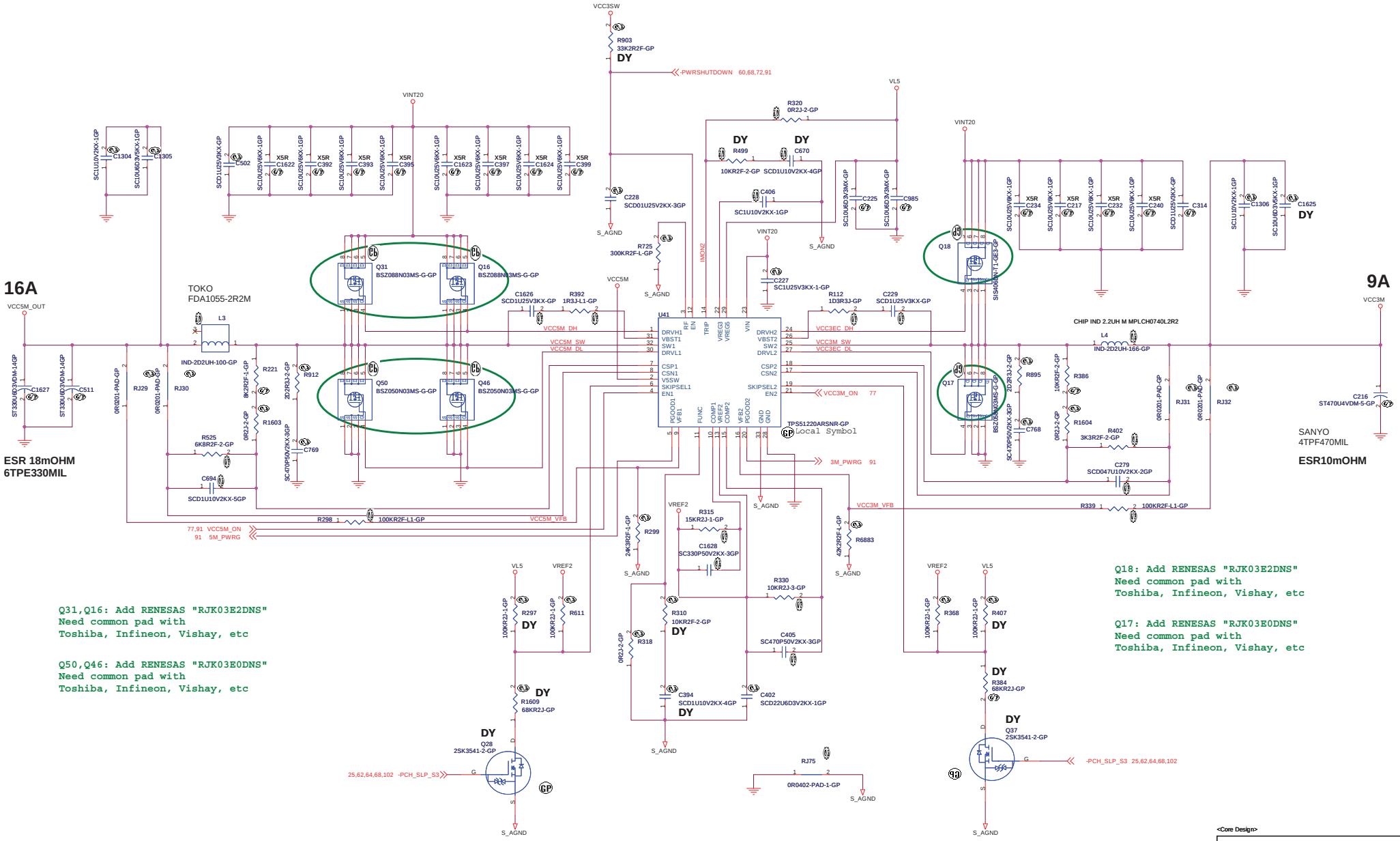
<Core Design>

Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title: POWER SEQUENCE

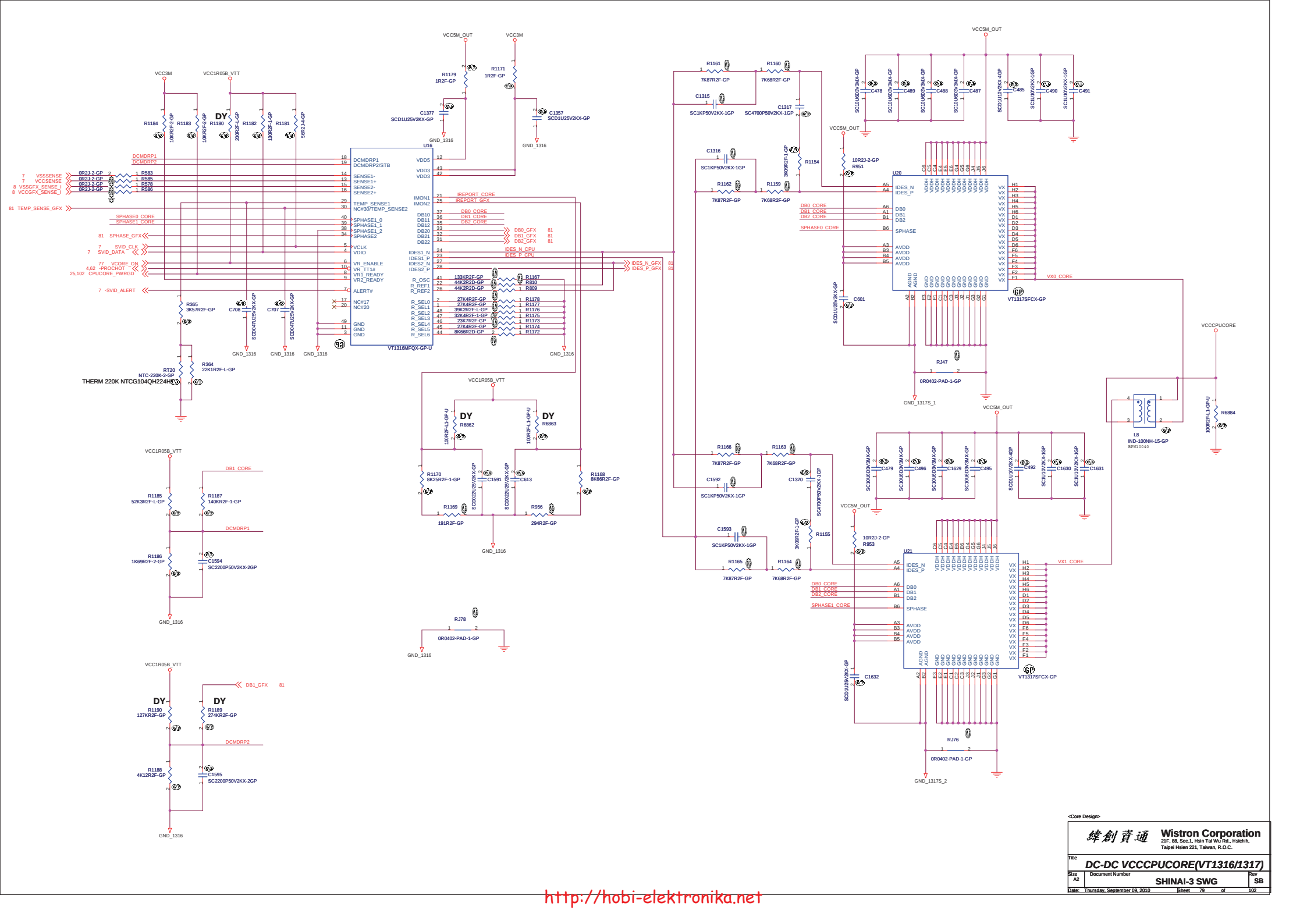
Size: A3 | Document Number: SHINAI-3 SWG | Rev: SB

Date: Tuesday, September 07, 2010 | Sheet 77 of 102



<Core Design>
緯創資通 Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title: DC-DC VCC3M/VCC5M(TPS51222)
Size: Custom | Document Number: SHINAI-3 SWG | Rev: SB
Date: Tuesday, September 07, 2010 | Sheet 78 of 102



<Core Design>

緯創資通 Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title: DC-DC VCCCPUCORE(VT1316/1317)

Size	Document Number	Rev
A2	SHINAI-3 SWG	SB

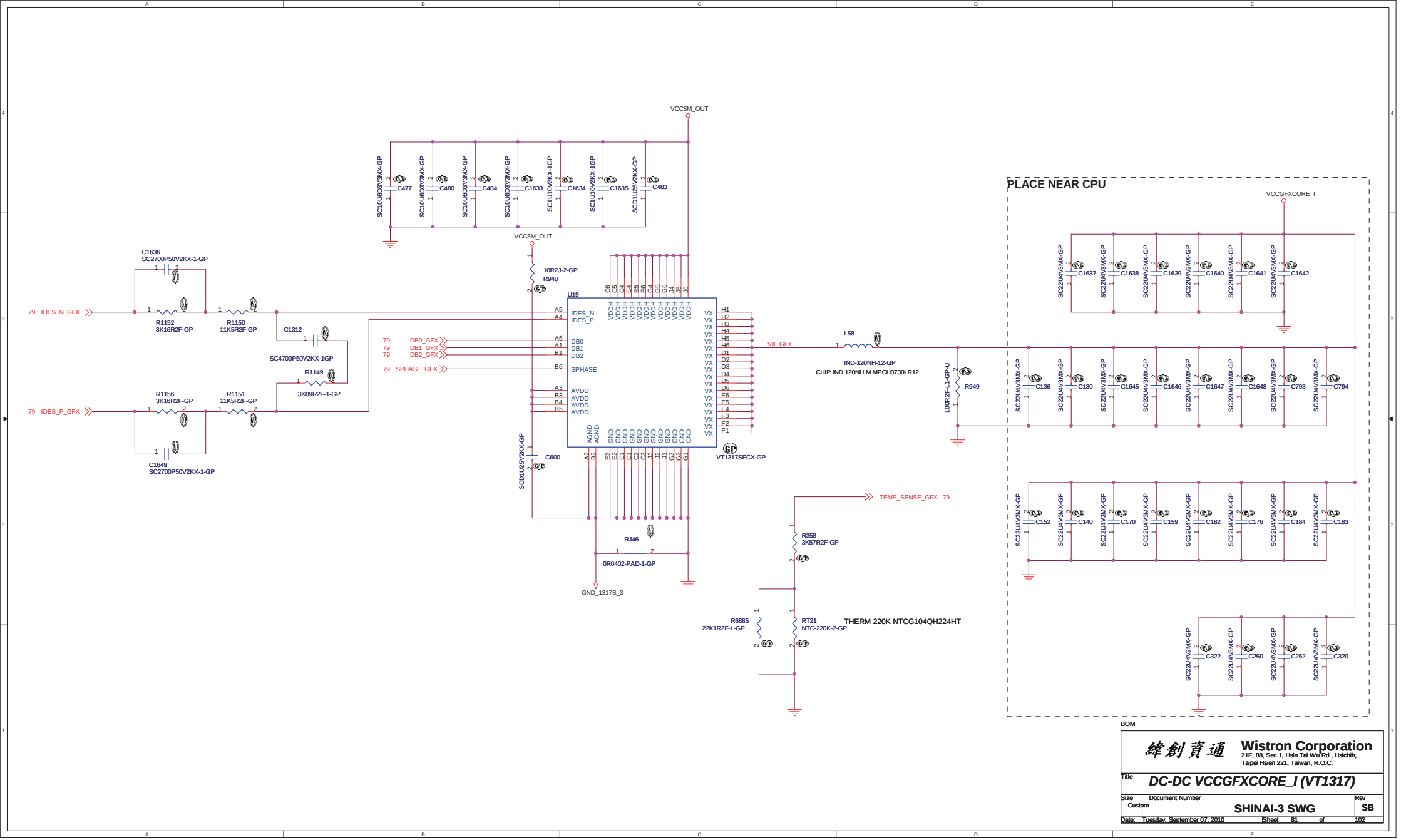
Date: Thursday, September 09, 2010 Sheet 79 of 102

BLANK

<Variant Name>

緯創資通 **Wistron Corporation**
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title: ***BLANK***		
Size: B	Document Number: **SHINAI-3 SWG**	Rev: SB
Date: Tuesday, September 07, 2010	Sheet 80 of 102	

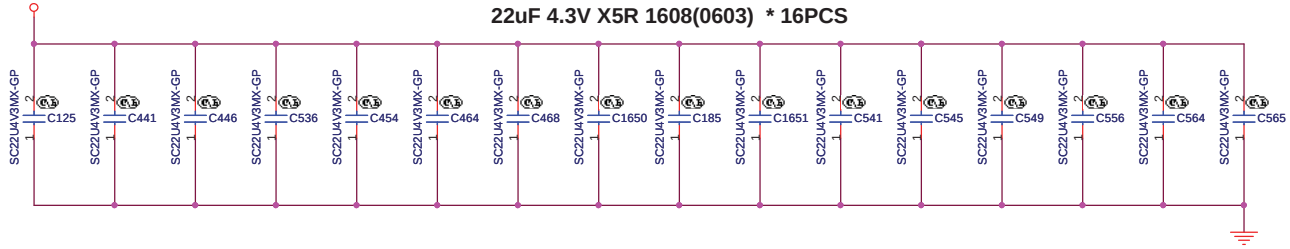


BOM

緯創資通	Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title	DC-DC VCCGFXCORE_I (VT1317)	
Size: Custom	Document Number: SHINAI-3 SWG	Rev: SB
Date: Tuesday, September 07, 2010	Sheet 81 of 102	

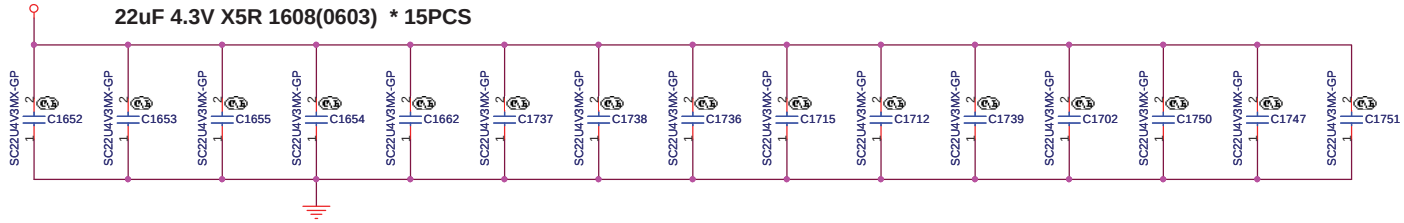
VCCCPUCORE

22uF 4.3V X5R 1608(0603) * 16PCS



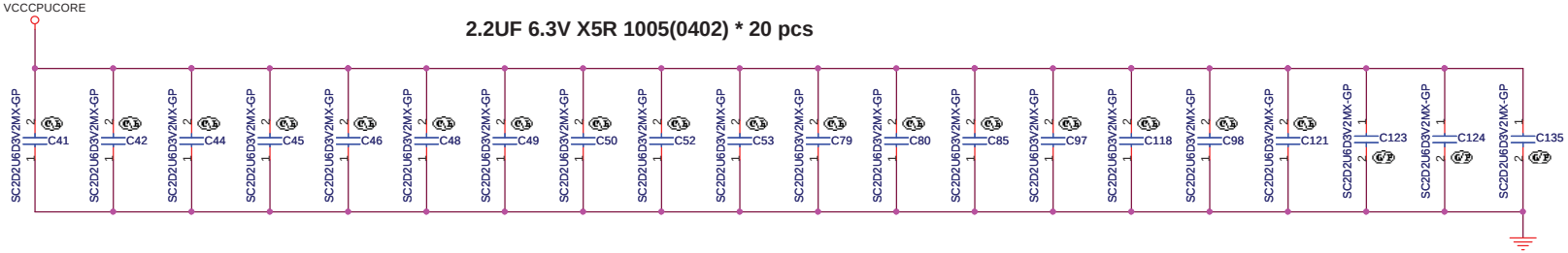
VCCCPUCORE

22uF 4.3V X5R 1608(0603) * 15PCS



VCCCPUCORE

2.2UF 6.3V X5R 1005(0402) * 20 pcs



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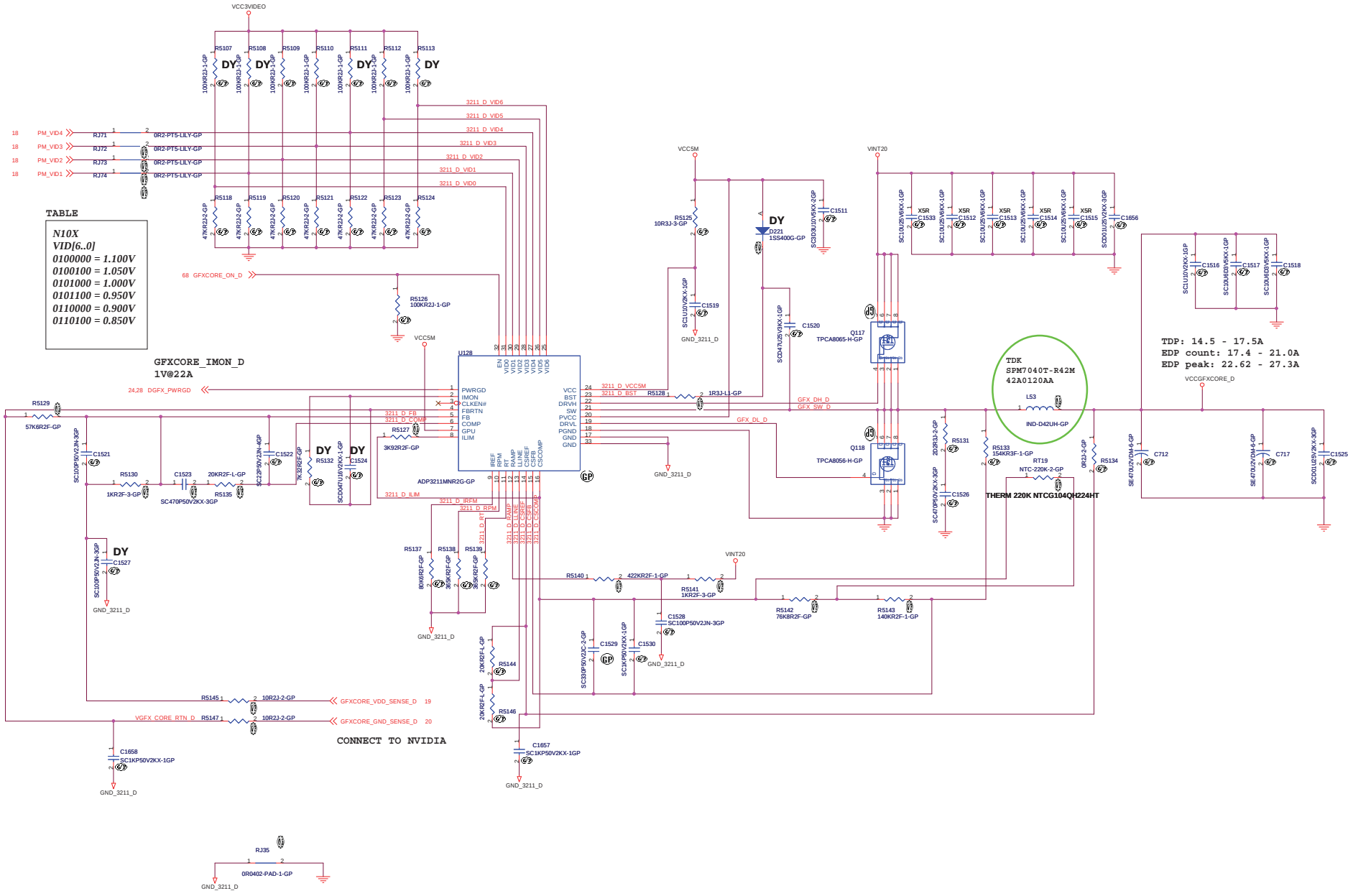
緯創資通 Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title: VCCCPUCORE DECOUPLING

Size	Document Number	Rev
A3	SHINAI-3 SWG	SB

Date: Tuesday, September 07, 2010 Sheet 82 of 102



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緯創資通 Wistron Corporation	21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.
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Size A2 / Document Number	SHINAI-3 SWG / Rev SB
Date: Tuesday, September 07, 2010	Sheet 83 of 102

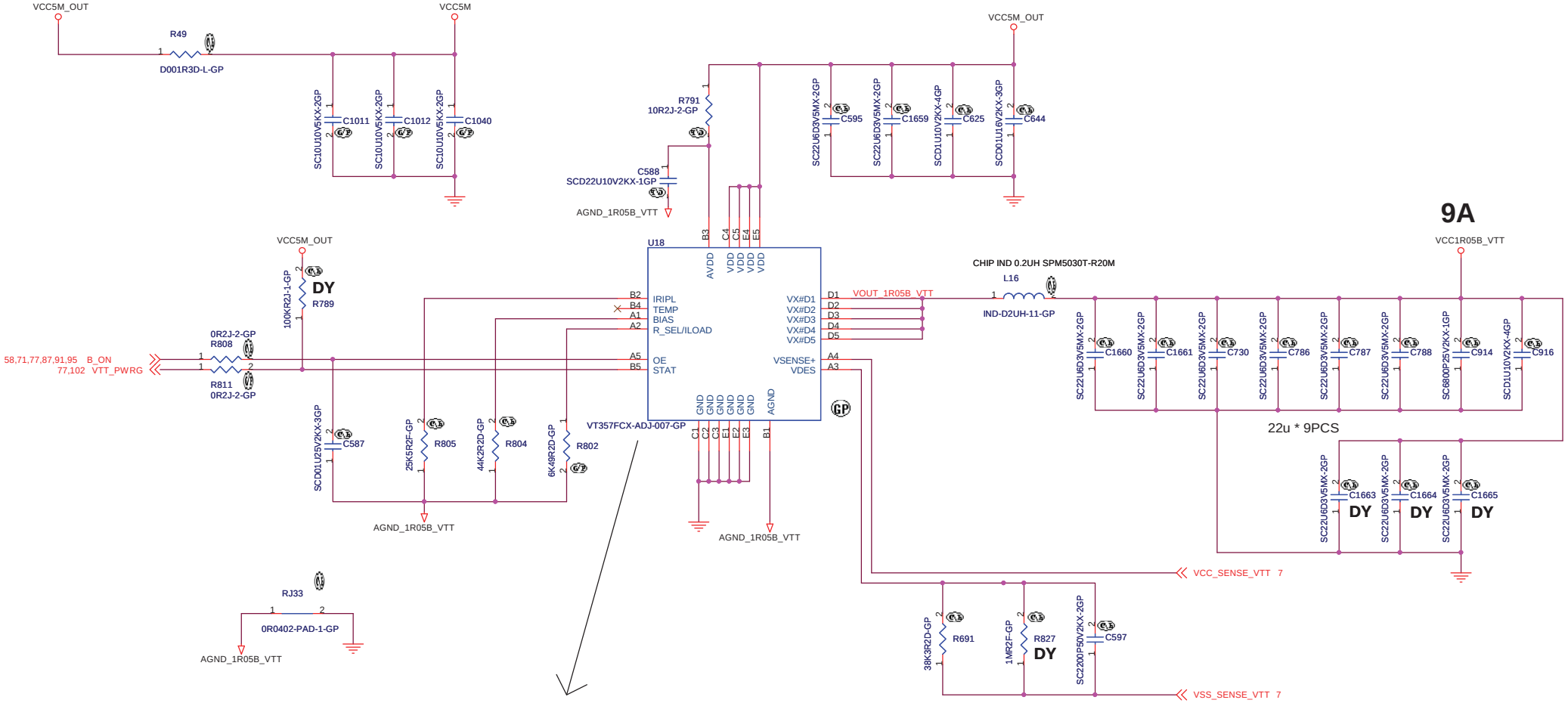

TABLE OF VCC1R05B_VTT POWER SOLUTION

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1	VT356FCX-ADJ-007	6.49K/0.5%	25.5K/1%
2	VT357FCX-ADJ-007	5.9K/0.5%	35.7K/1%

LOGIC

VSENSE-TRACE ROUTED DIFFERENTIALLY PARALLEL TO VSENSE+

```
1.10V R691 = 40.2K
1.05V R691 = 38.3K
VOUT =1.212*R691/44.2K
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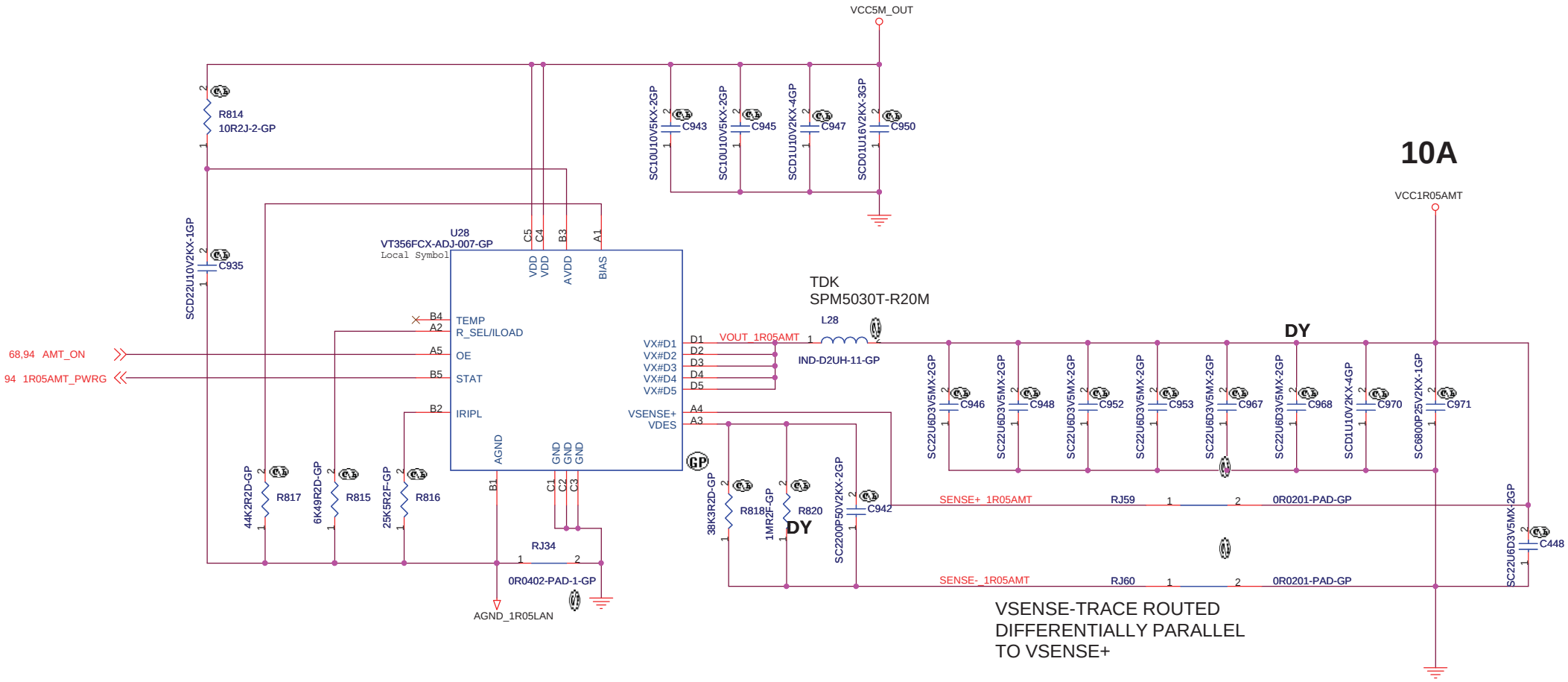
<Variant Name>

緯創資通 Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title: DC-DC VCC1R05B_VTT(VT357)

Size: A3 | Document Number: SHINAI-3 SWG | Rev: SB

Date: Tuesday, September 07, 2010 | Sheet 84 of 102



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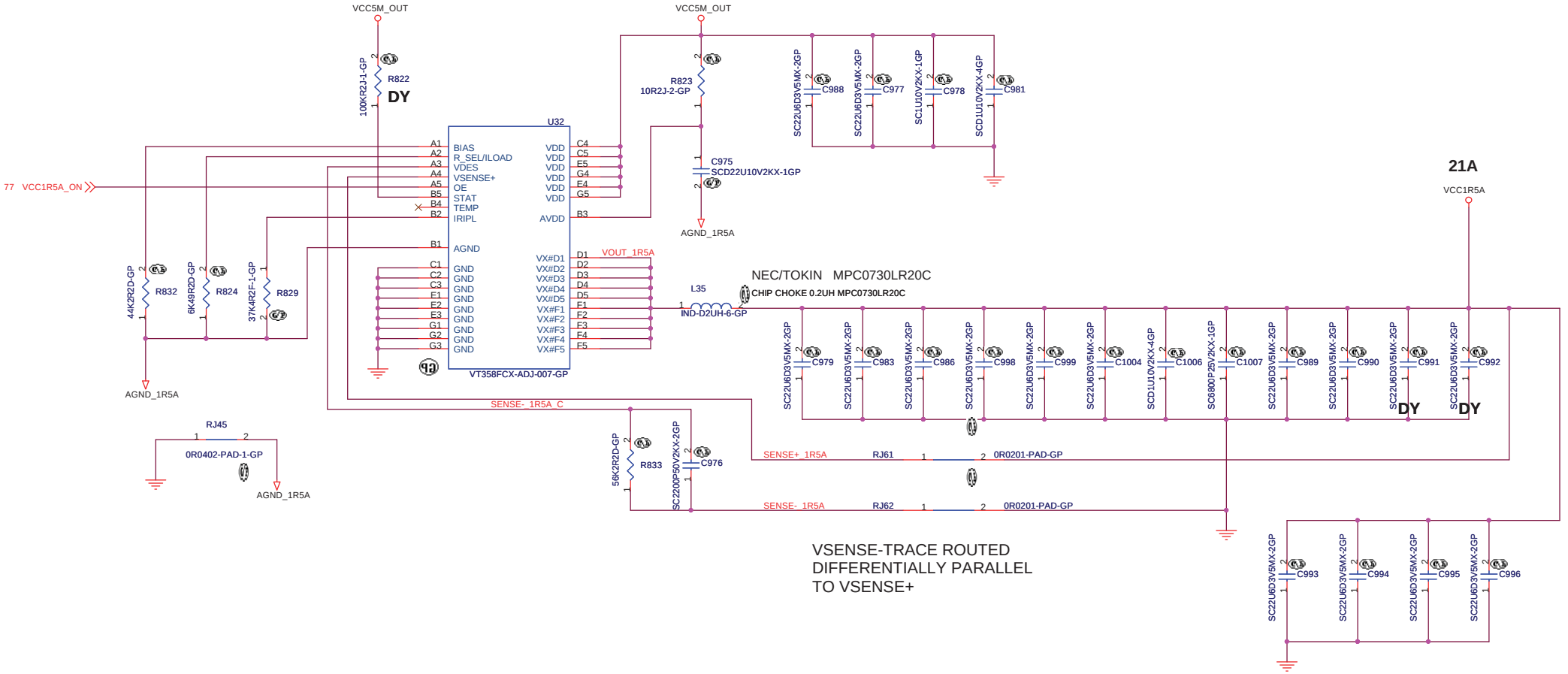
緯創資通 **Wistron Corporation**
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title: ***DC-DC VCC1R05AMT(VT356)***

Size	Document Number	Rev
Custom	SHINAI-3 SWG	SB

Date: Tuesday, September 07, 2010 Sheet 85 of 102



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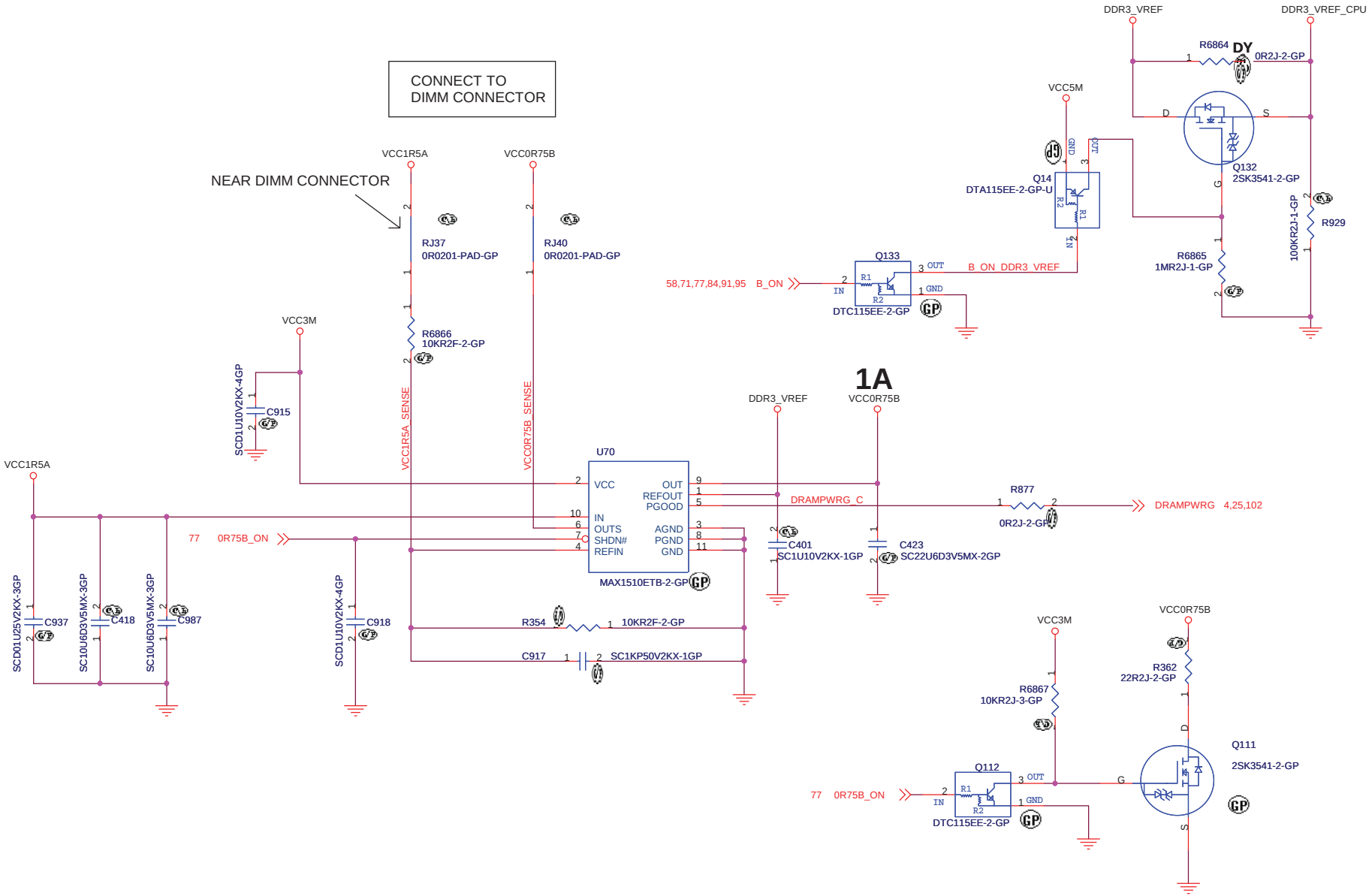
緯創資通 Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title: DC-DC VCC1R5A(VT358)

Size: A3 | Document Number: SHINAI-3 SWG | Rev: SB

Date: Tuesday, September 07, 2010 | Sheet 86 of 102



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緯創資通	**Wistron Corporation** 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.
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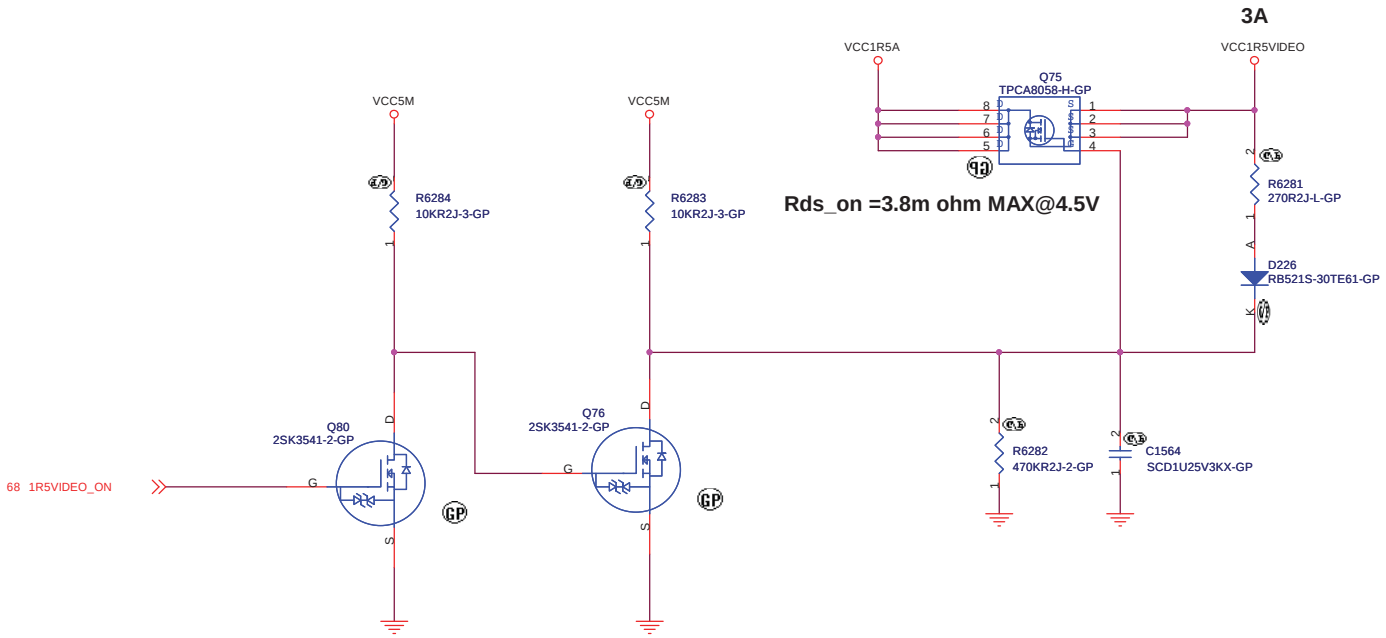
Size: Custom	Document Number: **SHINAI-3 SWG** Rev: **SB**
Date: Tuesday, September 07, 2010	Sheet 87 of 102



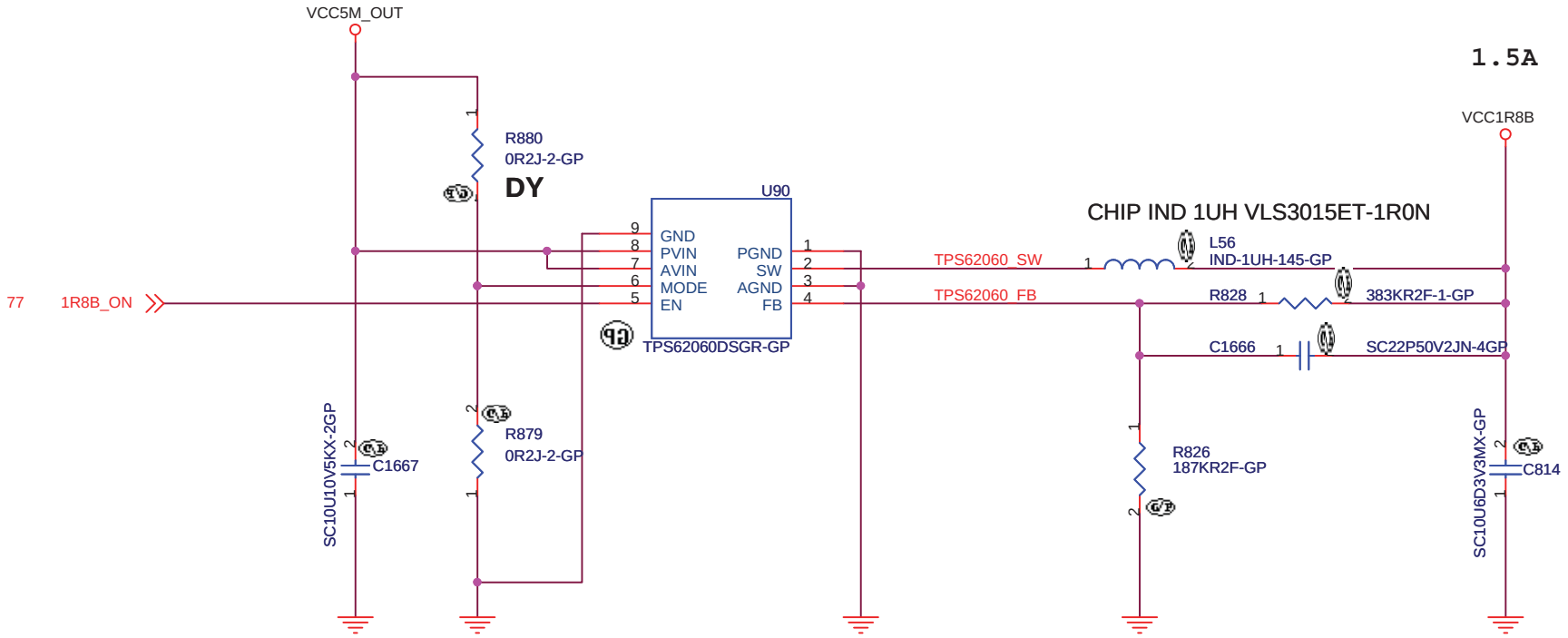
<Core Design>

緯創資通 **Wistron Corporation**
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title: *DC/DC VCC1R5VIDEO(VT356) / VCC1R8VIDEO(BD3551)*

Size	Document Number	Rev
A3	SHINAI-3 SWG	SB

Date: Tuesday, September 07, 2010 Sheet 88 of 102



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緯創資通 **Wistron Corporation**
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title: ***DC-DC VCC1R8B (TPS62290)***

Size	Document Number	Rev
A4	SHINAI-3 SWG	SB

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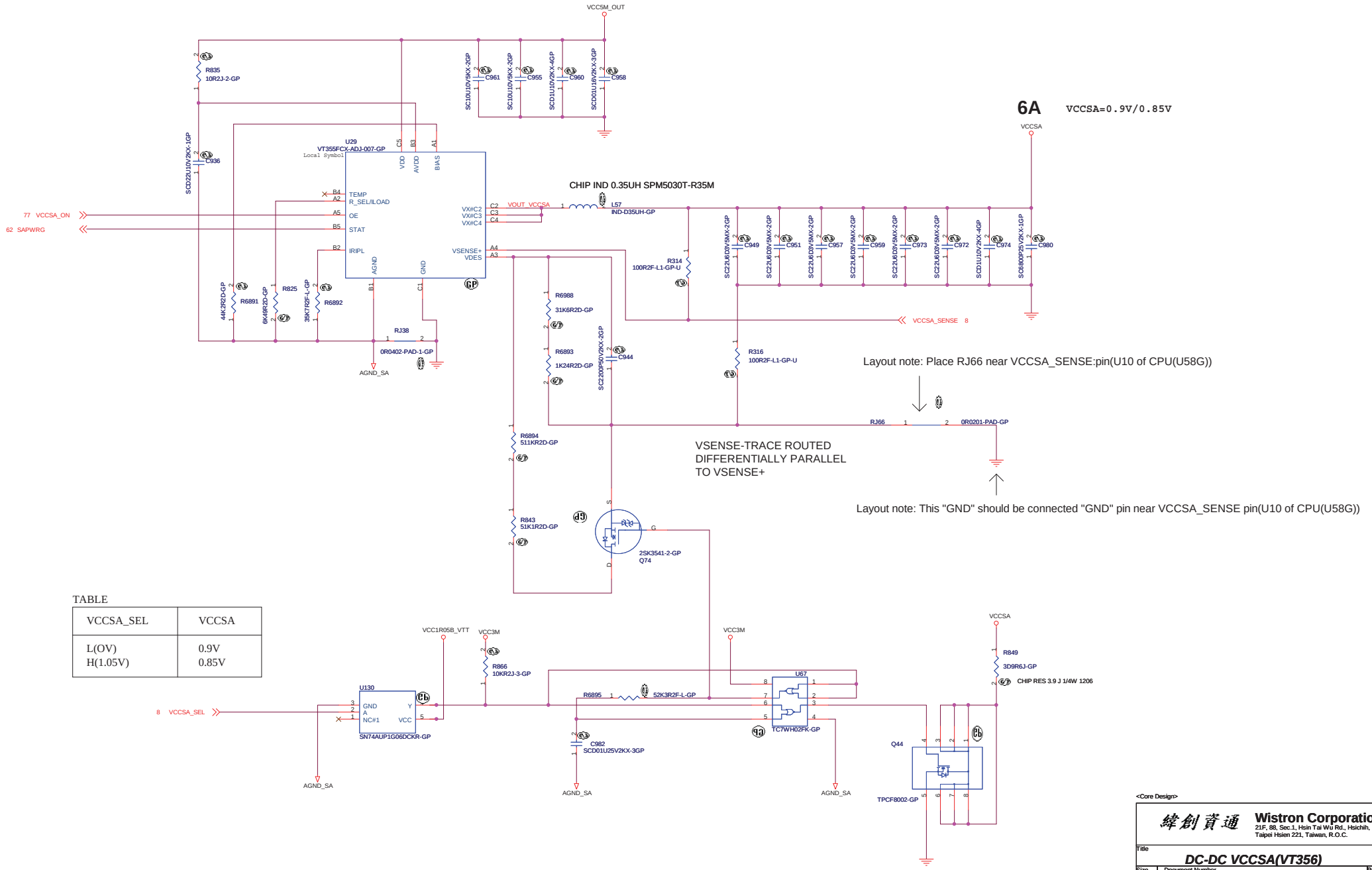

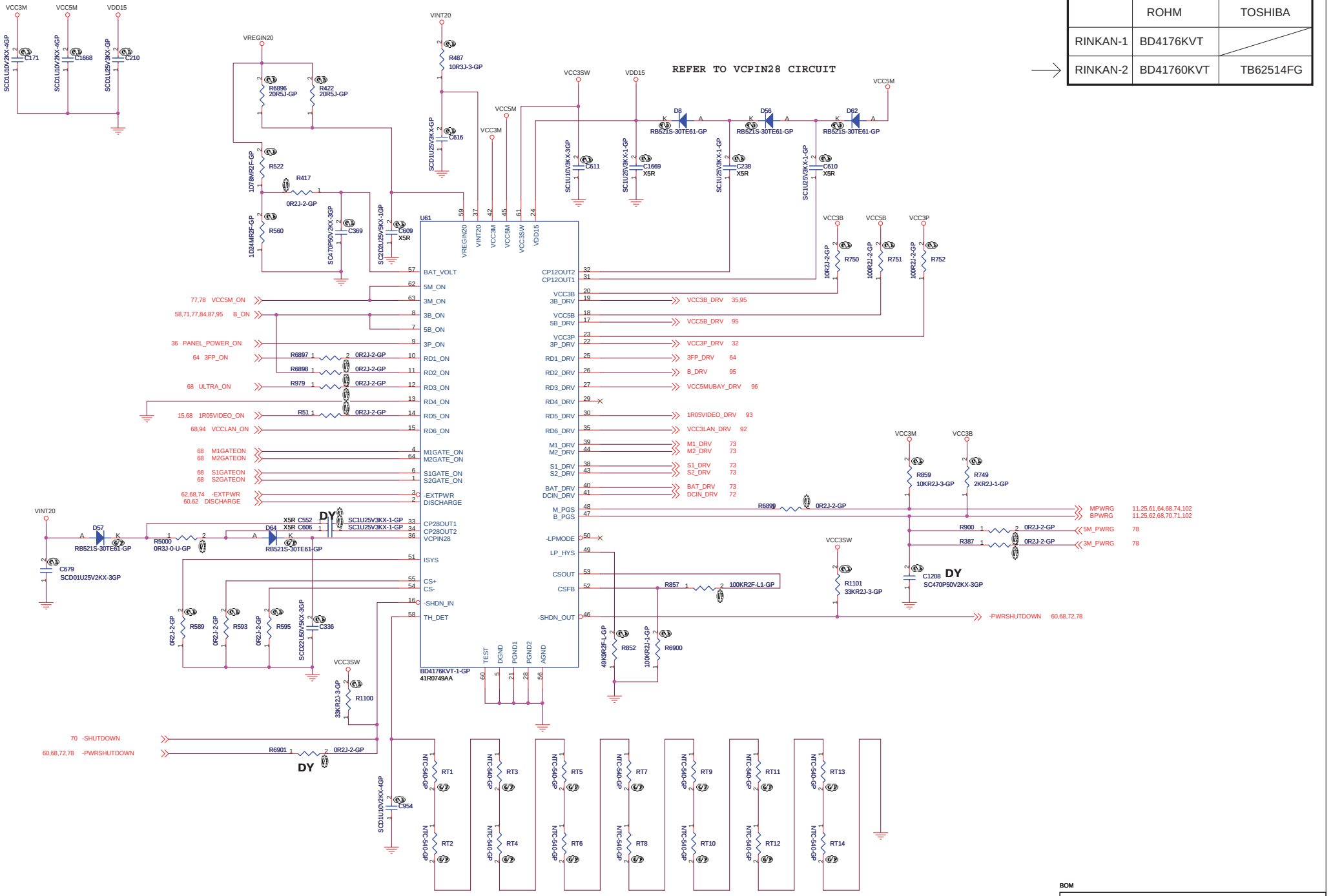

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RINKAN-1	BD4176KVT	
RINKAN-2	BD41760KVT	TB62514FG

BOM

Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title: DC-DC RINKAN-2

Size: Custom | Document Number: SHINAI-3 SWG | Rev: SB

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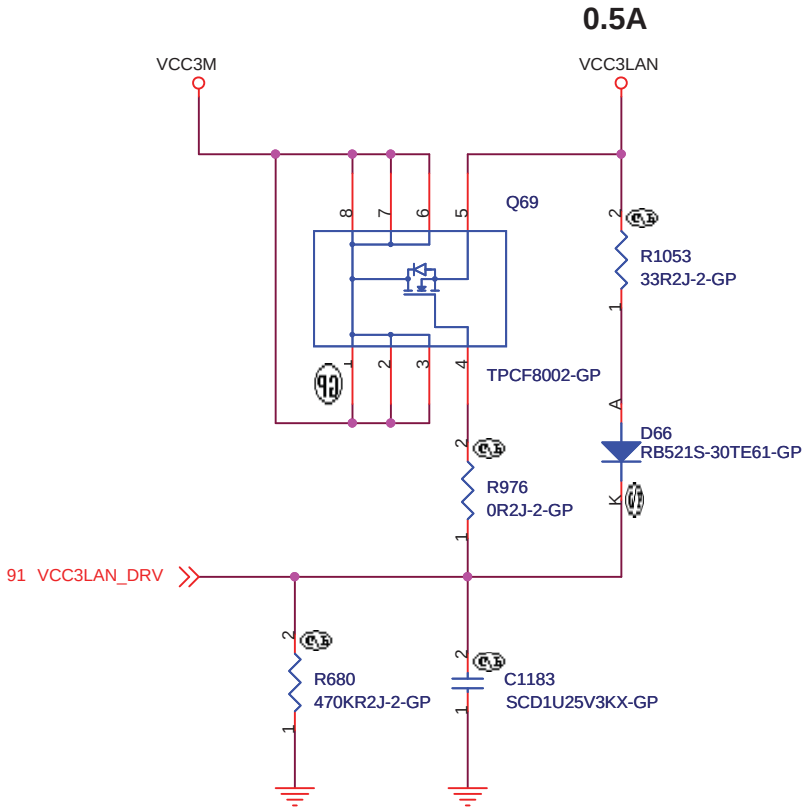

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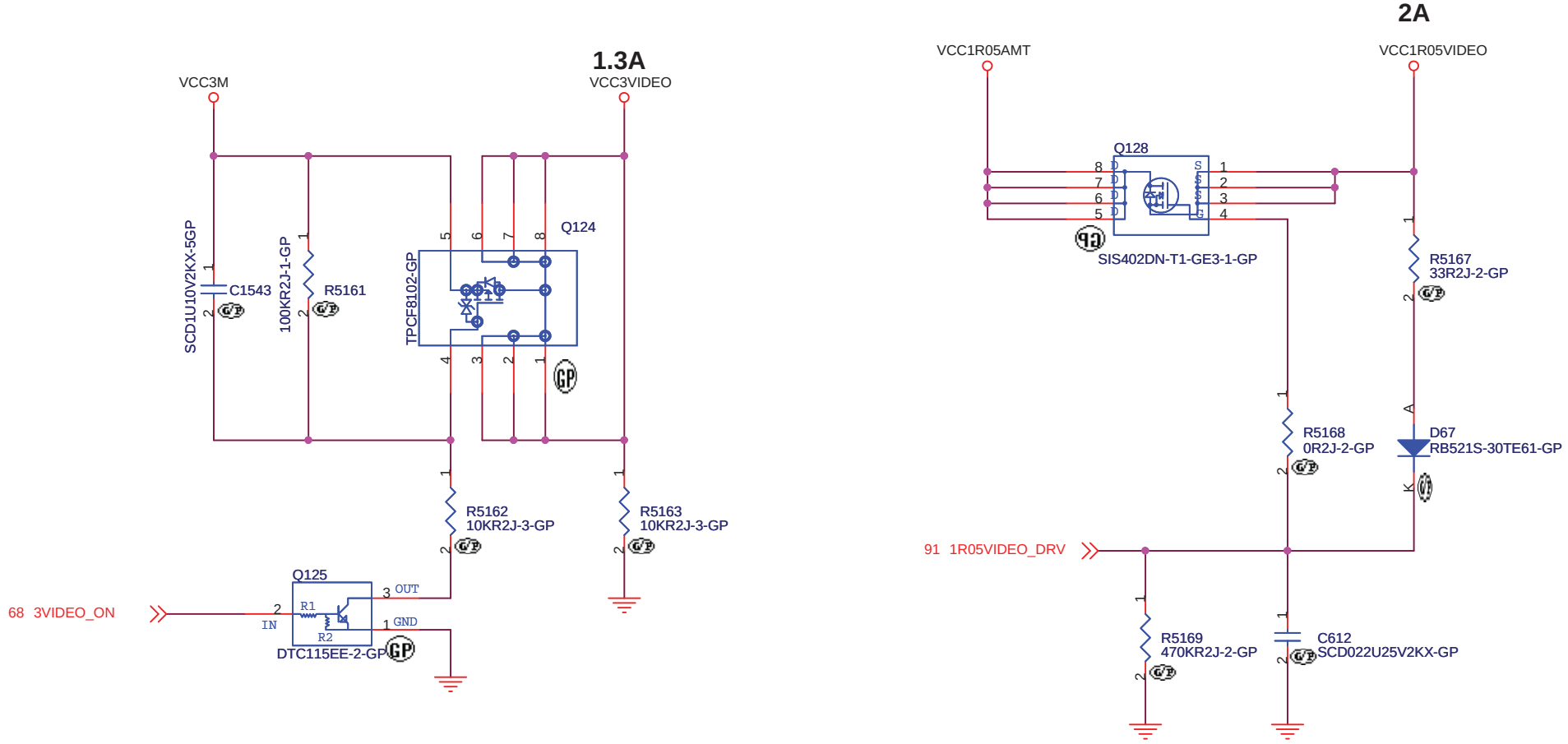
緯創資通 Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title: **LOAD SW LAN**

Size	Document Number	Rev
A4	SHINAI-3 SWG	SB

Date: Tuesday, September 07, 2010 Sheet 92 of 102



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緯創資通 **Wistron Corporation**
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

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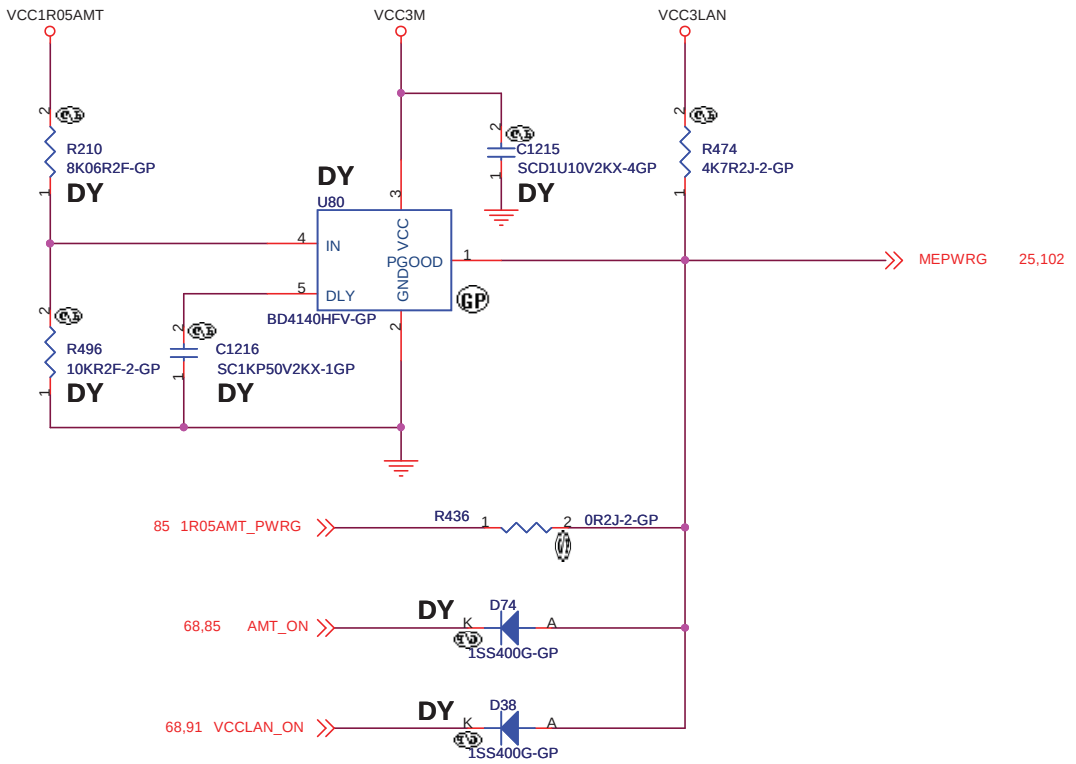
Size	Document Number	Rev
A4	SHINAI-3 SWG	SB

Date: Tuesday, September 07, 2010 Sheet 93 of 102



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緯創資通 **Wistron Corporation**
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

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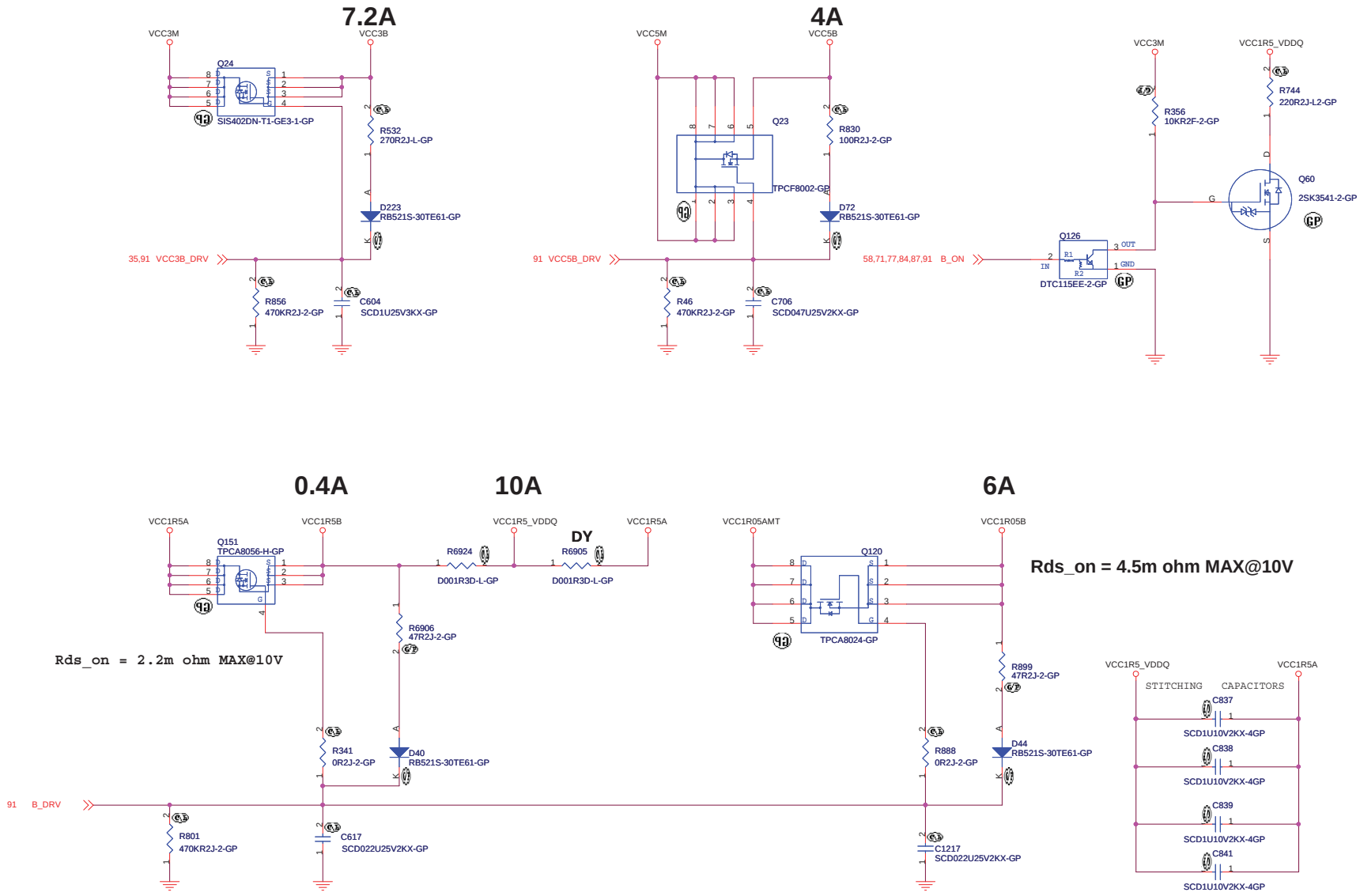
Size	Document Number	Rev
Custom	SHINAI-3 SWG	SB

Date: Tuesday, September 07, 2010 Sheet 94 of 102



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緯創資通 **Wistron Corporation**
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

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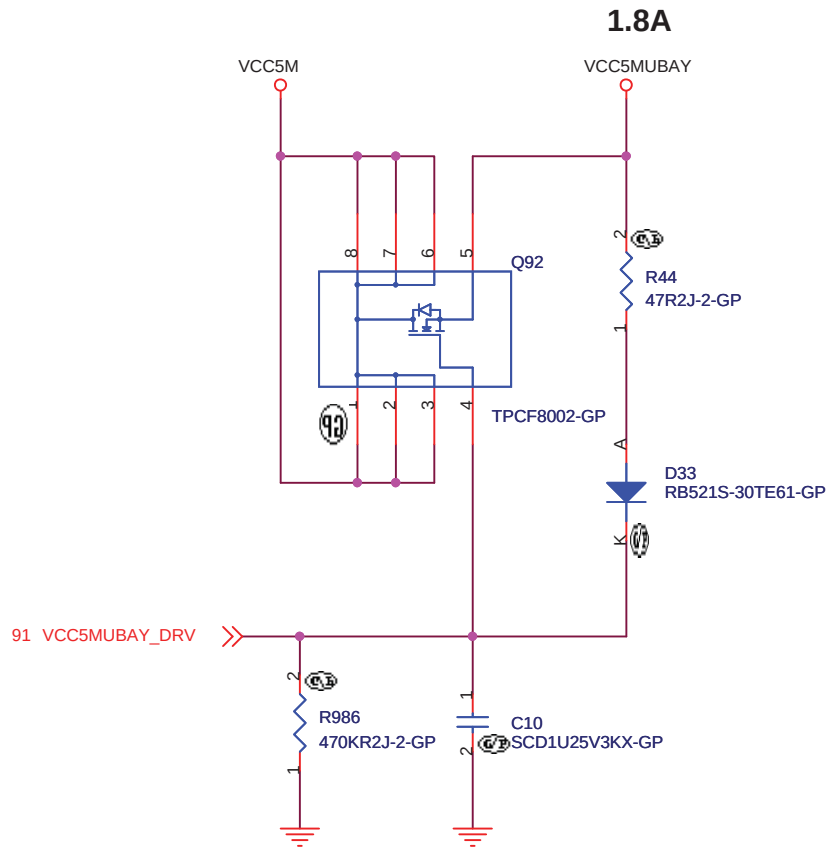
Date: Tuesday, September 07, 2010	Sheet 95 of 102	



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緯創資通 **Wistron Corporation**
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

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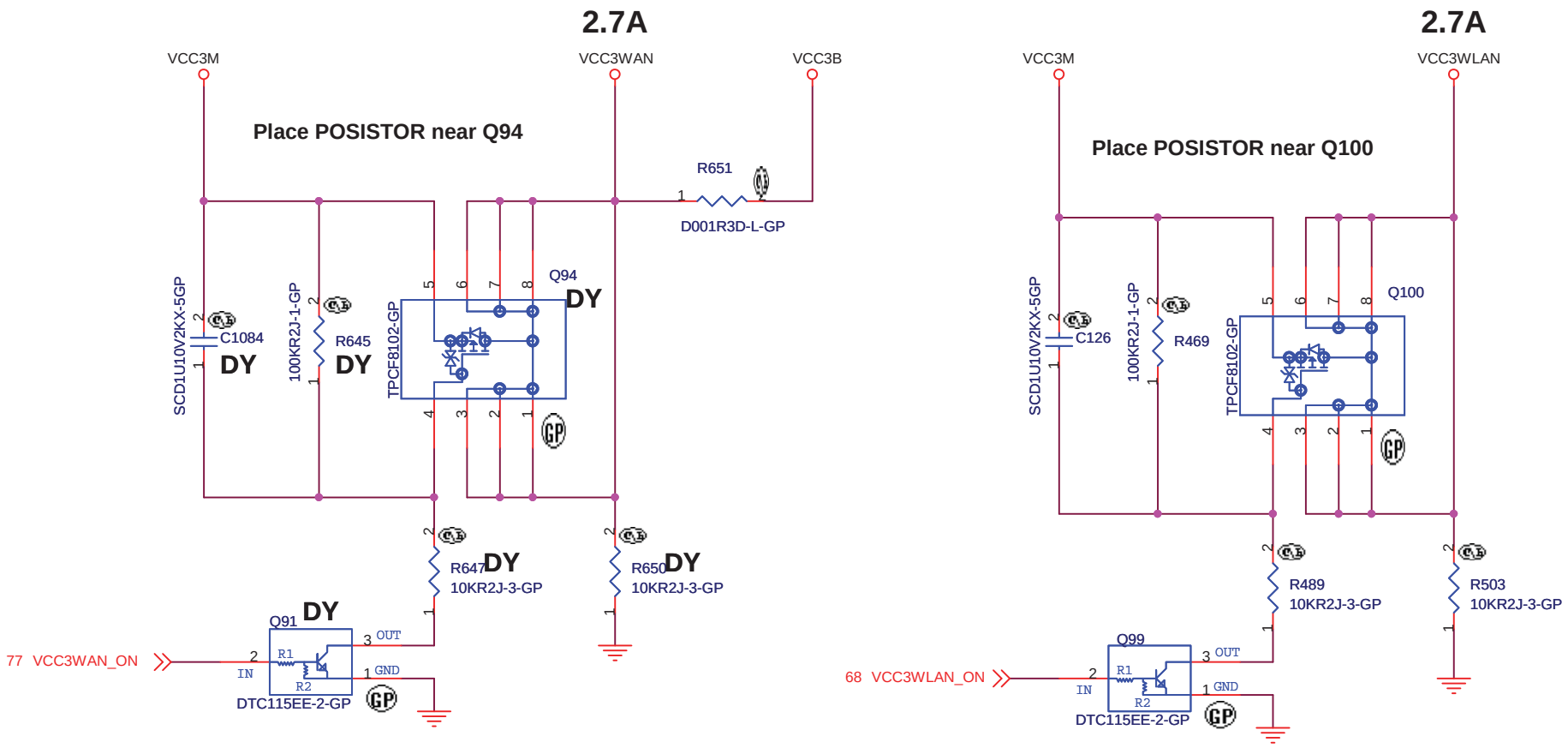
Size	Document Number	Rev
A4	SHINAI-3 SWG	SB

Date: Tuesday, September 07, 2010 Sheet 96 of 102



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Q94	ASM	NO_ASM
R650	ASM	NO_ASM
R645	ASM	NO_ASM
C1084	ASM	NO_ASM
R647	ASM	NO_ASM
Q91	ASM	NO_ASM

LOGIC

BOM

緯創資通 Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title: LOAD SW WAN & WLAN

Size: A4 | Document Number: SHINAI-3 SWG | Rev: SB

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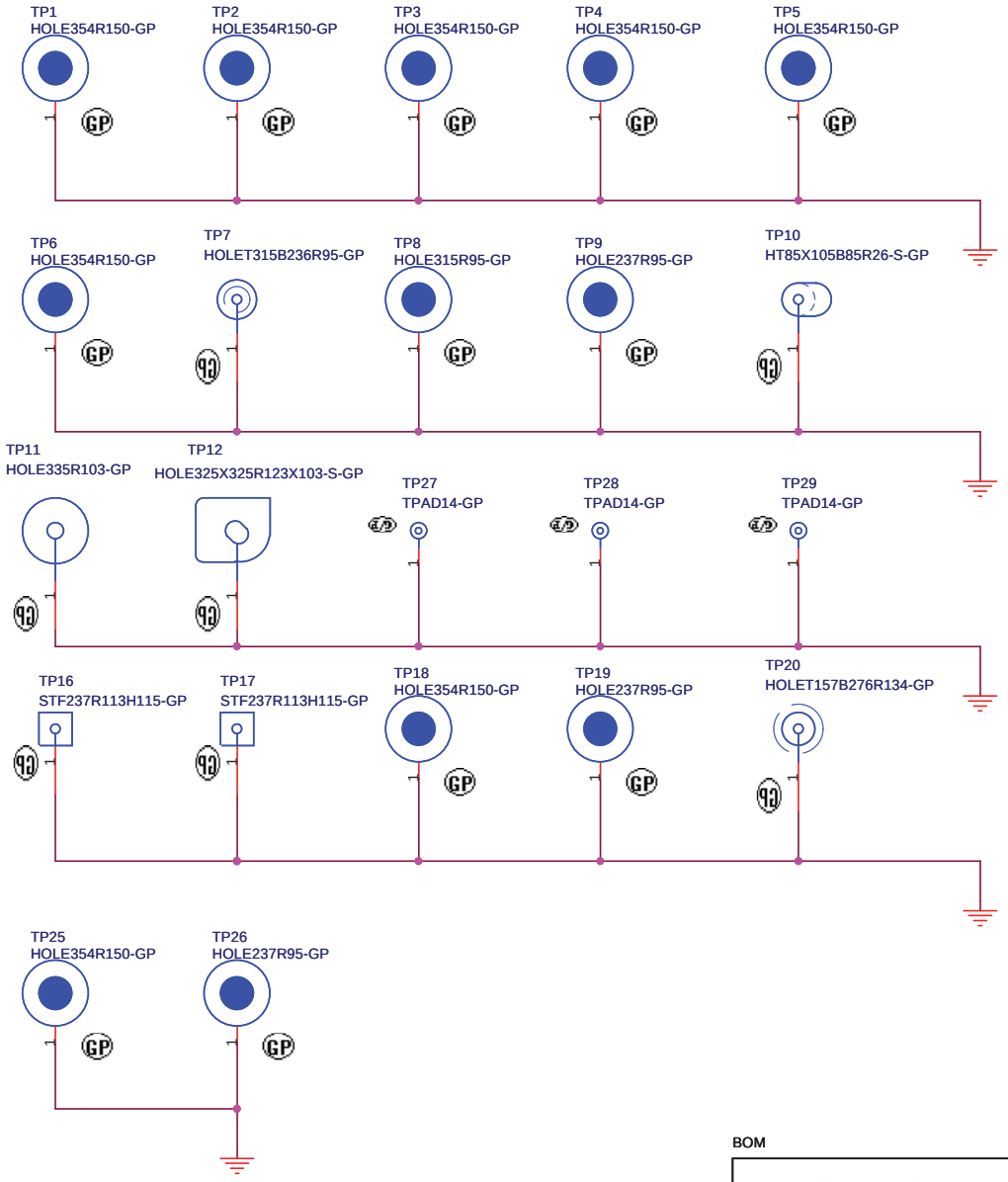
PTH FOR SCREW HOLE



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緯創資通 **Wistron Corporation**
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title: ***PTH FOR SCREW HOLES***

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<Variant Name>

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21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

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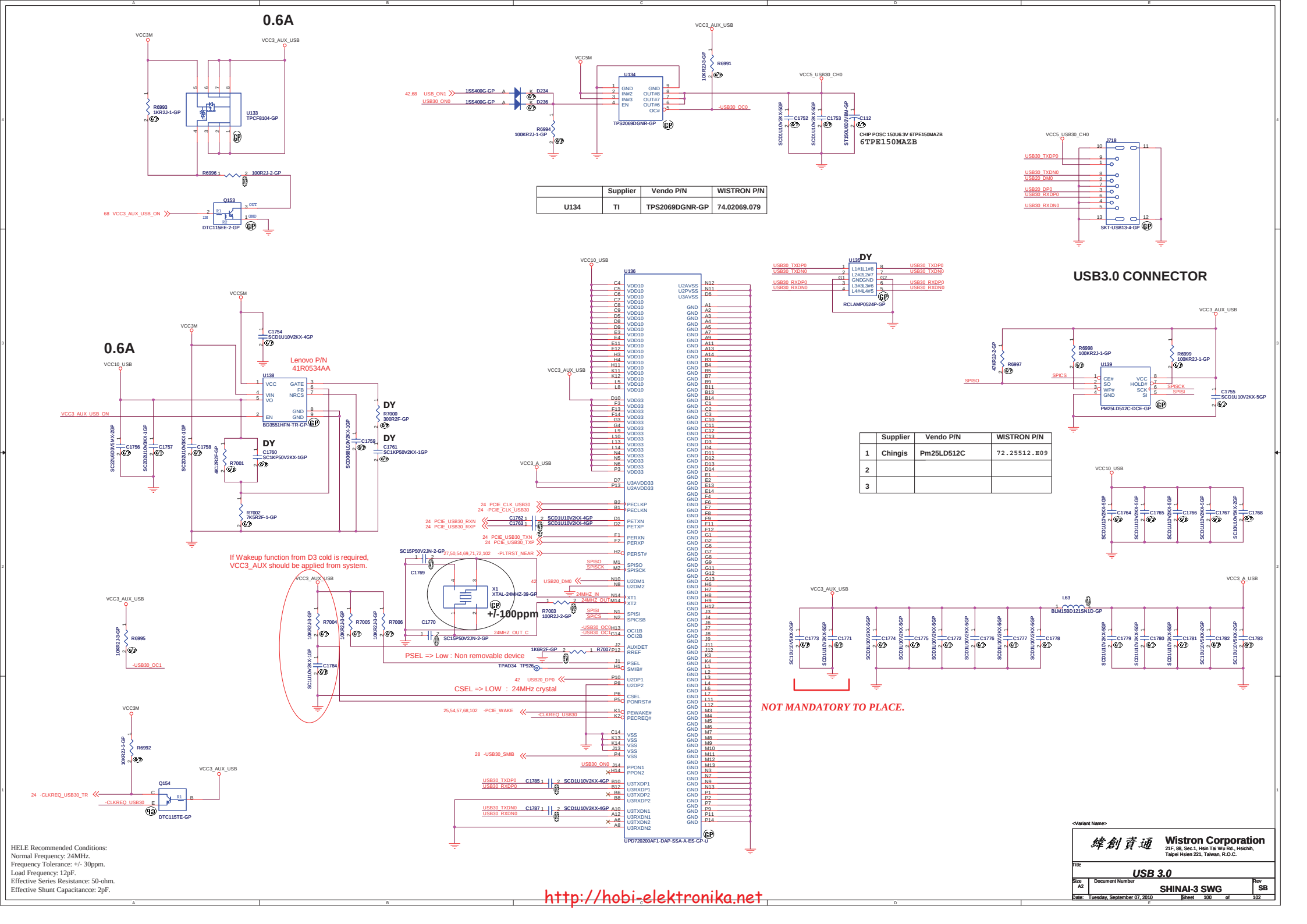
Size: B	Document Number: **SHINAI-3 SWG**	Rev: SB
Date: Tuesday, September 07, 2010	Sheet 99 of 102	



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U134	TI	TPS2069DGNR-GP	74.02069.079

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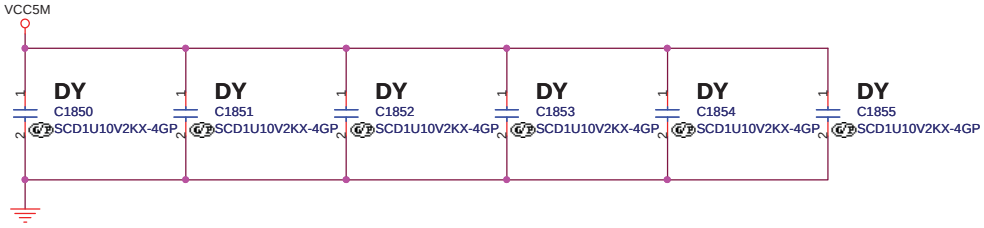

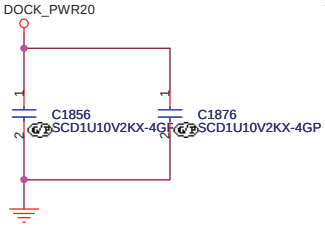

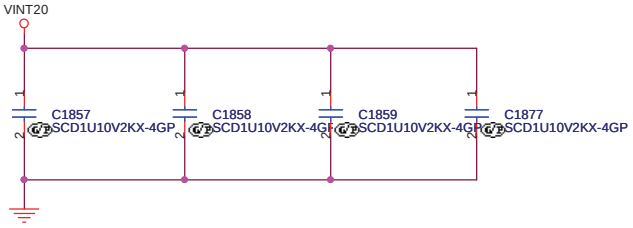

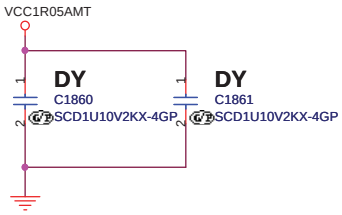

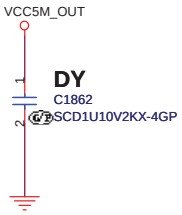

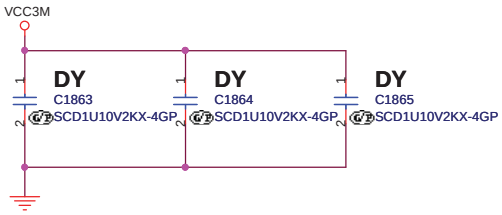

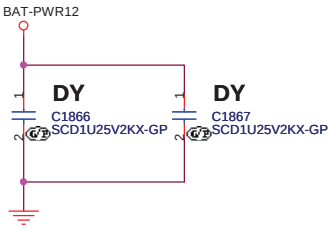

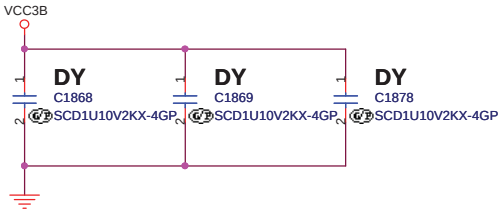

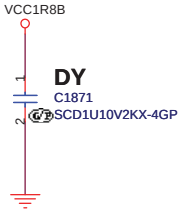

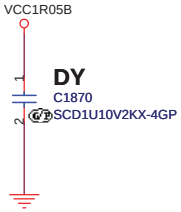

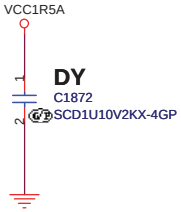

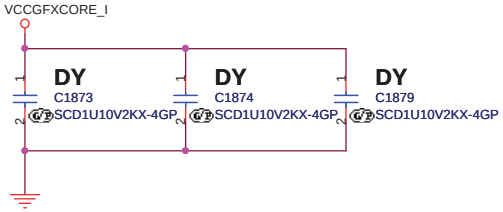

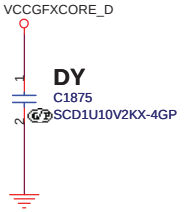

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緯創資通 **Wistron Corporation**
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title: ***EMI DECOUPLING***

Size	Document Number	Rev
B	SHINAI-3 SWG	SB

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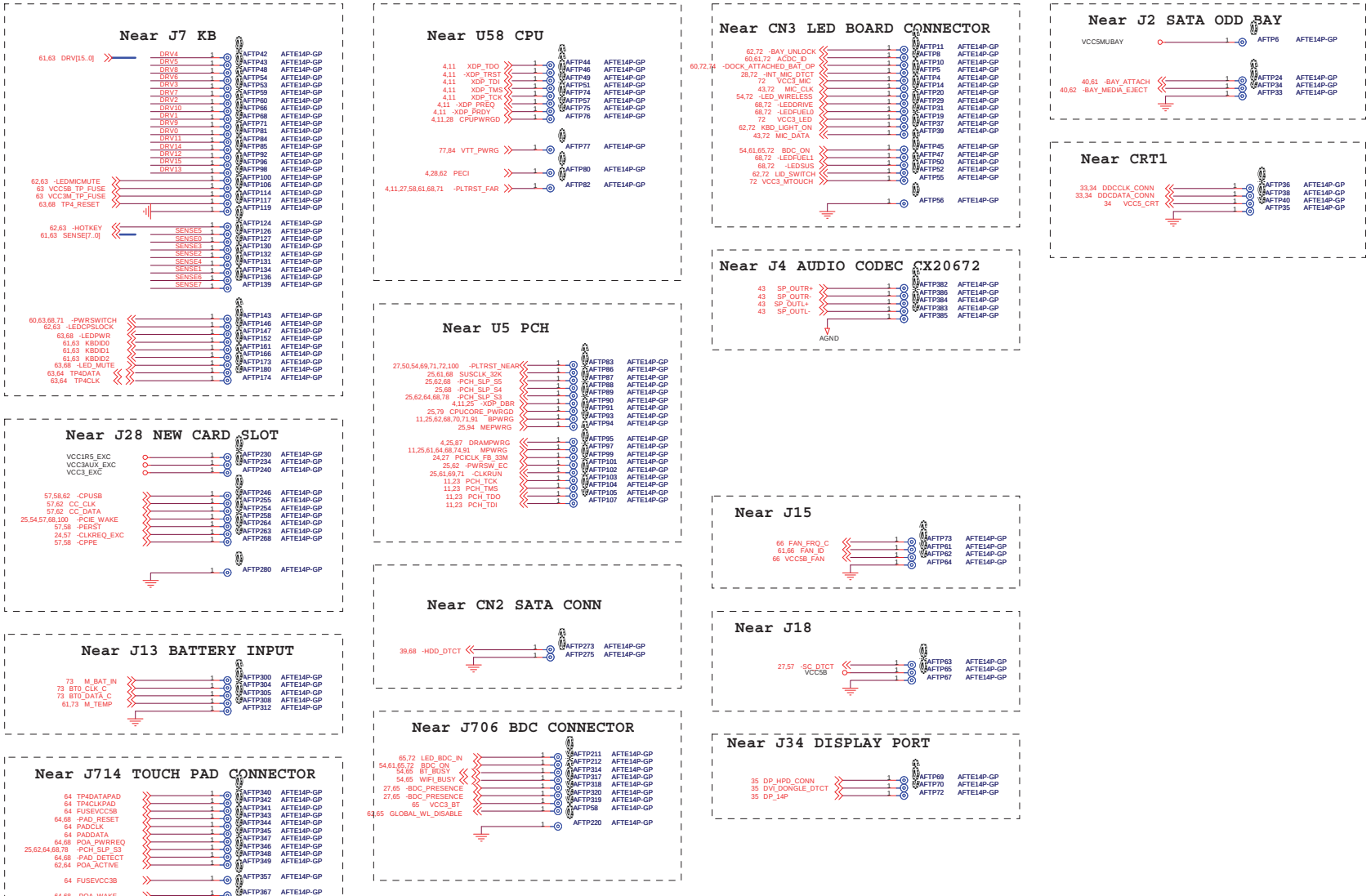

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Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title: TEST PAD

Size: A2 | Document Number: SHINAI-3 SWG | Rev: SB

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