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QIQY2 M/B Schematics Document

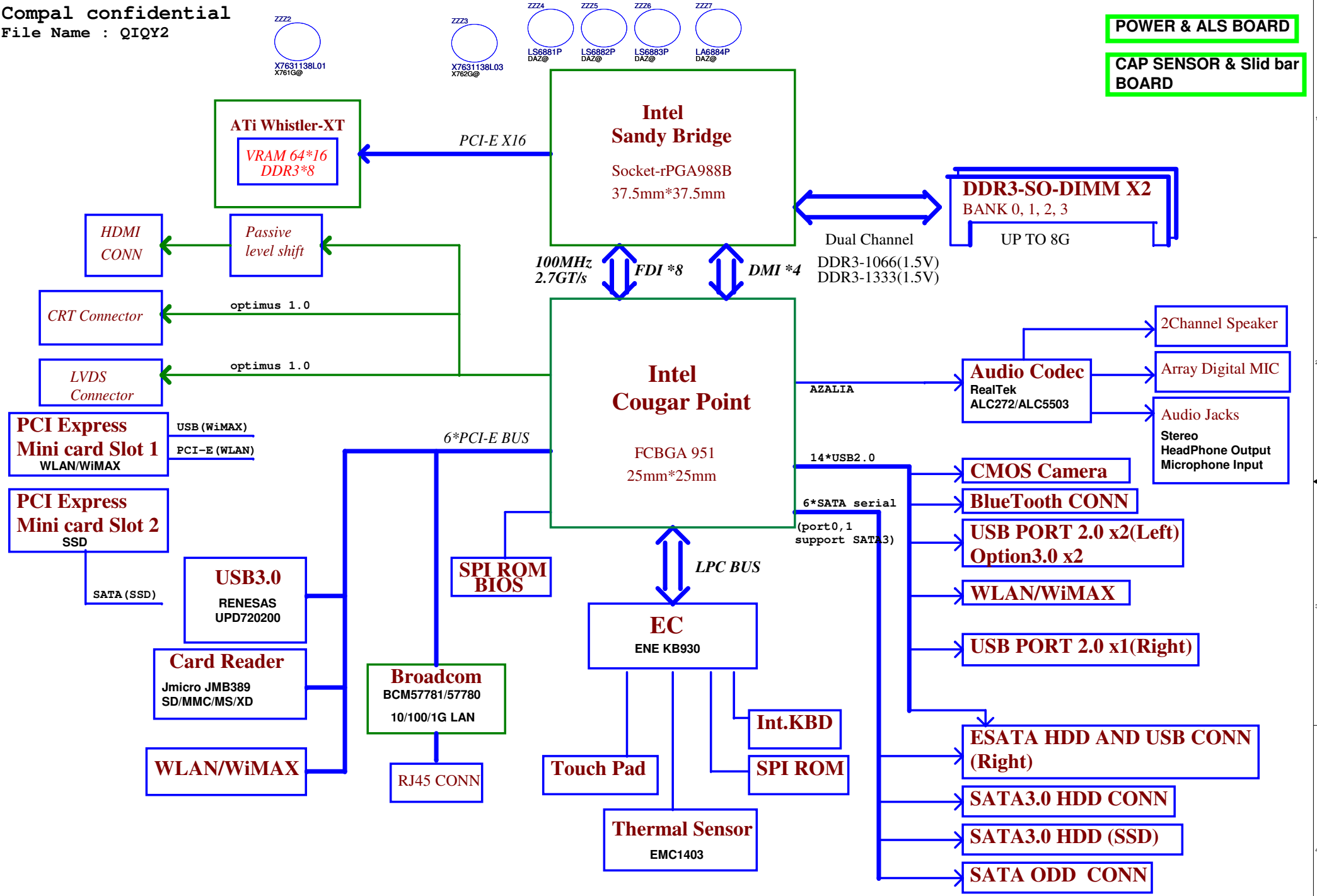
Intel Sandy Bridge Processor with DDRIII + Cougar Point PCH
ATi Whistler XT+DDR3

2011-05-11

REV: 1.0

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POWER & ALS BOARD
CAP SENSOR & Slid bar BOARD



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Voltage Rails

power plane	+B	+5VALW	+1.5V	+3VS +1.5VS +VCCP +CPU_CORE +VGA_CORE +GFX_CORE +1.8VS +0.75VS +1.05VS
State				
S0	○	○	○	○
S3	○	○	○	✗
S5 S4/AC	○	○	✗	✗
S5 S4/ Battery only	○	✗	✗	✗
S5 S4/AC & Battery don't exist	✗	✗	✗	✗

SMBUS Control Table

	SOURCE	VGA	BATT	KB930	SODIMM	WLAN WWAN	Thermal Sensor	PCH
SMB_EC_CK1 SMB_EC_DA1	KB930 +3VALW	X	V +3VALW	X	X	X	X	X
SMB_EC_CK2 SMB_EC_DA2	KB930 +3VALW	X	X	X	X	X	X	V +3VS
SMBCLK SMBDATA	PCH +3VALW	X	X	X	V +3VS	V +3VS	X	X
SML0CLK SML0DATA	PCH +3VALW	X	X	X	X	X	X	X
SML1CLK SML1DATA	PCH +3VALW	V +3VS	X	V +3VS	X	X	V +3VS	X

EC SM Bus1 address

EC SM Bus2 address

Device	Address	Device	Address
Smart Battery	0001 011X b	Thermal Sensor EMC1403-2	1001_101xb

PCH SM Bus address

Device	Address
DDR DIMM0	1001 000Xb
DDR DIMM2	1001 010Xb

STATE	SIGNAL	SLP_S1#	SLP_S3#	SLP_S4#	SLP_S5#	+VALW	+V	+VS	Clock
Full ON		HIGH	HIGH	HIGH	HIGH	ON	ON	ON	ON
S1 (Power On Suspend)		LOW	HIGH	HIGH	HIGH	ON	ON	ON	LOW
S3 (Suspend to RAM)		LOW	LOW	HIGH	HIGH	ON	ON	OFF	OFF
S4 (Suspend to Disk)		LOW	LOW	LOW	HIGH	ON	OFF	OFF	OFF
S5 (Soft OFF)		LOW	LOW	LOW	LOW	ON	OFF	OFF	OFF

Board ID / SKU ID Table for AD channel

Vcc	3.3V +/- 5%			
Ra/Rc/Re	100K +/- 5%			
Board ID	Rb / Rd / Rf	VAD_BID min	VAD_BID typ	VAD_BID max
0	0	0 V	0 V	0 V
1	8.2K +/- 5%	0.216 V	0.250 V	0.289 V
2	18K +/- 5%	0.436 V	0.503 V	0.538 V
3	33K +/- 5%	0.712 V	0.819 V	0.875 V
4	56K +/- 5%	1.036 V	1.185 V	1.264 V
5	100K +/- 5%	1.453 V	1.650 V	1.759 V
6	200K +/- 5%	1.935 V	2.200 V	2.341 V
7	NC	2.500 V	3.300 V	3.300 V

BOARD ID Table

Board ID	PCB Revision
0	0.1
1	
2	
3	
4	
5	
6	
7	

BOM Structure Table

BTO Item	BOM Structure
UMA	
UMA Only	UMA_ONLY@
Optimus	OPTI@
VRAM	X76@
HDMI	HDMI@
Blue Tooth	BT@
USB3.0	USB30@
ESATA	ESATA@
USB Charger	USB_CHG@
No USB Charger	NO_CHG@
Unpop	@
Codec ALC272	272@
Codec ALC5503	5503@
LAN 57781	57781@
LAN 57780	57780@
Ventura Feature	
Camera	CMOS@

VRAM BOM Config

X761G@: X7625738L01	Samsung 1GB
Sub: X7625738L02	Hynix 1GB
X762G@: X7625738L03	Samsung 2GB
Sub: X7625738L04	Hynix 2GB

USB Port Table

USB 2.0	USB 1.1	Port	3 External USB Port
EHCI1	UHCI0	0	USB/Cable (Right Side)
		1	USB Port (Right Side COMBO)
	UHCI1	2	USB/B (Left Side)
		3	USB/B (Left Side)
	UHCI2	4	
		5	Camera
EHCI2	UHCI3	6	
		7	
	UHCI4	8	
		9	Mini Card(WLAN)
	UHCI5	10	
		11	
		12	
UHCI6	13	Blue Tooth	

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Power-Up/Down Sequence

- All the ASIC supplies must fully reach their respective nominal voltages within 20 ms of the start of the ramp-up sequence, though a shorter ramp-up duration is preferred.
- VDDR3 should ramp-up before or simultaneously with VDDC.
- For LVDS, DPx_VDD10 should ramp-up before DPx_VDD18 and the PCIe Reference clock should begin before DPx_VDD18. For power-down, DPx_VDD18 should ramp-down before DPx_VDD10.
- The external pull-ups on the DDC/AUX signals (if applicable) should ramp-up before or after both VDDC and VDD_CT have ramped up.
- VDDC and VDD_CT should not ramp-up simultaneously. (e.g., VDDC should reach 90% before VDD_CT starts to ramp-up (or vice versa).)

VDDR3(3.3VGS)

PCIE_VDDC(1.0V)

VDDR1(1.5VGS)

VDDC/VDDCI(1.12V)

VDD_CT(1.8V)

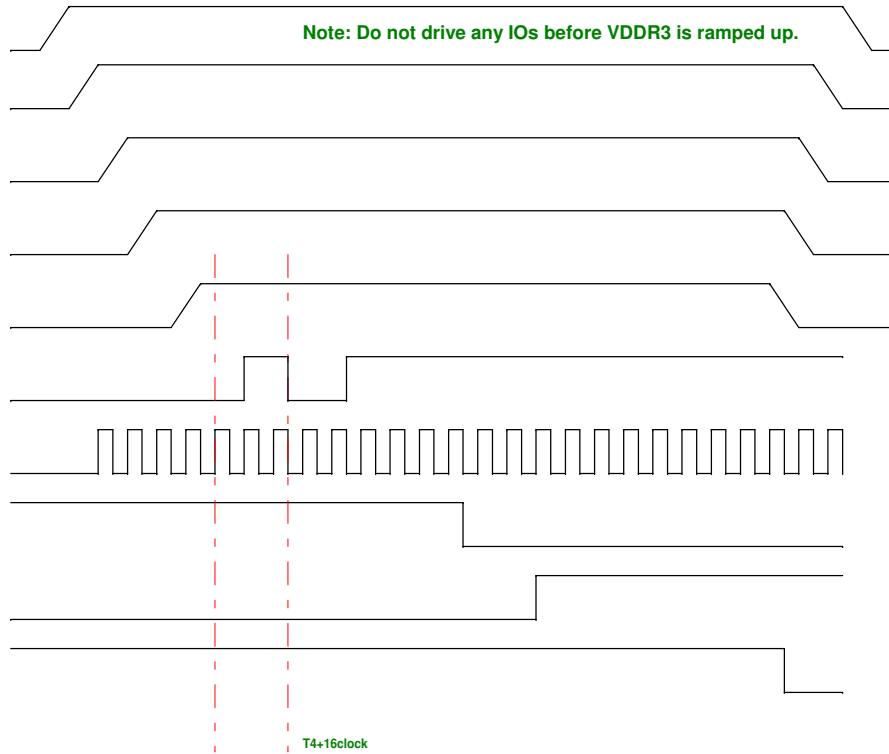
PERSTb

REFCLK

Straps Reset

Straps Valid

Global ASIC Reset



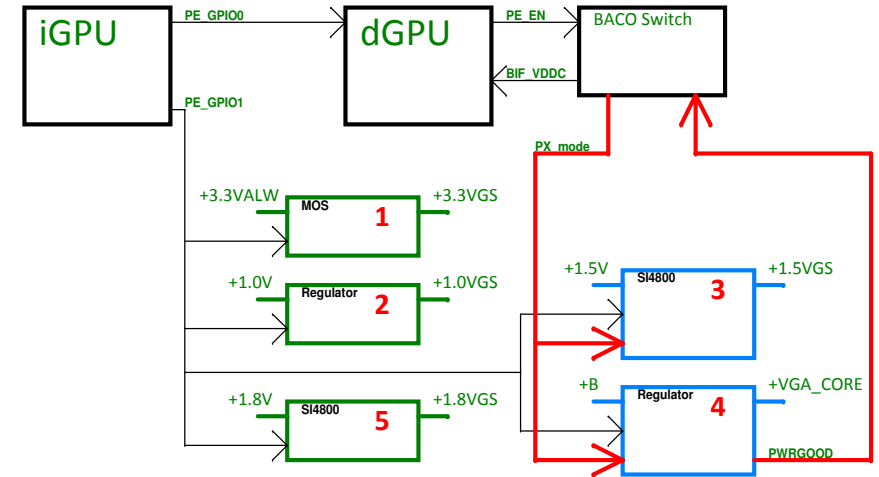
Without BACO option :

PE_GPIO0 : Low -> Reset dGPU ; High ->Normal operation
 PE_GPIO1 : Low -> dGPU Power OFF ; High -> dGPU Power ON

BACO option :

PE_GPIO0 : High ->Normal operation (dGPU is not reseton BACO mode)
 PE_GPIO1 : Low -> dGPU Power OFF ; High -> dGPU Power ON (always High)

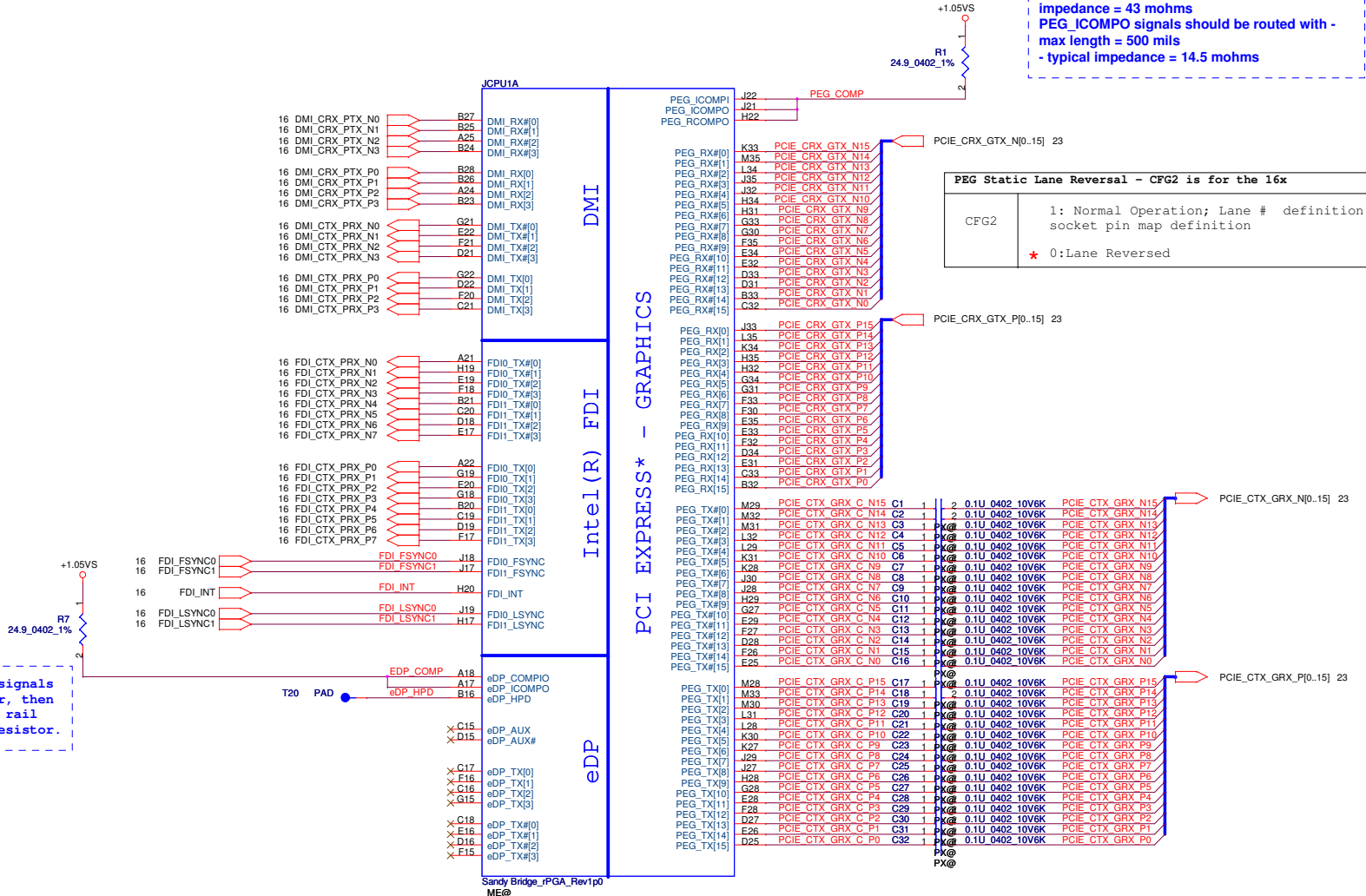
dGPU Power Pins	Voltage	PX 3.0	BACO Mode	Max current
PCIE_PVDD, PCIE_VDDR, TSVDD, VDDR4, VDD_CT, DPE_PVDD, DP[F:E]_VDD18, DP[D:A]_PVDD, DP[D:A]_VDD18, AVDD, VDD1DI, A2VDDQ, VDD2DI, DPLL_PVDD, MPV18, and SPV18	1.8V	OFF	ON	1679mA
DP[F:E]_VDD10, DP[D:A]_VDD10, DPLL_VDDC, and SPV10	1.0V	OFF	ON	575mA
PCIE_VDDC	1.0V	OFF	ON	2A
VDDR3 , and A2VDD	3.3V	OFF	ON	190mA
BIF_VDDC (current consumption = 55mA@1.0V, in BACO mode)	Same as VDDC	OFF	ON Same as PCIE_VDDC	70mA
VDDR1	1.5V	OFF	OFF	2.8A
VDDC/VDDCI	1.12V	OFF	OFF	12.9A



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PEG_ICOMPI and RCOMPO signals should be shorted and routed with - max length = 500 mils - typical impedance = 43 mohms
 PEG_ICOMPO signals should be routed with - max length = 500 mils - typical impedance = 14.5 mohms

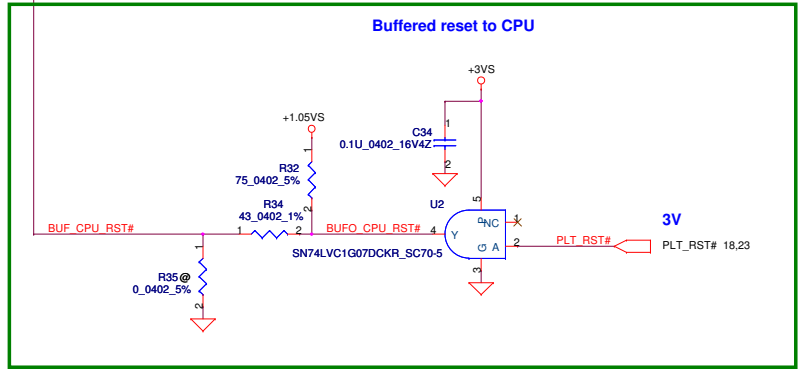
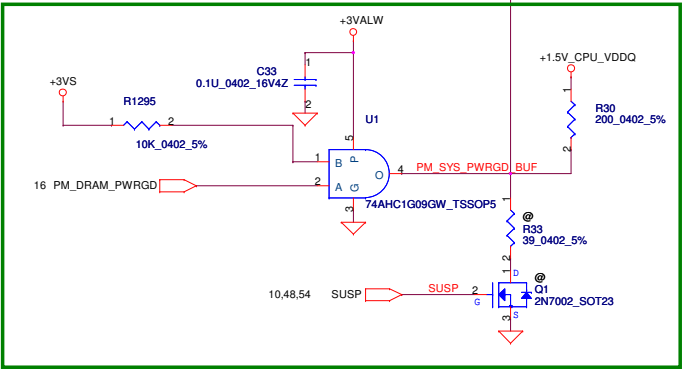
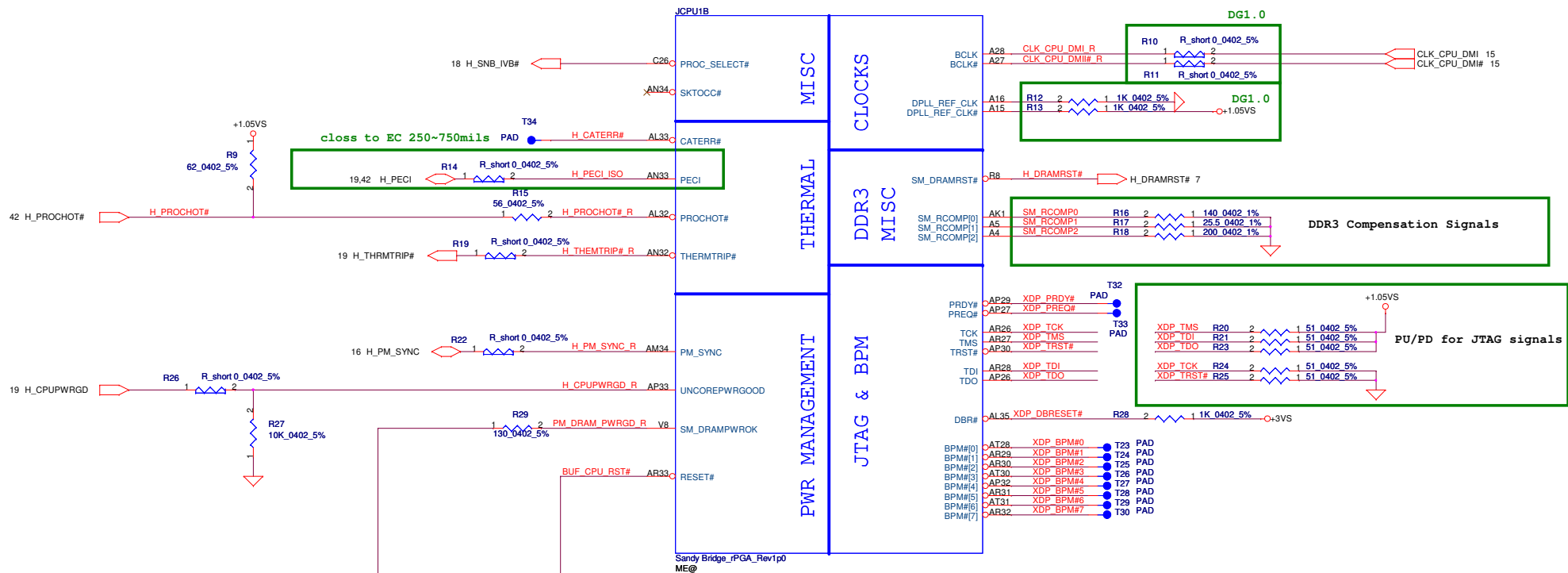
eDP_COMPIO and ICOMPO signals should be tied together, then connected to the VCCIO rail via a single 24.9ohm resistor.



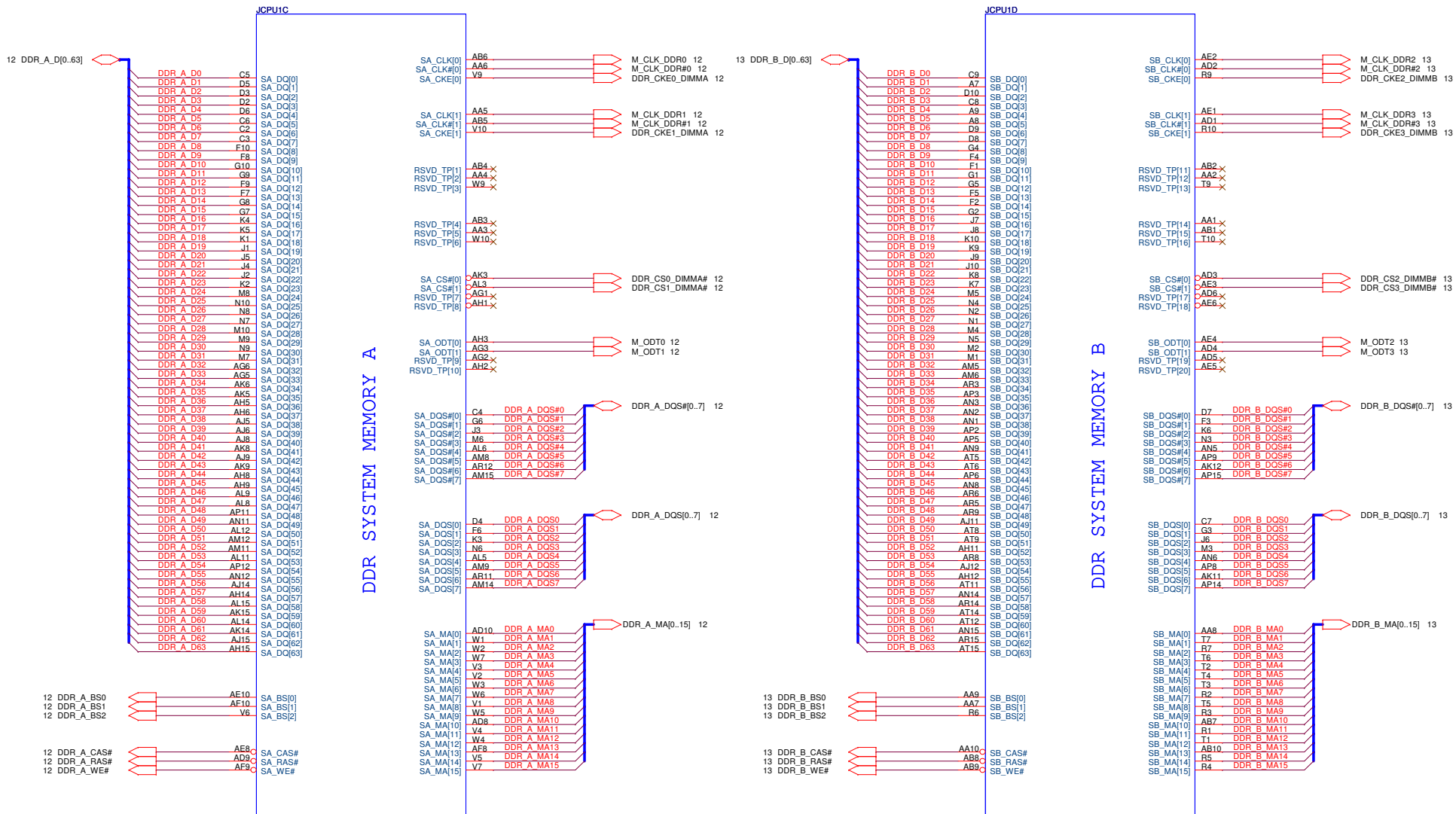
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Compal Electronics, Inc.			
PROCESSOR(I7) DMI, FDI, PEG			
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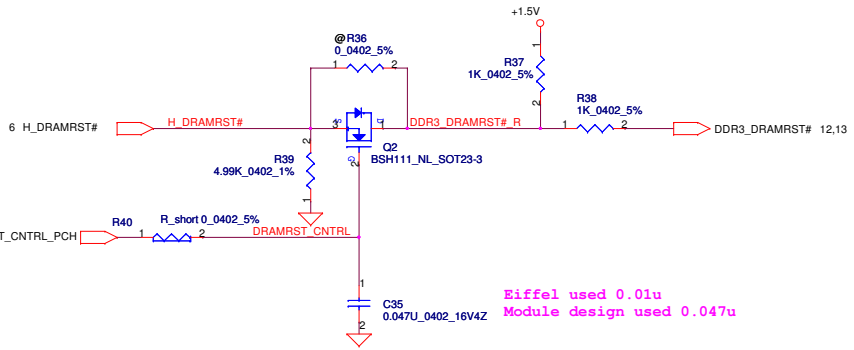
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DDR SYSTEM MEMORY A

DDR SYSTEM MEMORY B

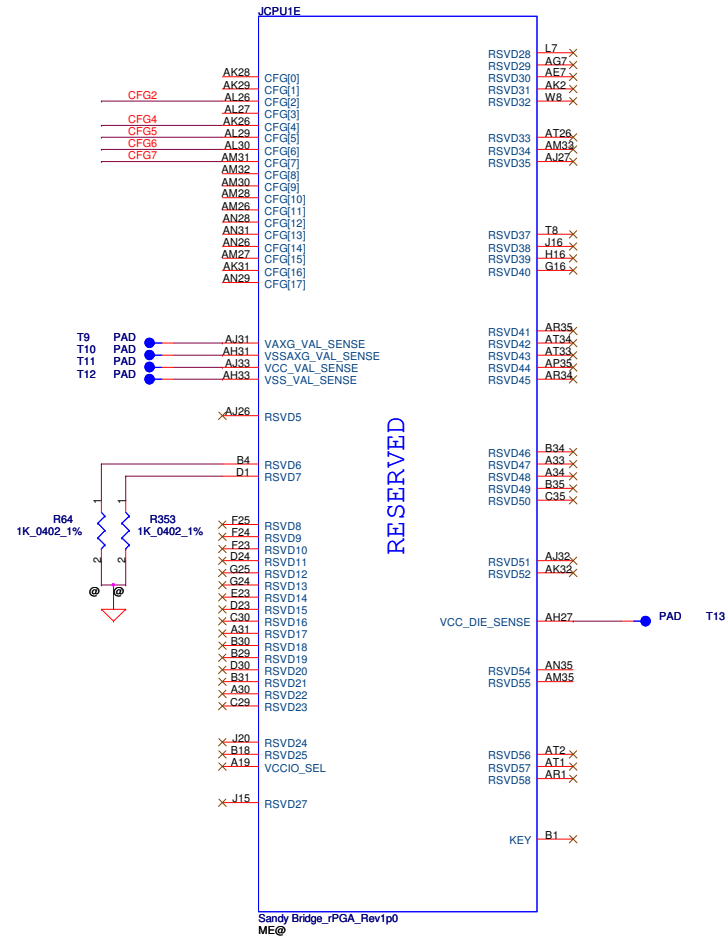
Sandy Bridge_PGA_Rev1p0 ME@



Eiffel used 0.01u
Module design used 0.047u

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2010/11/30		2011/08		PROCESSOR(3/7) DDRIII	
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CFG Straps for Processor

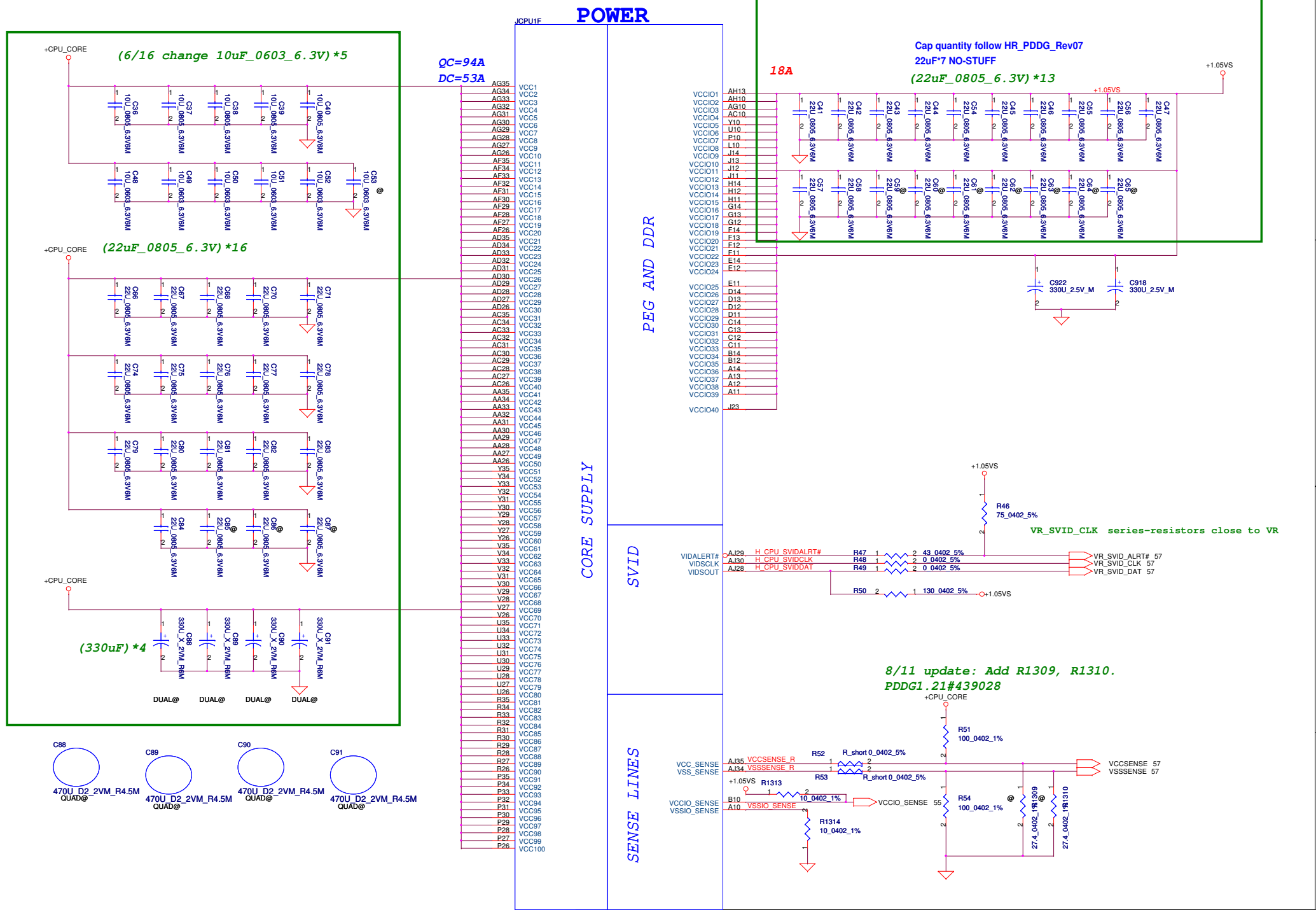


PEG Static Lane Reversal - CFG2 is for the 16x	
CFG2	1: Normal Operation; Lane # definition matches socket pin map definition * 0: Lane Reversed

Display Port Presence Strap	
CFG4	* 1 : Disabled; No Physical Display Port attached to Embedded Display Port 0 : Enabled; An external Display Port device is connected to the Embedded Display Port

PCIe Port Bifurcation Straps	
CFG[6:5]	* 11: (Default) x16 - Device 1 functions 1 and 2 disabled 10: x8, x8 - Device 1 function 1 enabled ; function 2 disabled 01: Reserved - (Device 1 function 1 disabled ; function 2 enabled) 00: x8,x4,x4 - Device 1 functions 1 and 2 enabled

PEG DEFER TRAINING	
CFG7	1: (Default) PEG Train immediately following xxRESETB de assertion 0: PEG Wait for BIOS for training

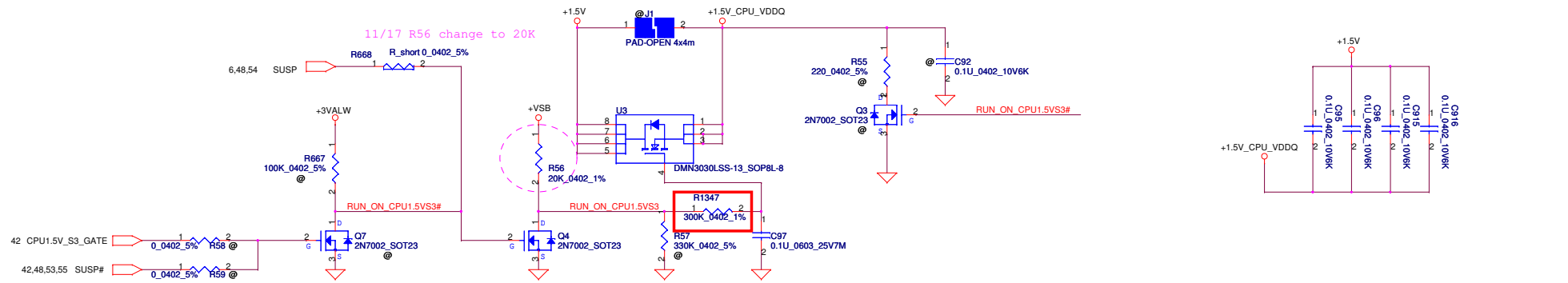


Sandy Bridge_rPGA Rev1.0
ME@

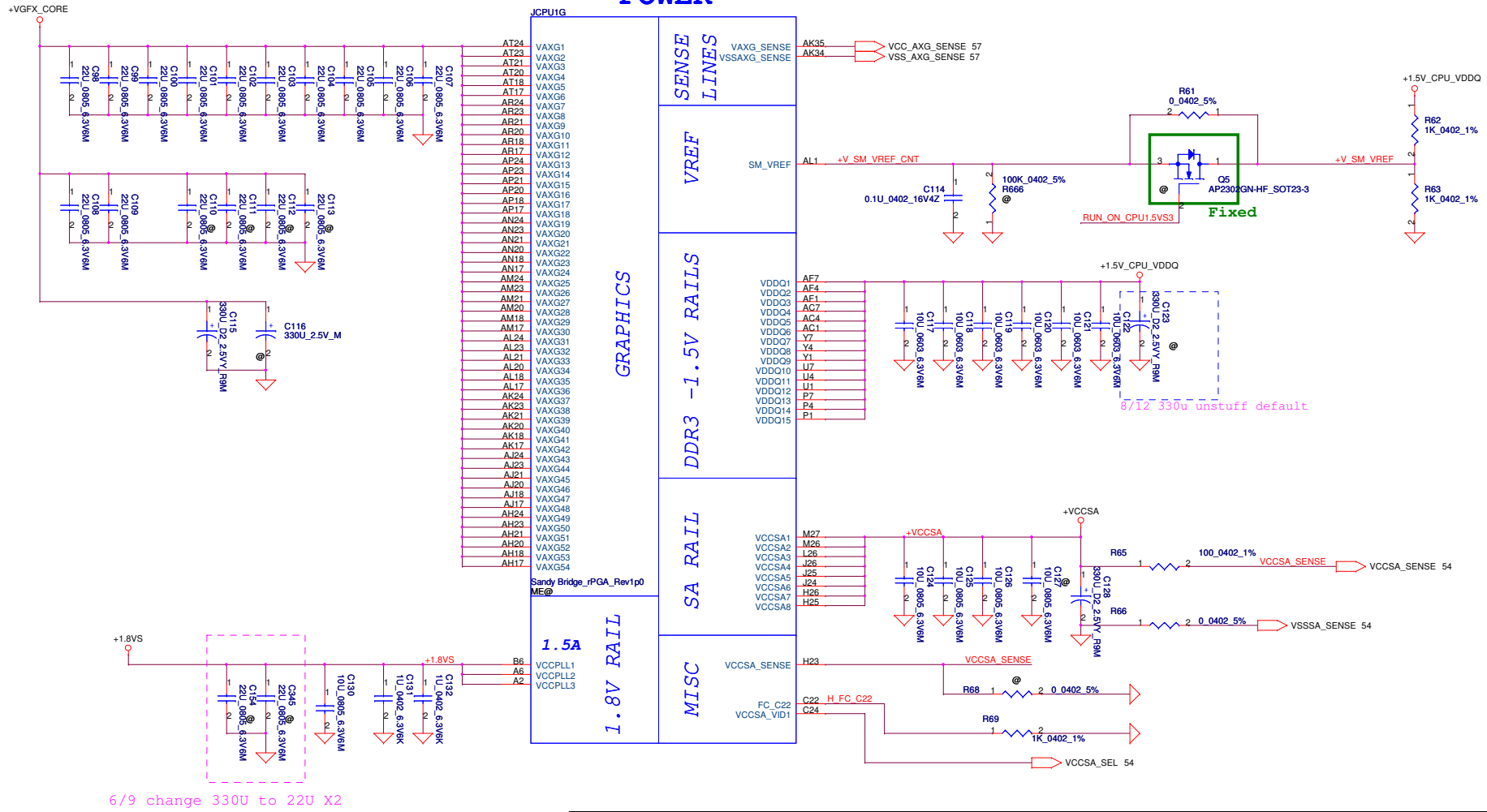
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PROCESSOR(S/7) PWR,BYPASS			
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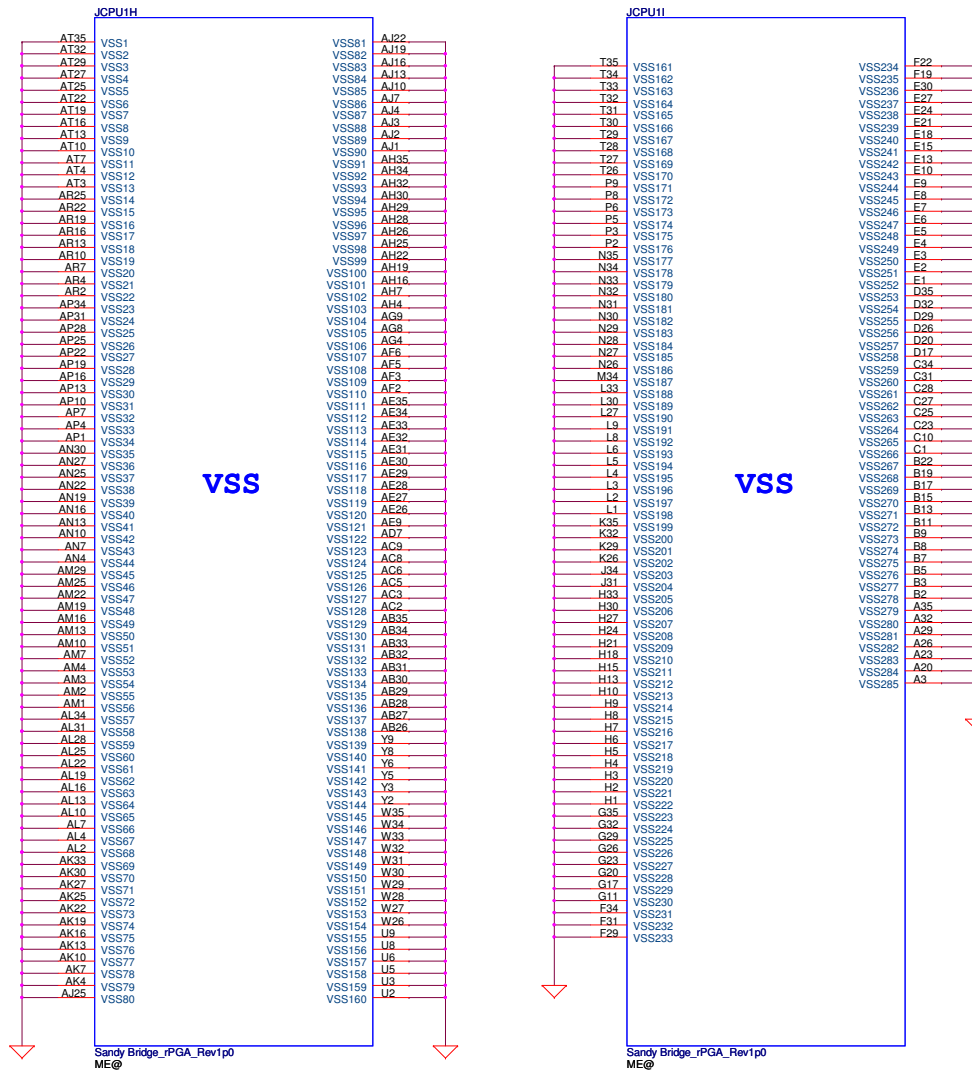


POWER

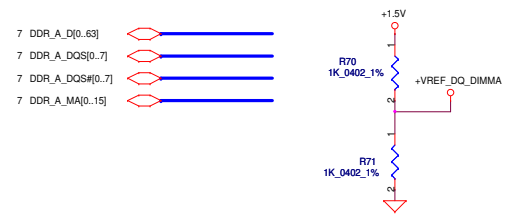
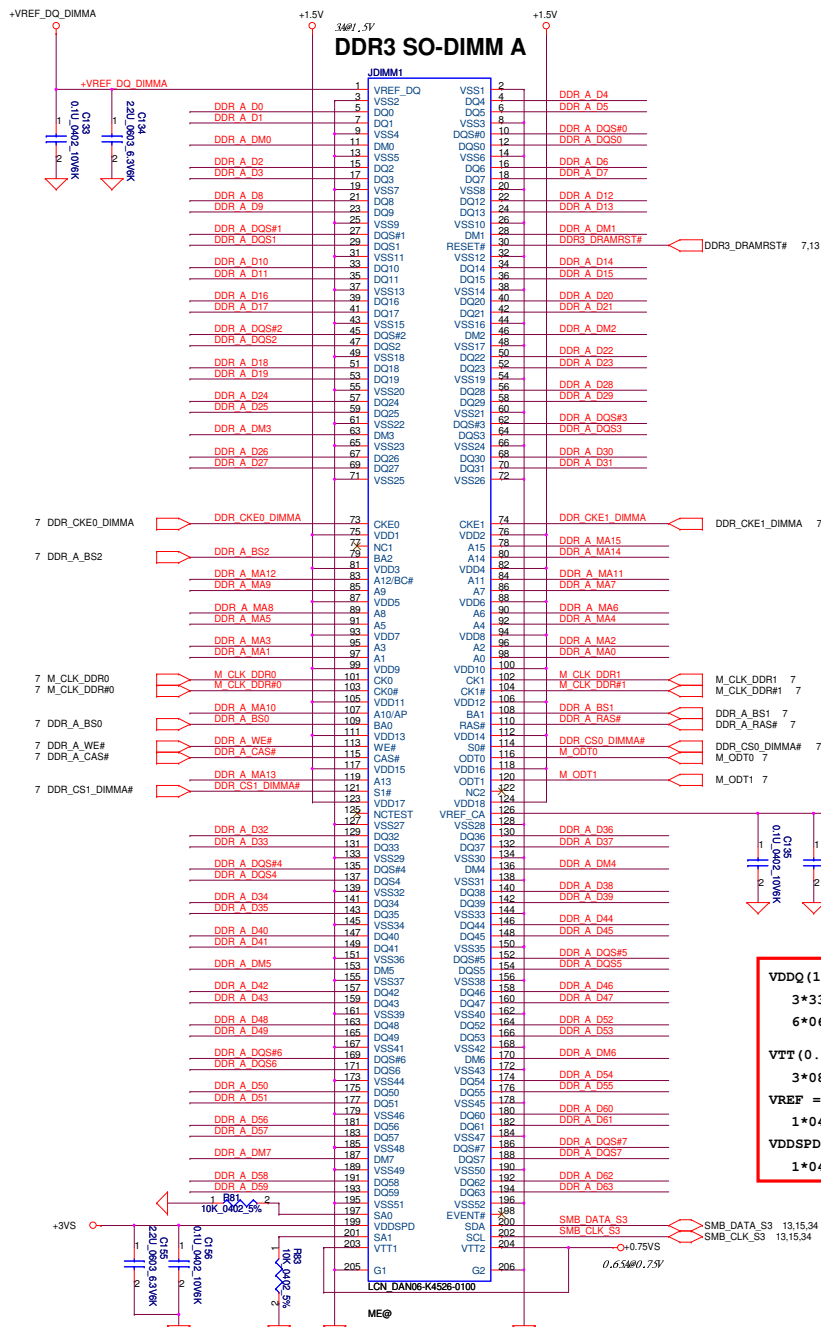


6/9 change 330U to 22U X2

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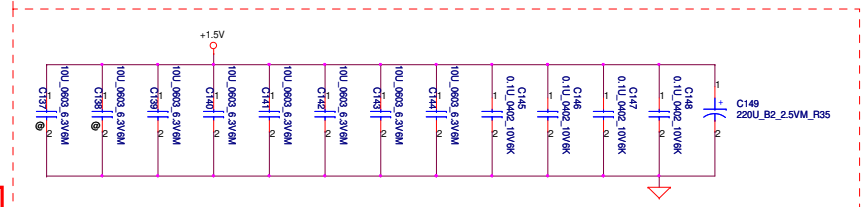


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Layout Note:
Place near DIMM

$(10\mu F_{0603_6.3V}) * 8$
 $(0.1\mu F_{402_10V}) * 4$



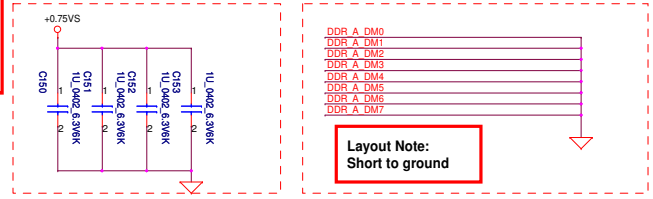
Layout Note:
Place near DIMM

VDDQ (1.5V) =
 $3 * 330\mu f / 12m\ ohm$ (TOTAL FOR 2 SO-DIMMs)
 $6 * 0603\ 10\mu f$ (PER CONNECTOR)

VTT (0.75V) =
 $3 * 0805\ 10\mu f + 4 * 0402\ 1\mu f$

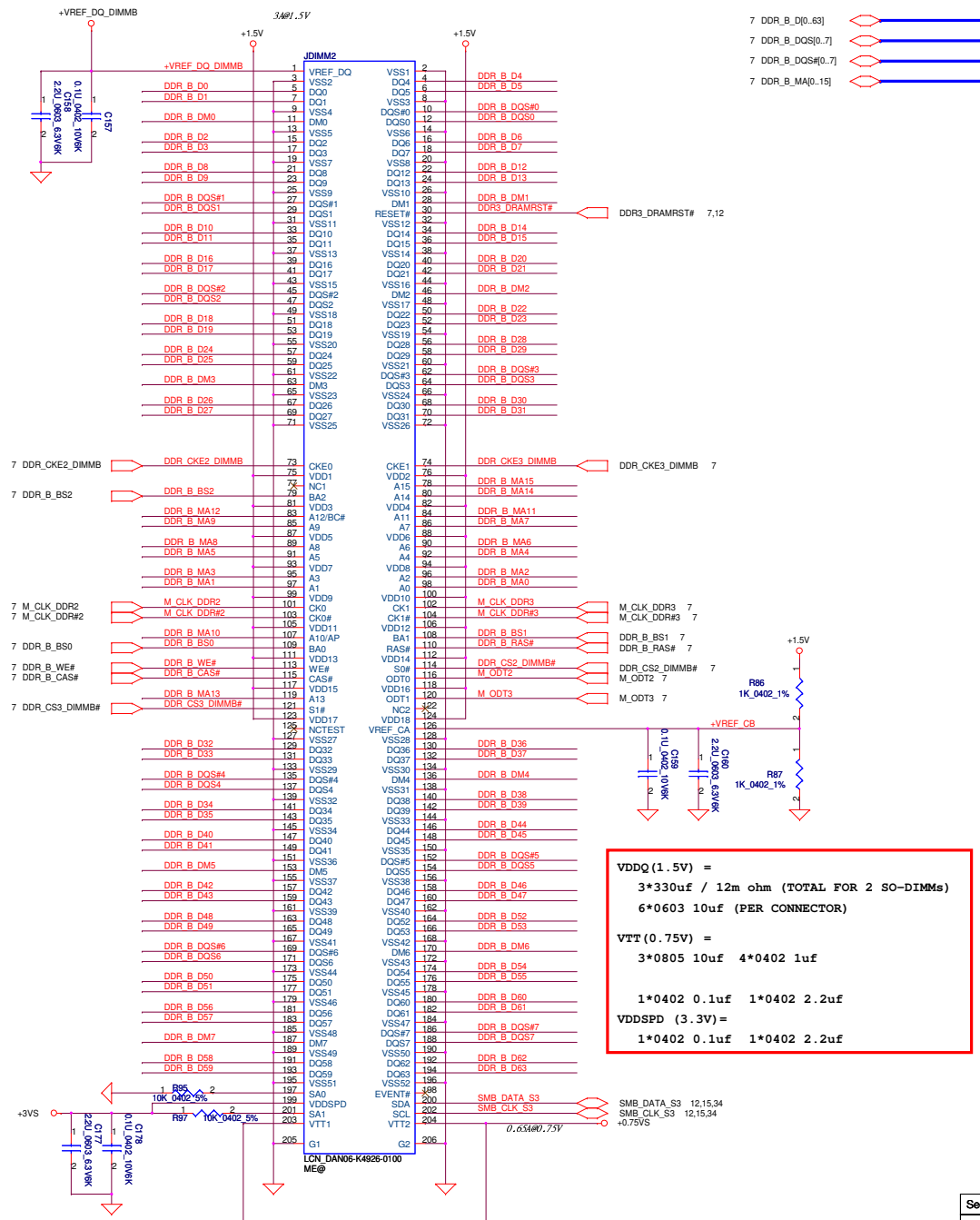
VREF =
 $1 * 0402\ 0.1\mu f + 1 * 0402\ 2.2\mu f$

VDDSPD (3.3V) =
 $1 * 0402\ 0.1\mu f + 1 * 0402\ 2.2\mu f$

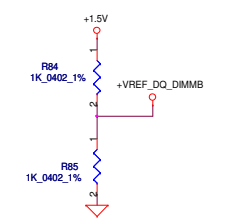


Layout Note:
Short to ground

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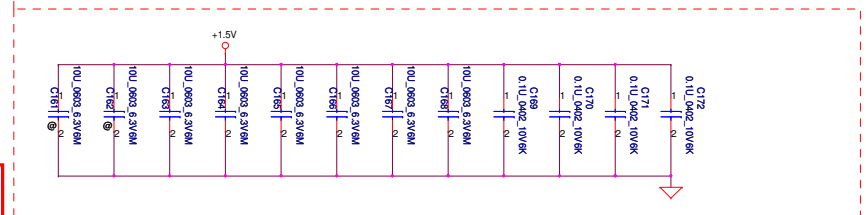
- 7 DDR_B_D[0..63]
- 7 DDR_B_DQS[0..7]
- 7 DDR_B_DQS# [0..7]
- 7 DDR_B_MA[0..15]



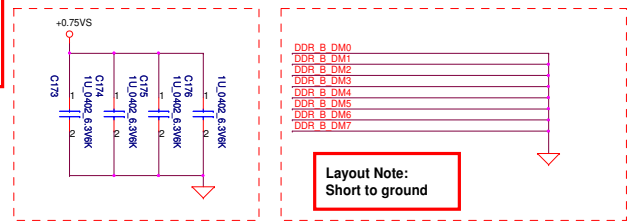
For Arranale only +VREF_DQ_DIMMB supply from a external 1.5V voltage divide circuit.
07/17/2009

Layout Note:
Place near DIMM

$(10\mu F_{0603_6.3V}) * 8$
 $(0.1\mu F_{402_10V}) * 4$



Layout Note:
Place near DIMM



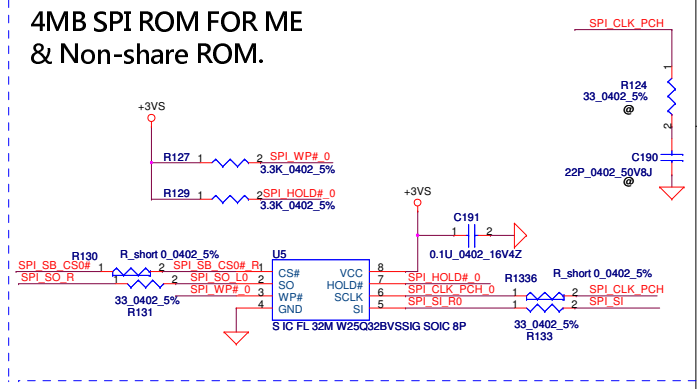
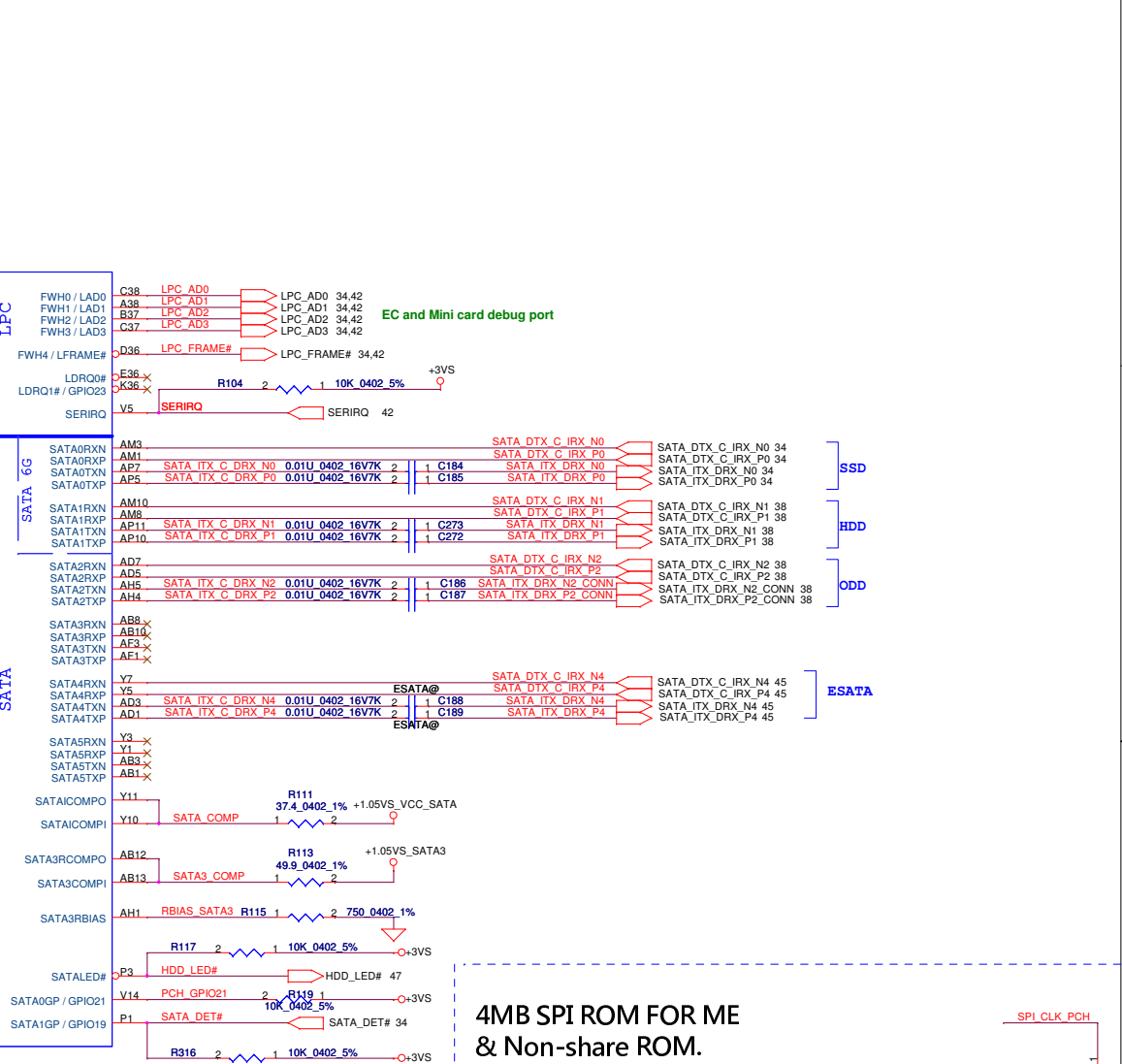
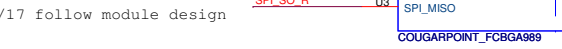
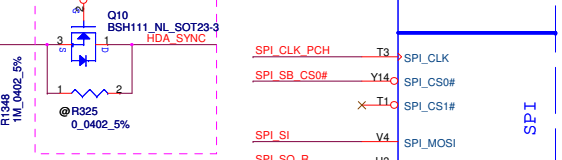
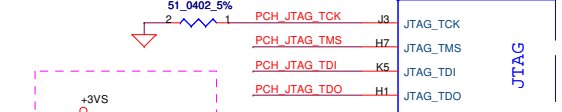
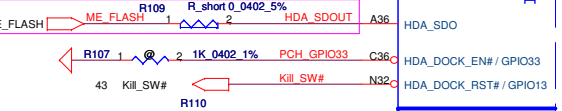
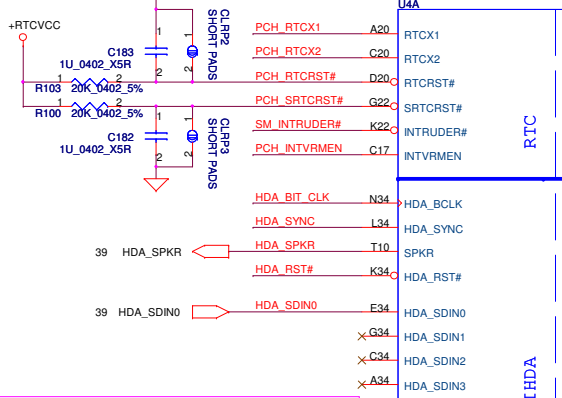
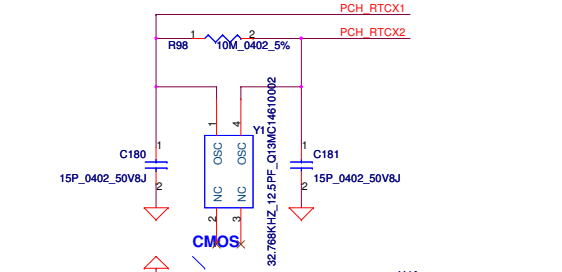
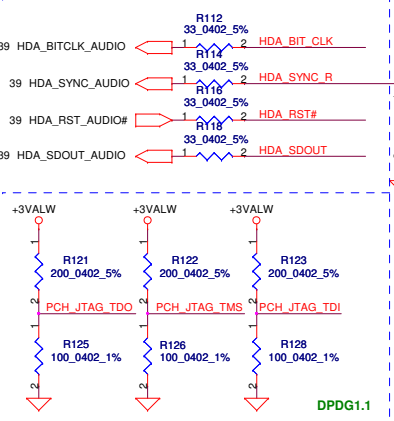
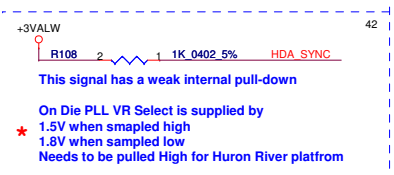
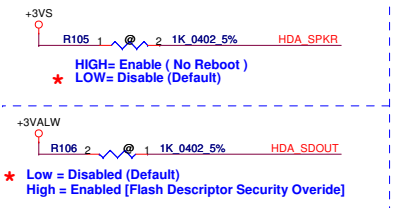
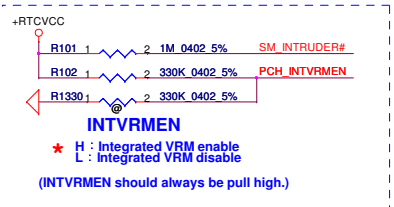
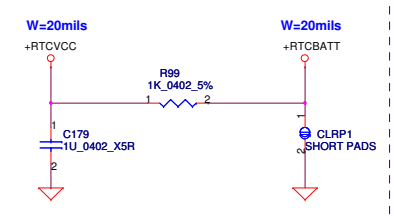
Layout Note:
Short to ground

VDDQ (1.5V) =
 $3 * 330\mu f / 12m\ ohm\ (TOTAL\ FOR\ 2\ SO-DIMMS)$
 $6 * 0603\ 10\mu f\ (PER\ CONNECTOR)$

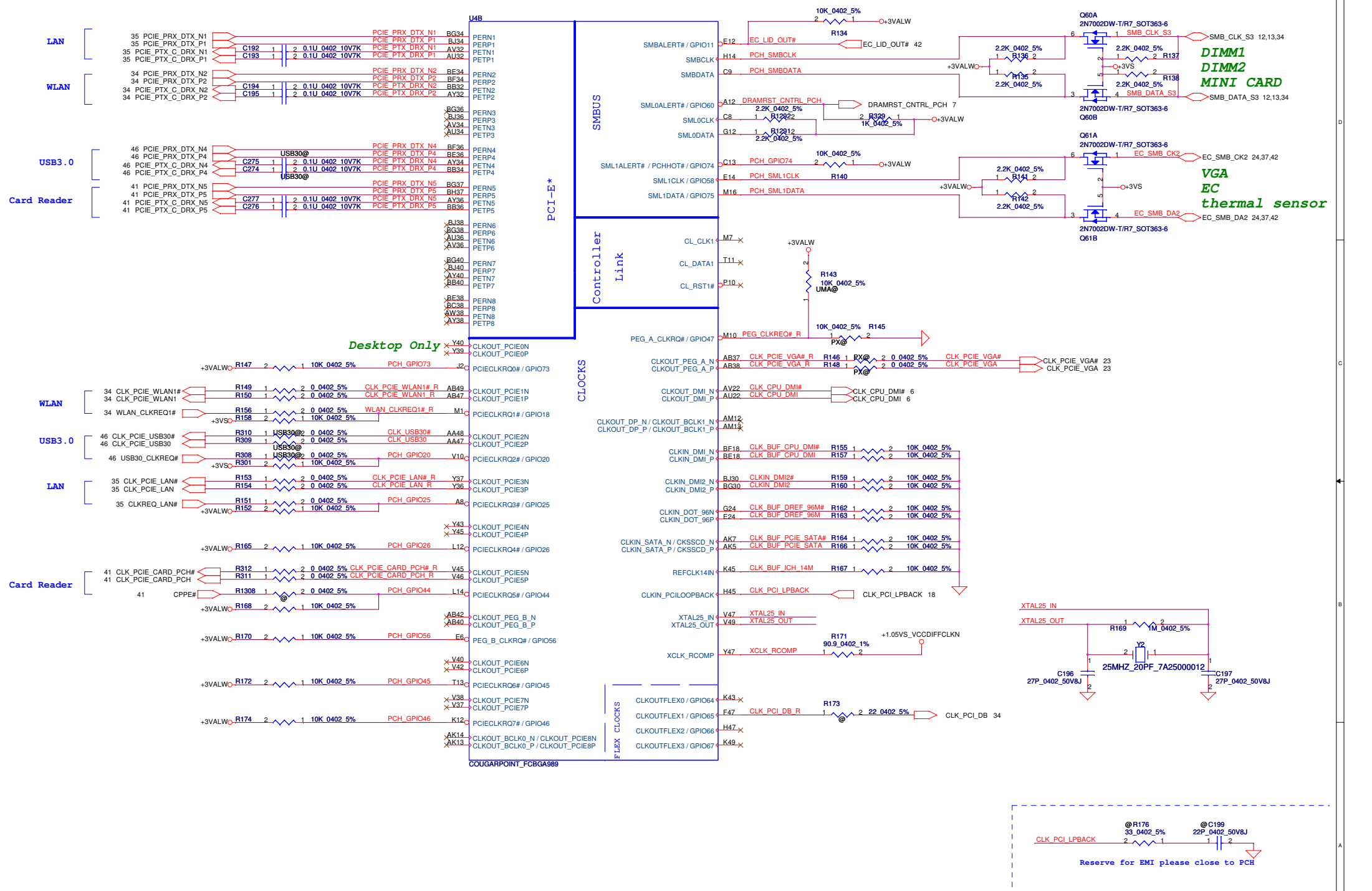
VTT (0.75V) =
 $3 * 0805\ 10\mu f\ 4 * 0402\ 1\mu f$

VDDSPD (3.3V) =
 $1 * 0402\ 0.1\mu f\ 1 * 0402\ 2.2\mu f$
 $1 * 0402\ 0.1\mu f\ 1 * 0402\ 2.2\mu f$

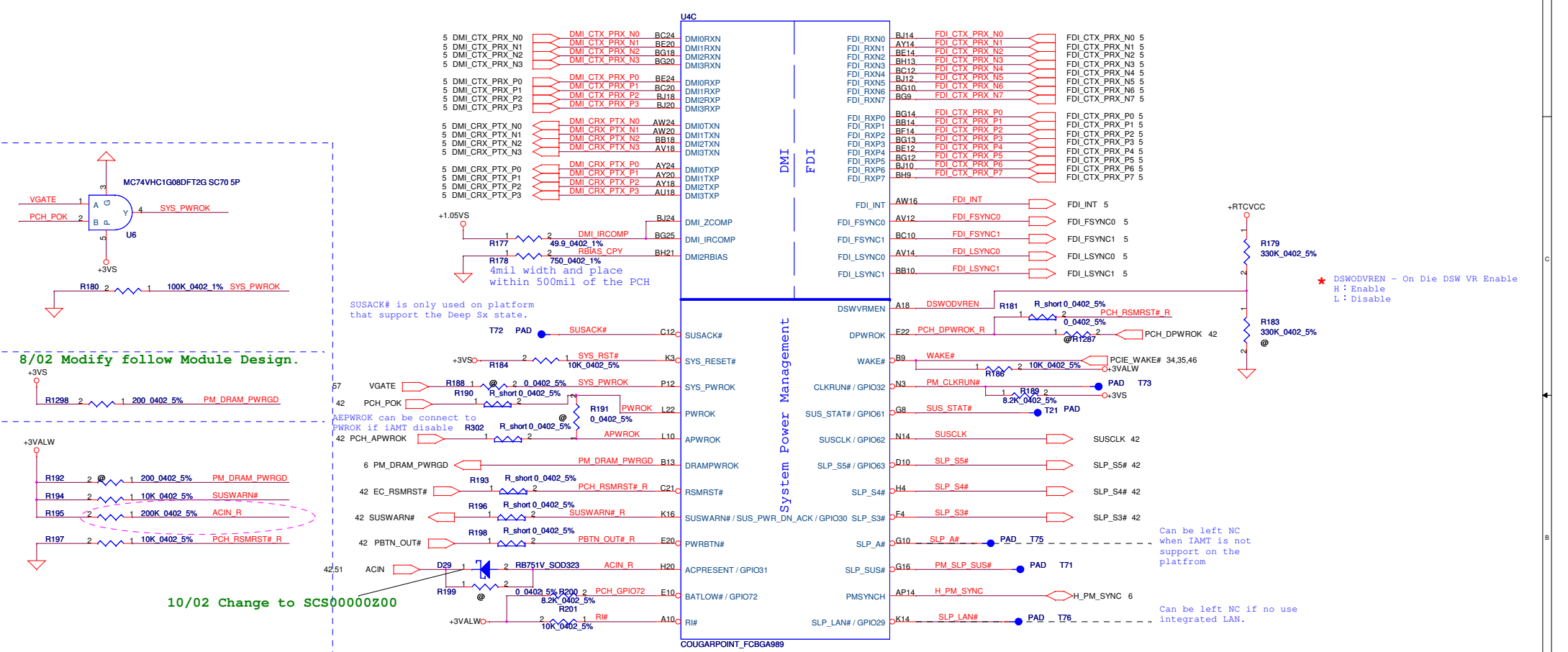
Security Classification	Compal Secret Data		Title	
Issued Date	2010/11/30	Deciphered Date	2011/08	DDR3-SODIMM SLOT2
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Size	Document Number	Rev	Date	
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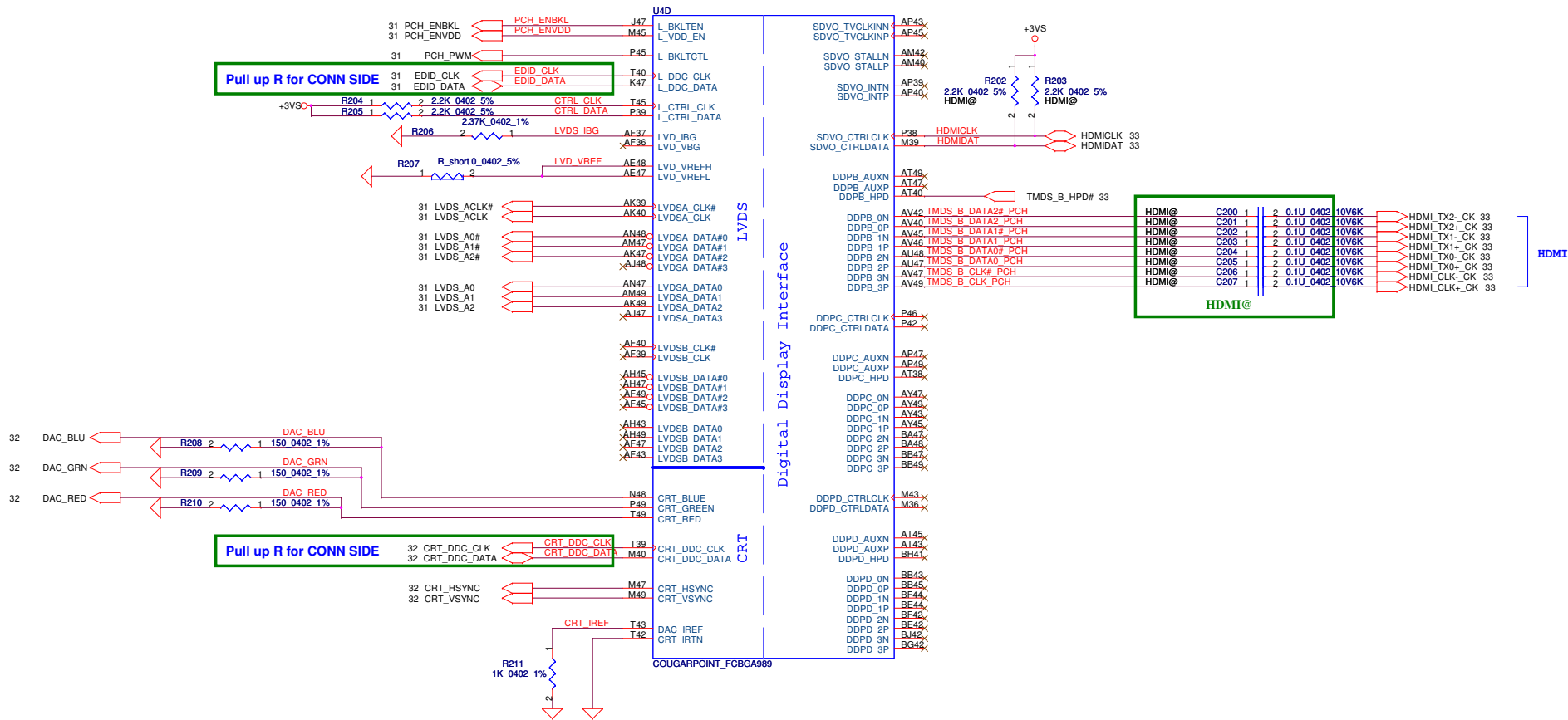
Security Classification	Compal Secret Data		Title	
Issued Date	2010/11/30	Deciphered Date	2011/08	Compal Electronics, Inc.
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Date: Wednesday, May 11, 2011			Sheet 14	of 60



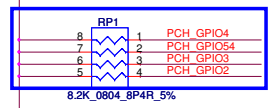
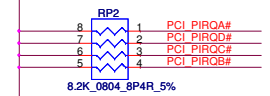
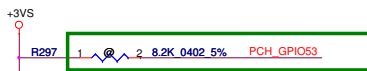
Security Classification	Compal Secret Data			Title	
Issued Date	2010/11/30	Deciphered Date	2011/08	PCH (2/8) PCIE, SMBUS, CLK	
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				QIQY2 LA6884P	1.0
				Date: Wednesday, May 11, 2011	Sheet 15 of 60



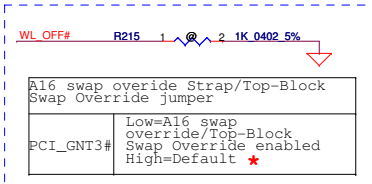
Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2010/11/30	Deciphered Date	2011/08	Title PCH (3/8) DMI,FDI,PM,	
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Date: Wednesday, May 11, 2011				Sheet	16 of 60



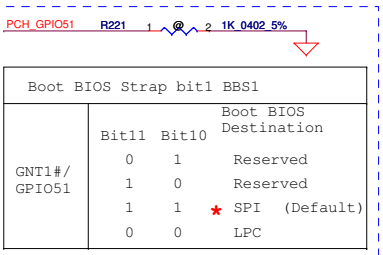
Security Classification	Compal Secret Data			Compal Electronics, Inc.	
Issued Date	2010/11/30	Deciphered Date	2011/08	Title PCH (4/9) LVDS,CRT,DP,HDMI	
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				Date	Wednesday, May 11, 2011
				Sheet	17 of 60
				Rev	1.0



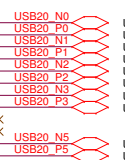
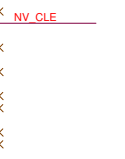
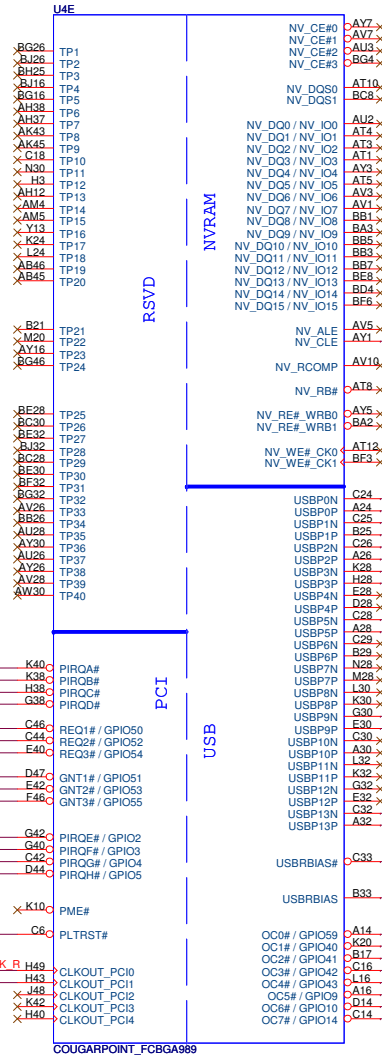
modify 5/10



GPIO53=This Signal has a weak internal pull-up.
NOTE: The internal pull-up is disabled after PLTRST# deasserts.



Boot BIOS Strap bit1 BBS1			
	Bit11	Bit10	Boot BIOS Destination
GNT1#/GPIO51	0	1	Reserved
	1	0	Reserved
	1	1	* SPI (Default)
	0	0	LPC



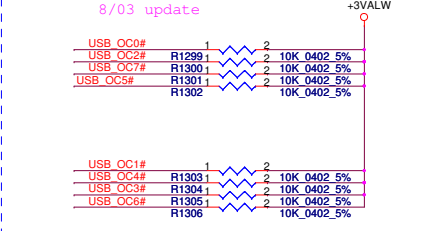
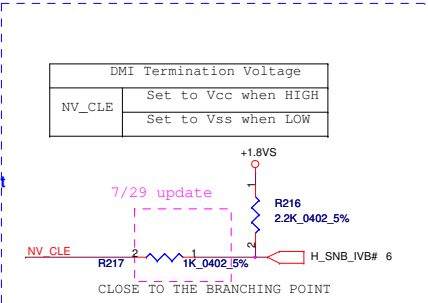
USB DEBUG=PORT1 AND PORT9

RIGHT USB (CABLE) for SW request
RIGHT USB (COMBO)
LEFT USB

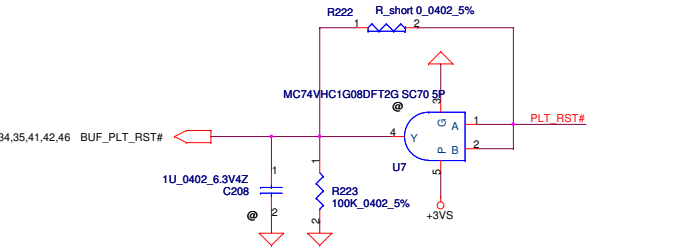
USB Camera

WLAN

Bluetooth



Within 500 mils



Security Classification	Compal Secret Data	
Issued Date	2010/11/30	Deciphered Date 2011/08

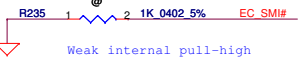
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Compal Electronics, Inc.		
PCH (5/9) PCI, USB		
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6/24 Change to @ follow module design and double check on module design meeting

ICC_EN#
Integrated Clock Chip Enable

H ; Disable
L ; Enable



Weak internal pull-high

GPIO28
On-Die PLL Voltage Regulator

This signal has a weak internal pull up
H : On-Die voltage regulator enable
L : On-Die PLL Voltage Regulator disable

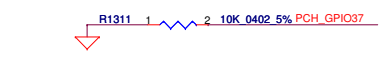
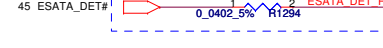


PCH_GPIO27 (Have internal Pull-High)

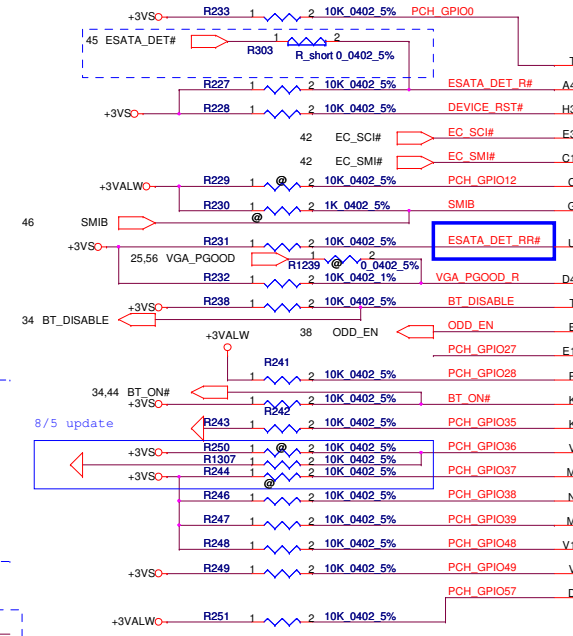
High: VCCVRM VR Enable
Low: VCCVRM VR Disable



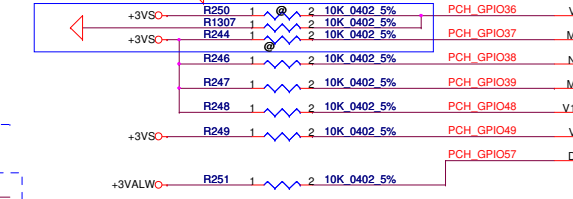
7/29 update for ESATA detect



7/29 update for ESATA detect



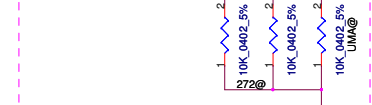
8/5 update



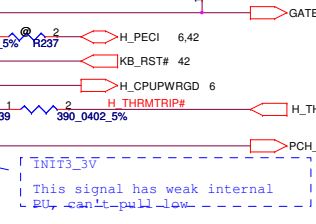
GPIO		CPU/MISC		NCTF	
BMBUS# / GPIO0	TACH4 / GPIO68	C40	PCH_GPIO68	VSS_NCTF_1	BG2
TACH1 / GPIO1	TACH5 / GPIO69	C41	PCH_GPIO69	VSS_NCTF_2	BG4
TACH2 / GPIO6	TACH6 / GPIO70	C41	PCH_GPIO70	VSS_NCTF_3	BH3
TACH3 / GPIO7	TACH7 / GPIO71	A40	PCH_GPIO71	VSS_NCTF_4	BH4
GPIO8				VSS_NCTF_5	BH4
LAN_PHY_PWR_CTRL / GPIO12				VSS_NCTF_6	BH4
GPIO15				VSS_NCTF_7	BH4
SATA4GP / GPIO16				VSS_NCTF_8	BH4
TACH0 / GPIO17				VSS_NCTF_9	D1
SCLOCK / GPIO22				VSS_NCTF_10	D49
GPIO24 / MEM_LED				VSS_NCTF_11	E1
GPIO27				VSS_NCTF_12	E49
GPIO28				VSS_NCTF_13	F1
STP_PCIF / GPIO34				VSS_NCTF_14	F49
GPIO35					
SATA2GP / GPIO36					
SATA3GP / GPIO37					
SLOAD / GPIO38					
SDATAOUT0 / GPIO39					
SDATAOUT1 / GPIO48					
SATA5GP / GPIO49					
GPIO57					

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PCH_GPIO69
PCH_GPIO70
PCH_GPIO71



6/23 update for MB ID



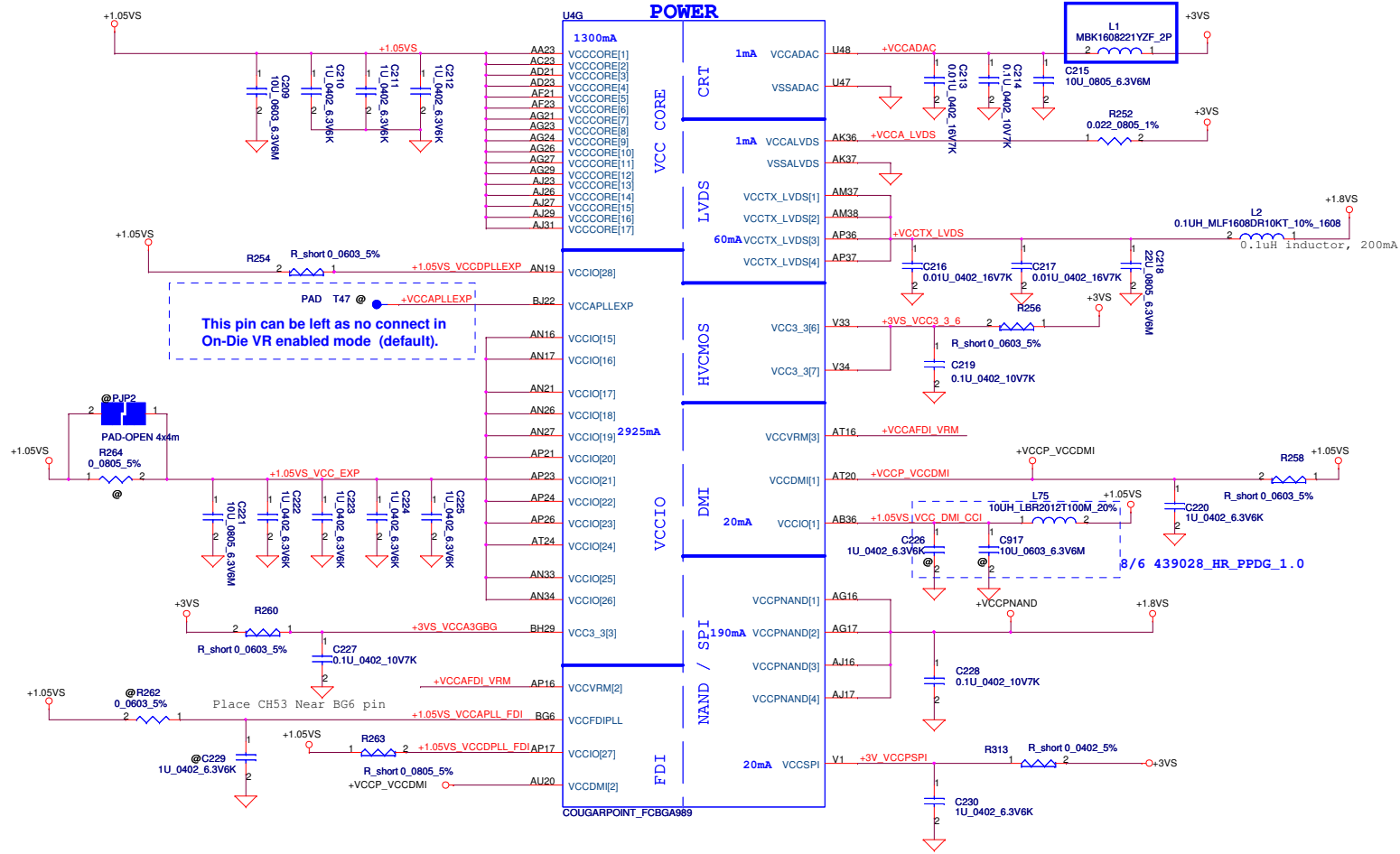
INIT3_BV
This signal has weak internal BU, can't pull low.

Intel schematic review recommend.

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Issued Date	2010/11/30	Deciphered Date
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Compal Electronics, Inc.		
Title PCH (6/9) GPIO, CPU, MISC		
Size	Document Number	Rev
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modify 5/02 from bead SM01000AX00
change to 1 ohm SD014100B80



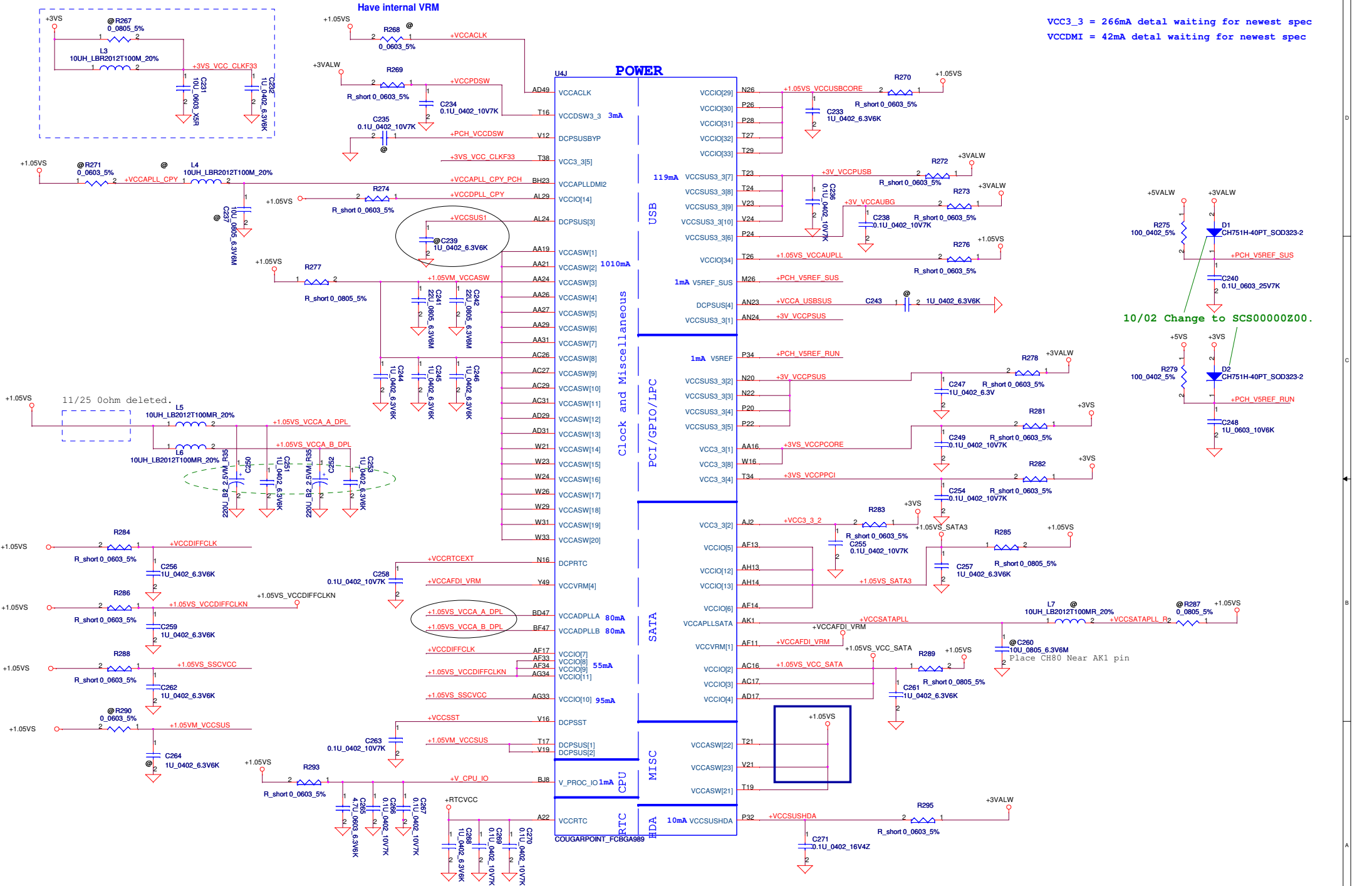
This pin can be left as no connect in On-Die VR enabled mode (default).

Intel HR_PDDG_1.21
1.5S rail. Default is to populate to enable VccVRM.
VCCVRM = 160mA detail waiting for newest spec

PCH Power Rail Table		
Voltage Rail	Voltage	SO Iccmax Current (A)
V_PROC_IO	1.05	0.001
V5REF	5	0.001
V5REF_Sus	5	0.001
Vcc3_3	3.3	0.266
VccADAC	3.3	0.001
VccADPLLA	1.05	0.08
VccADPLLB	1.05	0.08
VccCore	1.05	1.3
VccDMI	1.05	0.042
VccIO	1.05	2.925
VccASW	1.05	1.01
VccSPI	3.3	0.02
VccDSW	3.3	0.003
VccpNAND	1.8	0.19
VccRTC	3.3	6 uA
VccSus3_3	3.3	0.119
VccSusHDA	3.3 / 1.5	0.01
VccVRM	1.8 / 1.5	0.16
VccCLKDMI	1.05	0.02
VccSSC	1.05	0.095
VccDIFFCLKN	1.05	0.055
VccALVDS	3.3	0.001
VccTX_LVDS	1.8	0.06

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Compal Electronics, Inc.			
Title	PCH (719) PWR		
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VCC3_3 = 266mA detail waiting for newest spec
 VCCDMI = 42mA detail waiting for newest spec

10/02 Change to SCS00000Z00.

POWER

USB

Clock and Miscellaneous

PCI/GPIO/LPC

SATA

MISC

CPU

RTC

HDA

RTC

RTC

Security Classification	Compal Secret Data		
Issued Date	2010/11/30	Deciphered Date	2011/08
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Compal Electronics, Inc.			
PCH (8/9) PWR			
Size	Document Number	Rev	
Customer	QIQY2 LA6884P	1.0	
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H5		U4H	
AA17	VSS[1]	VSS[80]	AK38
AA2	VSS[2]	VSS[81]	AK4
AA3	VSS[3]	VSS[82]	AK42
AA33	VSS[4]	VSS[83]	AK46
AA34	VSS[5]	VSS[84]	AK8
AB11	VSS[6]	VSS[85]	AL16
AB14	VSS[7]	VSS[86]	AL17
AB39	VSS[8]	VSS[87]	AL19
AB4	VSS[9]	VSS[88]	AL2
AB43	VSS[10]	VSS[89]	AL21
AB5	VSS[11]	VSS[90]	AL23
AB7	VSS[12]	VSS[91]	AL26
AC19	VSS[13]	VSS[92]	AL27
AC2	VSS[14]	VSS[93]	AL31
AC21	VSS[15]	VSS[94]	AL33
AC24	VSS[16]	VSS[95]	AL34
AC33	VSS[17]	VSS[96]	AL46
AC34	VSS[18]	VSS[97]	AM11
AC48	VSS[19]	VSS[98]	AM14
AD10	VSS[20]	VSS[99]	AM36
AD11	VSS[21]	VSS[100]	AM39
AD12	VSS[22]	VSS[101]	AM43
AD13	VSS[23]	VSS[102]	AM45
AD19	VSS[24]	VSS[103]	AM46
AD24	VSS[25]	VSS[104]	AM7
AD26	VSS[26]	VSS[105]	AN2
AD27	VSS[27]	VSS[106]	AN29
AD33	VSS[28]	VSS[107]	AN3
AD34	VSS[29]	VSS[108]	AN31
AD36	VSS[30]	VSS[109]	AP12
AD37	VSS[31]	VSS[110]	AP19
AD38	VSS[32]	VSS[111]	AP28
AD39	VSS[33]	VSS[112]	AP30
AD4	VSS[34]	VSS[113]	AP32
AD40	VSS[35]	VSS[114]	AP38
AD42	VSS[36]	VSS[115]	AP4
AD43	VSS[37]	VSS[116]	AP42
AD45	VSS[38]	VSS[117]	AP44
AD46	VSS[39]	VSS[118]	AP8
AD8	VSS[40]	VSS[119]	AR2
AE2	VSS[41]	VSS[120]	AR48
AE3	VSS[42]	VSS[121]	AT11
AF10	VSS[43]	VSS[122]	AT13
AF12	VSS[44]	VSS[123]	AT18
AD14	VSS[45]	VSS[124]	AT22
AD16	VSS[46]	VSS[125]	AT26
AF16	VSS[47]	VSS[126]	AT28
AF19	VSS[48]	VSS[127]	AT30
AF24	VSS[49]	VSS[128]	AT32
AF26	VSS[50]	VSS[129]	AT34
AF27	VSS[51]	VSS[130]	AT39
AF29	VSS[52]	VSS[131]	AT42
AF31	VSS[53]	VSS[132]	AT46
AF38	VSS[54]	VSS[133]	AT7
AF4	VSS[55]	VSS[134]	AU24
AF42	VSS[56]	VSS[135]	AU30
AF46	VSS[57]	VSS[136]	AV16
AF5	VSS[58]	VSS[137]	AV20
AF7	VSS[59]	VSS[138]	AV24
AF8	VSS[60]	VSS[139]	AV30
AG19	VSS[61]	VSS[140]	AV38
AG2	VSS[62]	VSS[141]	AV4
AG31	VSS[63]	VSS[142]	AV43
AG48	VSS[64]	VSS[143]	AV8
AH11	VSS[65]	VSS[144]	AW14
AH3	VSS[66]	VSS[145]	AW18
AH36	VSS[67]	VSS[146]	AW2
AH39	VSS[68]	VSS[147]	AW22
AH40	VSS[69]	VSS[148]	AW26
AH42	VSS[70]	VSS[149]	AW28
AH46	VSS[71]	VSS[150]	AW32
AH7	VSS[72]	VSS[151]	AW34
AJ19	VSS[73]	VSS[152]	AW36
AJ21	VSS[74]	VSS[153]	AW40
AJ24	VSS[75]	VSS[154]	AW48
AJ33	VSS[76]	VSS[155]	AY11
AJ34	VSS[77]	VSS[156]	AY12
AK12	VSS[78]	VSS[157]	AY22
AK3	VSS[79]	VSS[158]	AY28

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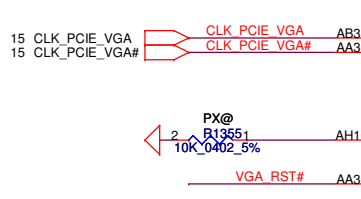
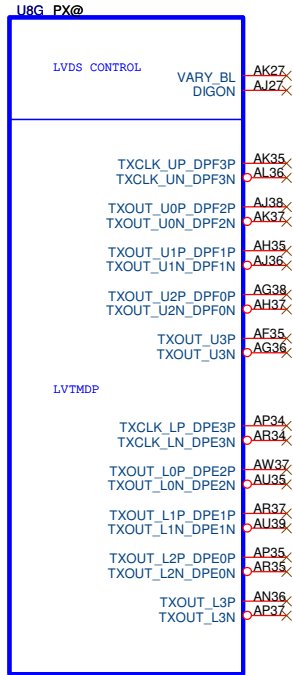
U4I			
AY4	VSS[159]	VSS[259]	H46
AY42	VSS[160]	VSS[260]	K18
AY46	VSS[161]	VSS[261]	K26
AY8	VSS[162]	VSS[262]	K39
B11	VSS[163]	VSS[263]	K46
B15	VSS[164]	VSS[264]	K7
B19	VSS[165]	VSS[265]	L18
B23	VSS[166]	VSS[266]	L2
B27	VSS[167]	VSS[267]	L20
B31	VSS[168]	VSS[268]	L26
B35	VSS[169]	VSS[269]	L28
B39	VSS[170]	VSS[270]	L48
B7	VSS[171]	VSS[271]	M12
F45	VSS[172]	VSS[272]	M12
BB12	VSS[173]	VSS[273]	P16
BB16	VSS[174]	VSS[274]	M18
BB20	VSS[175]	VSS[275]	M24
BB22	VSS[176]	VSS[276]	M24
BB24	VSS[177]	VSS[277]	M30
BB28	VSS[178]	VSS[278]	M32
BB30	VSS[179]	VSS[279]	M34
BB38	VSS[180]	VSS[280]	M38
BB4	VSS[181]	VSS[281]	M4
BB46	VSS[182]	VSS[282]	M42
BC14	VSS[183]	VSS[283]	M46
BC18	VSS[184]	VSS[284]	M8
BC2	VSS[185]	VSS[285]	N18
BC22	VSS[186]	VSS[286]	P30
BC26	VSS[187]	VSS[287]	N47
BC32	VSS[188]	VSS[288]	P11
BC34	VSS[189]	VSS[289]	P18
BC36	VSS[190]	VSS[290]	T33
AM7	VSS[191]	VSS[291]	P40
BC42	VSS[192]	VSS[292]	P43
BC48	VSS[193]	VSS[293]	P47
BD46	VSS[194]	VSS[294]	P7
BE5	VSS[195]	VSS[295]	R2
BE26	VSS[196]	VSS[296]	R48
BE37	VSS[197]	VSS[297]	T12
BE40	VSS[198]	VSS[298]	T31
BE10	VSS[199]	VSS[299]	T37
BE12	VSS[200]	VSS[300]	T4
BF16	VSS[201]	VSS[301]	W34
BF20	VSS[202]	VSS[302]	T46
BF22	VSS[203]	VSS[303]	T47
BF24	VSS[204]	VSS[304]	T9
BF26	VSS[205]	VSS[305]	V11
BF28	VSS[206]	VSS[306]	V17
BD3	VSS[207]	VSS[307]	V26
BF30	VSS[208]	VSS[308]	V27
BF38	VSS[209]	VSS[309]	V29
BF40	VSS[210]	VSS[310]	V31
BF8	VSS[211]	VSS[311]	V36
BG17	VSS[212]	VSS[312]	V39
BG33	VSS[213]	VSS[313]	V43
BG41	VSS[214]	VSS[314]	V7
BG44	VSS[215]	VSS[315]	W17
BG8	VSS[216]	VSS[316]	W19
BH11	VSS[217]	VSS[317]	W2
BH15	VSS[218]	VSS[318]	W27
BH17	VSS[219]	VSS[319]	W48
BH19	VSS[220]	VSS[320]	Y12
H10	VSS[221]	VSS[321]	Y38
BH27	VSS[222]	VSS[322]	Y4
BH31	VSS[223]	VSS[323]	Y42
BH33	VSS[224]	VSS[324]	Y6
BH35	VSS[225]	VSS[325]	Y6
BH39	VSS[226]	VSS[326]	BG2a
BH43	VSS[227]	VSS[327]	N24
BH7	VSS[228]	VSS[328]	AJ3
D1	VSS[229]	VSS[329]	AD47
D12	VSS[230]	VSS[330]	B43
D16	VSS[231]	VSS[331]	BE10
D18	VSS[232]	VSS[332]	BG41
D2	VSS[233]	VSS[333]	G14
D24	VSS[234]	VSS[334]	H16
D26	VSS[235]	VSS[335]	T36
D30	VSS[236]	VSS[336]	T36
D32	VSS[237]	VSS[337]	BG22
D34	VSS[238]	VSS[338]	BG24
D38	VSS[239]	VSS[339]	C22
D42	VSS[240]	VSS[340]	AP13
D8	VSS[241]	VSS[341]	M14
E18	VSS[242]	VSS[342]	M14
E26	VSS[243]	VSS[343]	AP3
G18	VSS[244]	VSS[344]	AP1
G20	VSS[245]	VSS[345]	BE16
G26	VSS[246]	VSS[346]	BC16
G28	VSS[247]	VSS[347]	BG28
G36	VSS[248]	VSS[348]	B128
G48	VSS[249]	VSS[349]	
H12	VSS[250]	VSS[350]	
H18	VSS[251]	VSS[351]	
H22	VSS[252]	VSS[352]	
H24	VSS[253]		
H26	VSS[254]		
H30	VSS[255]		
H32	VSS[256]		
H34	VSS[257]		
F3	VSS[258]		

COUGARPOINT_FCBGA989

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				Date	Rev
				Wednesday, May 11, 2011	1.0
				Sheet	22 of 60

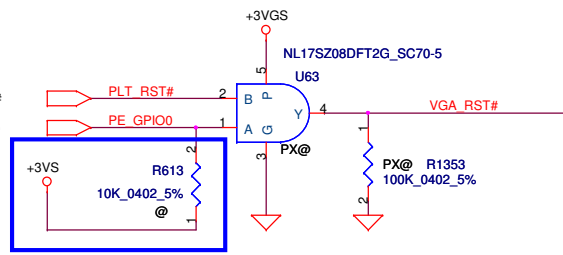
5 PCIE_CTX_GRX_P[15..0] → PCIE_CTX_GRX_P[15..0]
 5 PCIE_CTX_GRX_N[15..0] → PCIE_CTX_GRX_N[15..0]

LVDS Interface



PCIE LANE

modify 3/17



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Size B	Document Number			Rev	1.0
Date:	Wednesday, May 11, 2011	Sheet	23	of 60	

CONFIGURATION STRAPS
 ALLOW FOR PULLUP PADS FOR THESE STRAPS AND IF THESE GPIOS ARE USED, THEY MUST NOT CONFLICT DURING RESET

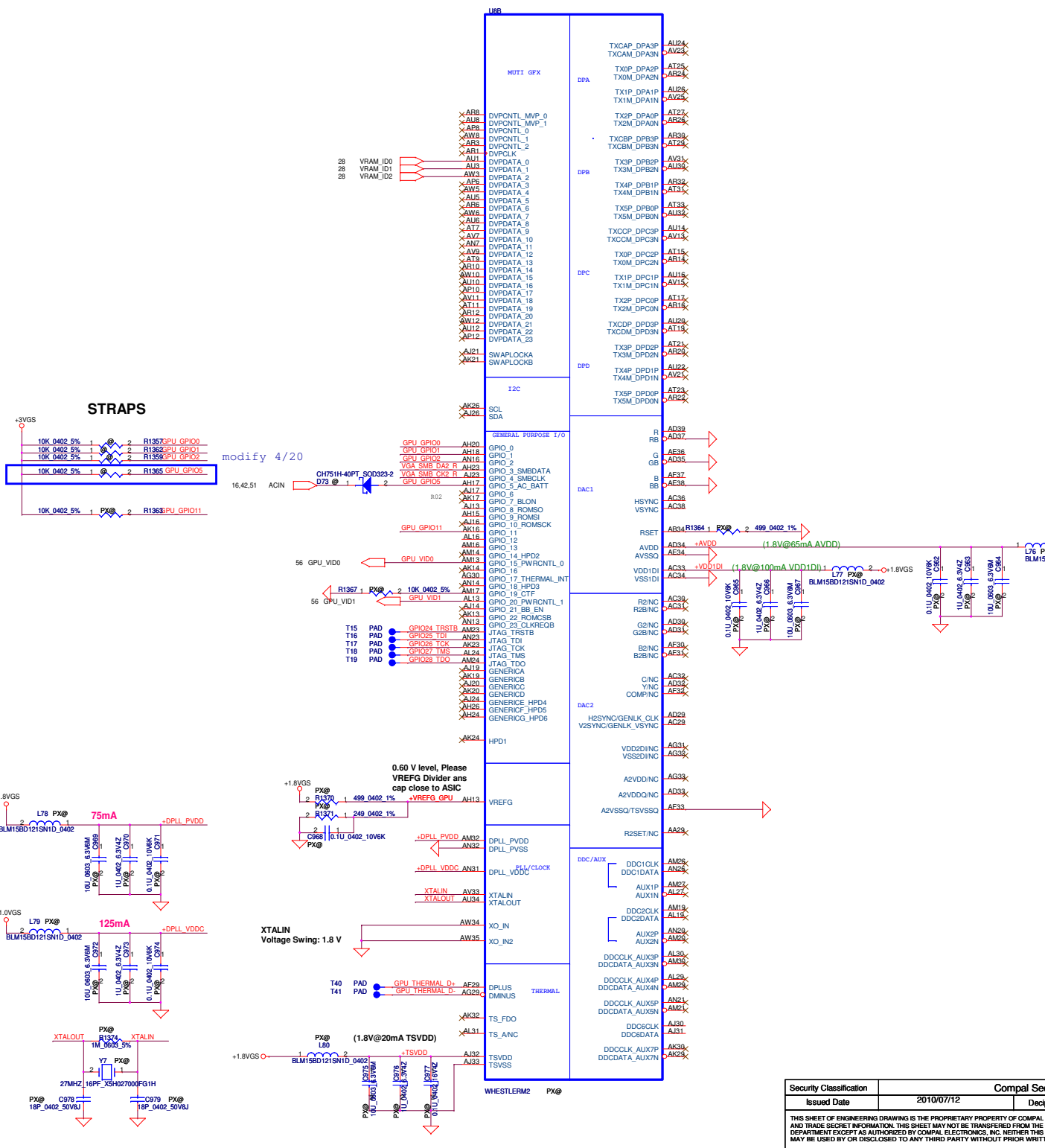
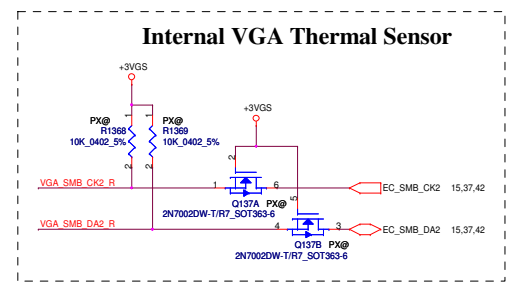
RECOMMENDED SETTINGS
 0 = DO NOT INSTALL RESISTOR
 1 = INSTALL 10K RESISTOR
 X = DESIGN DEPENDANT
 NA = NOT APPLICABLE

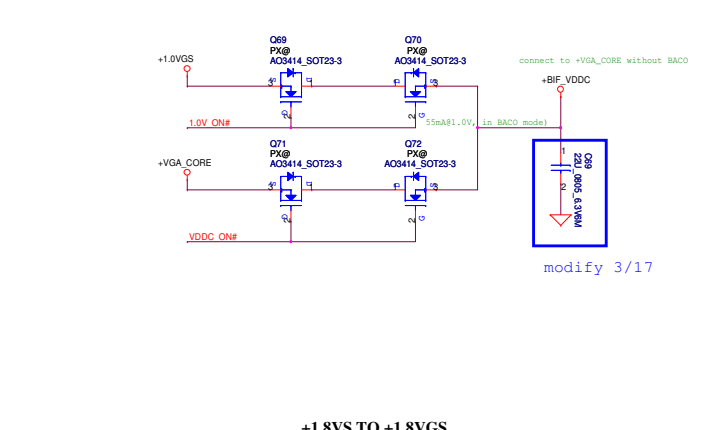
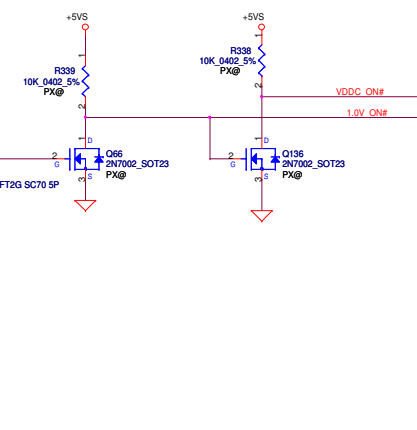
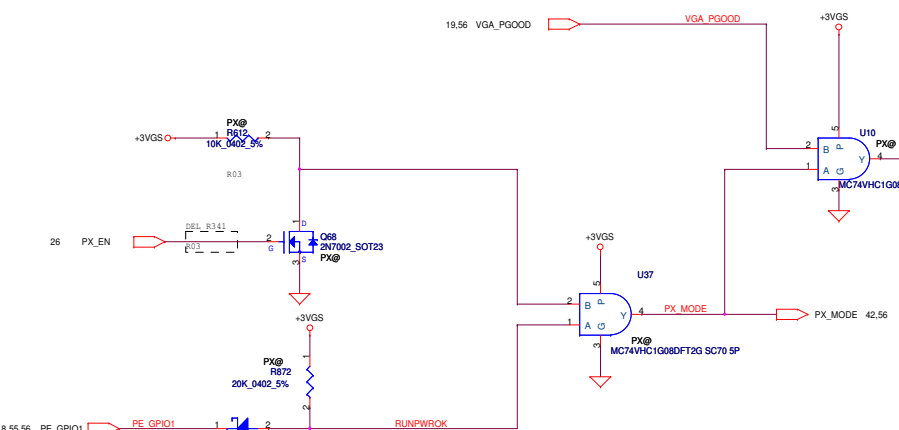
STRAPS	PIN	DESCRIPTION OF DEFAULT SETTINGS	RECOMMENDED SETTINGS
TX_PWRS_ENB	GPIO0	PCIE FULL TX OUTPUT SWING	X
TX_DEEMPH_EN	GPIO1	PCIE TRANSMITTER DE-EMPHASIS ENABLED	X
RSVD	GPIO2	RESERVED	0
RSVD	GPIO8	RESERVED	0
BIF_VGA_DIS	GPIO9	VGA ENABLED	0
RSVD	GPIO21	RESERVED	0
BIOS_ROM_EN	GPIO_22_ROMCSB	ENABLE EXTERNAL BIOS ROM	X
ROMIDCFG(2:0)	GPIO[13:11]	SERIAL ROM TYPE OR MEMORY APERTURE SIZE SELECT	001
VIP_DEVICE_STRAP_ENA	V2SYNC	IGNORE VIP DEVICE STRAPS	0
RSVD	H2SYNC		0
RSVD	GENERICC		0
AUD[1]	HSYNC	AUD[1] AUD[0] 0 0 No audio function 0 1 Audio for DisplayPort and HDMI if dongle is detected 1 0 Audio for DisplayPort only 1 1 Audio for both DisplayPort and HDMI	11
AUD[0]	VSYN		

AMD RESERVED CONFIGURATION STRAPS
 ALLOW FOR PULLUP PADS FOR THESE STRAPS BUT DO NOT INSTALL RESISTOR. IF THESE GPIOS ARE USED, THEY MUST KEEP "LOW" AND NOT CONFLICT DURING RESET

GPIO21 H2SYNC GENERICC GPIO2 GPIO8

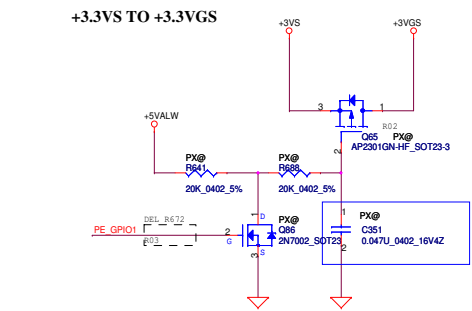
TX_PWRS_ENB	GPIO0	Transmitter Power Saving Enable 0: 50% Tx output swing for mobile mode 1: full Tx output swing (Default setting for Desktop)
TX_DEEMPH_EN	GPIO1	PCI Express Transmitter De-emphasis Enable 0: Tx de-emphasis disabled for mobile mode 1: Tx de-emphasis enabled (Default setting for desktop)



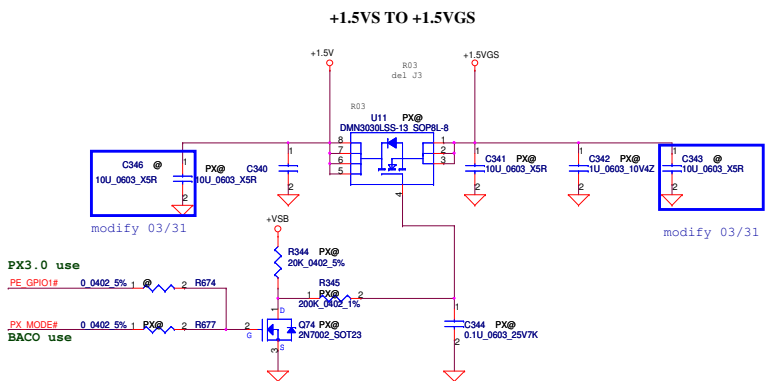
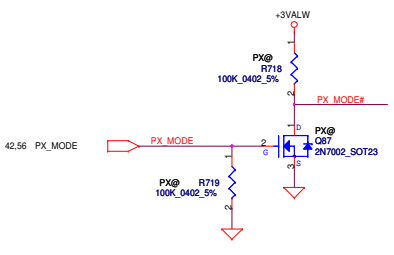
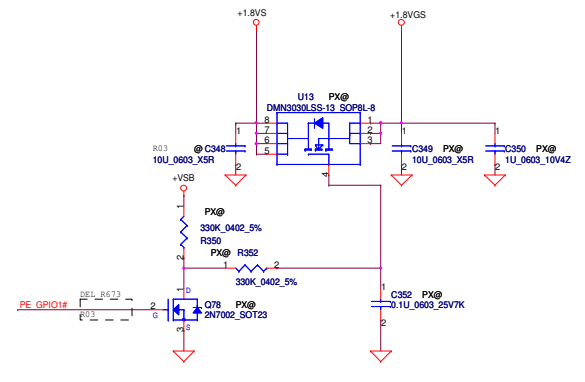
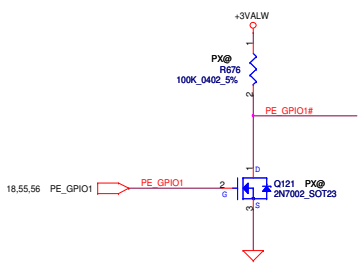


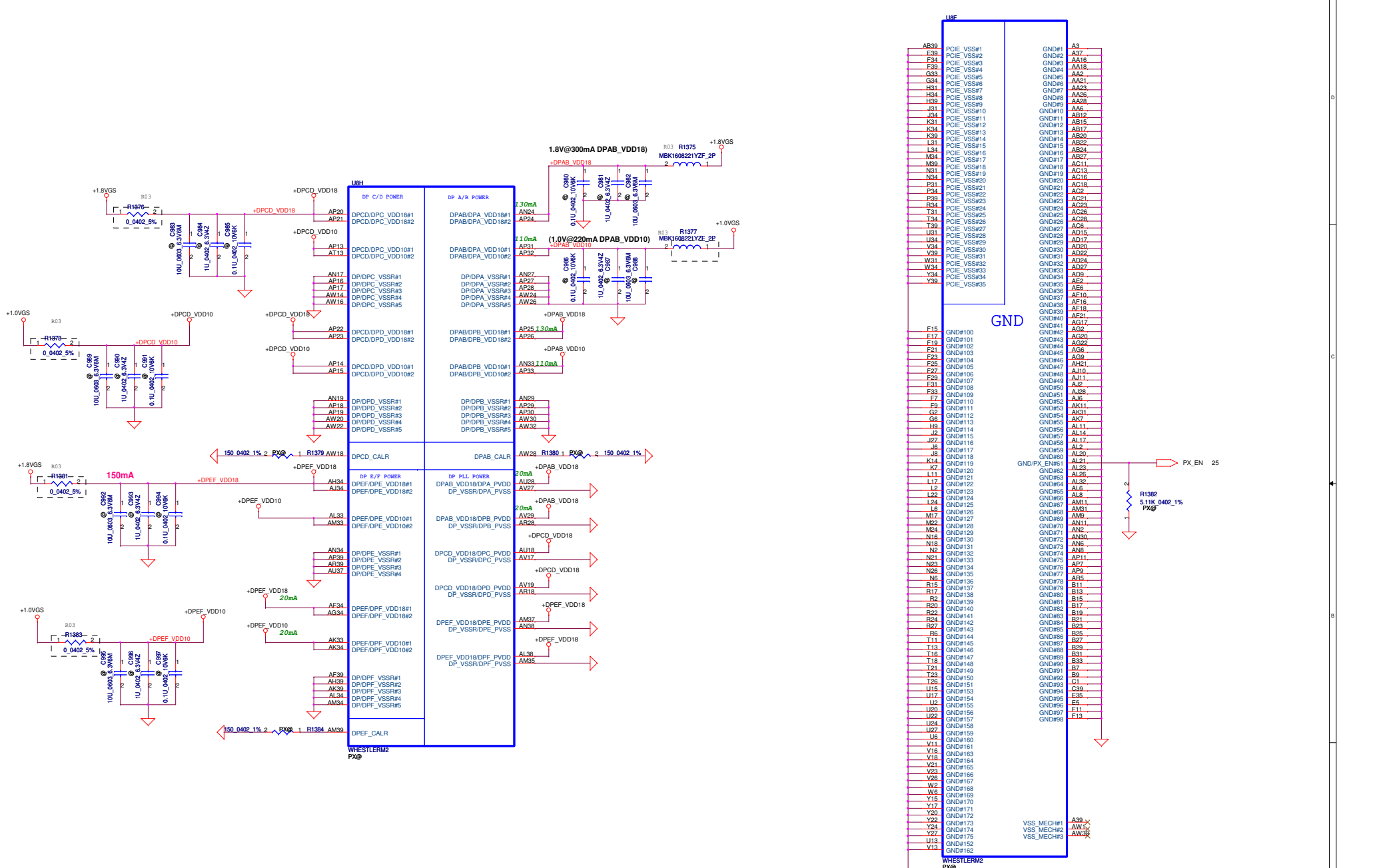
18.55.56 PE_GPIO1 PE_GPIO1 1 2
 CH751H-40PT_S0D323-2
 D28 with leakage need to check

	BACO mode	dGPU1
PX_EN	1	0
PE_GPIO1	0	1
PX_MODE	0	1
VGA_PGOOD	0	1
VDDC_ON#	0	1
1.0V_ON#	1	0
+BIF_VDDC	+1.0VGS	VGA_CORE

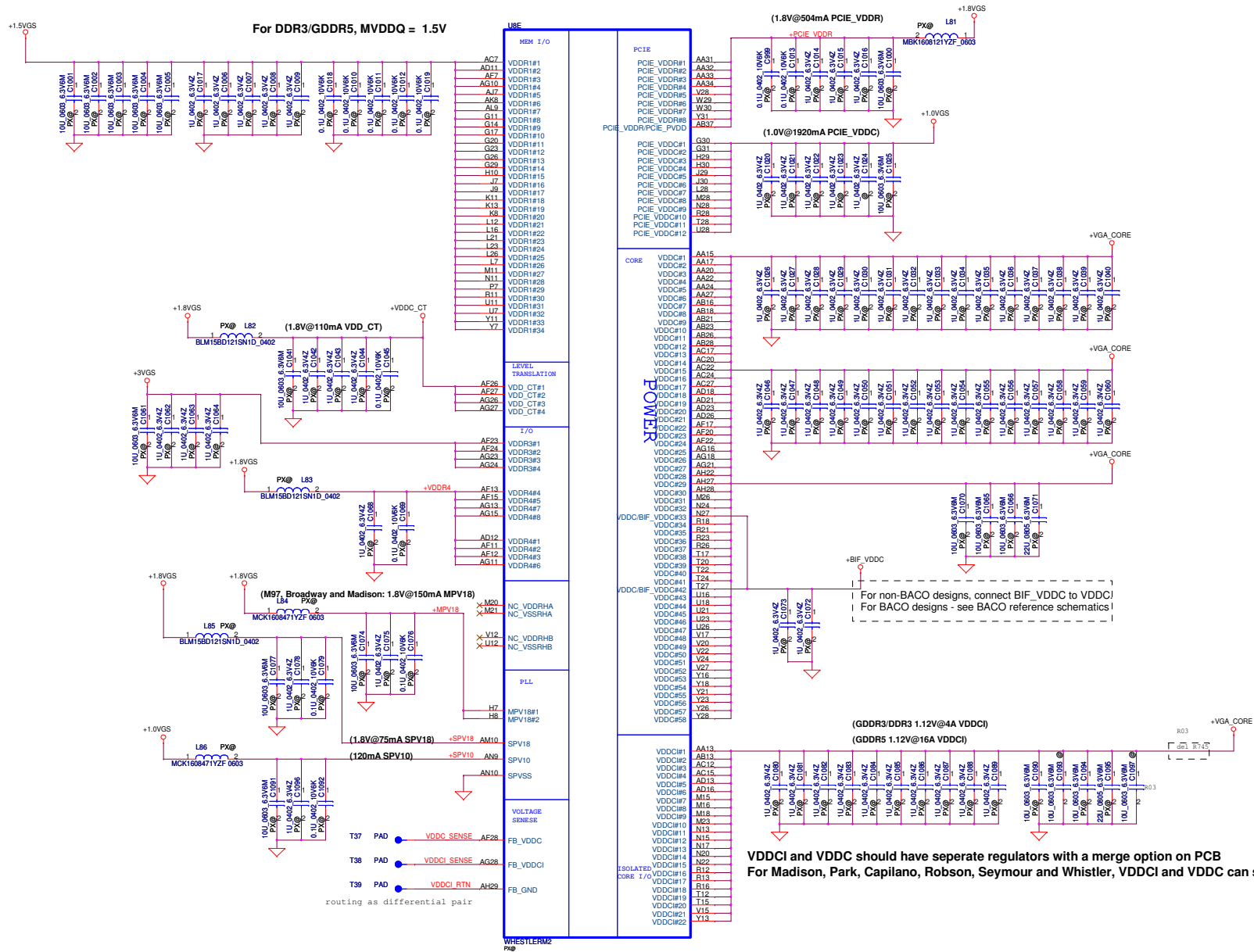


modify 03/25

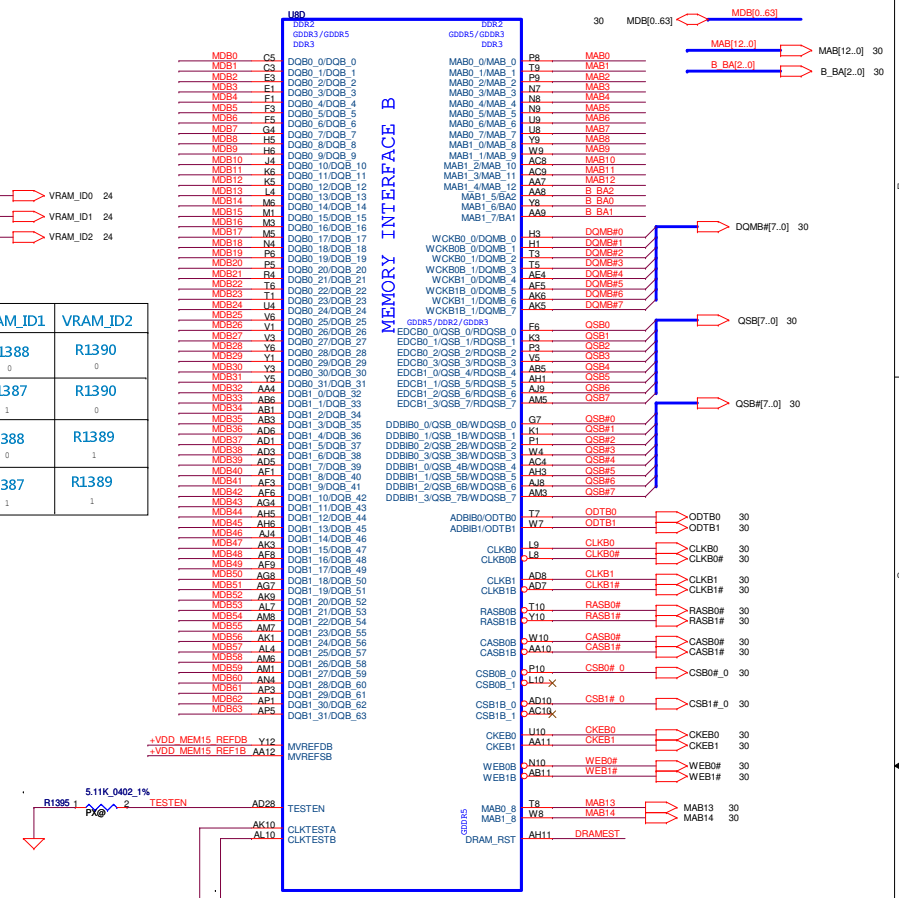
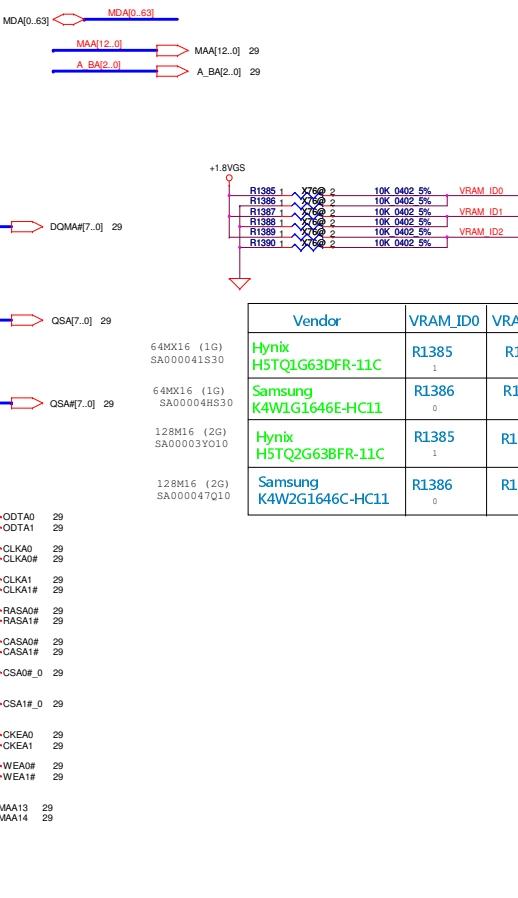
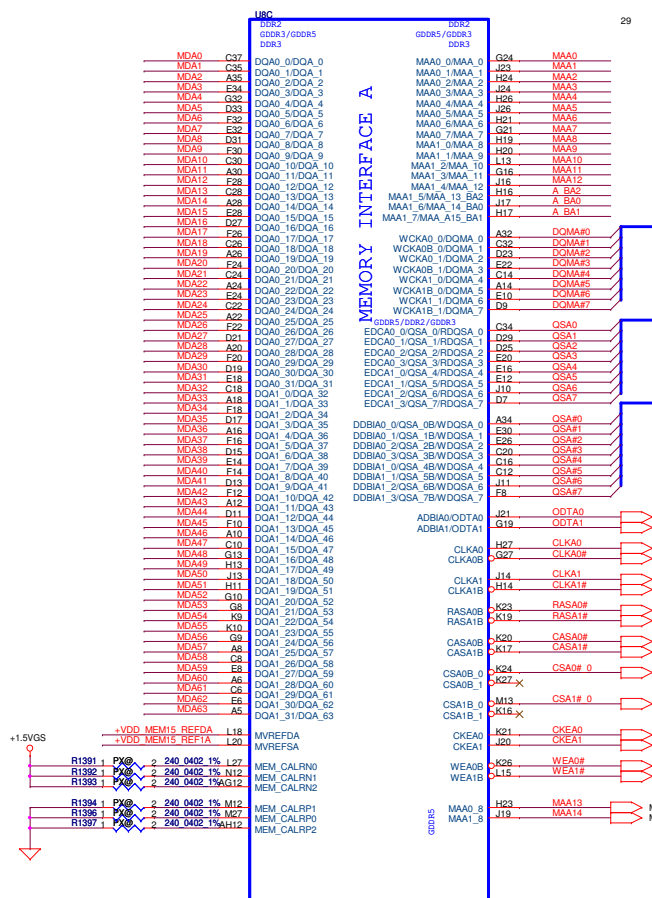




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Size	Document Number	Rev	1.0	
Date:	Wednesday, May 11, 2011	Sheet	26	of 60

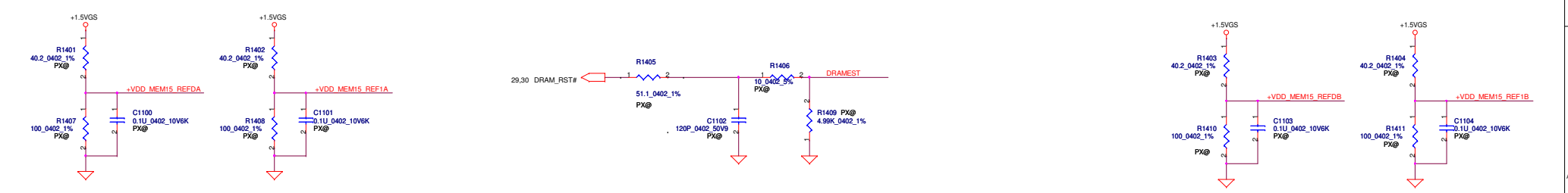


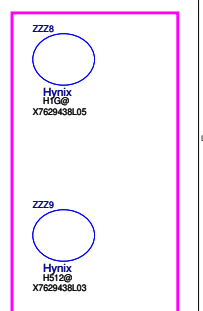
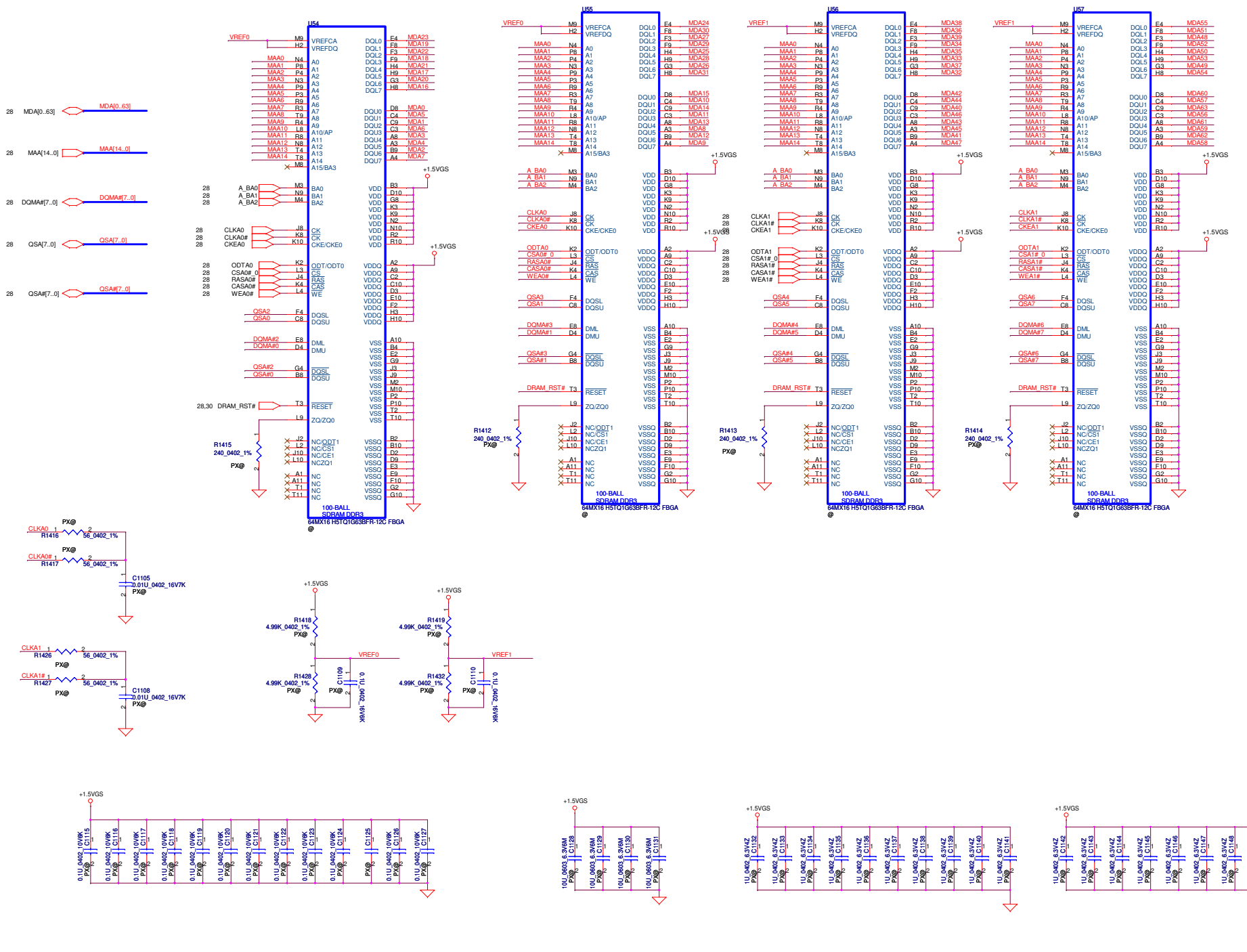
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Issued Date	2010/07/12	Deciphered Date	2012/07/11	
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Title	ATI Whistler M2 Power			
Size	Document Number	Rev	1.0	
Date:	Wednesday, May 11, 2011	Sheet	27	of 60



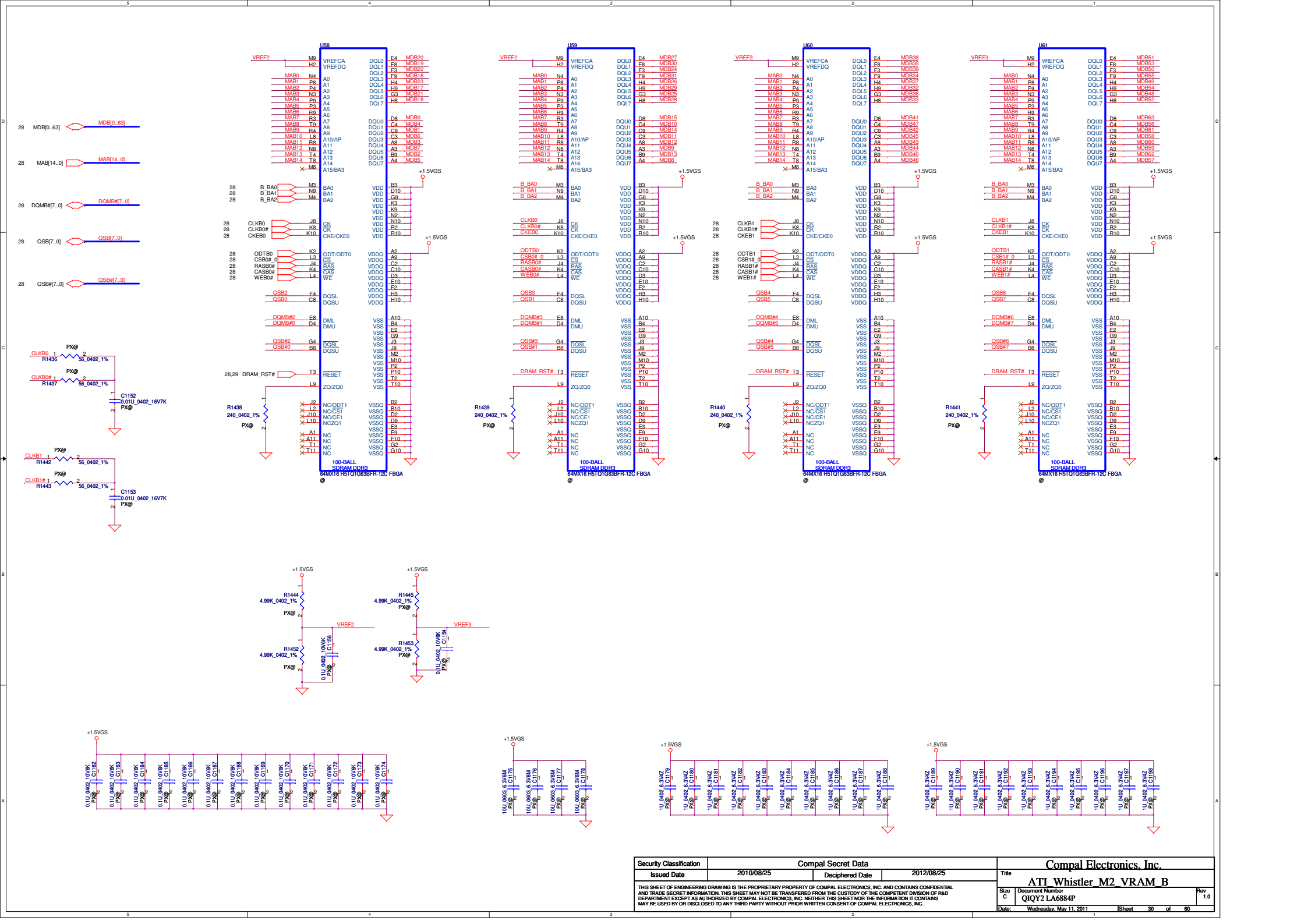
This basic topology should be used for DRAM_RST for DDR3/GDDR5. These Capacitors and Resistor values are an example only. The Series R and || Cap values will depend on the DRAM load and will have to be calculated for different Memory, DRAM Load and board to pass Reset Signal Spec.
Place all these components very close to GPU (Within 25mm) and keep all component close to each other (within 5mm) except Rser2

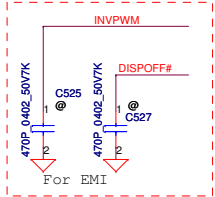
route 50ohms single-ended/100ohms diff and keep short
Debug only, for clock observation, if not needed, DNI
5mil 5mil



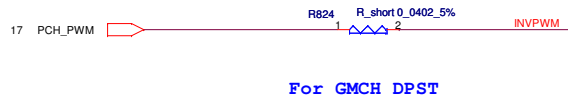
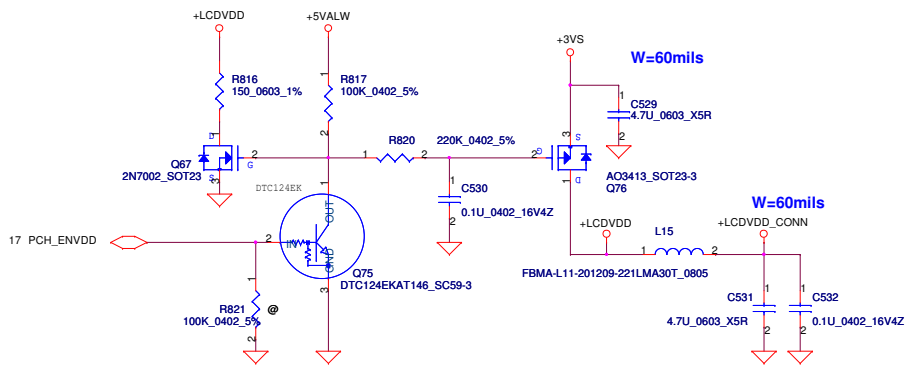


Security Classification	Compal Secret Data			ATI Whistler M2 VRAM A
Issued Date	2010/08/25	Deciphered Date	2012/08/25	Size C
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				Rev 1.0
				Date: Wednesday, Mar 11, 2011 Sheet 29 of 60

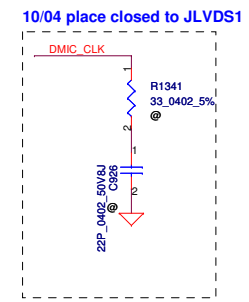
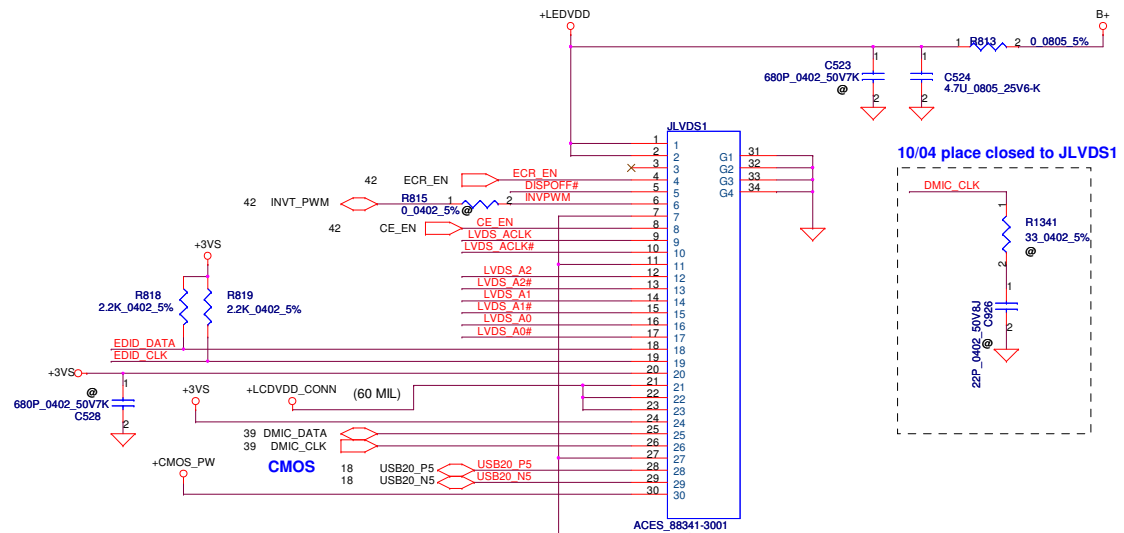




LCD POWER CIRCUIT

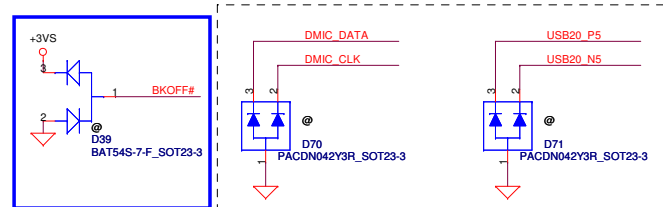


- 17 EDID_CLK \rightarrow EDID_CLK
- 17 EDID_DATA \rightarrow EDID_DATA
- 17 LVDS_A0 \rightarrow LVDS_A0
- 17 LVDS_A0# \rightarrow LVDS_A0#
- 17 LVDS_A1 \rightarrow LVDS_A1
- 17 LVDS_A1# \rightarrow LVDS_A1#
- 17 LVDS_A2 \rightarrow LVDS_A2
- 17 LVDS_A2# \rightarrow LVDS_A2#
- 17 LVDS_ACLK \rightarrow LVDS_ACLK
- 17 LVDS_ACLK# \rightarrow LVDS_ACLK#

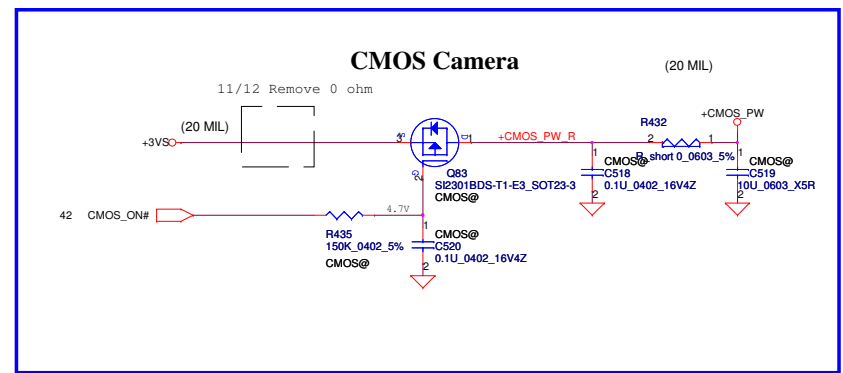
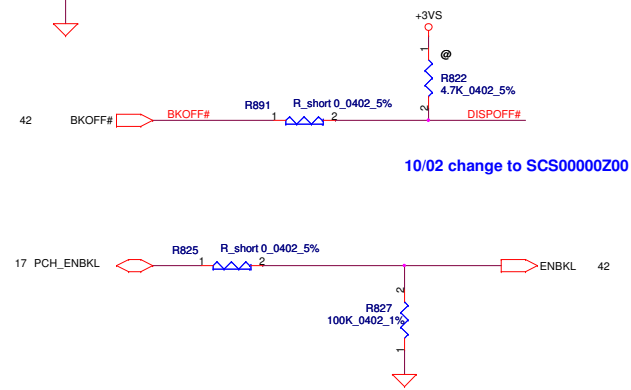


9/21 Rev0.2 update from 40 to 30 pin.

8/4 add ESD request

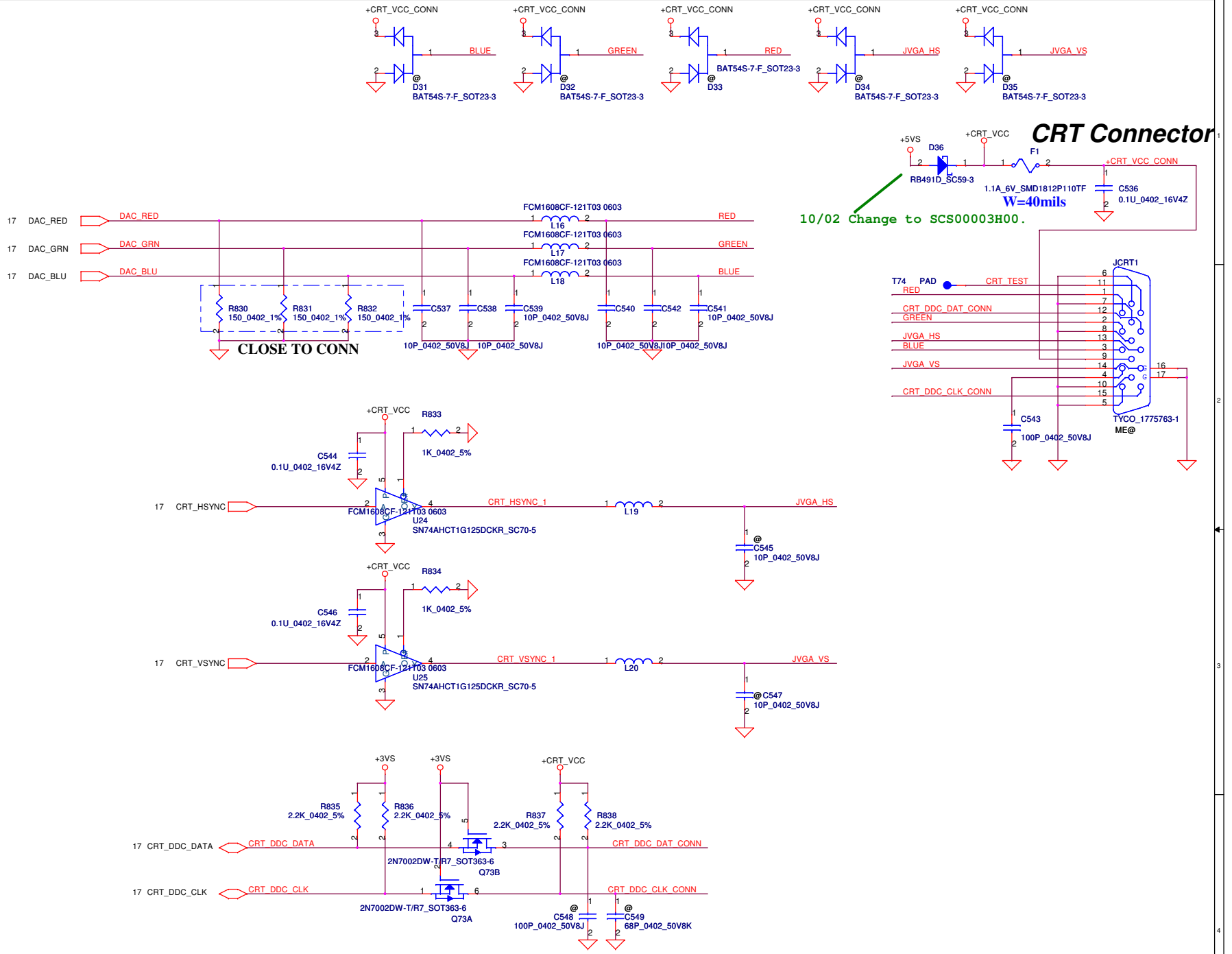


modify 4/20

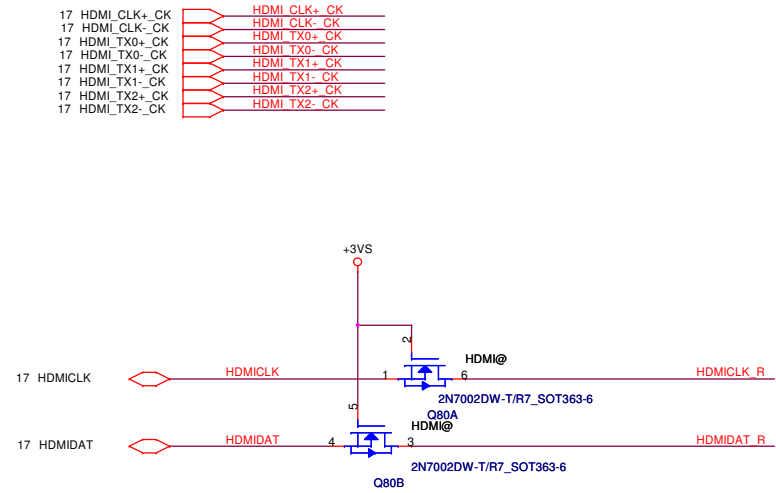
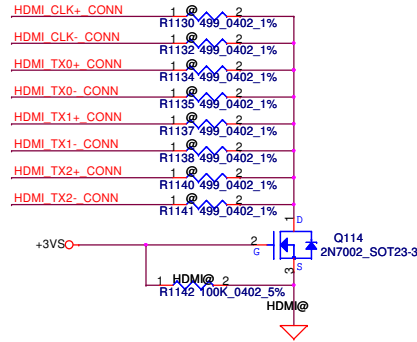
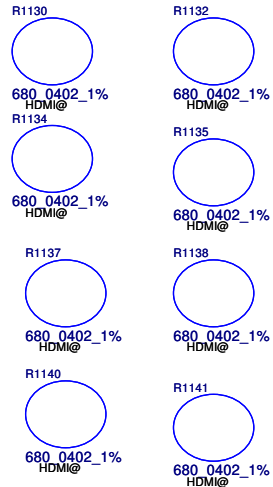


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				LVDS/CAMERA		
				Size	Document Number	Rev
				B	QIY2 LA6884P	1.0
				Date:	Wednesday, May 11, 2011	Sheet 31 of 60

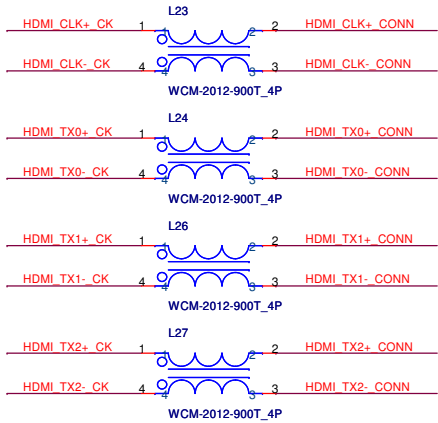
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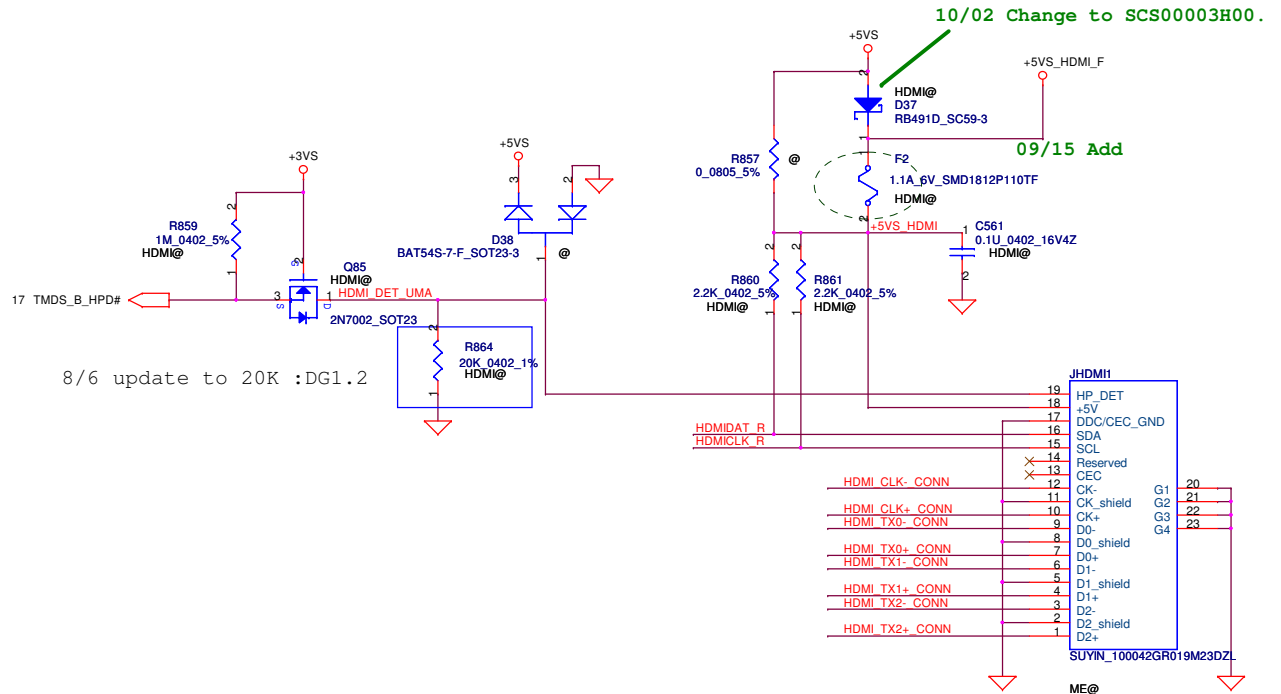
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Size	Document Number	Rev		1.0	
Custom	QIQY2 LA6884P	Date:		Wednesday, May 11, 2011 Sheet 32 of 60	



DVT, Change to SM070000I00 for EMI request.



HDMI CLK+ CK	R865	1	2	0	0402_5%	HDMI CLK+ CONN
HDMI CLK- CK	R866	1	2	0	0402_5%	HDMI CLK- CONN
HDMI TX0+ CK	R867	1	2	0	0402_5%	HDMI TX0+ CONN
HDMI TX0- CK	R868	1	2	0	0402_5%	HDMI TX0- CONN
HDMI TX1+ CK	R869	1	2	0	0402_5%	HDMI TX1+ CONN
HDMI TX1- CK	R870	1	2	0	0402_5%	HDMI TX1- CONN
HDMI TX2+ CK	R871	1	2	0	0402_5%	HDMI TX2+ CONN
HDMI TX2- CK	R890	1	2	0	0402_5%	HDMI TX2- CONN

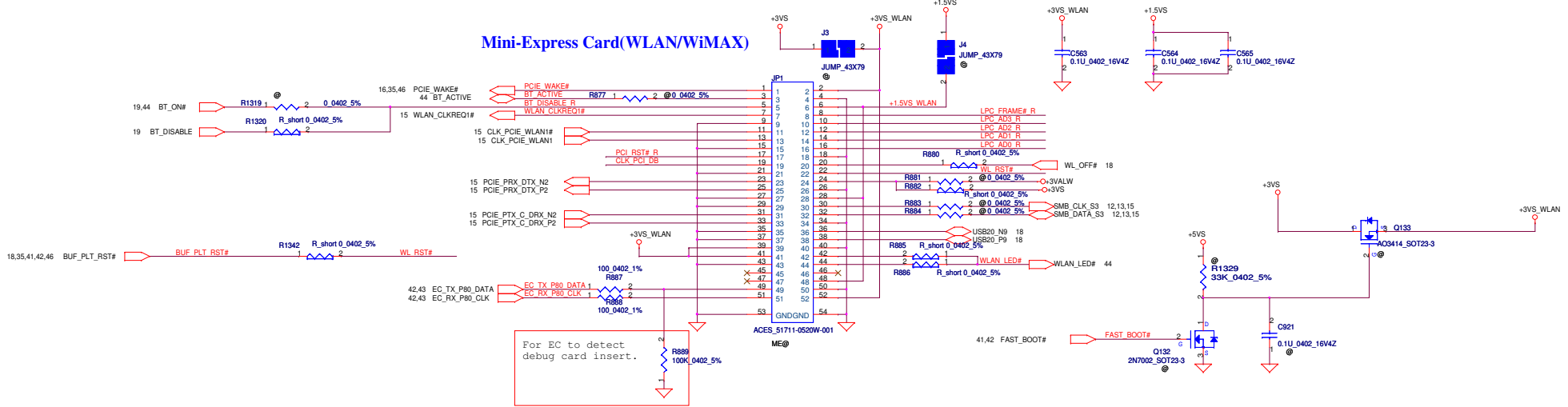


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				QIQY2 LA6884P	
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Mini-Express Card for WLAN/WiMAX(Half) Mini-Express Card for SSD(Full)

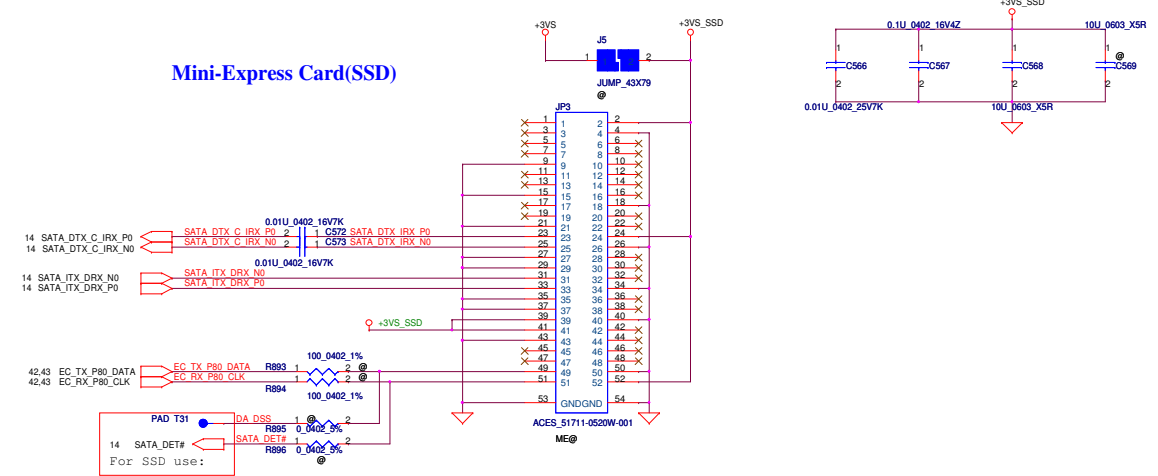
Reserve for SW mini-pcie debug card.
Series resistors closed to KBC side.

LPC_FRAME# R	R873	1	2	0.0402_5%	LPC_FRAME#	LPC_FRAME#	14,42
LPC_ADS3 R	R874	1	2	0.0402_5%	LPC_ADS3	LPC_ADS3	14,42
LPC_ADS2 R	R875	1	2	0.0402_5%	LPC_ADS2	LPC_ADS2	14,42
LPC_ADS1 R	R876	1	2	0.0402_5%	LPC_ADS1	LPC_ADS1	14,42
LPC_ADD0 R	R877	1	2	0.0402_5%	LPC_ADD0	LPC_ADD0	14,42
LPC_ADD1 R	R878	1	2	0.0402_5%	LPC_ADD1	LPC_ADD1	14,42
LPC_ADD2 R	R879	1	2	0.0402_5%	LPC_ADD2	LPC_ADD2	14,42
PCI_RST# R	R879	1	2	0.0402_5%	BUF_PLT_RST#	CLK_PCI_DB	15

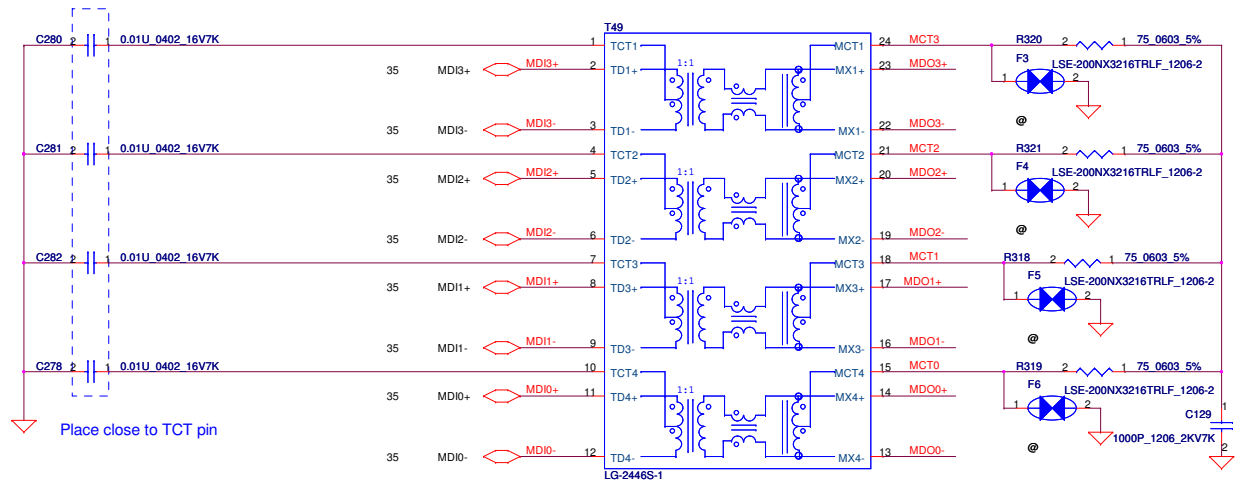


SSD Active:0.22W(0.06A)

Mini-Express Card(SSD)

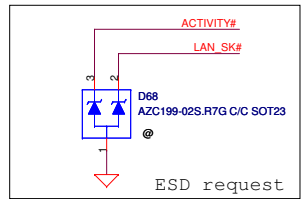
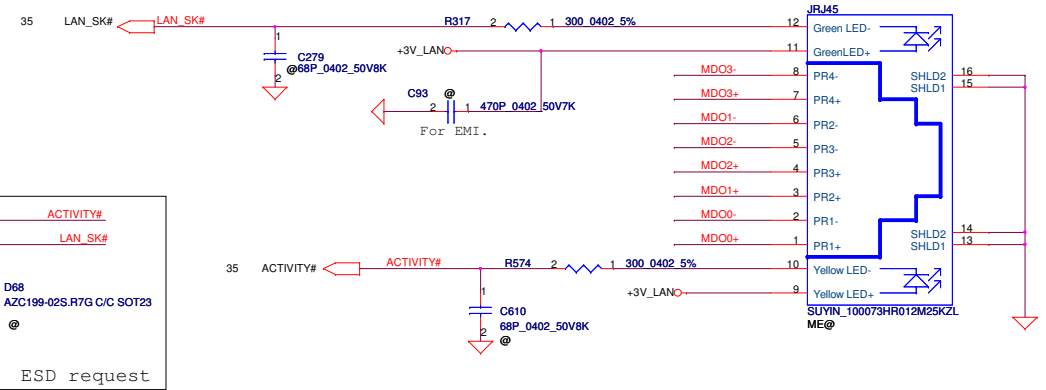


Close to T14

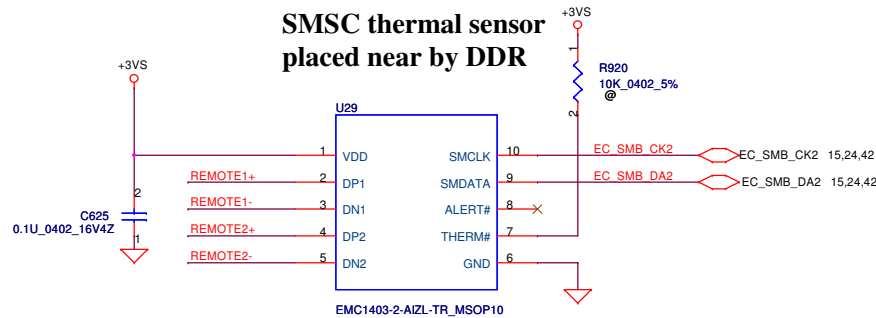
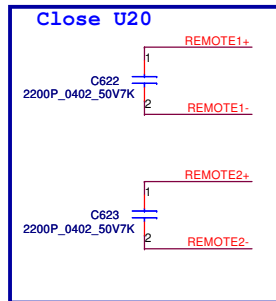


Place close to TCT pin

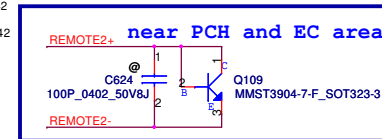
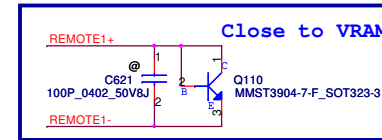
RJ45 Conn.



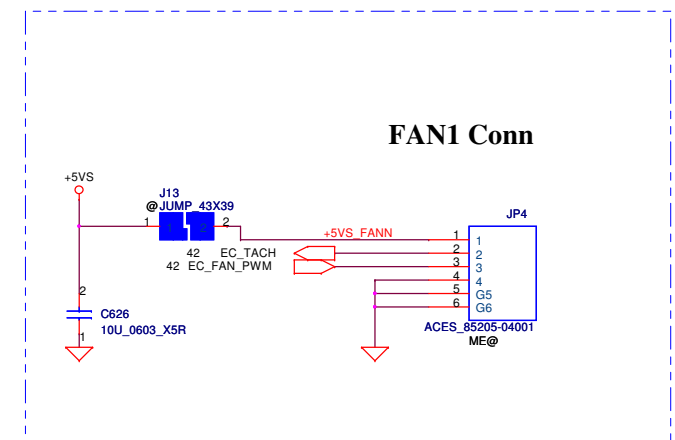
Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2010/11/30	Deciphered Date	2011/08	Title	
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Size	Document Number	Rev		Date	
Custom	QIQY2 LA6884P	1.0		Wednesday, May 11, 2011	
				Sheet	36 of 60



Address 1001_101xb
8/02 Change PN to SA000046C00, Fintek.

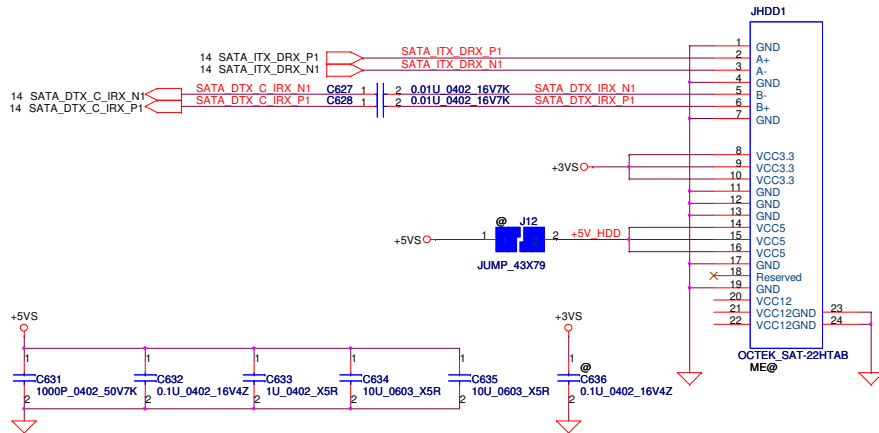


REMOTE1, 2+/-:
Trace width/space: 10/10 mil
Trace length: <8"

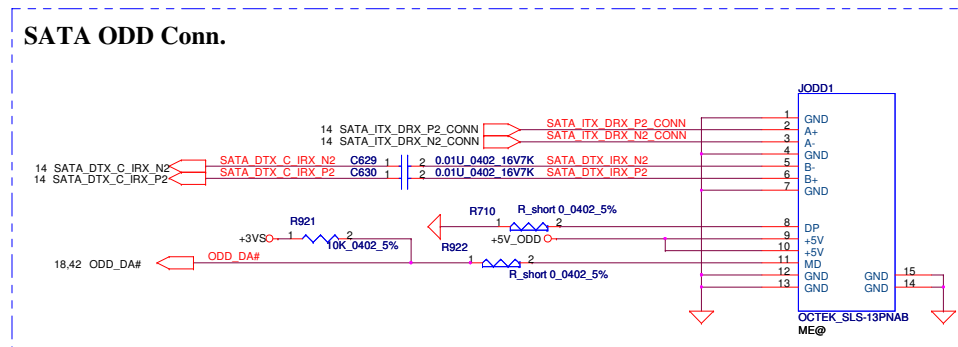


Security Classification		Compal Secret Data		Compal Electronics, Ltd.	
Issued Date	2010/11/30	Deciphered Date	2011/08	Title	EMC1403 Thermal sensor/FAN
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				Date: Wednesday, May 11, 2011	Rev 1.0
				Sheet 37 of 60	

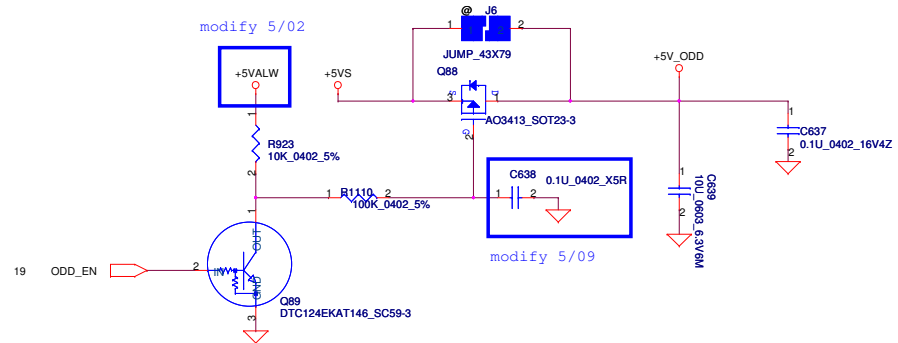
SATA HDD Conn.



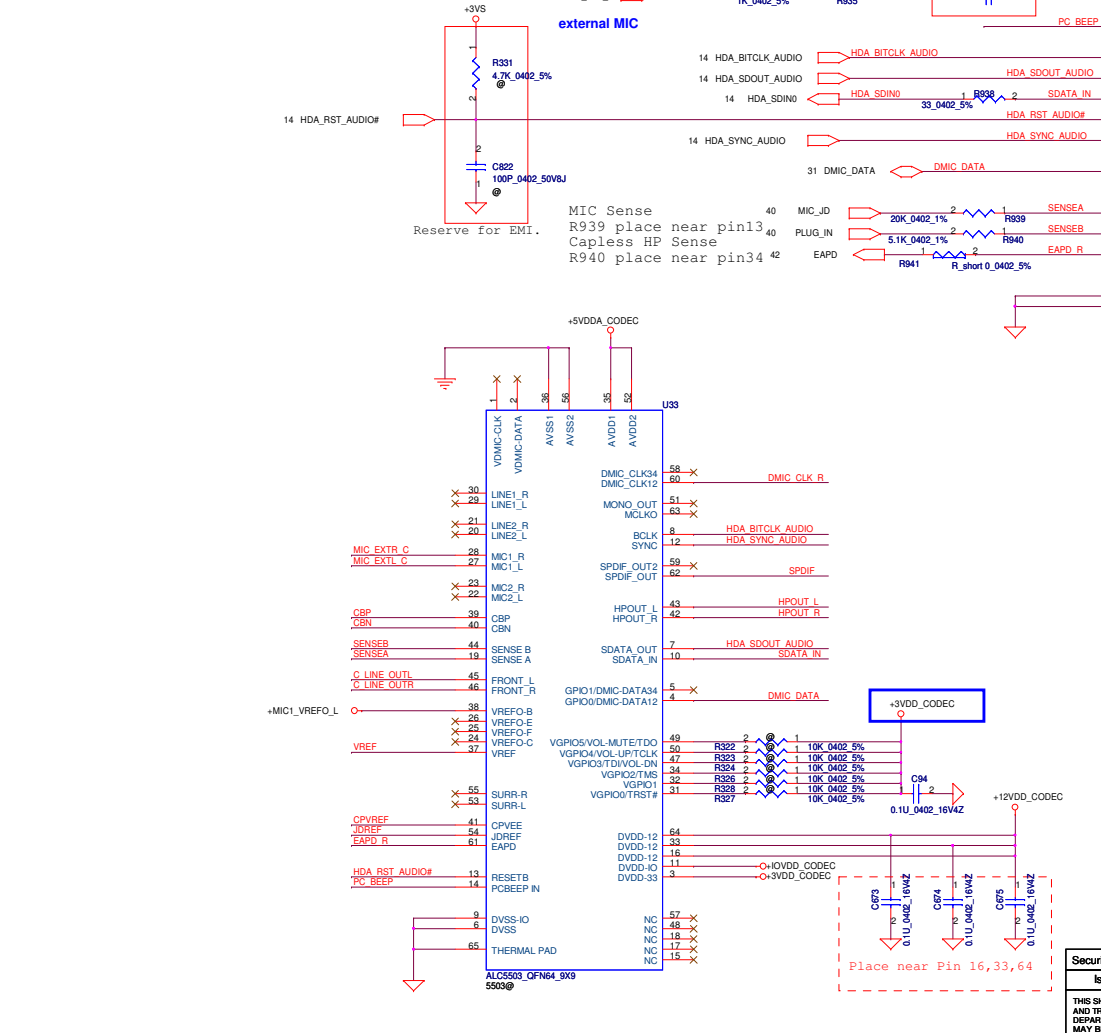
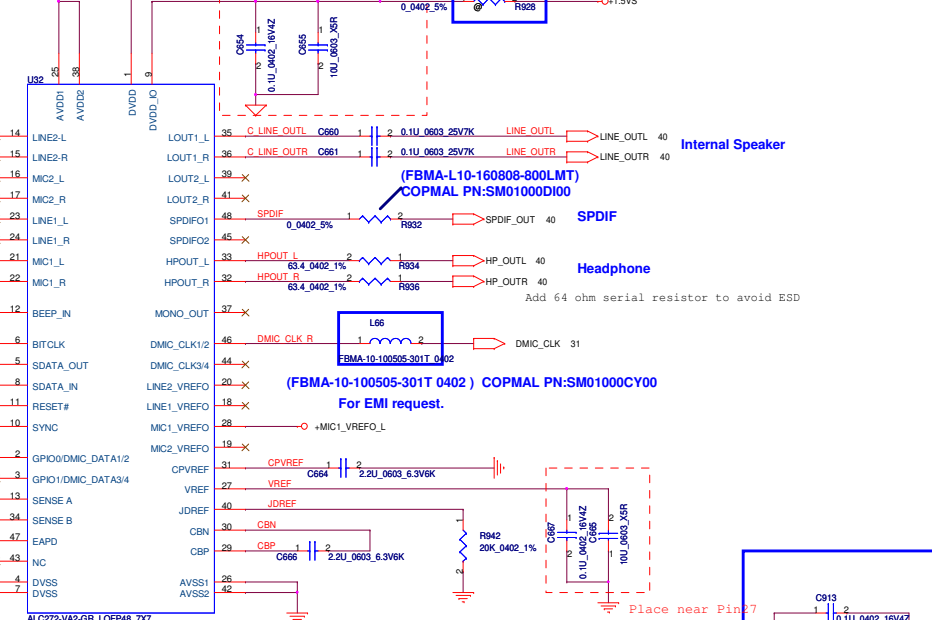
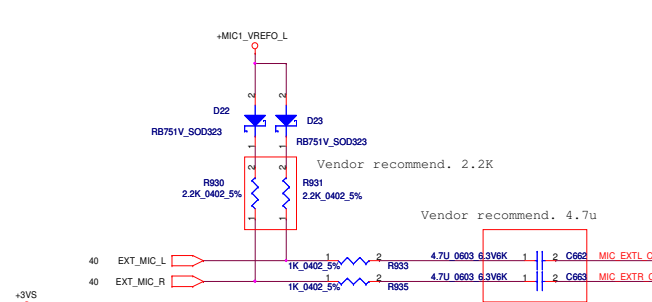
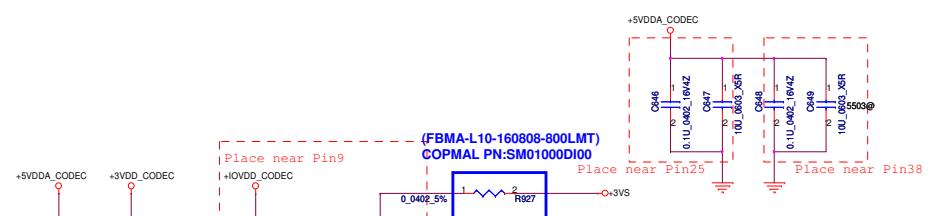
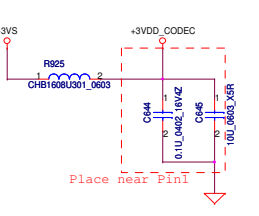
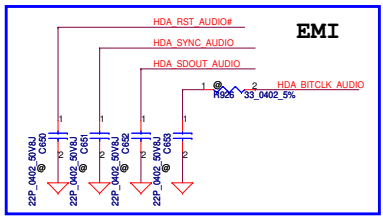
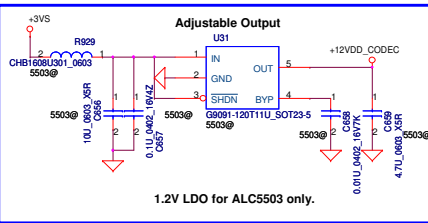
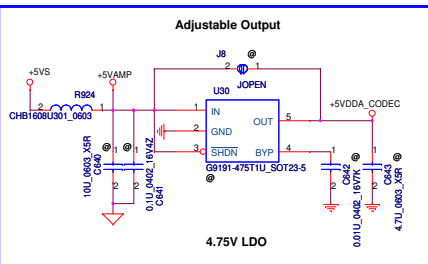
SATA ODD Conn.



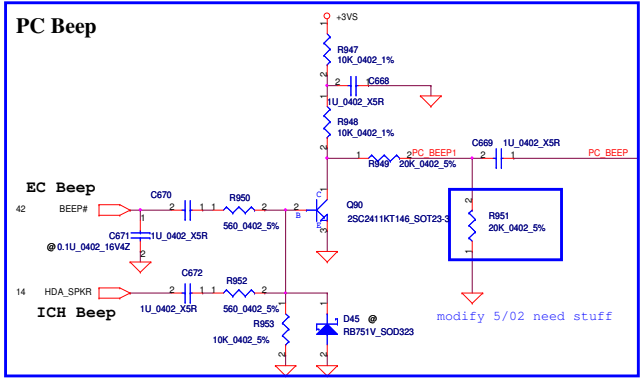
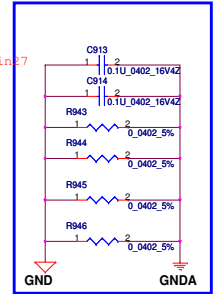
ODD Power Control



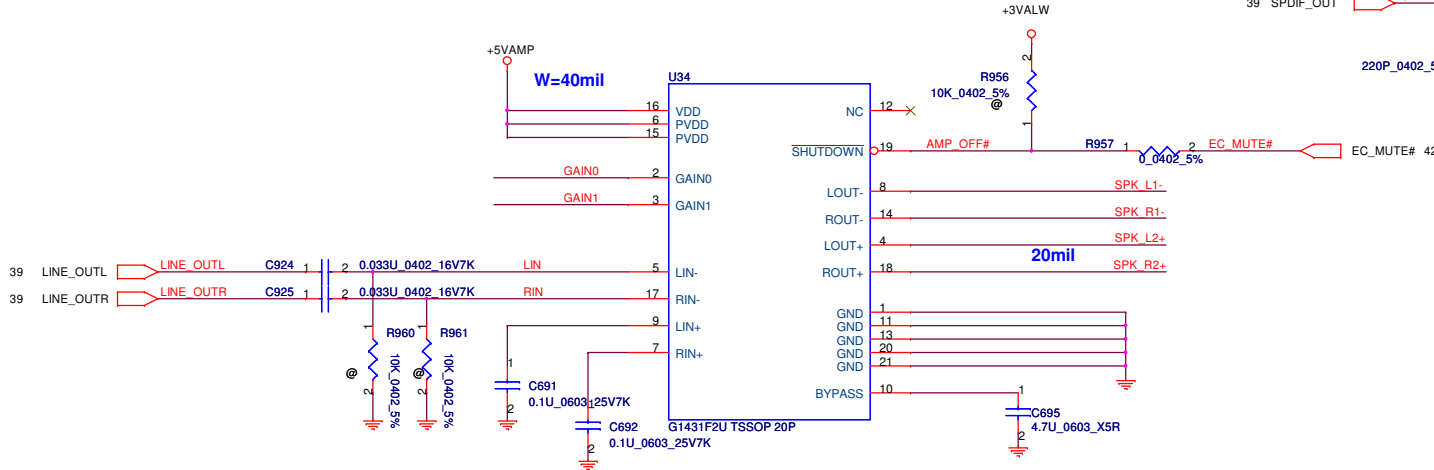
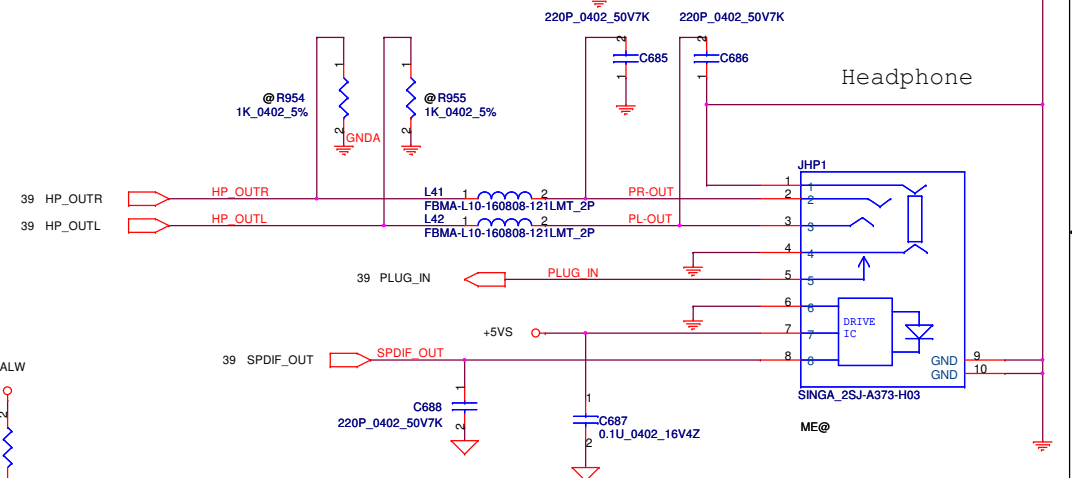
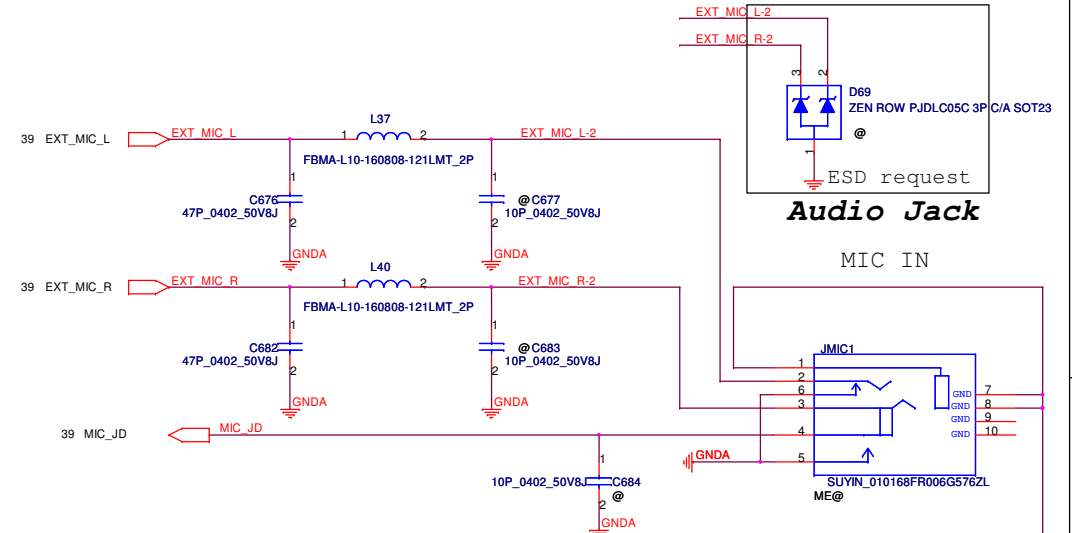
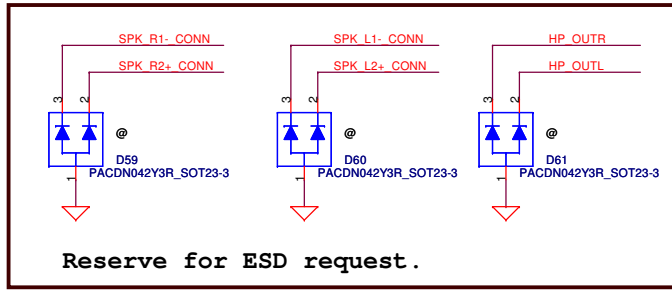
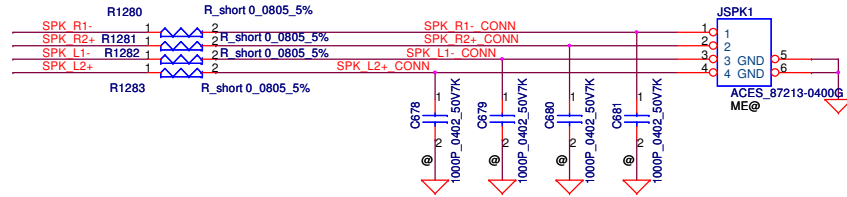
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				Sheet 38 of 60	



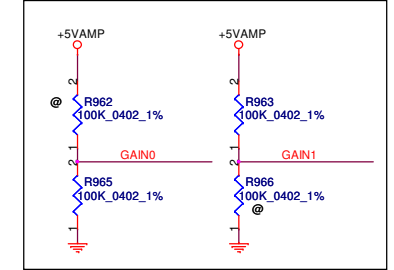
Pin Assignment	Location	Function
LINE-OUT (Pin35/36)	Internal	Int Speaker
Capless HP-OUT (Pin32/33)	External	Headphone out
LINE1 (Pin23/24)	External	Line in
MIC1 (Pin21/22)	External	Mic in
MIC2 (Pin16/17)	Internal	Internal Mic



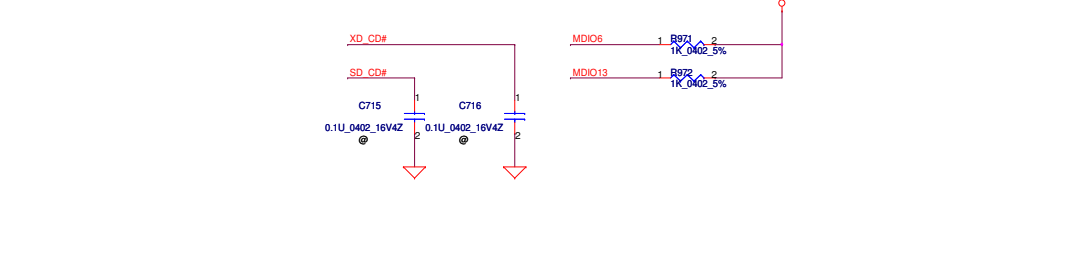
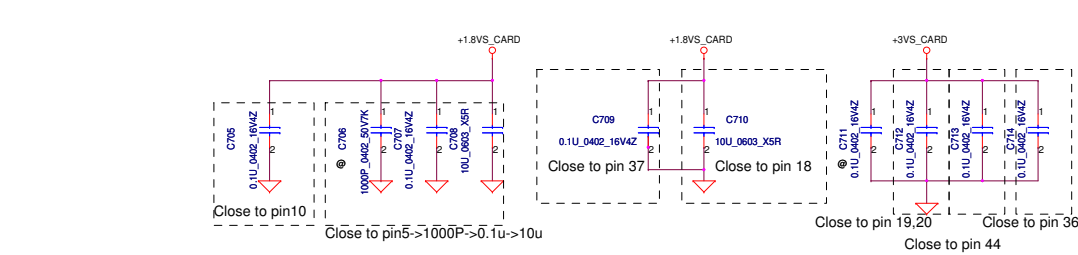
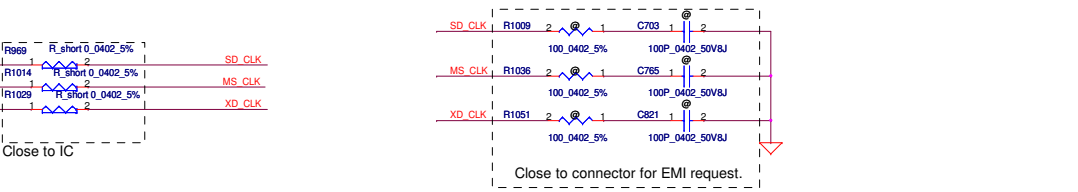
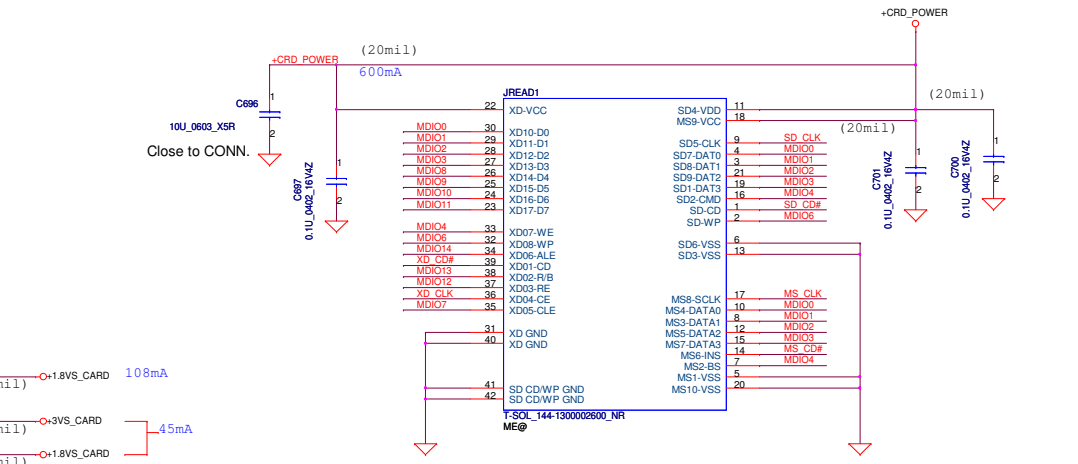
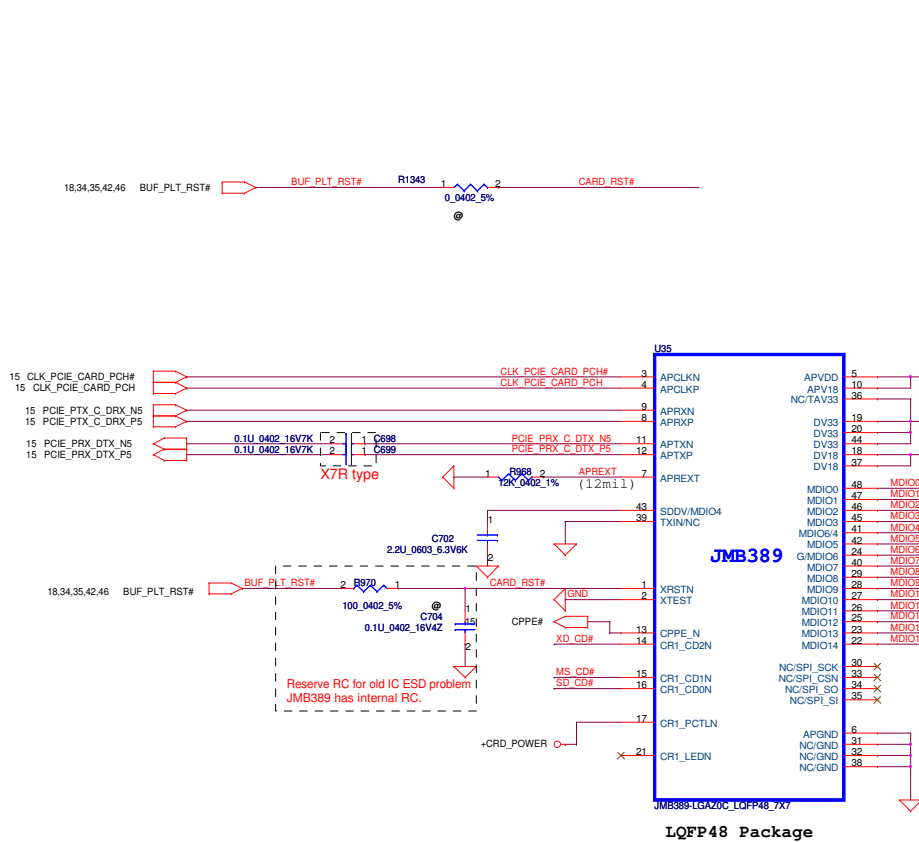
wide 20MIL



GAIN0	GAIN1	Gain
0	0	6dB
0	1	10dB
1	0	15.6dB
1	1	21.6dB

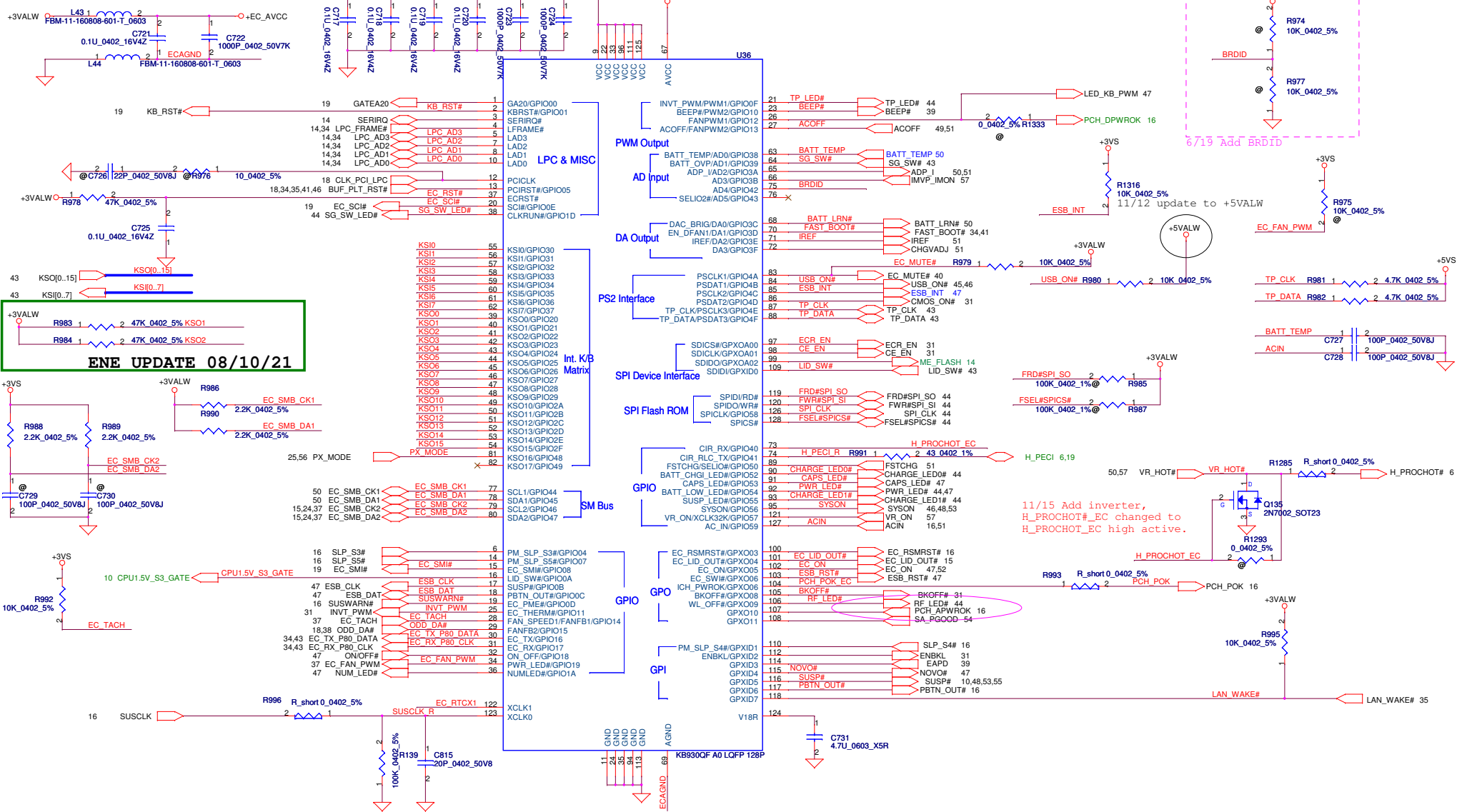


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Size	Custom	Document Number	KIWB1/B2_LA4601P	Rev	1.0
Date:	Wednesday, May 11, 2011	Sheet	40	of	60

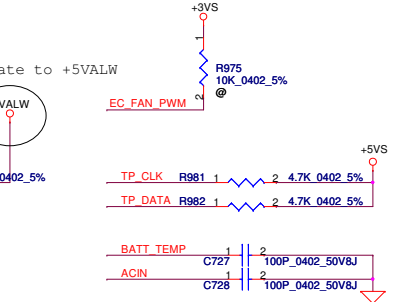
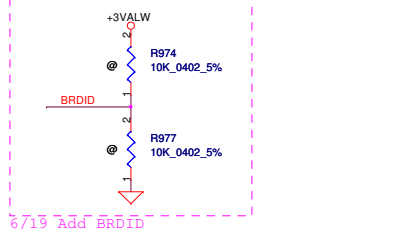


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Custom	QIY2 LA6884P	1.0			

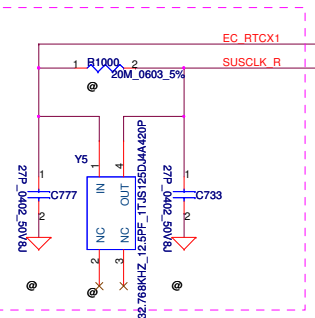
10/02 Change to SM01000550.



ENE UPDATE 08/10/21



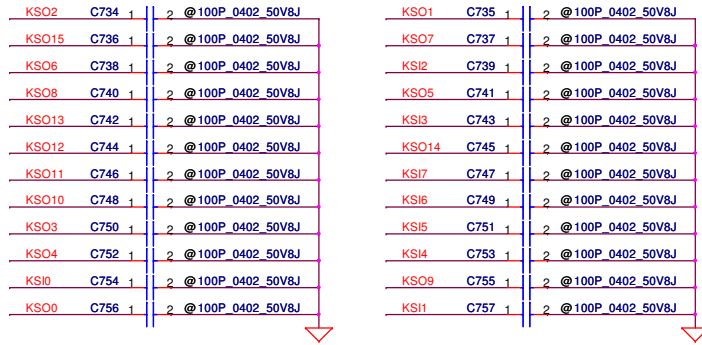
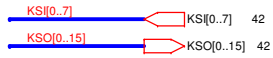
11/15 Add inverter, H_PROCHOT#_EC changed to H_PROCHOT_EC high active.



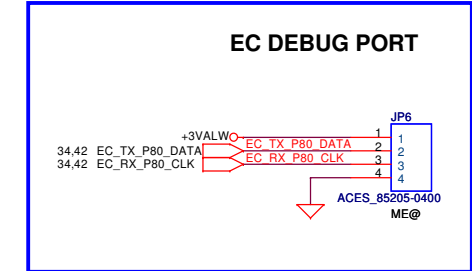
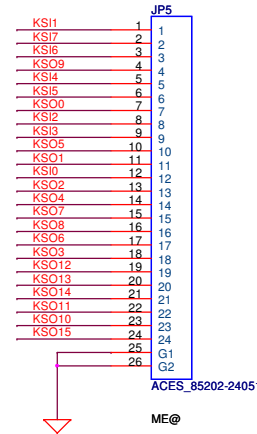
6/15 add XTAL

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Customer	Document Number			Rev	
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				BIOS & EC I/O Port	
				QIQY2 LA6884P	

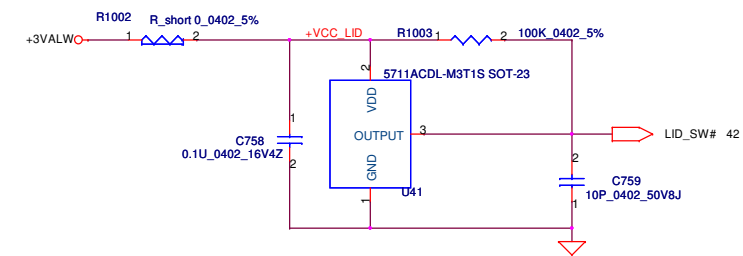
INT_KBD Conn.



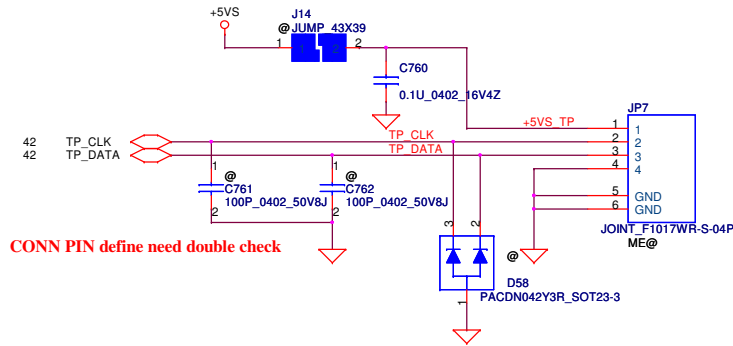
CONN PIN define need double check



Lid Switch

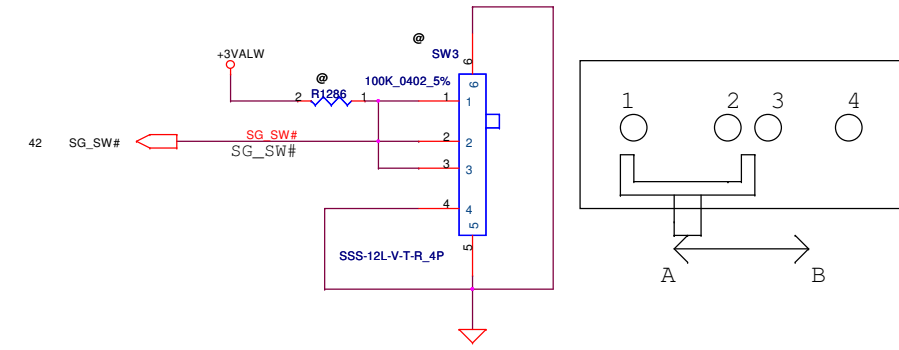
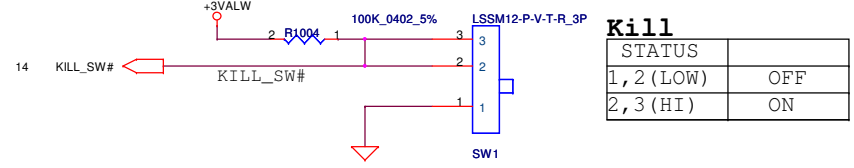


To TP/B Conn.

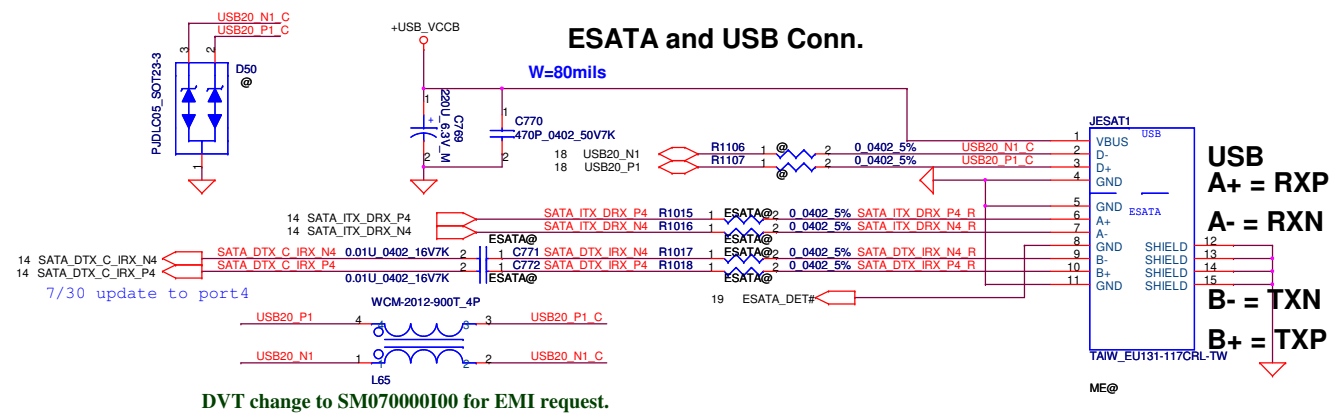
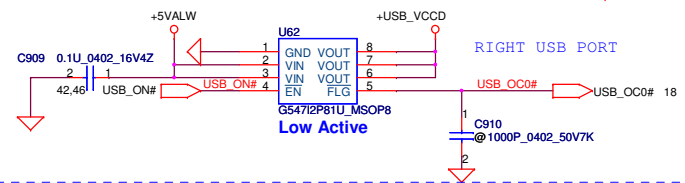
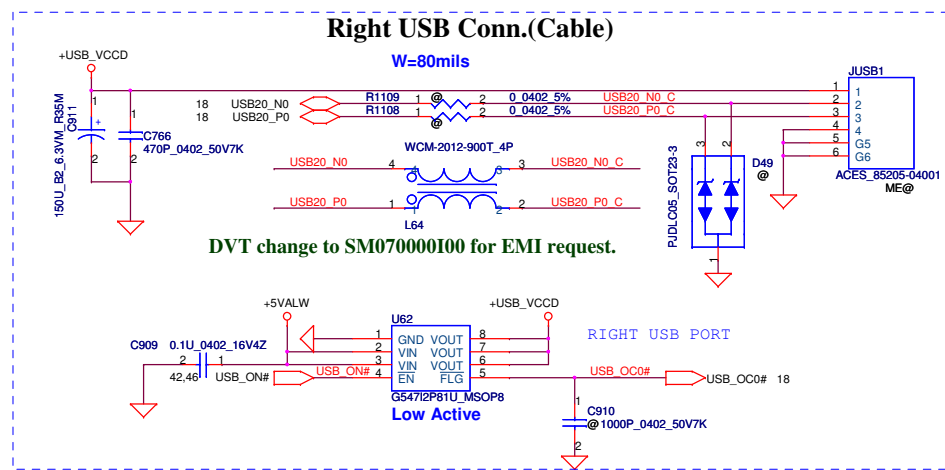
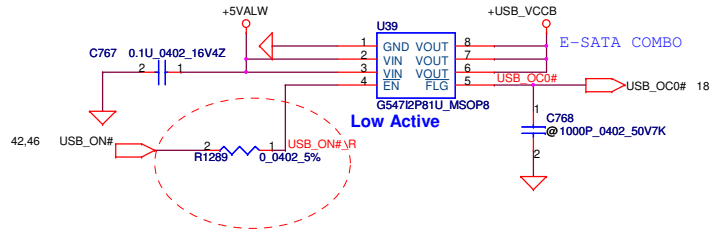


CONN PIN define need double check

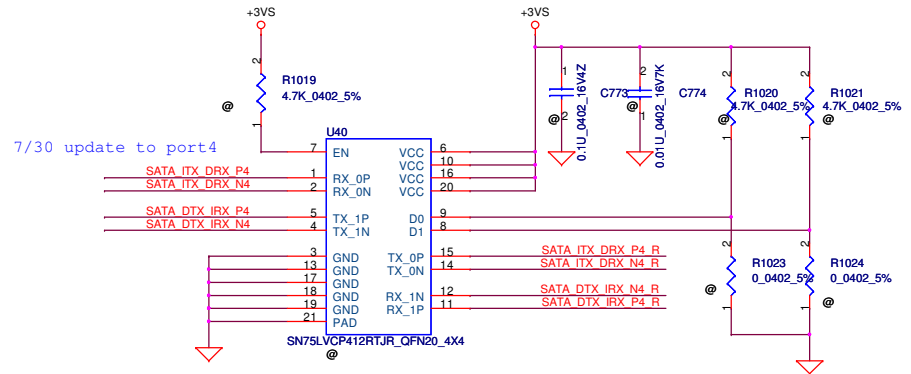
Kill Switch



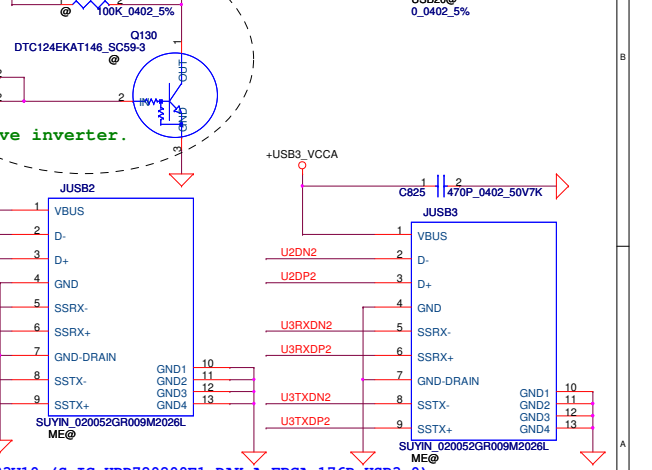
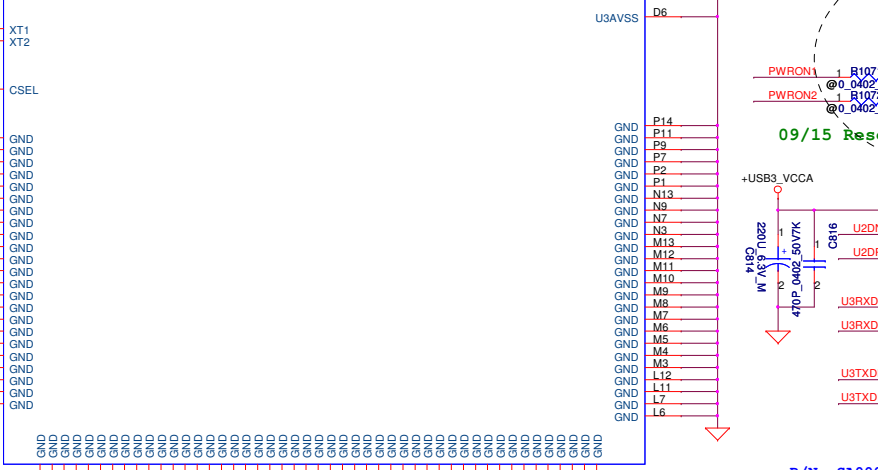
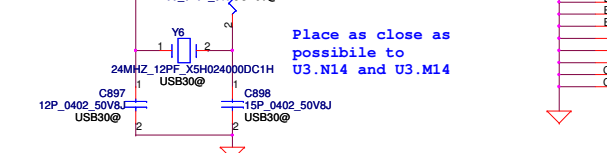
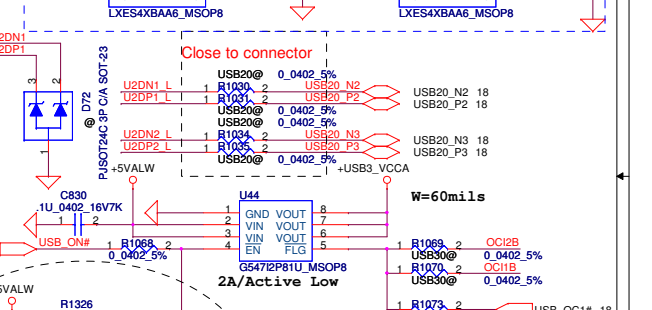
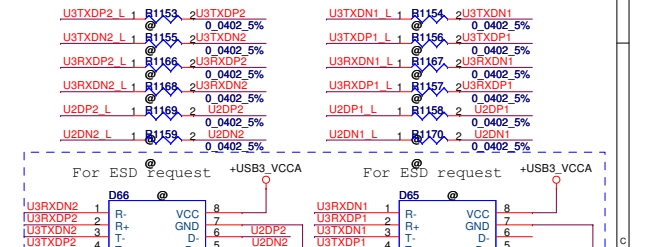
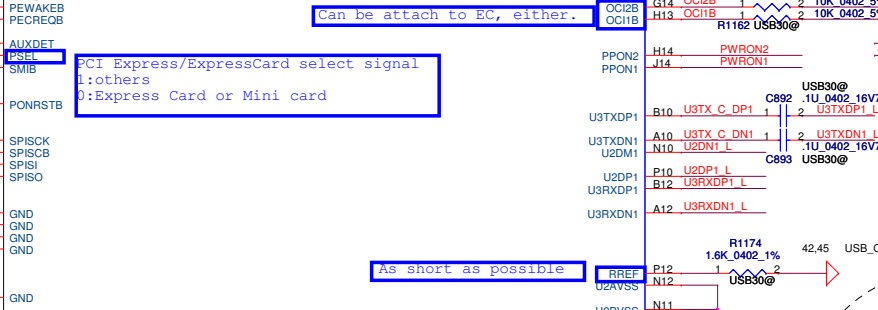
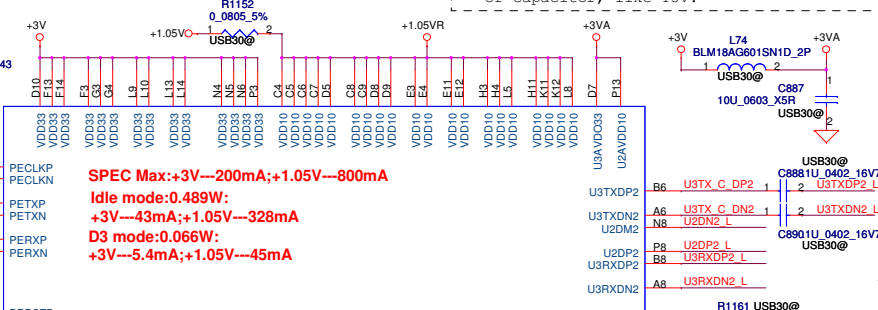
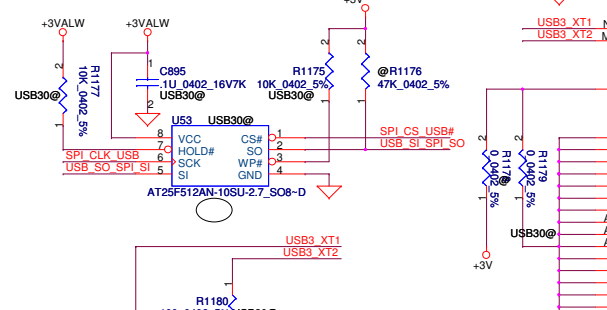
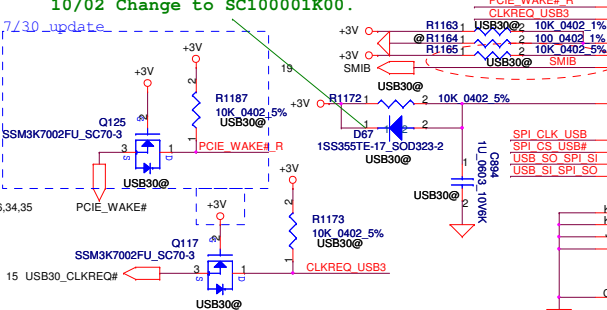
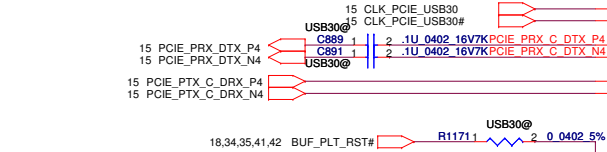
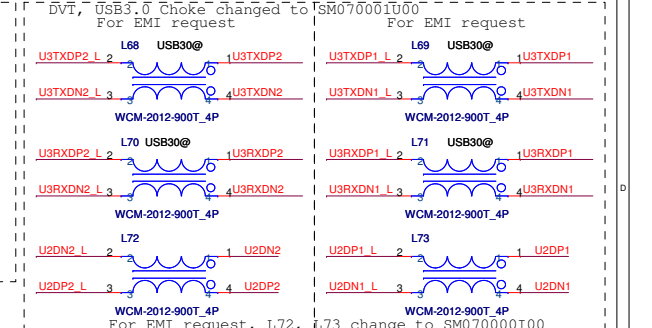
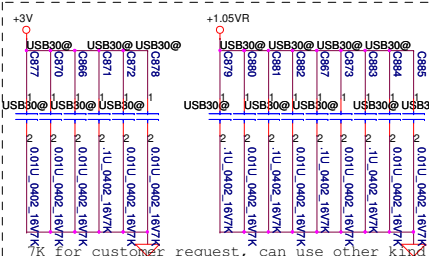
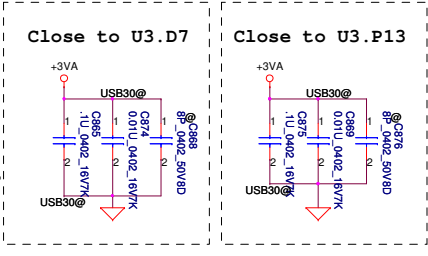
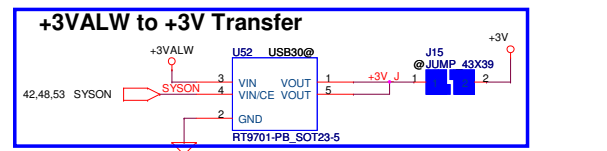
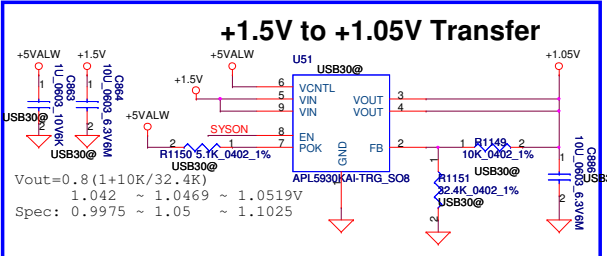
Security Classification		Compal Secret Data		Title	
Issued Date	2010/11/30	Deciphered Date	2011/08	Compal Electronics, Inc. KB /SW /LPC Debug Conn.	
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USB
A+ = RXP
A- = RXN
B- = TXN
B+ = TXP



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Size	Document Number	Rev		1.0	
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Pin compare table for support USB remote wakeup or not

	AUXDET(Pin J2)	CSEL(Pin P6)	CLK
Support USB remote wakeup	pull high 10k to VDD33	Tied to GND	Must use 24MHz crystal: mount Y1,R19,C40,C41
Not support USB remote wakeup	Tied to GND	pull high to VDD33	Can use either 48MHz or 24MHz When use 48MHz clock: mount R22,R25

Security Classification

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Title

USB3.0 PD720200

Size

Customer

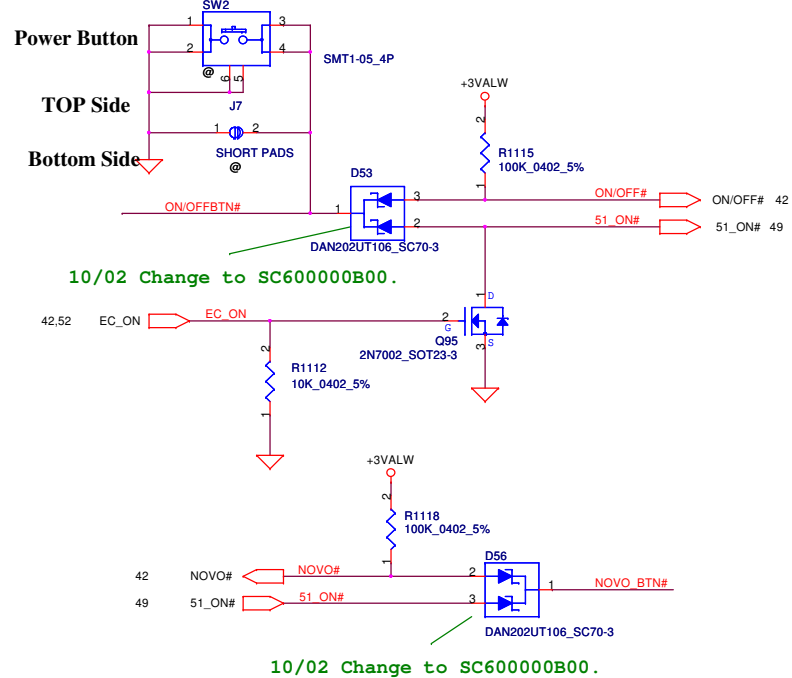
Date

Wednesday, May 11, 2011

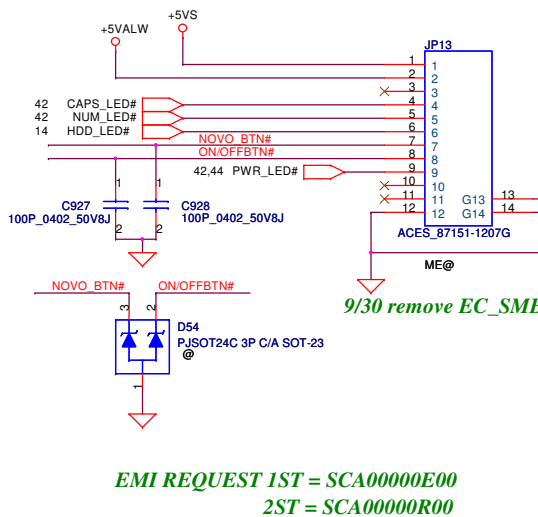
Sheet

46 of 60

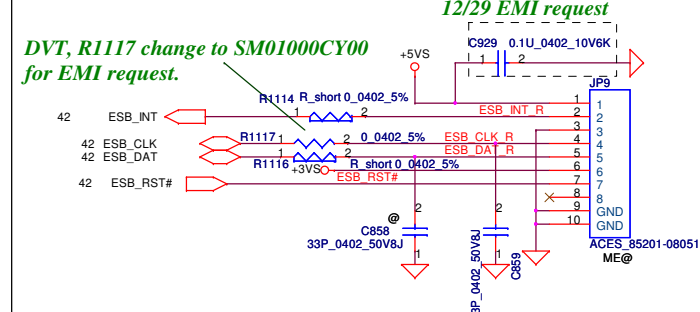
ON/OFF switch



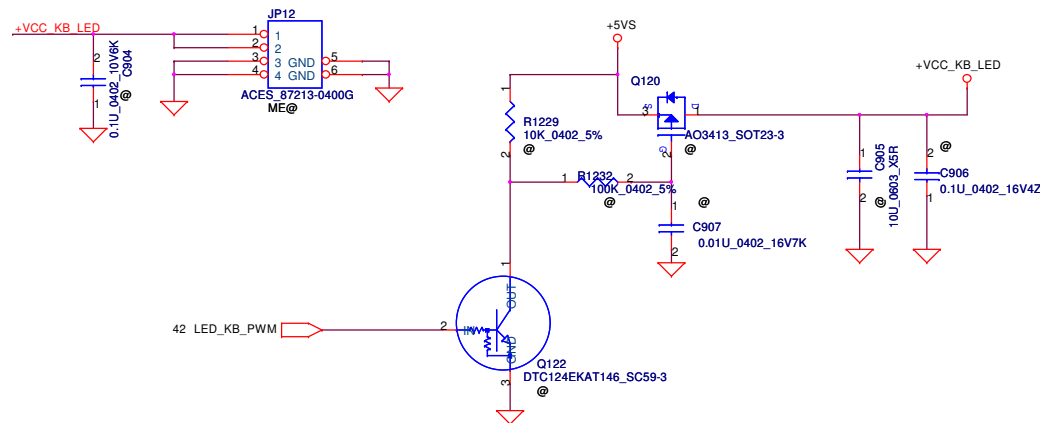
Power Bottom Board Conn. 10pin



Slider Bar Board Mdule Conn. 6pin

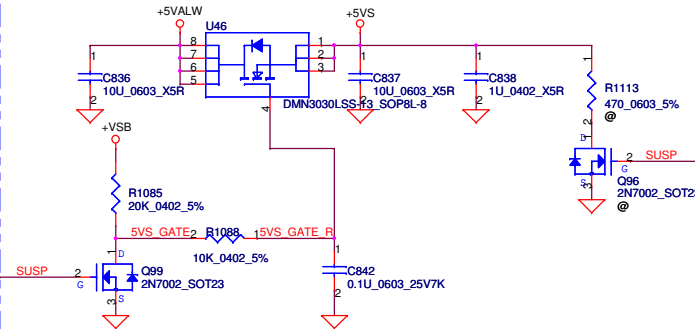


KB Lighting CONN.4pin

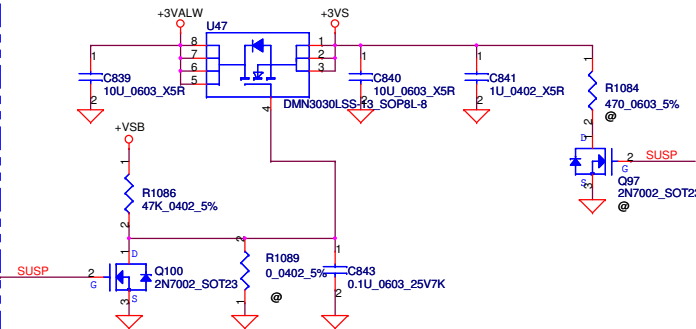


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Size Custom	Document Number	QIQY2 LA6884P		Rev 1.0
Date: Wednesday, May 11, 2011	Sheet	47	of	60

+5VALW TO +5VS

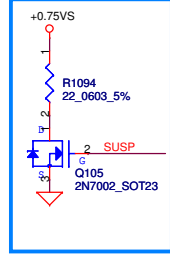
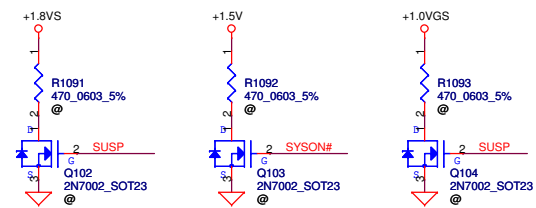
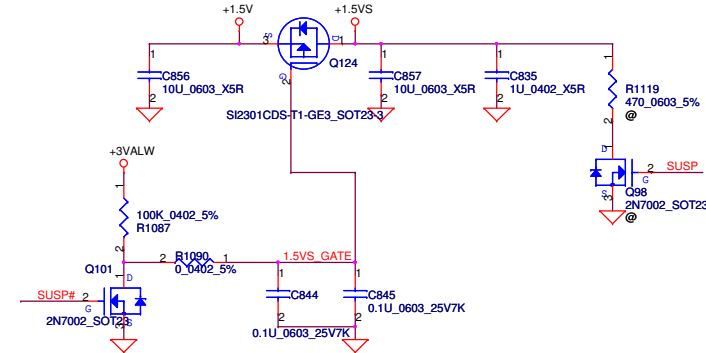


+3VALW TO +3VS

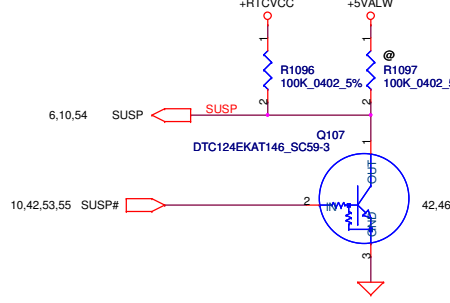
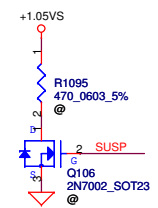


+1.5V to +1.5VS

5/10 change SI2301 to AP2301 GN-HF

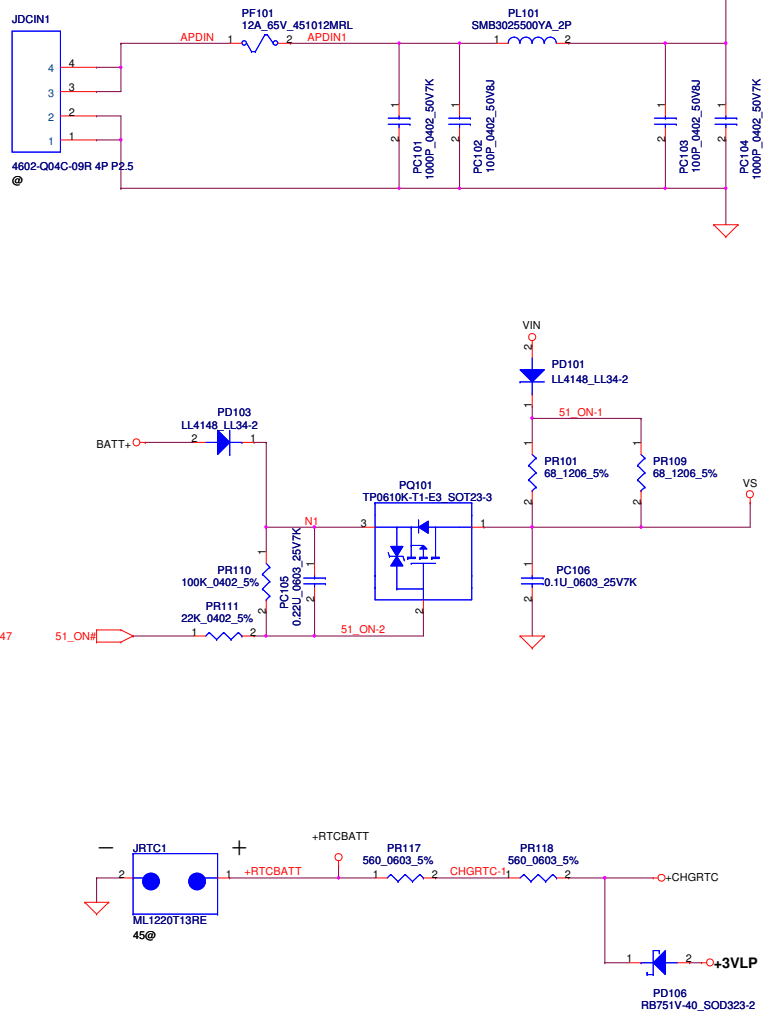


For Intel S3 Power Reduction.

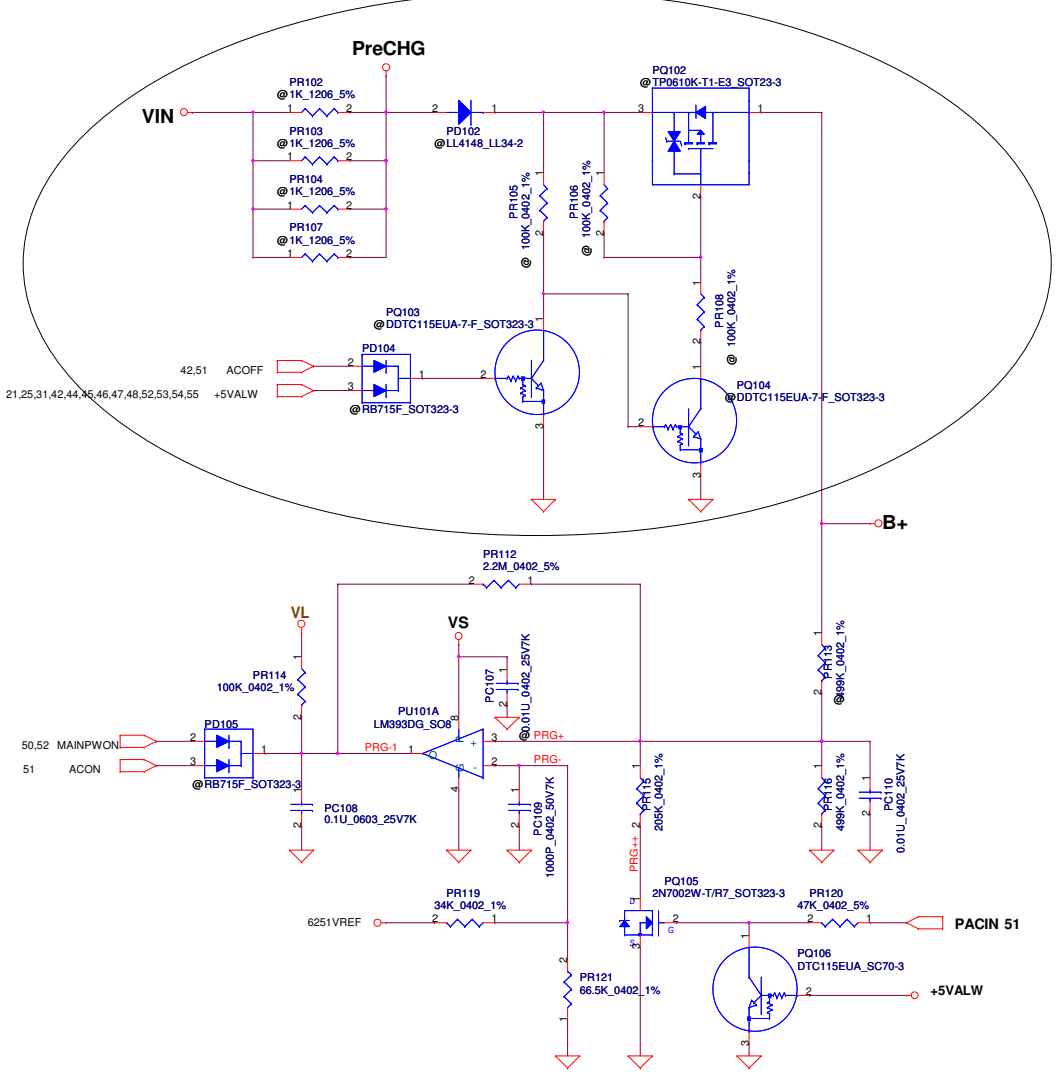


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Size	Document Number	Date		Rev	1.0
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DC030006J00



Precharge detector
15.97V/14.84V FOR
ADAPTOR



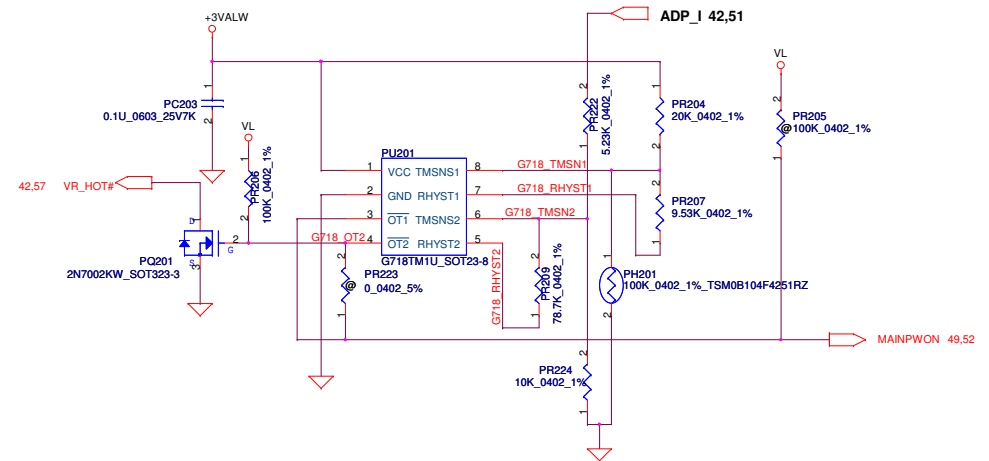
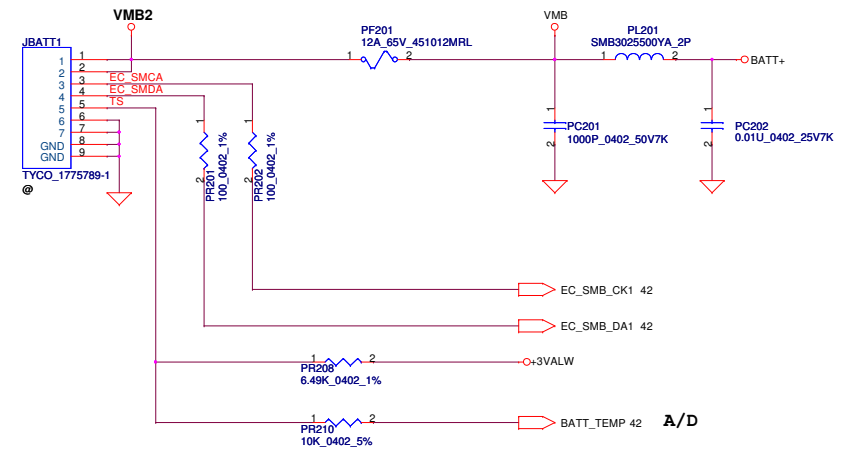
ACIN

Precharge detector		
Min.	typ.	Max.
L-->H	14.991V	15.381V 15.782V
H-->L	13.860V	14.247V 14.621V

BATT ONLY

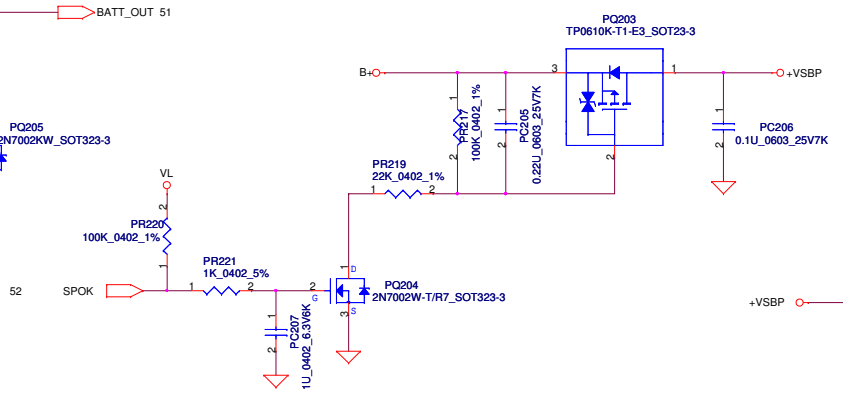
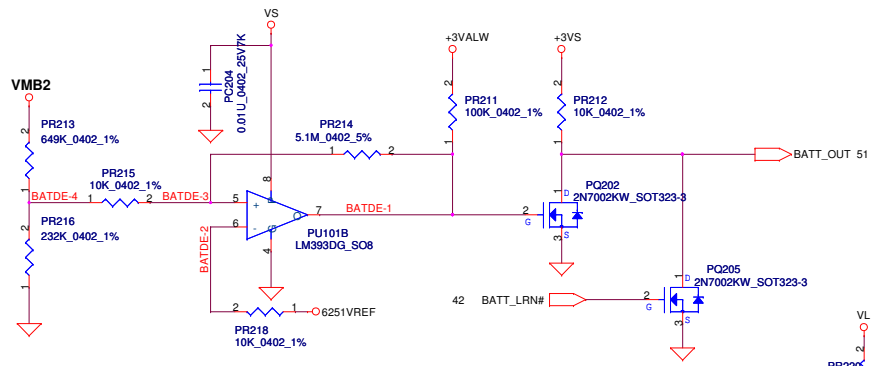
Precharge detector		
Min.	typ.	Max.
L-->H	7.196V	7.349V 7.505V
H-->L	6.138V	6.214V 6.056V

PH201 under CPU bottom side :
 CPU thermal protection at 95 degree C
 Recovery at 56 degree C



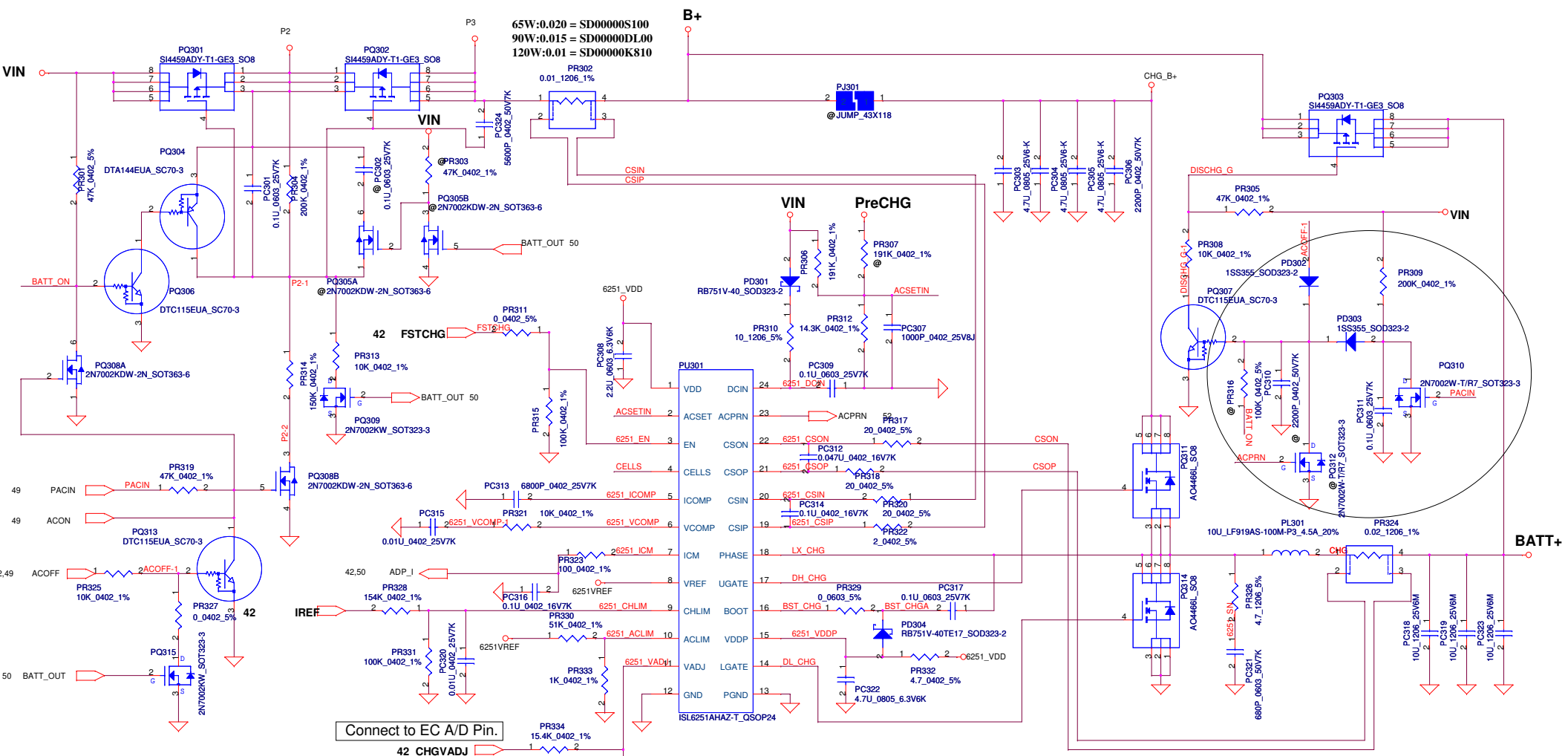
PR209
 90W : 20.5K ohm
 120W : 78.7K ohm

PR222
 90W : 6.65K ohm
 120W : 5.23K ohm



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Compal Electronics, Inc. PWR-BATTERY CONN/OTP		
Title	PIQY0/Y1	
Size	Document Number	Rev
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Connect to EC A/D Pin.
42 CHGVADJ

CHGVADJ=(Vcell-4)/0.10627	
Vcell	CHGVADJ
4V	0V
4.2V	1.882V
4.35V	3.2935V

CC=0.25A-3A
IREF=1.016*Icharge
IREF=0.254V-3.048V
VCHLIM need over 95mV

65W Adapter
 $V_{acli} = 2.39 * (1.96K / (1.96K + 16.9K)) = 0.2484V$
 $I_{in} = (1/0.02) * ((0.05 * V_{acli}) / (2.39 + 0.05))$
 where $V_{acli} = 0.2484V$, $I_{in} = 2.76A$

90W Adapter
 $V_{acli} = 2.39 * (2.87K / (2.87K + 16.9K)) = 0.347V$
 $I_{in} = (1/0.015) * ((0.05 * V_{acli}) / (2.39 + 0.05))$
 where $V_{acli} = 0.347V$, $I_{in} = 3.82A$

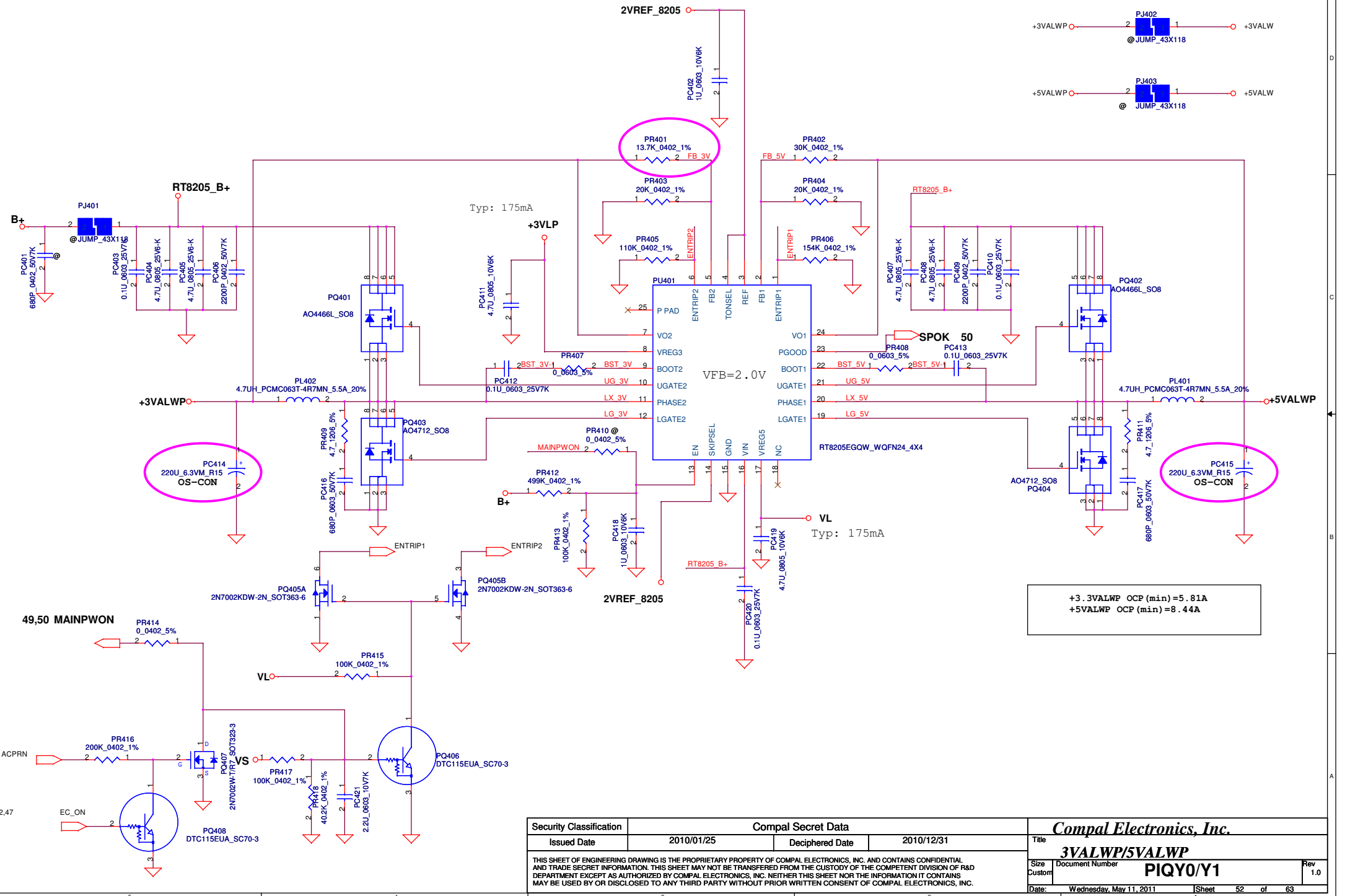
120W Adapter
 $V_{acli} = 2.39 * (1K / (1K + 50K)) = 0.047V$
 $I_{in} = (1/0.01) * ((0.05 * V_{acli}) / (2.39 + 0.05))$
 where $V_{acli} = 0.047V$, $I_{in} = 5.1A$

65W : PR330=16.9K, PR333=1.96K
 90W : PR330=16.9K, PR333=2.87K
 120W : PR330=50K, PR333=1K

3cell : GND
 4cell : VDD

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				Document Number
				PIQY0/Y1
				Rev
				1.0
				Date: Wednesday, May 11, 2011
				Sheet 51 of 63

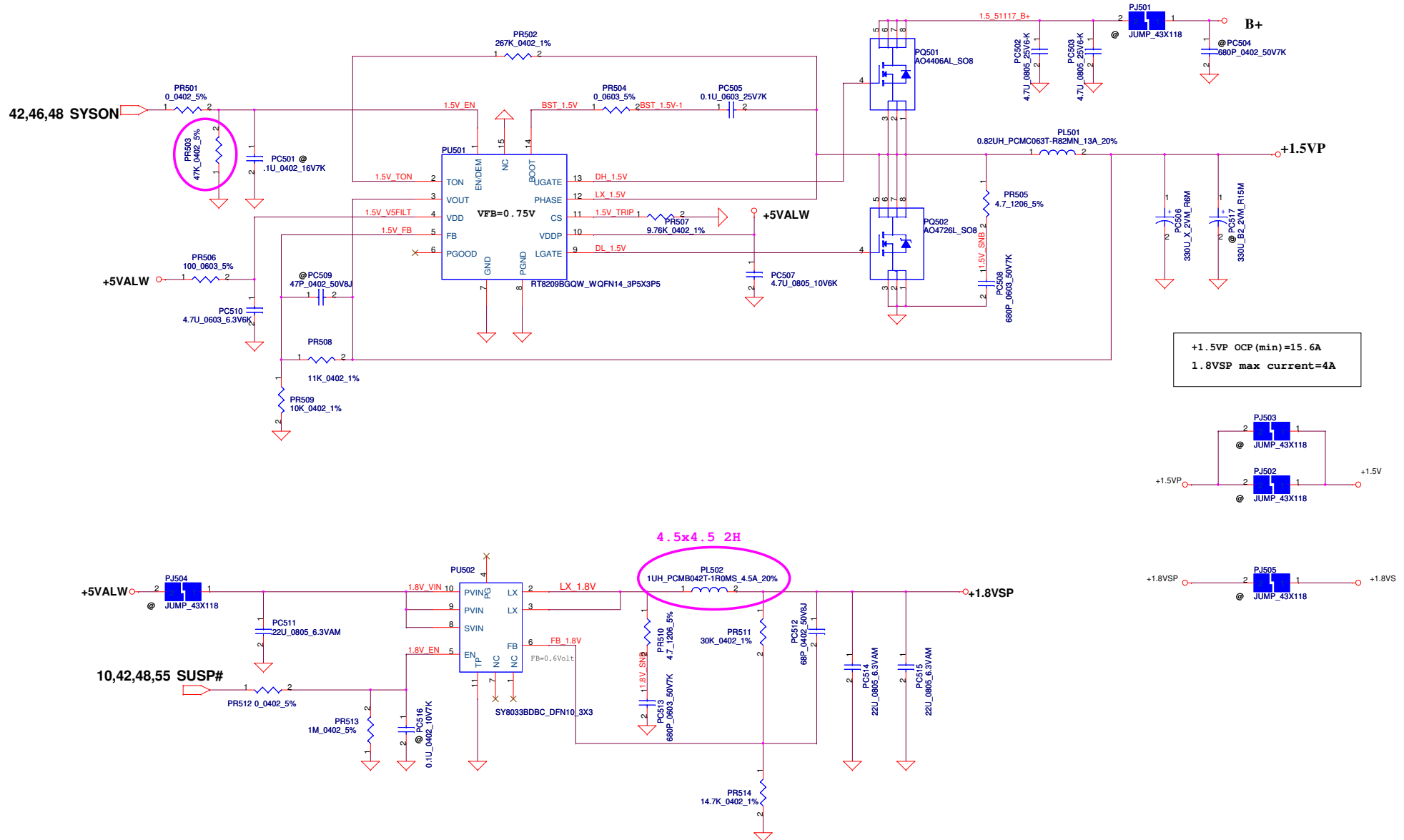
Note:
 Use TPS51125 IC can remove RTC referenece LDO
 Use TPS51427 IC must keep RTC referenece LDO



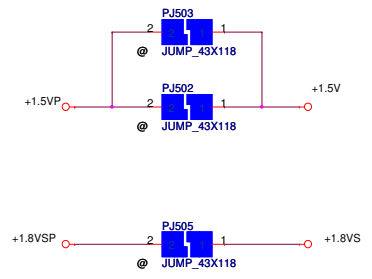
+3.3VALWP OCP (min)=5.81A
 +5VALWP OCP (min)=8.44A

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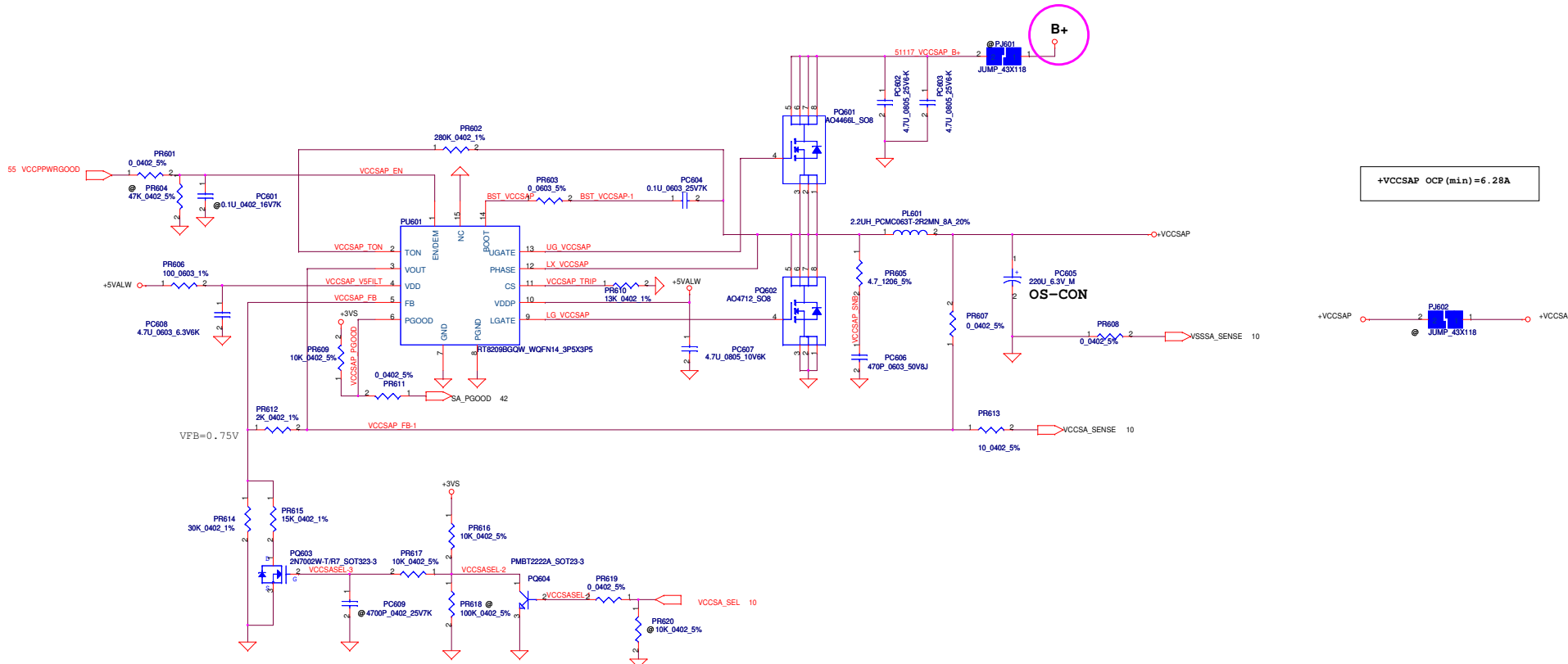
Compal Electronics, Inc.			
Title	3VALWP/5VALWP		
Size	Document Number	PIQY0/Y1	
Custom			Rev 1.0
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+1.5VP OCP (min)=15.6A
1.8VSP max current=4A



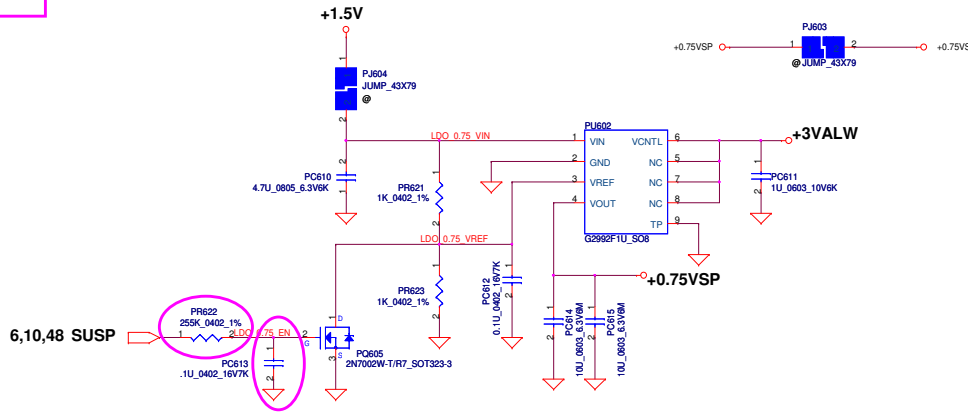
Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2010/01/25	Deciphered Date	2010/12/31	Title	PWR-+1.5VP/+1.8VSP
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				Custom	PIQY0/Y1
				Date:	Wednesday, May 11, 2011
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				Rev	1.0

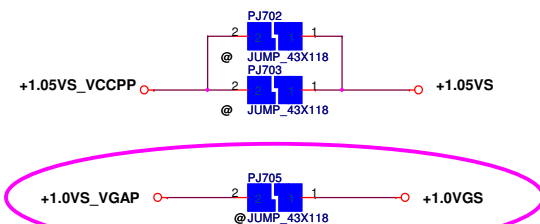
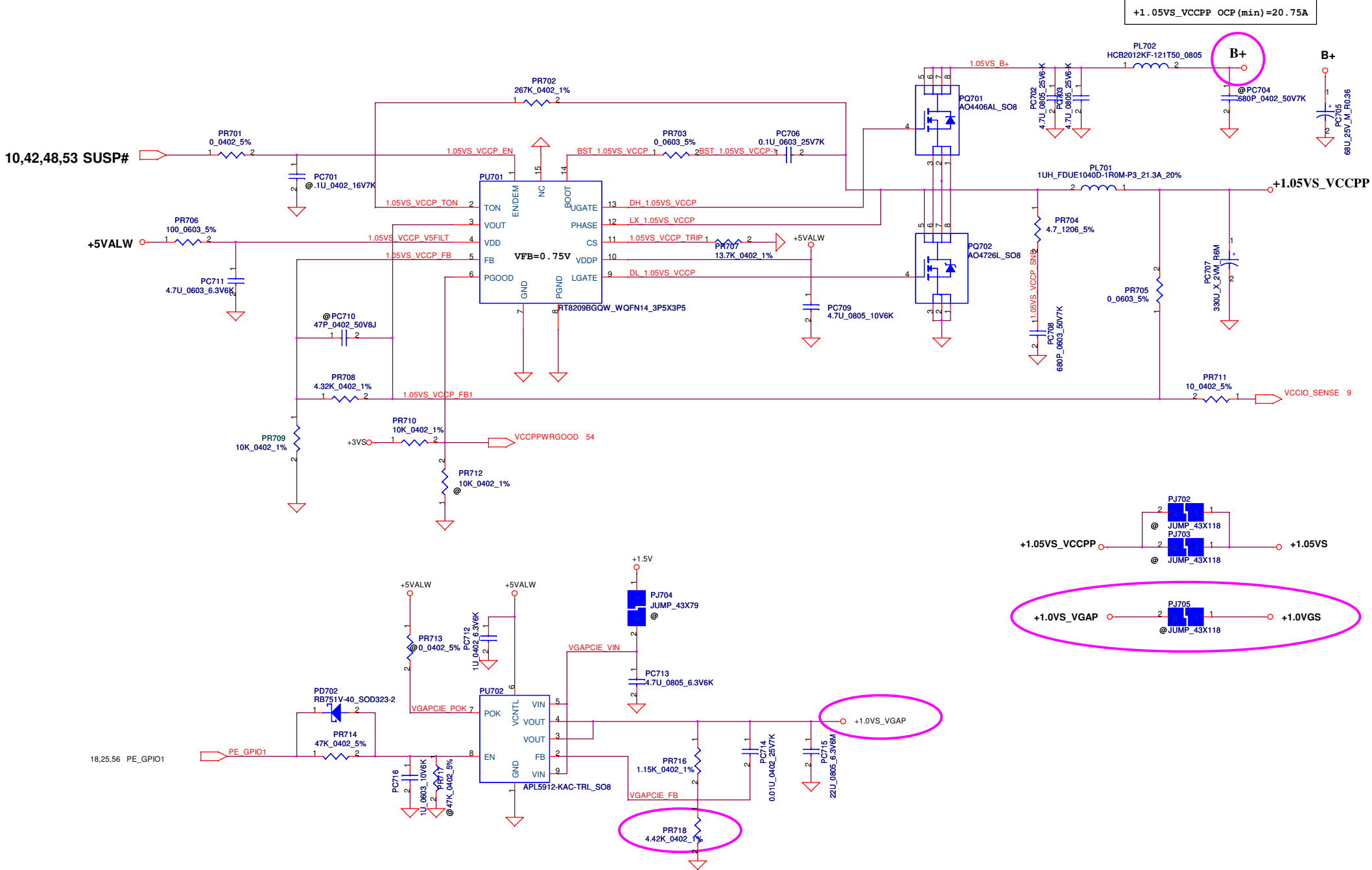


+VCCSAP OCP (min)=6.28A

VID[0]	VID[1]	VCCSA Vout	Require on 2011/ 2012 Required
0	0	0.9 V	Yes/Yes
0	1	0.8 V	Yes/Yes
1	1	0.75V	No/Yes
1	1	0.65V	No/Yes

Note: Use VCCSA_SEL to switch High & Low Level for VID[1] (ie. VCCSA_SEL) due to the VID[0] is don't care for this setting.

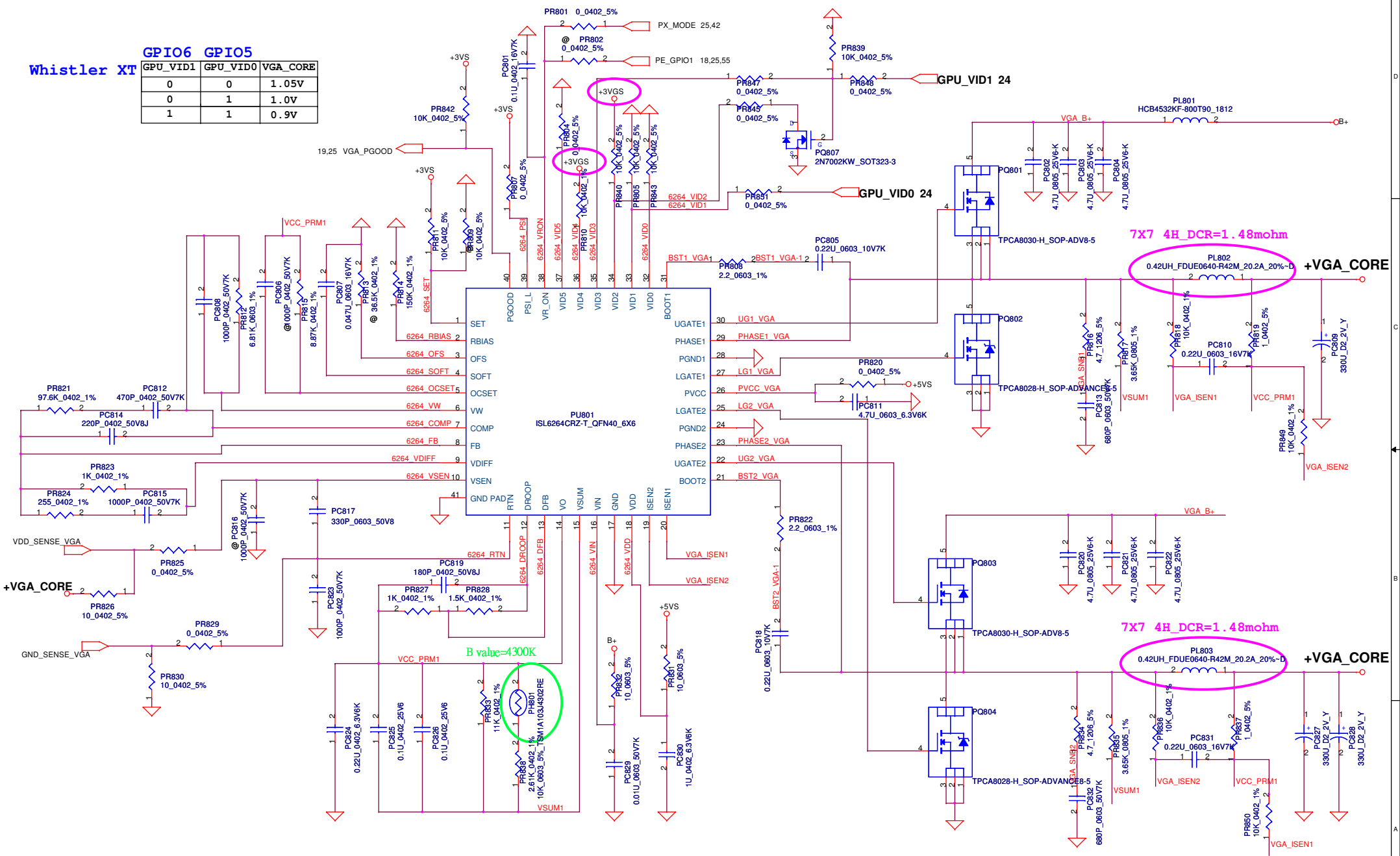




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Whistler XT

GPIO6	GPIO5	VGA_CORE
0	0	1.05V
0	1	1.0V
1	1	0.9V

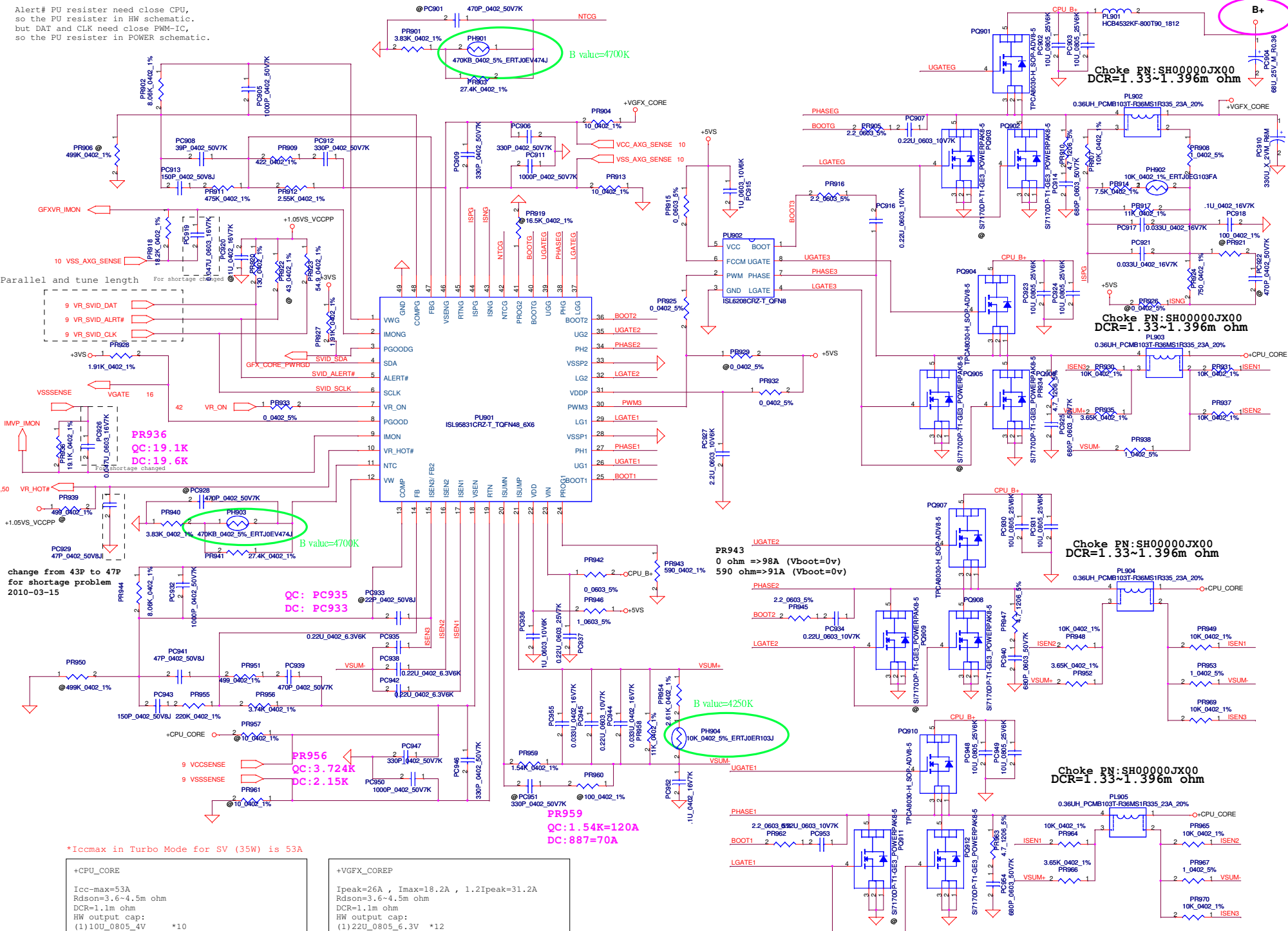


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Compal Electronics, Inc.		
Power-VGA_CORE		
Title	PIQY0/Y1	
Size	Document Number	Rev
Custom		1.0
Date:	Wednesday, May 11, 2011	Sheet 56 of 63

Alert# PU resistor need close CPU,
so the PU resistor in HW schematic.
but DAT and CLK need close IM-IC,
so the PU resistor in POWER schematic.



Parallel and tune length
For shortage changed

change from 43P to 47P
for shortage problem
2010-03-15

*Iccmax in Turbo Mode for SV (35W) is 53A

+CPU_CORE
Icc-max=53A
Rdson=3.6-4.5m ohm
DCR=1.1m ohm
HW output cap:
(1) 10U_0805_4V *10
(2) 22U_0805_6.3V *15
(3) 470U_D2_2V *4 (ESR=4.5m ohm)

*OCP setting value=71.5A

+VGFX_COREP
Ipeak=26A, Imax=18.2A, 1.2Ipeak=31.2A
Rdson=3.6-4.5m ohm
DCR=1.1m ohm
HW output cap:
(1) 22U_0805_6.3V *12
(2) 470U_D2_2V *2 (ESR=4.5m ohm)

*OCP setting value=37A

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Compal Electronics, Inc.		
Title PWR +CPU_CORE+/VGFX_CORE		
Size	Document Number	Rev
Customer	PIQY0/Y1	1.0
Date:	Wednesday, May 11, 2011	Sheet 57 of 63

Version change list (P.I.R. List)

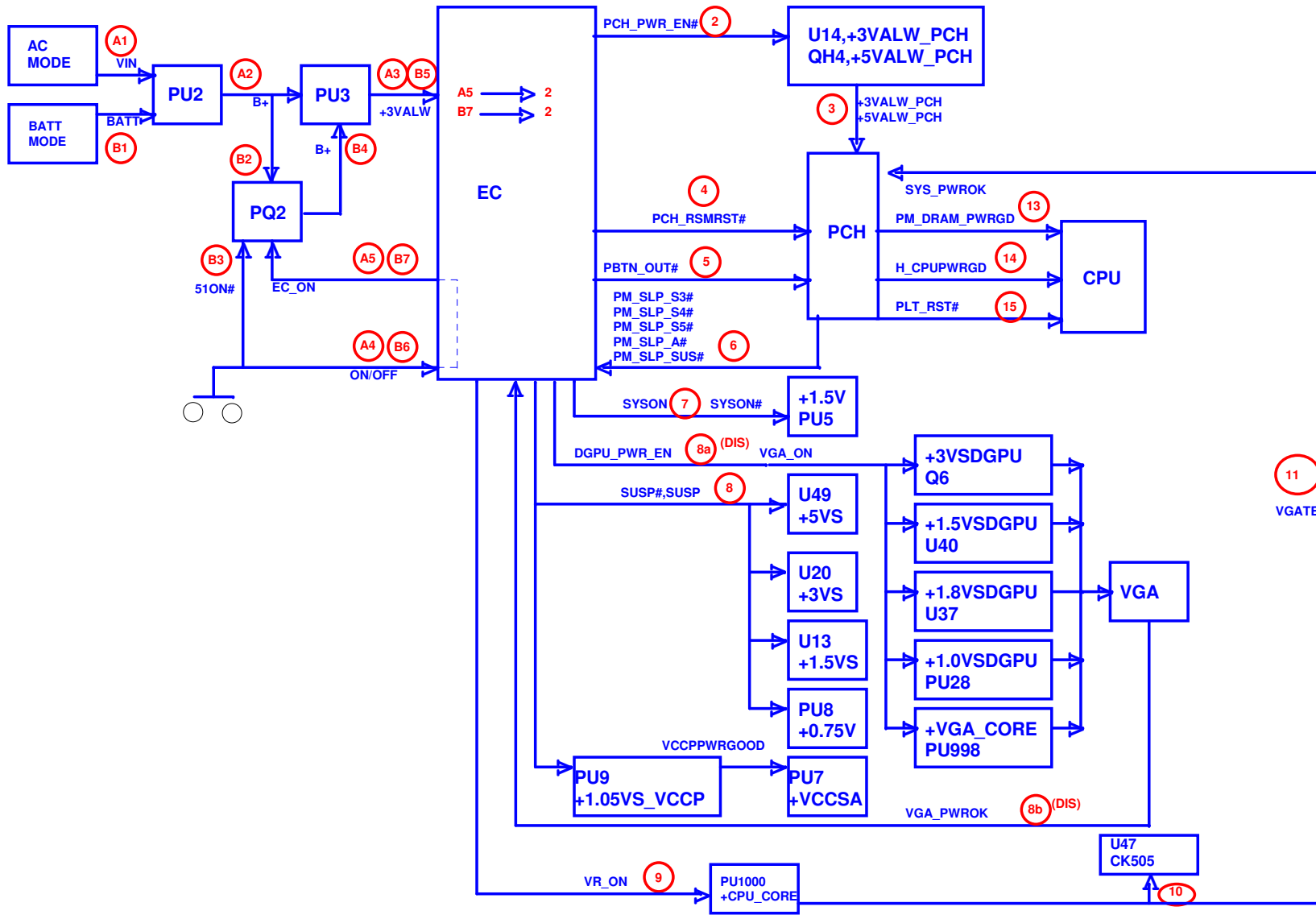
Item	Reason for change	PG#	Modify List	Date	Phase
1	Disable Pre-charge		PR102, PR103, PR104, PR107, PD102, PD104, PQ102, PQ103, PQ104		
2	Back to Back MOS change		PQ301, PQ302, PQ303		
3	Battery Learning function		PQ205		
4					
5					
6					
7					
8					
9					
10					
11					
12					
13					
14					
15					
16					
17					

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				Size Custom	Document Number
Date:				Wednesday, May 11, 2011	Sheet 58 of 63

QIQY2 HW PIR List

NO	DATE	PAGE	MODIFICATION LIST	PURPOSE
----- SDV				
1		P23	Change U8 to SA00004R500	Customer need to change
2		P23	Del C998/ C1067/ C343/ C339/ C382/ C376/ C377/ R692	For VGA function
3		P23	Change Q69 to Q75, Q68 to Q76, U56 to U62, U60 to U64, Q87 to Q109, Q86 to Q110, C732 to C777, U59 to U66, R872 to R890, U37 to U41	For VGA function
4		P42	Del R994/ R999/ D46/ Q91	For EC function
5		P15	Del PJP2/ R67/ R257/ R261	For PCH function
6		P23	Del C386/ C732/ R1400/ R1360	For VGA function
7		P33	Del R354/ R3461/ R1372/ R1373 Del R185/ R434/ Q84/ R1022/ Q92/ CV253-CV260/ Q115/R1144/ R1143/ L67/ R997/ R1321	For HDMI function
8		P44	BT_OFF# change to BT_ON#	For BT function
9		P35	EC_PME# change to LAN_WAKE#	For LAN wake funcion
10		P32	CMOS_OFF# change to CMOS_ON#	For CMOS function
11		P45	U11 from SB000007010 change to SB548000210	For +1.5VGS function
12		P32	D31-D35 from +5VS change to +CRT_VCC_CONN	For CRT function
13		P15	U4 from SA00004EEJ0 change to SA00004EEV0	PCH from rev.B2 change to rev.B3
14		P14	R133 from 0 ohm change to 33 ohm	For BIOS ROM function
----- SIT				
1		P23	Add C69 22uF_0805 cap for +BIF_VDDC.	For vendor feedback
2		P23	Reserve R613 & R614 to pull up +3VS.	For VGA function
3		P23	Change 0 ohm to R_short --- R10/R1002/R1014/R1029/R109/ R11/R1111/R1114/R1116/R1280/R1281/R1282/R1283/R1285/ R130/R1320/R1336/R1342/R1323/R14/R181R19/R190/R193/R196/ R198/R207/R22/R222/R254R256/R258/R26/R260/R263/R265/R269 /R270/R272R273/R274/R276/R277/R278/R281/R282/R283/R284/ R285/R286/R288/R289/R293/R295/R302/R303/R313/R40/R432/ R52/R53/R668/R710/R824/R825/R880/R882/R885/R886/R891/ R922/R941/R969/R973/R993/R996.	For IO/ PCH/ VGA function
4		P45	U39/ U62 /U44 from SA000039E00 change to SA00003TV00.	For USB function
5		P18	Del net name PCH_GPI051 connect to RP1 pin2.	For VGA function
6		P25	C351 from 0.1uF_0603 change to 0.047uF_0402.	For VGA function
7		P10	ADD PJP2 jump.	For PCH function
8		P43	U41 from SA000031C00 change to SA00001TC00.	For LID function
9		P25	R352 from 0 ohm change to 330K ohm.	For VGA function
10		P25	Add C343 & C346 for reserve.	For VGA function
11		P25	Q121 from SBX01240010 change to SB000008J10.	For VGA function

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				Size Custom	Document Number
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				Customer	QIQY2 LA6884P
				Date	Wednesday, May 11, 2011
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