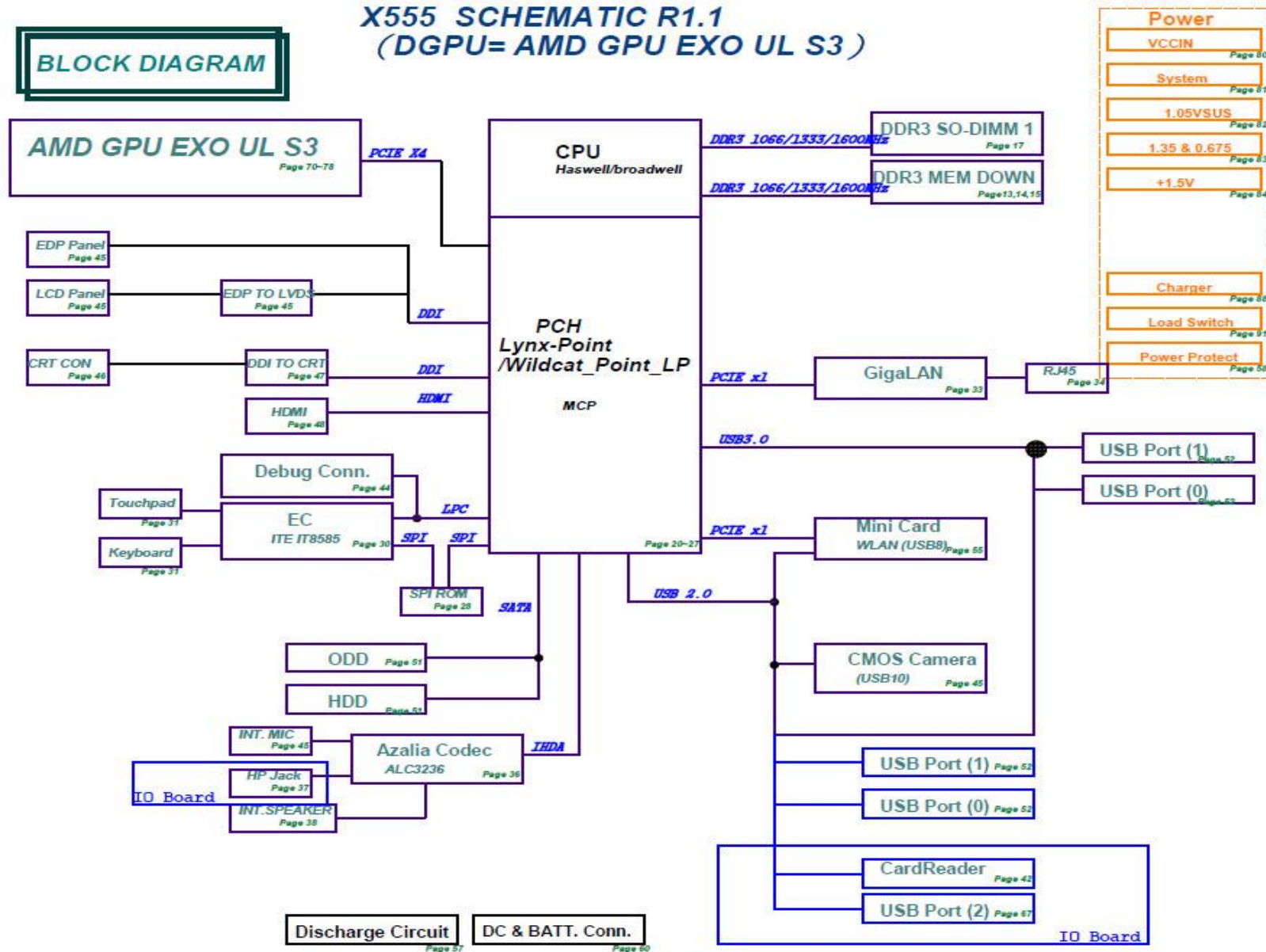
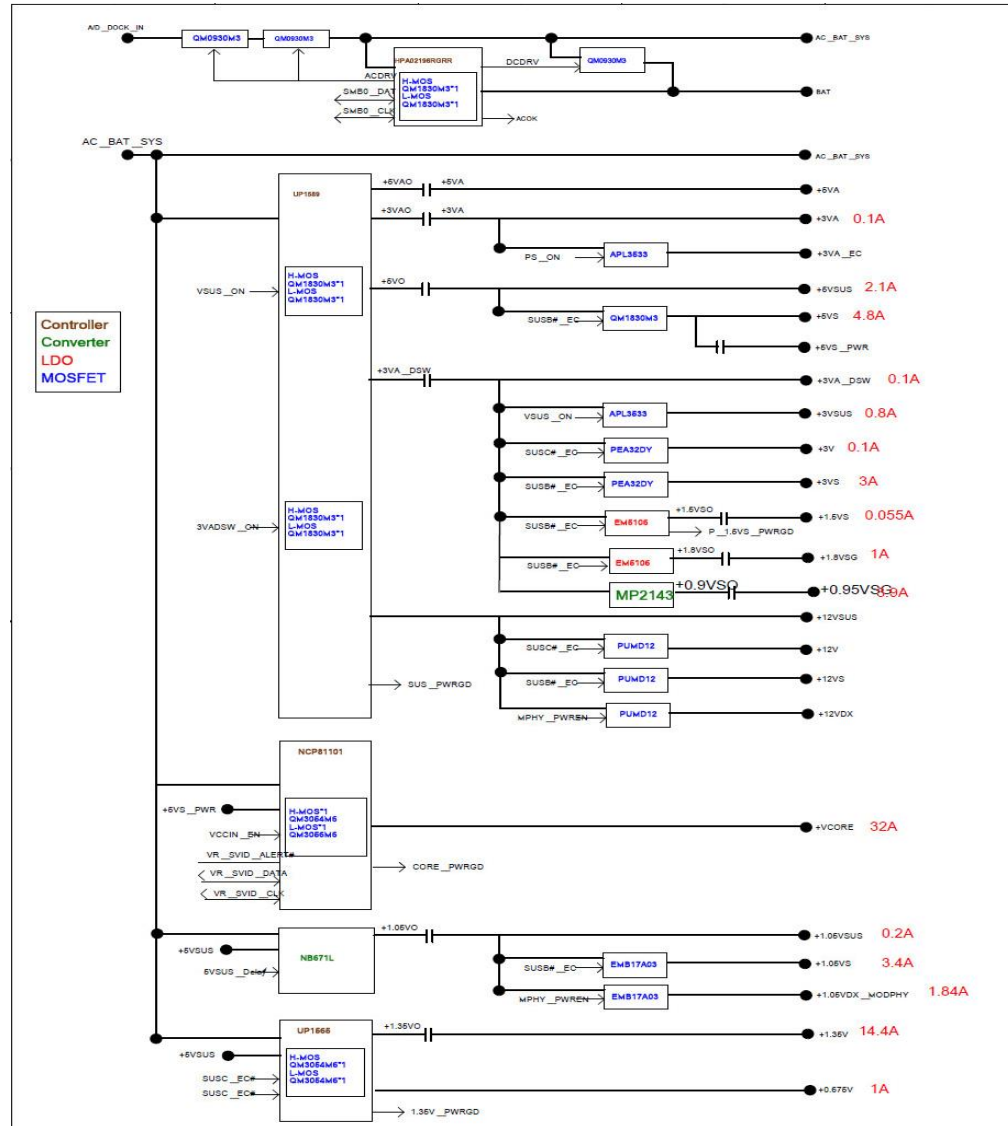


BLOCK DIAGRAM

X555 SCHEMATIC R1.1
(DGPU= AMD GPU EXO UL S3)

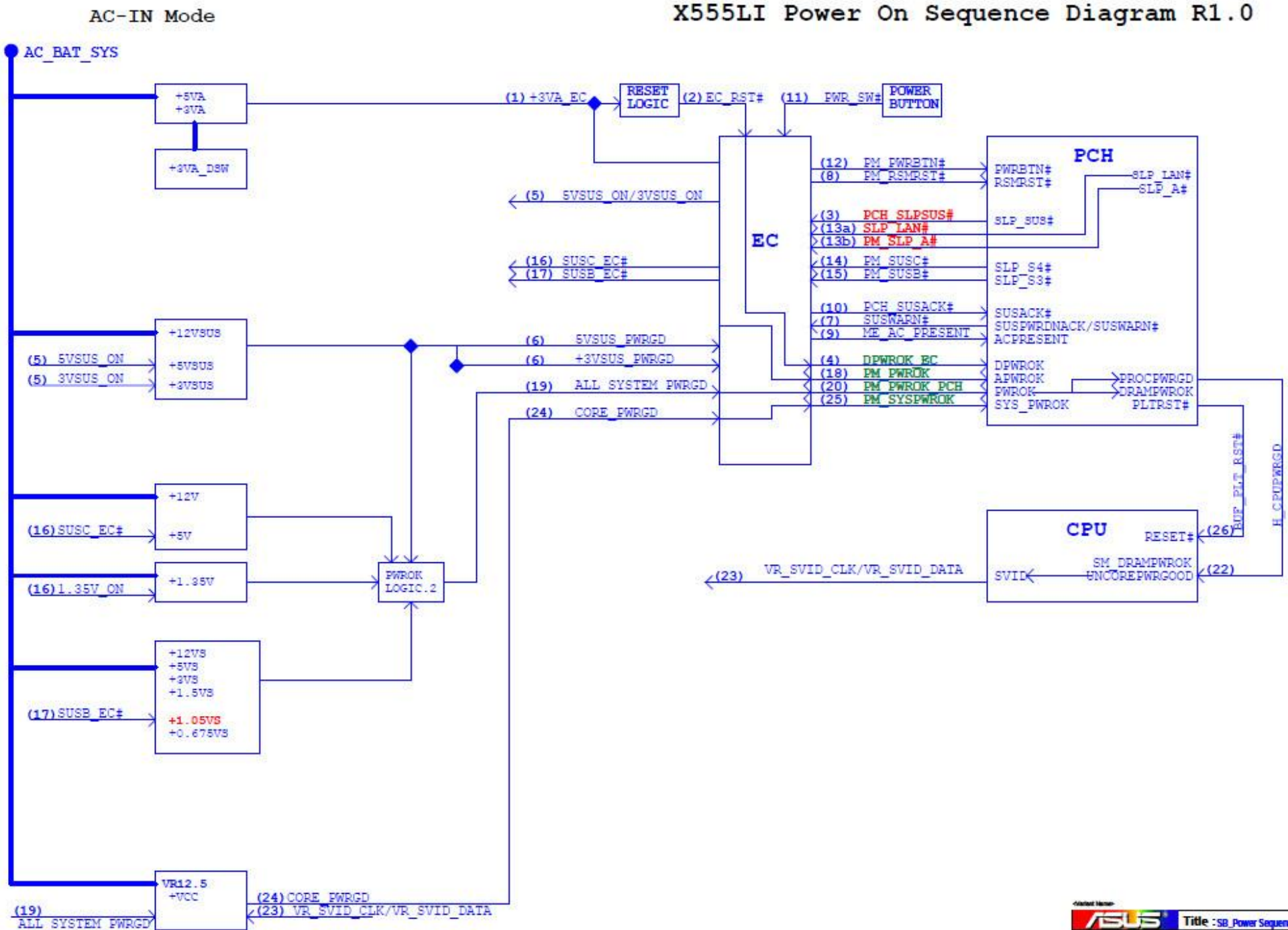


POWER FLOW



POWER ON SEQUENCE

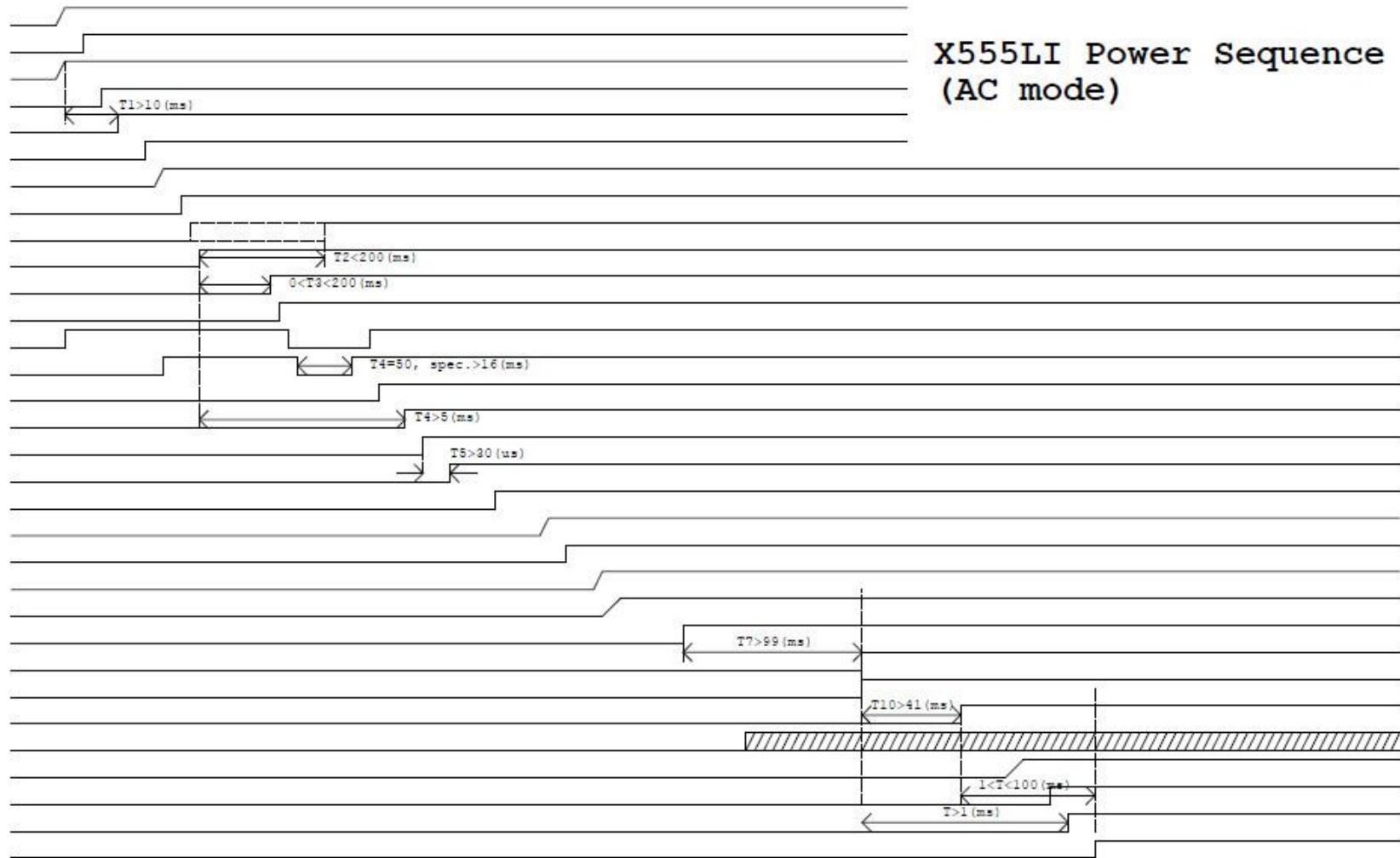
X555LI Power On Sequence Diagram R1.0



POWER ON SEQUENCE (AC Mode)

AC-IN Mode

- 1 +3VA/+5VA/+3VA_EC
- 2 EC_RST#
- +3VA_DSW
- 3 PCH_SLPSUS#
- 4 DPWROK_EC
- 5 SVSUS_ON/3VSUS_ON
- +5VSUS/+3VSUS
- SVSUS_PWRGD/+3VSUS_PWRGD
- 7 SUSWARN#
- 8 PM_RSMRST#
- 9 ME_AC_PRESENT
- 10 PCH_SUSACK#
- 11 PWR_SW#
- 12 PM_PWRBTN#
- 13(a) SLP_LAN#
- 13(b) PM_SLP_A#
- 14 PM_SUSC#
- 15 PM_SUSB#
- 16 SUSC_EC#
- +5V/1.35V
- 17 SUSB_EC#
- +5VS/+3VS/+1.5VS
- +3VS6/+1.8VS6/+1.35VS6/+0.95VS6
- 23 ALL_SYSTEM_PWRGD
- 24 PM_PCHPWROK
- 25 PM_SYSEFWROK
- 27 H_CPUPWRGD
- 28 VR_SVID_CLK/VR_SVID_DATA
- +VOCIN
- 29 CORE_PWRGD
- 30 PCH_SUS_STAT#
- 31 BUF_PLT_RST#

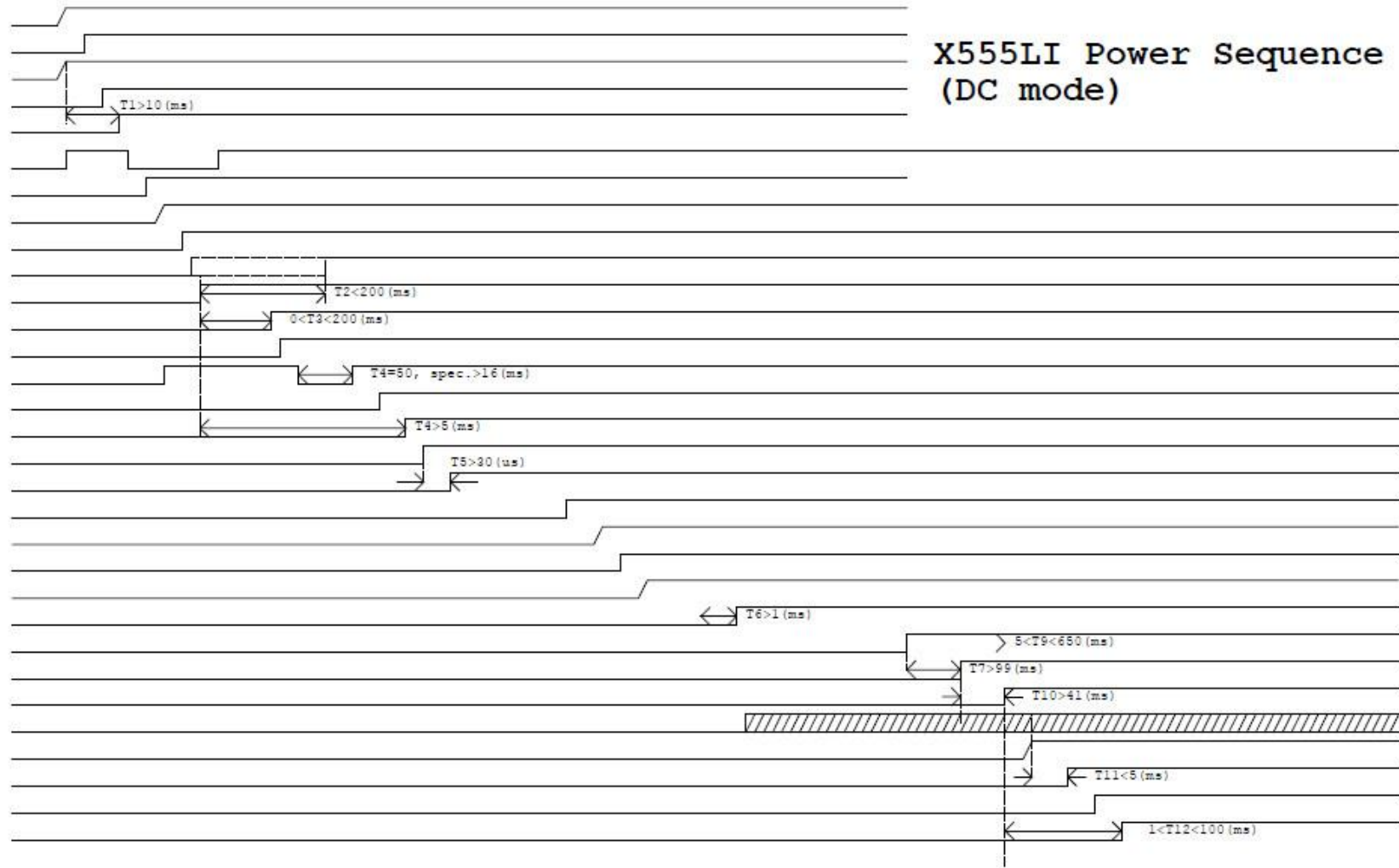


X555LI Power Sequence
(AC mode)

POWER ON SEQUENCE (DC Mode)

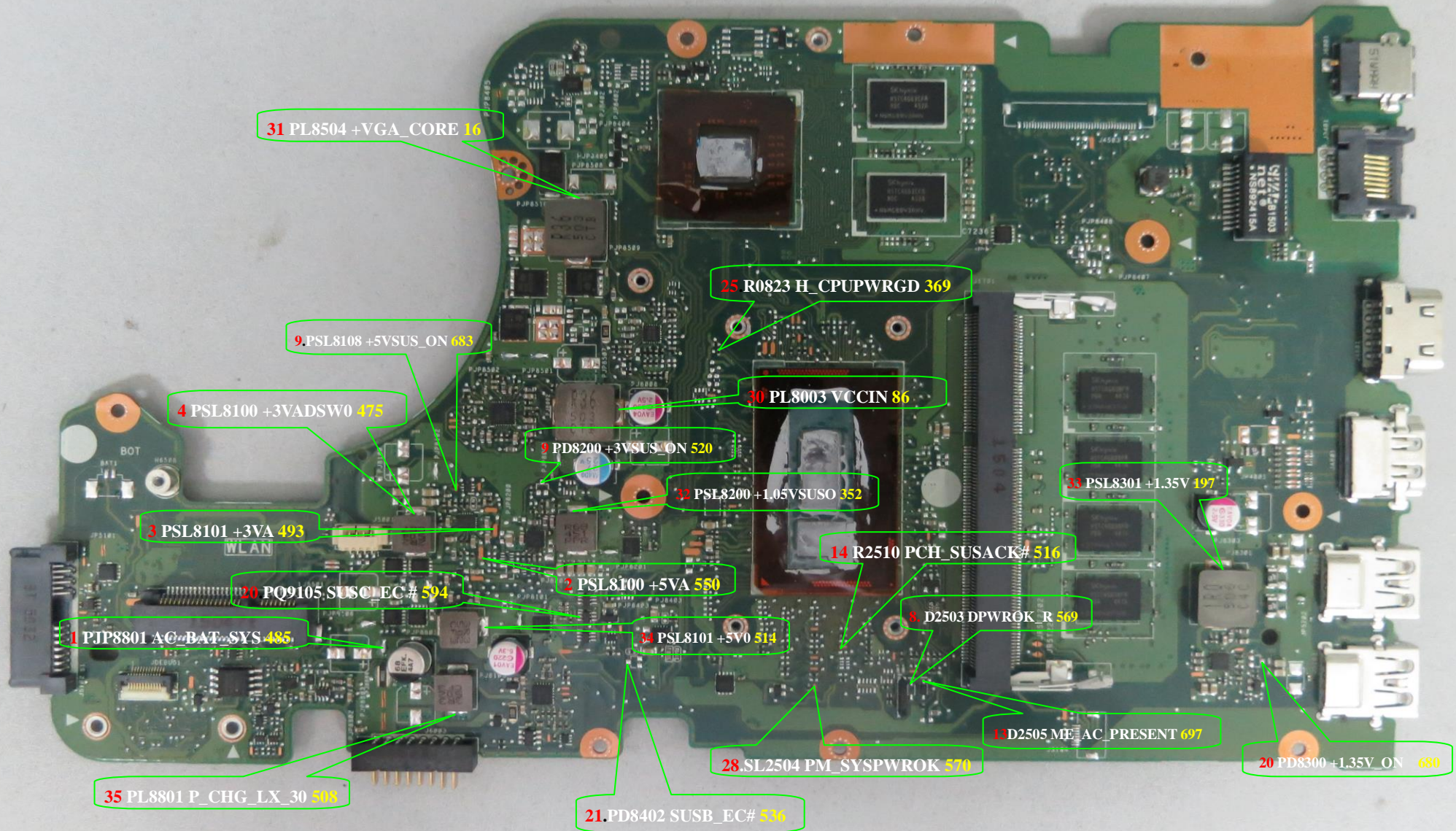
DC-IN Mode

- 1 +3VA/+5VA/+3VA_EC
- 2 EC_RST#
- +3VA_DSW
- 3 PCH_SLP_SUS#
- 4 DPWROK_EC
- 5 PWR_SW#
- 6 SVSUS_ON/3VSUS_ON
- +5VSUS/+3VSUS
- 7 SVSUS_PWRGD/+3VSUS_PWRGD
- 8 SUSWARN#
- 9 PM_RSMRST#
- 10 ME_AC_PRESENT
- 11 PCH_SUSACK#
- 12 PM_PWRBTN#
- 13(a) SLP_LAN#
- 13(b) PM_SLP_A#
- 14 PM_SUSC#
- 15 PM_SUSB#
- 16 SUSC_EC#
- +5V/1.35V
- 17 SUSB_EC#
- +5VS/+3VS/+1.8VS/+1.05VS/+0.675VS
- 19 PM_PWROK
- 20 ALL_SYSTEM_PWRGD
- 21 PM_PWROK_PCH
- 22 H_CPUPWRGD
- 23 VR_SVID_CLK/VR_SVID_DATA
- +VCCIN
- 25 CORE_PWRGD
- 26 PM_SYSPWROK
- 27 BUF_PLT_RST#



X555LI Power Sequence
(DC mode)

Signal Measure Point-Bottom



Signal Measure Point-Top

