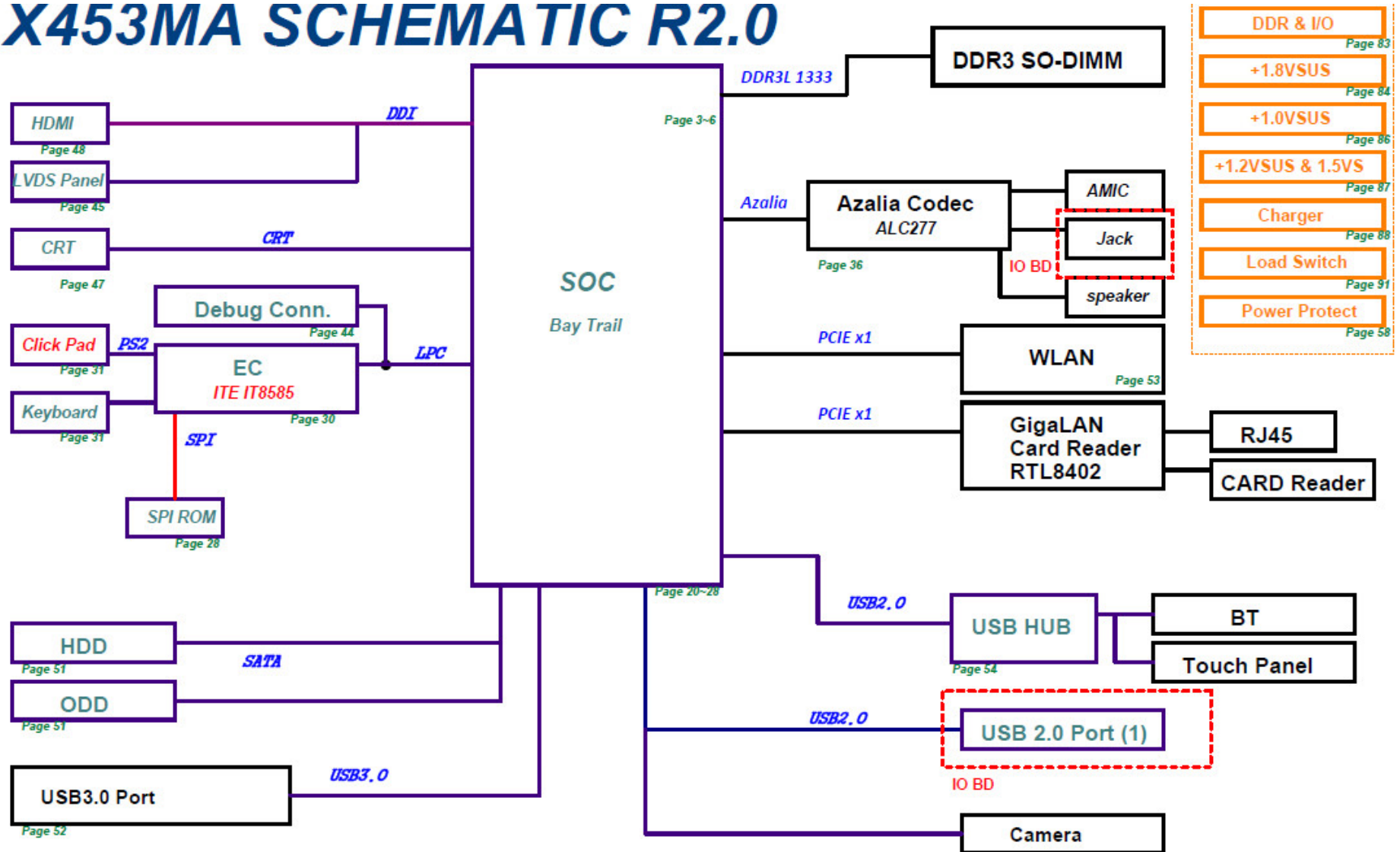
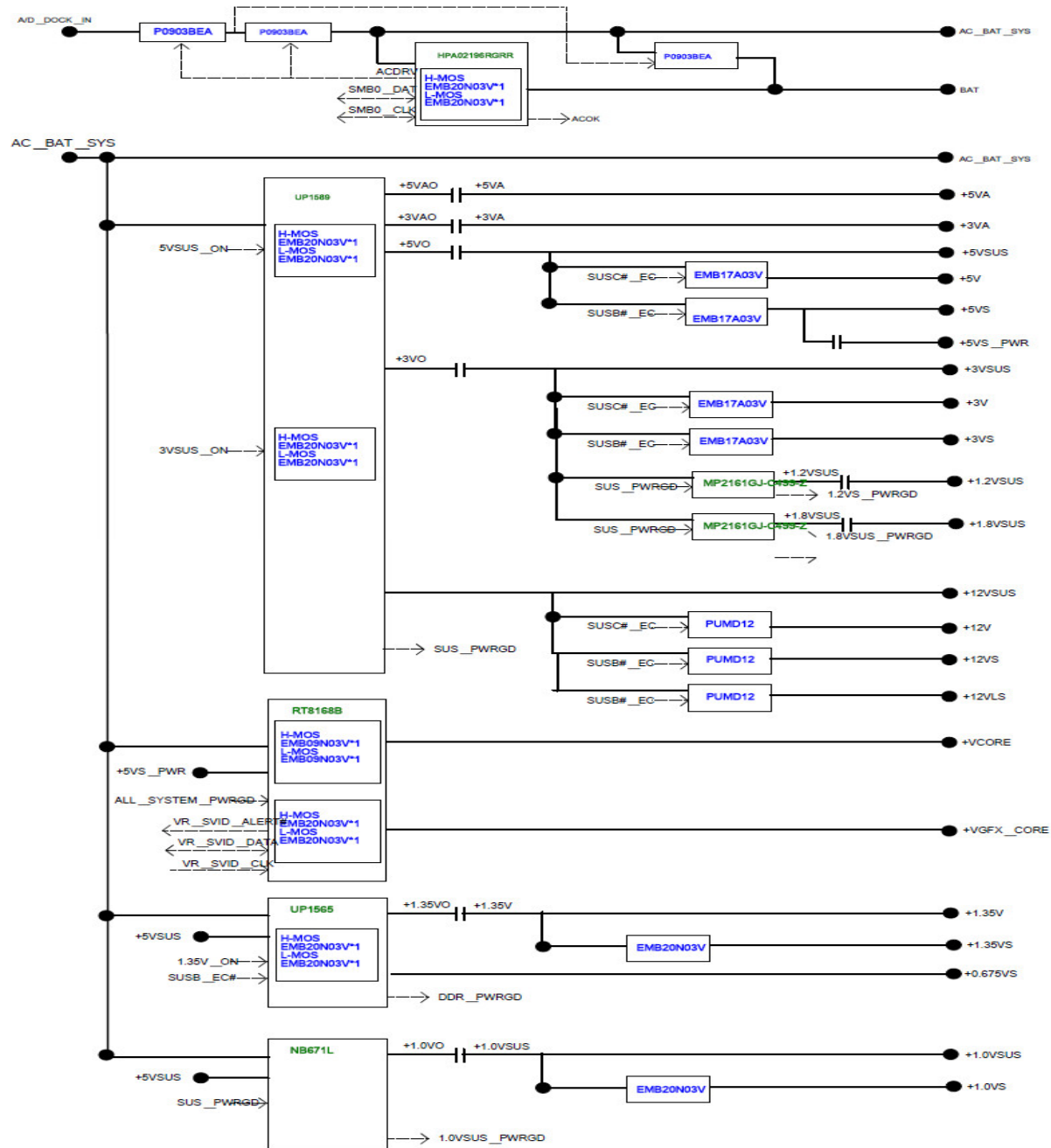


## BLOCK DIAGRAM

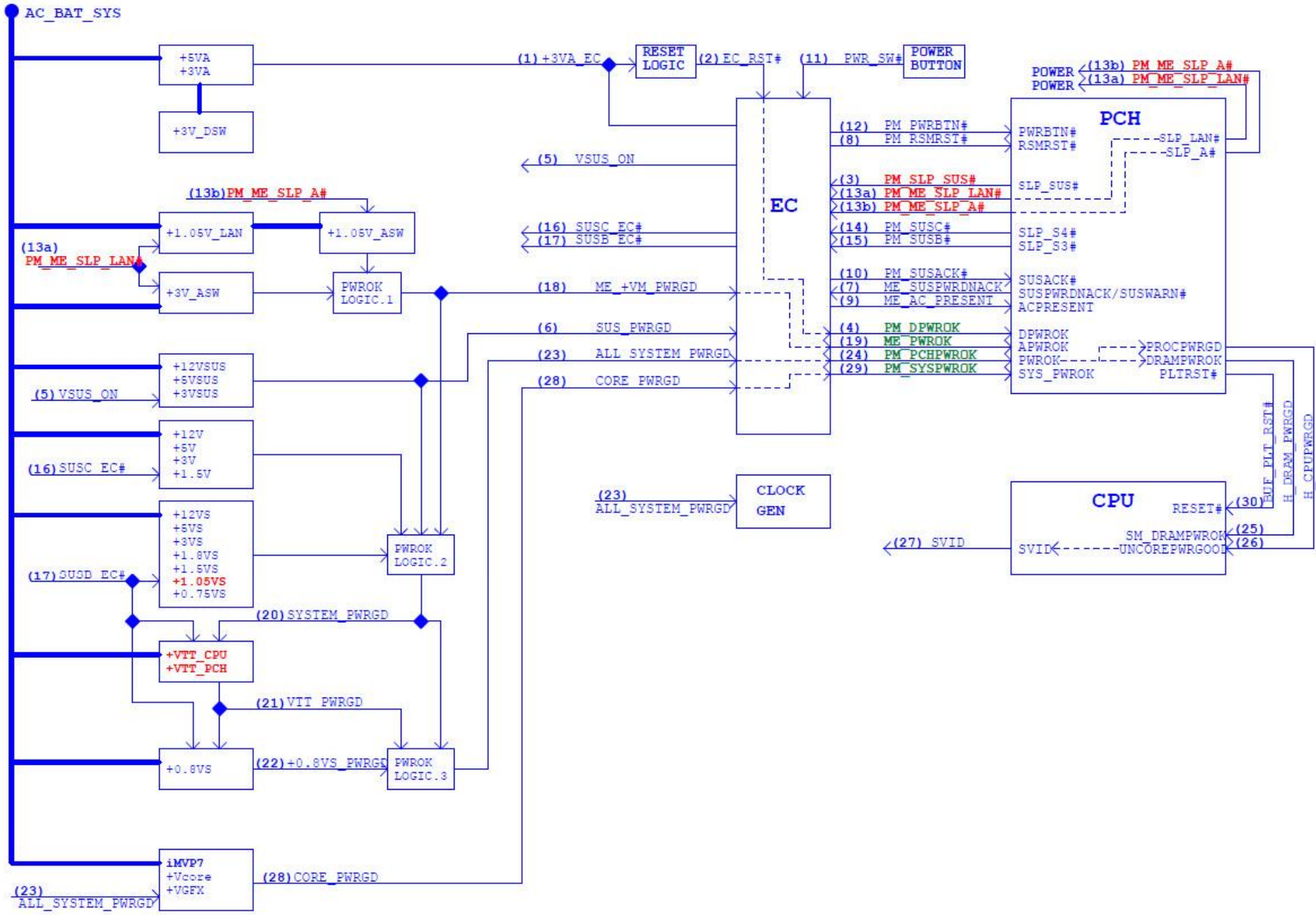
# X453MA SCHEMATIC R2.0



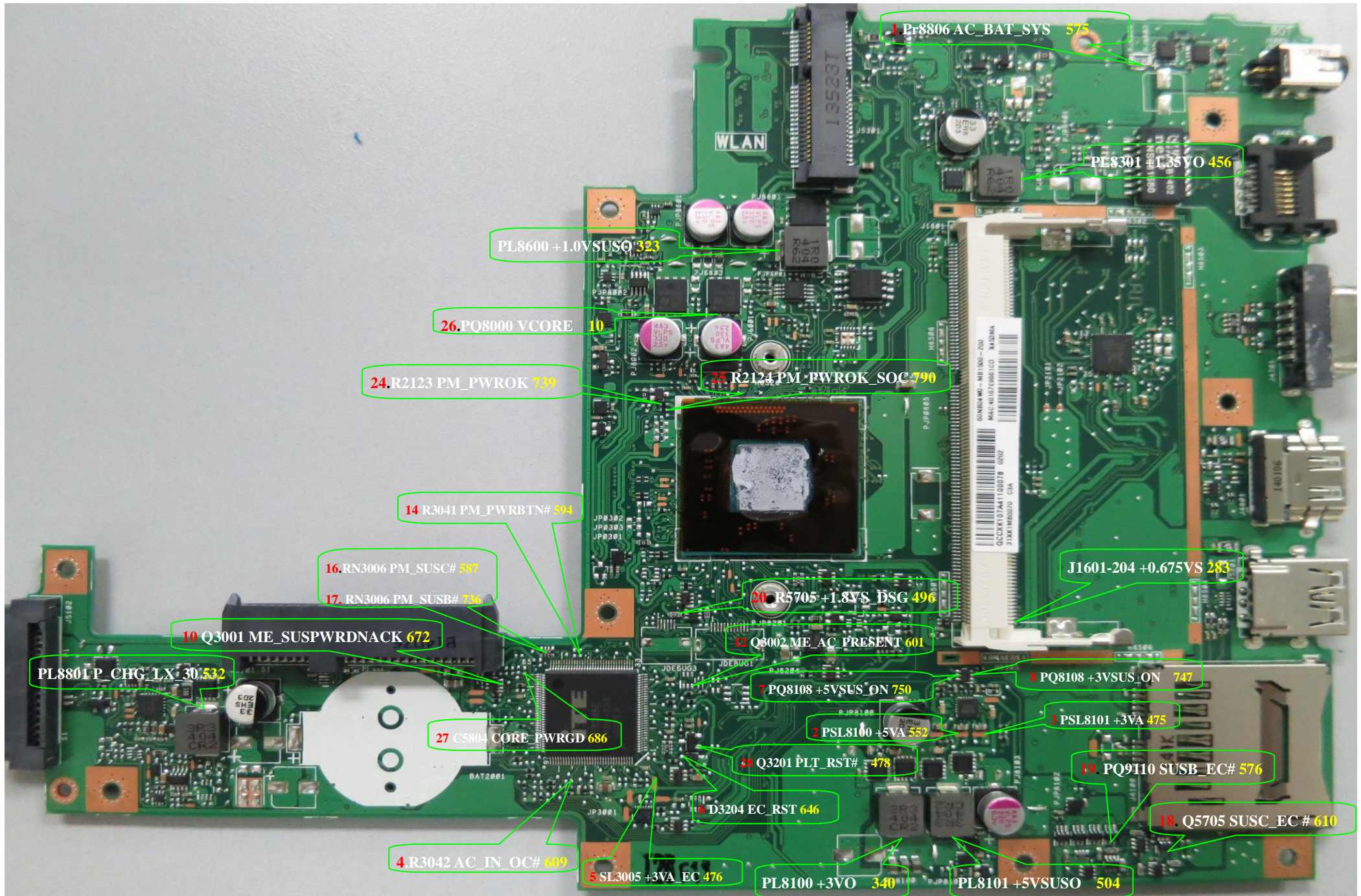
# POWER FLOW



# POWER ON SEQUENCE



# Signal Measure Point-Bottom



1.P8806 AC\_BAT\_SYS 575

PL8301 +1.35V0 456

PL8600 +1.0VSUS0 323

26.P08000 VCORE 10

24.R2123 PM\_PWROK 739

25.R2124 PM\_PWROK\_SOC 790

14.R3041 PM\_PWRBTN# 594

16.RN3006 PM\_SUSC# 587

17.RN3006 PM\_SUSB# 736

20.R5705 +1.8VS DSG 496

12.Q3002 ME\_AC\_PRESENT 601

10.Q3001 ME\_SUSPWRDNACK 672

J1601-204 +0.675VS 283

7.PQ8108 +5VSUS\_ON 750

8.PQ8108 +3VSUS\_ON 747

3.PSL8101 +3VA 475

2.PSL8100 +5VA 552

19.PQ9110 SUSB\_EC# 576

18.Q5705 SUSC\_EC# 610

16.D3204 EC\_RST 646

4.R3042 AC\_IN\_OC# 609

5.SL3005 +3VA\_EC 476

PL8100 +3V0 340

PL8101 +5VSUS0 504

27.C5804 CORE\_PWRGD 686

PL8801 P\_CHG\_LX 30 532

PL8801 P\_CHG\_LX 30 532

# Signal Measure Point-Top

