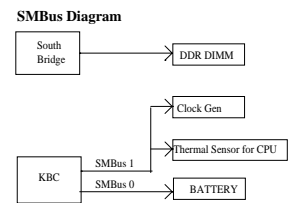
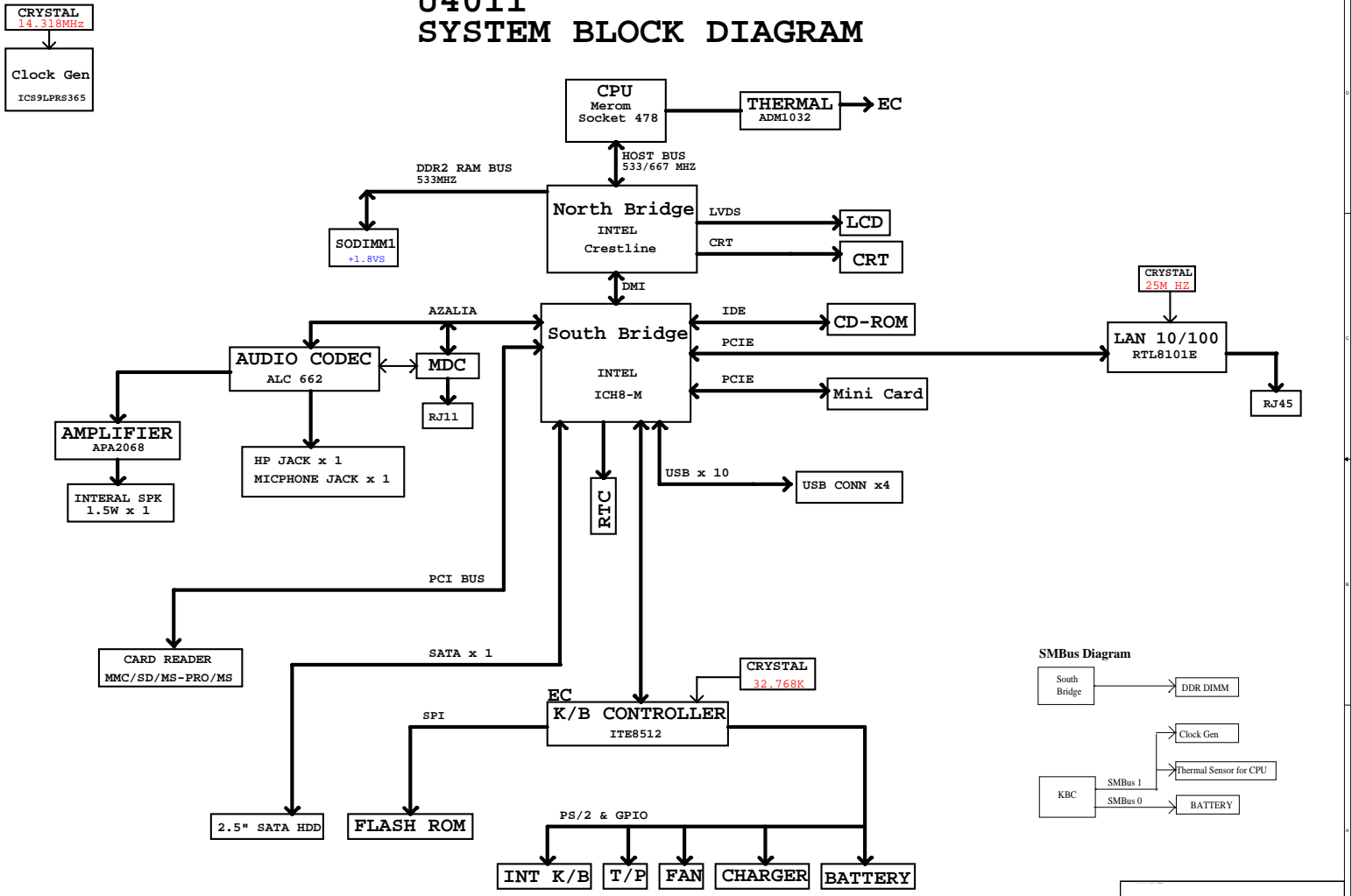
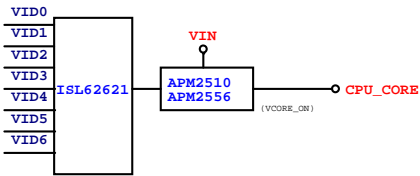
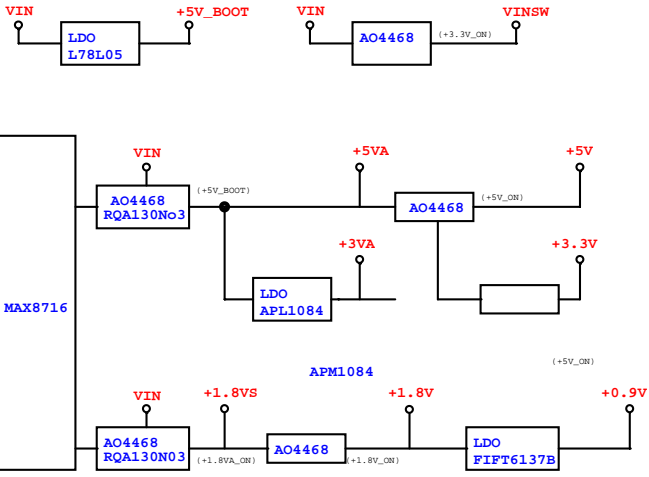
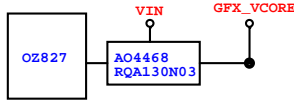


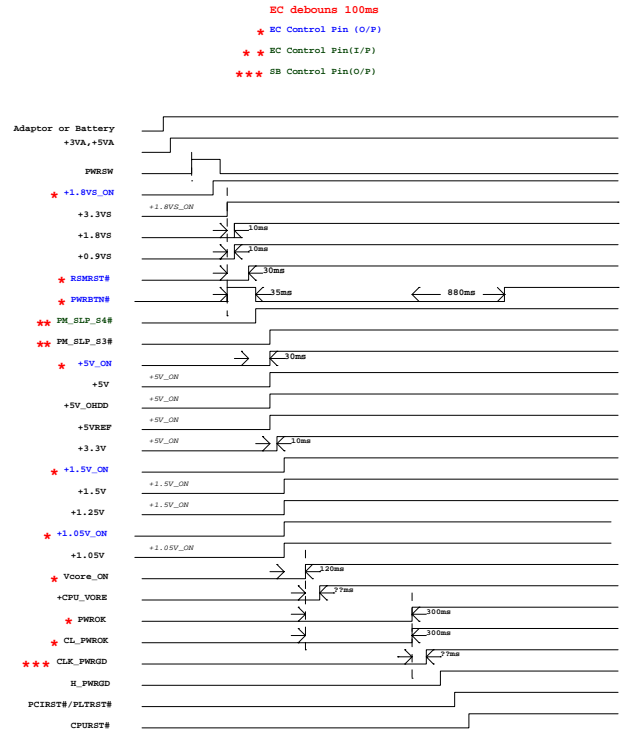
U40II SYSTEM BLOCK DIAGRAM



POWER BLOCK DIAGRAM



POWER Sequence



POWER DIAGRAM & SEQUENCE		
File	Document Number	Rev
	300	C
Date	Feb, December 07, 2007	Sheet 3 of 33

SIS968 GPIO	
GPIO0	NC
GPIO1	NC
GPIO2	PM_THROTTING#
GPIO3	EC_EXTSMI#
GPIO4	PM_CLKRUN#
GPIO5	NC
GPIO6	NC
GPIO7	NC
GPIO8	NC
GPIO9	NC
GPIO10	SLP_S5#
GPIO11	AGPSTOP_N
GPIO12	DPSLP#
GPIO13	SB DPRSLPVR
GPIO14	NC
GPIO15	SLP_S3#
GPIO16	NC
GPIO17	H_A20GATE
GPIO18	H_RCIN#
GPIO19	SB_SMB_CLK
GPIO20	SB_SMB_DATA

ITE8512E GPIO	
GPIO0	AUX_PWRGD
GPIO1	DDR_V_SWM#
GPIO2	BTU_BEEP
GPIO3	RFLED_ON
GPIO4	SCROLL/3G_LED
GPIO5	NUM_LED
GPIO6	CAPS_LED
GPIO7	PWRON_LED
GPIO8	PM_SLP_S5#
GPIO9	PM_SLP_S3#
GPIO10	WBCDM_ON
GPIO11	BAT_SMBCLK
GPIO12	BAT_SMBDAT
GPIO13	H_A20GATE
GPIO14	H_RCIN#
GPIO15	BT_ON
GPIO16	EC_VID5
GPIO17	SMBCLK_EC
GPIO18	SMBDAT_EC
GPIO19	EC_VID2
GPIO20	RF_SW_ON#
GPIO21	EC_VID1
GPIO22	INTERNET#
GPIO23	SILENT#
GPIO24	EC_PREST#
GPIO25	PWRBTN#
GPIO26	EC_LPCRST#
GPIO27	EC_EXTSMI#
GPIO28	EC_EXTSMI#
GPIO29	H_PROCHOT#
GPIO30	CHG_ON
GPIO31	LCDSW
GPIO32	EC_PWR_ON
GPIO33	SET_V
GPIO34	PWROK
GPIO35	VCORE_ON
GPIO36	LID#
GPIO37	AC_IN/OUTH
GPIO38	FAN_SPD# or RTCRST
GPIO39	AMP_MUTE#
GPIO40	3G_ON
GPIO41	EC_BSELL1
GPIO42	CHG_G_LED
GPIO43	CHG_R_LED
GPIO44	TP_CLK
GPIO45	TP_DATA
GPIO46	VGA_SMBCLK
GPIO47	VGA_SMBDAT
GPIO48	EC_VID3
GPIO49	EC_WDOG_OK
GPIO50	FLFRAME#
GPIO51	NEW_CARD_PWR_ON#
GPIO52	+1.8V_ON
GPIO53	+1.8VS_ON
GPIO54	SENBAAT_V
GPIO55	+3.3VS_ON
GPIO56	+5V_ON
GPIO57	VDD_CORE_ON
GPIO58	EC_VID4

ITE8512E GPIO	
GPIO10	BATT_TEMP
GPIO11	ADAPTOR_I
GPIO12	BAT_V
GPIO13	CPPE#
GPIO14	BAT_I
GPIO15	EC_CPU_PWR
GPIO16	DDR2_TEMP
GPIO17	ADAP_IN
GPIO18	EC_BRGHT
GPIO19	CHG_I
GPIO20	FAN_CTRL0
GPIO21	SILENT_LED
GPIO22	SMP1_EN#
GPIO23	PM_THROTTING#

CPU				
CPU CORE(V)	ICC(mA)	W	TEMP()	
2.0G	1.525	35.7	54.3	69
2.2G	1.525	37.5	57.1	70
2.26G	1.525	38.1	58.0	70
2.4G	1.525	39.3	59.8	71
2.5G	1.525	40	61.0	72
2.53G	1.525	40.4	61.5	72
2.6G	1.525	41.05	62.6	72
2.66G	1.525	43.35	66.1	74
2.8G	1.525	44.86	68.4	75
3.06G	1.525	55.9	85.2	81
+1.5V	120	0.18		
+1.05V	2500	2.625		70

CLOCK GENERATOR+BUFFER			
VCC	ICC(mA)	W	TEMP()
+3.3V	400	1.32	
+1.8V	300	0.54	70

ITE8512E			
VCC	ICC(mA)	W	TEMP()
+3.3V	200	0.66	70
+3.3VA	500	1.65	

RTS5158			
VCC	ICC(mA)	W	TEMP()
+5V	76	0.38	85

RTL8201CL			
VCC	ICC(mA)	W	TEMP()
+3.3V	20	0.396	85

ALC662			
VCC	ICC(mA)	W	TEMP()
+3.3V	23	0.075	
+5VA	38	0.19	70

APA2068			
VCC	ICC(mA)	W	TEMP()
5V	20	0.1	85

ADM1032			
VCC	ICC	W	TEMP()
+3.3V	170uA	0.56mW	150

672MX			
VCC	ICC(mA)	W	TEMP()
+1.2V	2303	2.76	
+1.8V	1215	2.18	70
+1.05V	80	0.084	

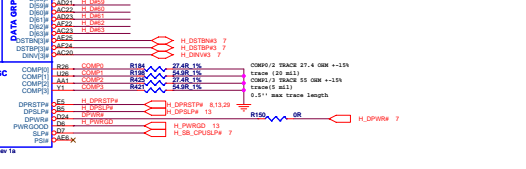
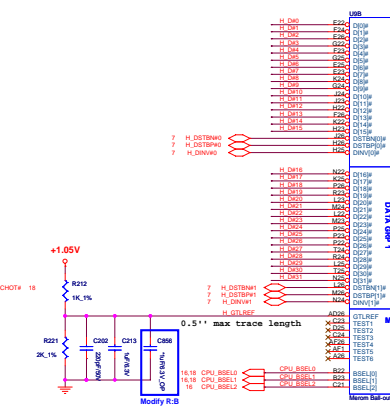
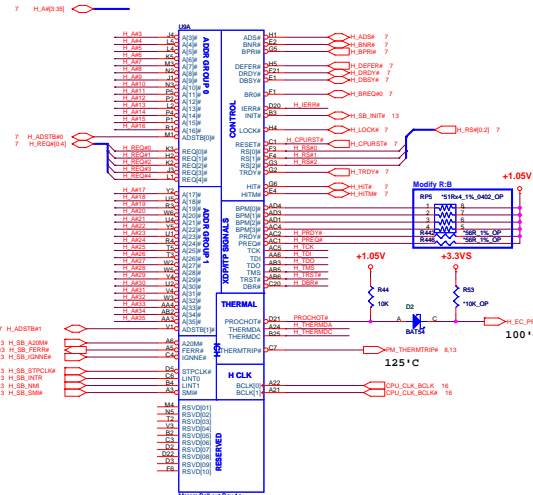
SIS968			
VCC	ICC(mA)	W	TEMP()
+3.3V	86	0.283	
+1.8V	851	1.531	70
+1.05V	22	0.022	

307LV			
VCC	ICC(mA)	W	TEMP()
+3.3V	236	1.107	70
+1.8V	565	0.775	

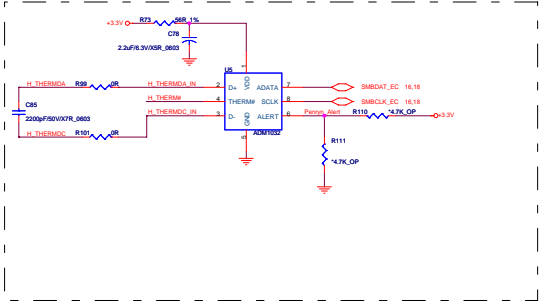
del VGA_TEMP

SMART POWER TABLE

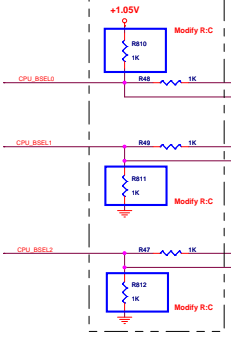
VID0	VID5	VID4	VID3	VID2	VID1	VID0	VCORE	+5V
0	0	0	0	0	0	0	1.5000	-0mW
0	0	0	0	0	0	1	1.4875	-2.5mW
0	0	0	0	0	1	0	1.4750	-5mW
0	0	0	0	1	0	0	1.4500	-50mW
0	0	0	1	0	0	0	1.4000	-100mW
0	1	0	0	0	0	0	1.3800	-200mW
1	0	0	0	0	0	0	1.2000	-400mW
0	0	0	0	0	0	0	0.7000	-800mW
0	1	1	0	1	1	1	1.1625	
0	0	1	0	0	0	1		
0	0	1	0	0	1	0		
0	0	1	0	1	0	0		
0	0	1	0	1	1	0		
0	0	1	1	0	0	1		
0	0	1	1	0	1	0		



CPU Thermal Sensor

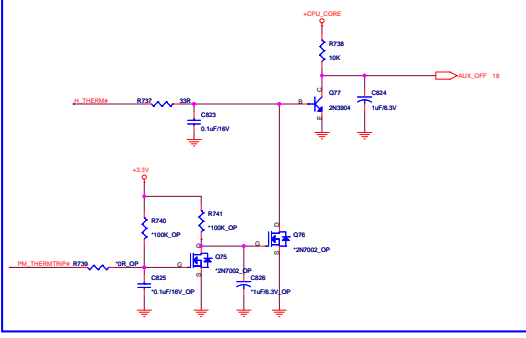


Close to NB

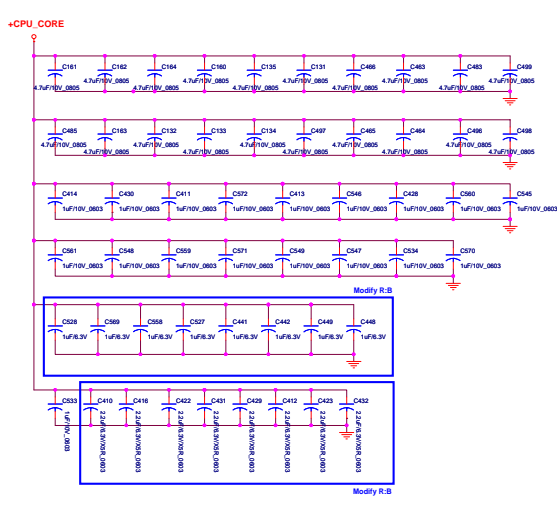
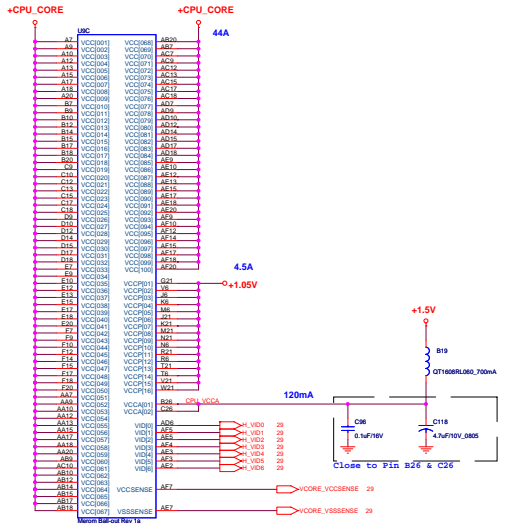


	BSKL2	BSEL1	BSEL0	MRZ
FSB600	0	1	0	200
FSB667	0	1	1	166
FSB533	0	0	1	133

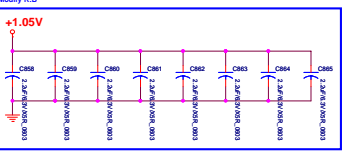
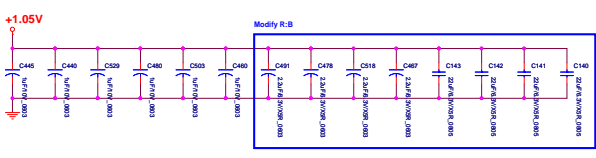
Modify R.B



CPU HOST & Thermal sensor			
Rev	Document Number	Doc ID	Rev C
001	3305	3305	
Date	Feb, October of 2007	Sheet	5 of 35



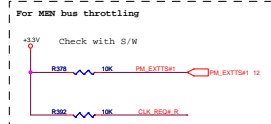
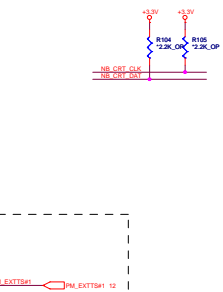
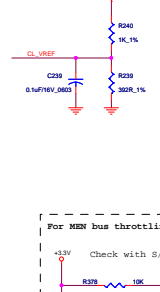
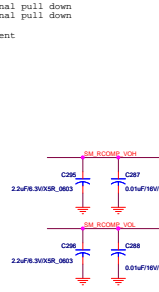
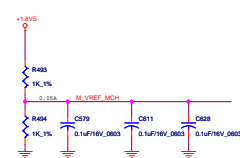
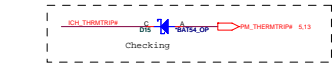
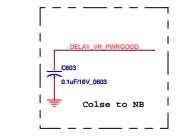
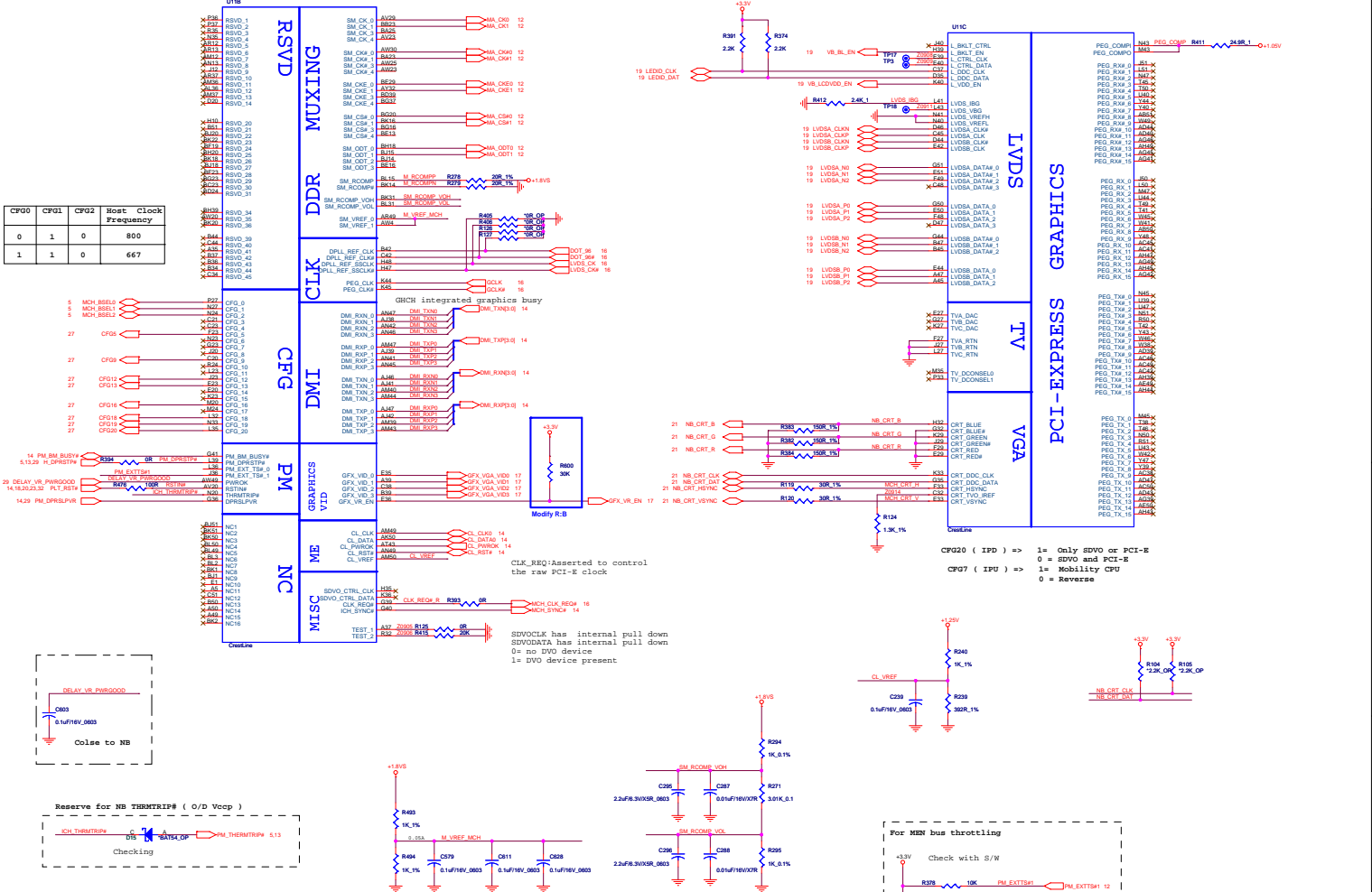
Pin	Signal	Signal
A1	VSS001	VSS001
A2	VSS002	VSS002
A3	VSS003	VSS003
A4	VSS004	VSS004
A5	VSS005	VSS005
A6	VSS006	VSS006
A7	VSS007	VSS007
A8	VSS008	VSS008
A9	VSS009	VSS009
A10	VSS010	VSS010
A11	VSS011	VSS011
A12	VSS012	VSS012
A13	VSS013	VSS013
A14	VSS014	VSS014
A15	VSS015	VSS015
A16	VSS016	VSS016
A17	VSS017	VSS017
A18	VSS018	VSS018
A19	VSS019	VSS019
A20	VSS020	VSS020
A21	VSS021	VSS021
A22	VSS022	VSS022
A23	VSS023	VSS023
A24	VSS024	VSS024
A25	VSS025	VSS025
A26	VSS026	VSS026
A27	VSS027	VSS027
A28	VSS028	VSS028
A29	VSS029	VSS029
A30	VSS030	VSS030
A31	VSS031	VSS031
A32	VSS032	VSS032
A33	VSS033	VSS033
A34	VSS034	VSS034
A35	VSS035	VSS035
A36	VSS036	VSS036
A37	VSS037	VSS037
A38	VSS038	VSS038
A39	VSS039	VSS039
A40	VSS040	VSS040
A41	VSS041	VSS041
A42	VSS042	VSS042
A43	VSS043	VSS043
A44	VSS044	VSS044
A45	VSS045	VSS045
A46	VSS046	VSS046
A47	VSS047	VSS047
A48	VSS048	VSS048
A49	VSS049	VSS049
A50	VSS050	VSS050
A51	VSS051	VSS051
A52	VSS052	VSS052
A53	VSS053	VSS053
A54	VSS054	VSS054
A55	VSS055	VSS055
A56	VSS056	VSS056
A57	VSS057	VSS057
A58	VSS058	VSS058
A59	VSS059	VSS059
A60	VSS060	VSS060
A61	VSS061	VSS061
A62	VSS062	VSS062
A63	VSS063	VSS063
A64	VSS064	VSS064
A65	VSS065	VSS065
A66	VSS066	VSS066
A67	VSS067	VSS067
A68	VSS068	VSS068
A69	VSS069	VSS069
A70	VSS070	VSS070
A71	VSS071	VSS071
A72	VSS072	VSS072
A73	VSS073	VSS073
A74	VSS074	VSS074
A75	VSS075	VSS075
A76	VSS076	VSS076
A77	VSS077	VSS077
A78	VSS078	VSS078
A79	VSS079	VSS079
A80	VSS080	VSS080
A81	VSS081	VSS081
A82	VSS082	VSS082
A83	VSS083	VSS083
A84	VSS084	VSS084
A85	VSS085	VSS085
A86	VSS086	VSS086
A87	VSS087	VSS087
A88	VSS088	VSS088
A89	VSS089	VSS089
A90	VSS090	VSS090
A91	VSS091	VSS091
A92	VSS092	VSS092
A93	VSS093	VSS093
A94	VSS094	VSS094
A95	VSS095	VSS095
A96	VSS096	VSS096
A97	VSS097	VSS097
A98	VSS098	VSS098
A99	VSS099	VSS099
A100	VSS100	VSS100



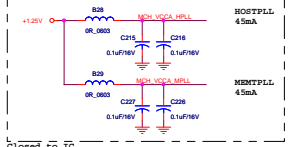
Pin	Signal	Signal
A1	VSS001	VSS001
A2	VSS002	VSS002
A3	VSS003	VSS003
A4	VSS004	VSS004
A5	VSS005	VSS005
A6	VSS006	VSS006
A7	VSS007	VSS007
A8	VSS008	VSS008
A9	VSS009	VSS009
A10	VSS010	VSS010
A11	VSS011	VSS011
A12	VSS012	VSS012
A13	VSS013	VSS013
A14	VSS014	VSS014
A15	VSS015	VSS015
A16	VSS016	VSS016
A17	VSS017	VSS017
A18	VSS018	VSS018
A19	VSS019	VSS019
A20	VSS020	VSS020
A21	VSS021	VSS021
A22	VSS022	VSS022
A23	VSS023	VSS023
A24	VSS024	VSS024
A25	VSS025	VSS025
A26	VSS026	VSS026
A27	VSS027	VSS027
A28	VSS028	VSS028
A29	VSS029	VSS029
A30	VSS030	VSS030
A31	VSS031	VSS031
A32	VSS032	VSS032
A33	VSS033	VSS033
A34	VSS034	VSS034
A35	VSS035	VSS035
A36	VSS036	VSS036
A37	VSS037	VSS037
A38	VSS038	VSS038
A39	VSS039	VSS039
A40	VSS040	VSS040
A41	VSS041	VSS041
A42	VSS042	VSS042
A43	VSS043	VSS043
A44	VSS044	VSS044
A45	VSS045	VSS045
A46	VSS046	VSS046
A47	VSS047	VSS047
A48	VSS048	VSS048
A49	VSS049	VSS049
A50	VSS050	VSS050
A51	VSS051	VSS051
A52	VSS052	VSS052
A53	VSS053	VSS053
A54	VSS054	VSS054
A55	VSS055	VSS055
A56	VSS056	VSS056
A57	VSS057	VSS057
A58	VSS058	VSS058
A59	VSS059	VSS059
A60	VSS060	VSS060
A61	VSS061	VSS061
A62	VSS062	VSS062
A63	VSS063	VSS063
A64	VSS064	VSS064
A65	VSS065	VSS065
A66	VSS066	VSS066
A67	VSS067	VSS067
A68	VSS068	VSS068
A69	VSS069	VSS069
A70	VSS070	VSS070
A71	VSS071	VSS071
A72	VSS072	VSS072
A73	VSS073	VSS073
A74	VSS074	VSS074
A75	VSS075	VSS075
A76	VSS076	VSS076
A77	VSS077	VSS077
A78	VSS078	VSS078
A79	VSS079	VSS079
A80	VSS080	VSS080
A81	VSS081	VSS081
A82	VSS082	VSS082
A83	VSS083	VSS083
A84	VSS084	VSS084
A85	VSS085	VSS085
A86	VSS086	VSS086
A87	VSS087	VSS087
A88	VSS088	VSS088
A89	VSS089	VSS089
A90	VSS090	VSS090
A91	VSS091	VSS091
A92	VSS092	VSS092
A93	VSS093	VSS093
A94	VSS094	VSS094
A95	VSS095	VSS095
A96	VSS096	VSS096
A97	VSS097	VSS097
A98	VSS098	VSS098
A99	VSS099	VSS099
A100	VSS100	VSS100

GCLK => PCI-E & DMI (100MHz)
 DRREFCLK => Display PLLA (min-ss 96MHz)
 DRSSREFCLK => Display LVDS PLLB (ss 100MHz)

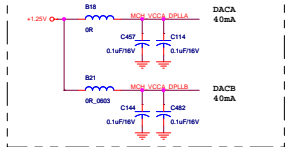
CF00	CF01	CF02	Host Clock Frequency
0	1	0	800
1	1	0	667



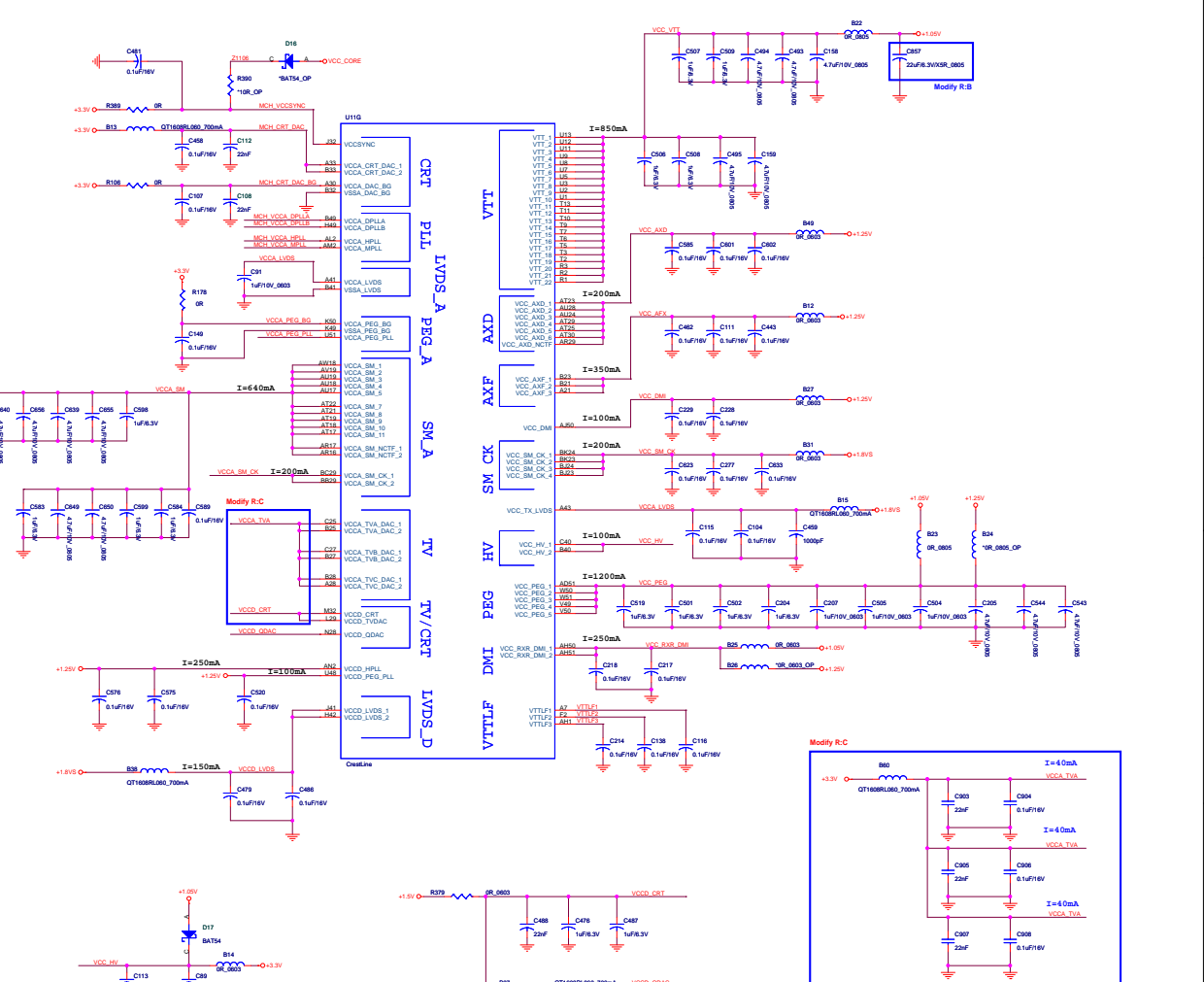
NB DDRCLK_VGA_PCIEPR-25	
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300	
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Date	2008, December 07, 2008
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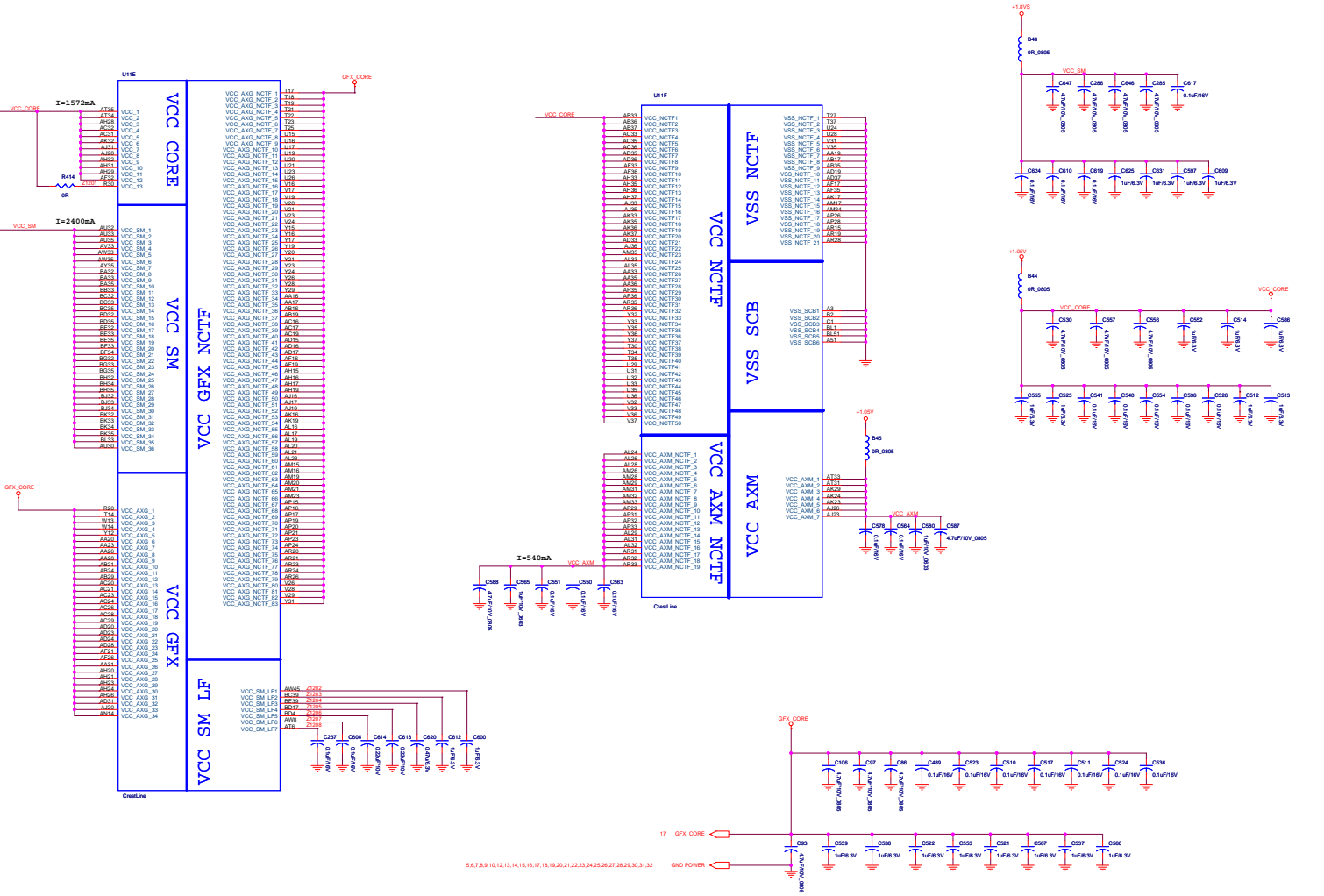
Closed to IC



Closed to IC

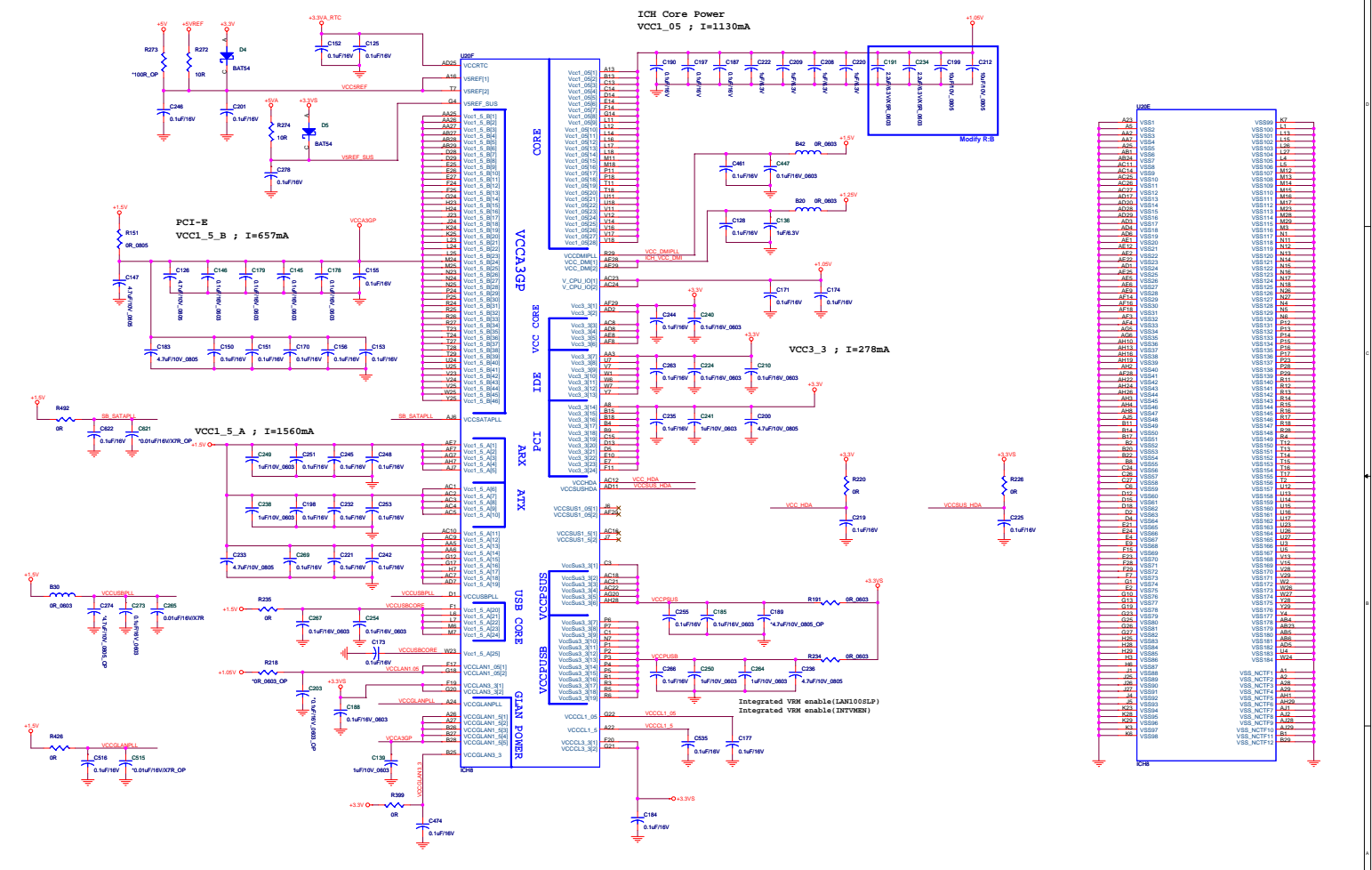


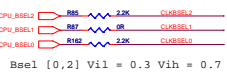
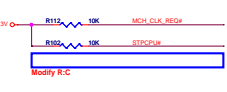
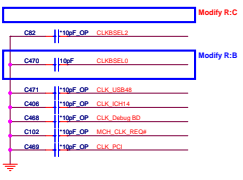
Rev	NB POWER-45		
Doc	Document Number		Rev C
300			
Date	Feb, December 07, 2007	Sheet	15 of 33



Doc	NB VSS_NCTF-55		
Doc	Document Number		
Rev	300		
Date	Feb, December of 2007	Sheet	11 of 33

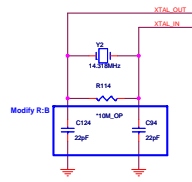
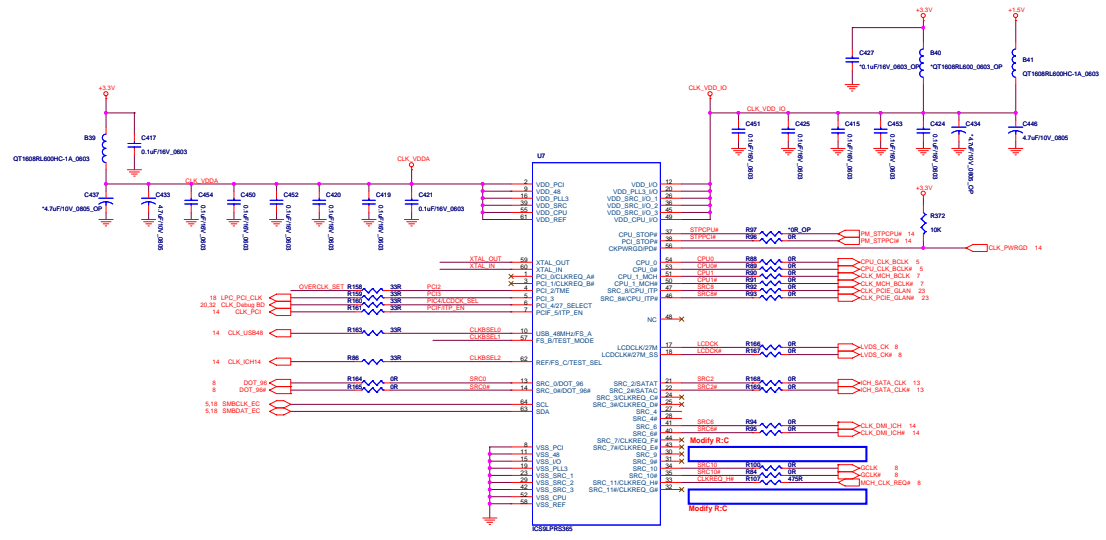
ICH Core Power
VCC1_05 ; I=1130mA





Bsel [0,2] Vil = 0.3 Vih = 0.7

	BSEL2	BSEL1	BSEL0	CPU	PCI	PCI-E
FSB500	0	1	0	200		100
FSB607	0	1	1	124	33	
FSB513	0	0	1	133		

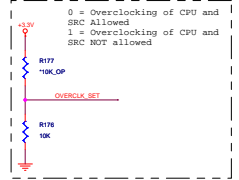
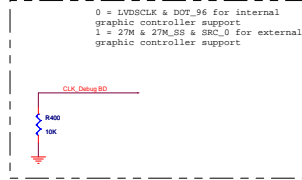
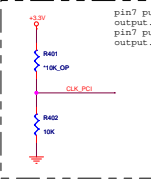


Ca = 2 * CL - (Ca + C1)
 CL = Crystal Load Cap = 25p
 C1 = IC Internal Cap = 5p
 Ca = 2p
 Ca = Crystal external Cap = 33p

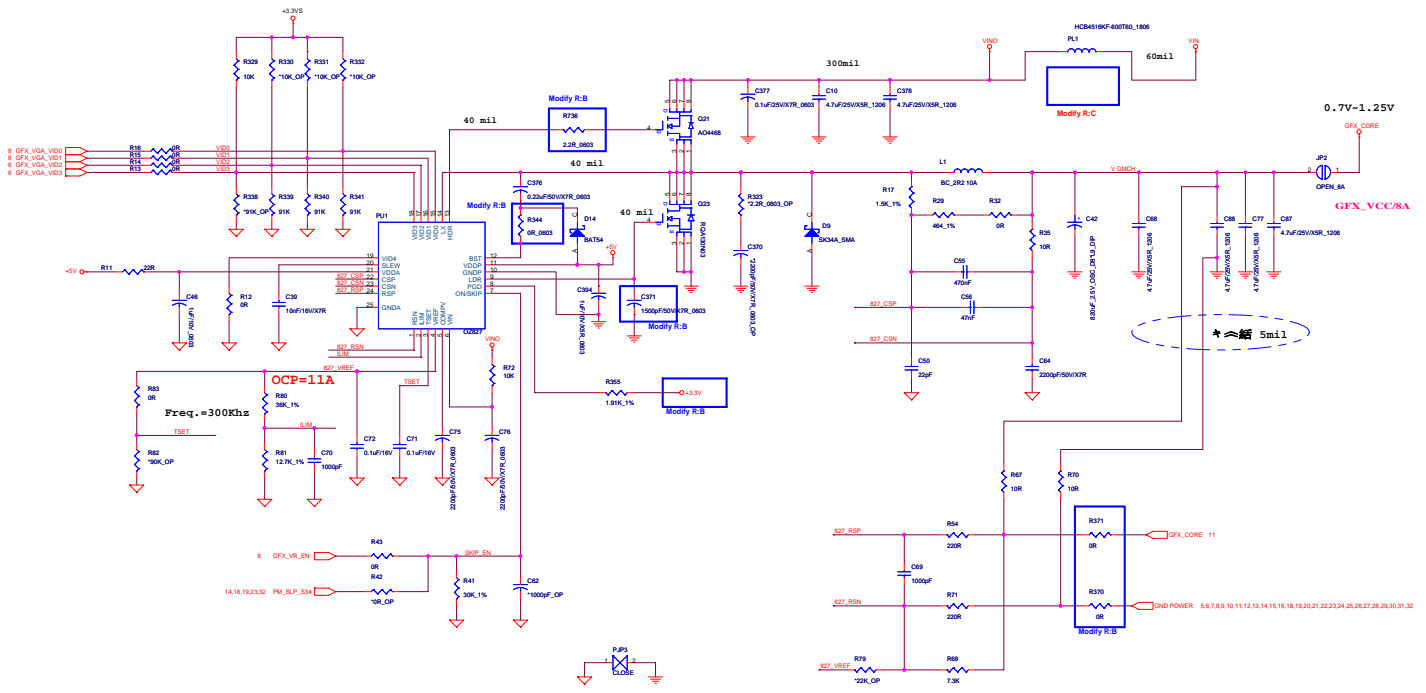
CLK REQ	From	SRC BUS
C	ICH_SATA	SRC2
G	MINI_GMIO	SRC9
B	LAN_10/100	SRC4
H	MCH_GCLK	SRC10

Pin Configuration

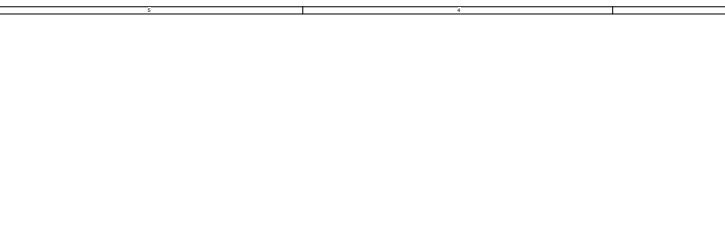
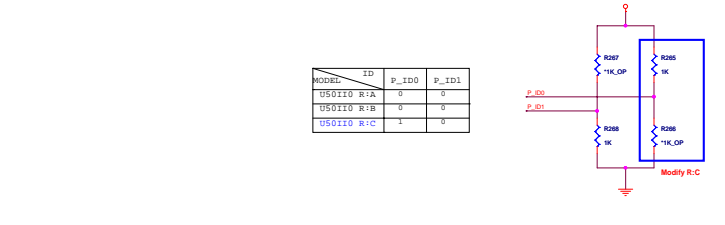
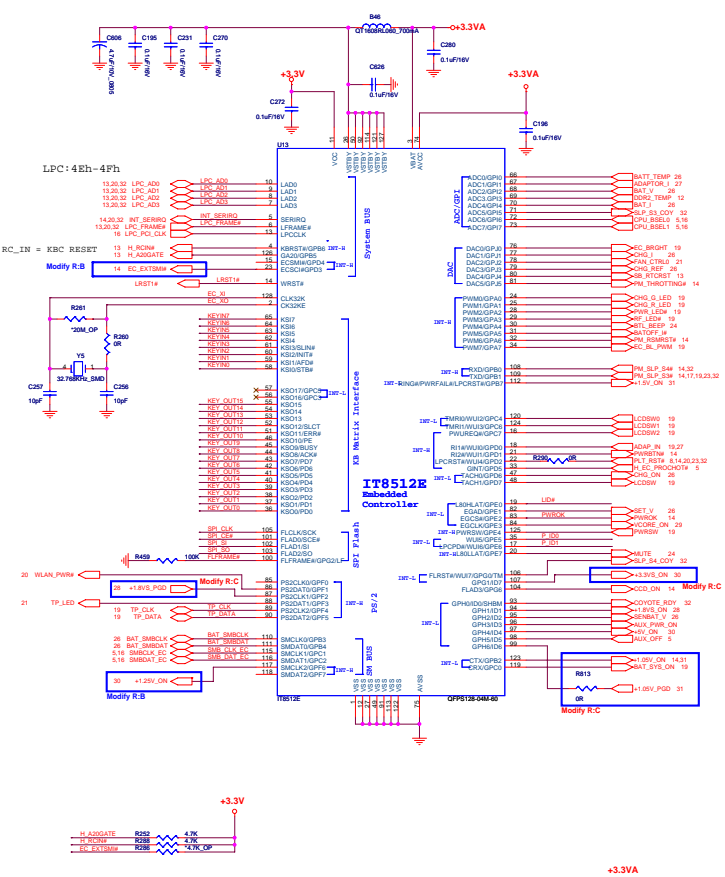
Pin	Function	Pin	Function
PIN0A_01	NC	PIN0A_17	NC
PIN0A_02	NC	PIN0A_18	NC
PIN0A_03	NC	PIN0A_19	NC
PIN0A_04	NC	PIN0A_20	NC
PIN0A_05	NC	PIN0A_21	NC
PIN0A_06	NC	PIN0A_22	NC
PIN0A_07	NC	PIN0A_23	NC
PIN0A_08	NC	PIN0A_24	NC
PIN0A_09	NC	PIN0A_25	NC
PIN0A_10	NC	PIN0A_26	NC
PIN0A_11	NC	PIN0A_27	NC
PIN0A_12	NC	PIN0A_28	NC
PIN0A_13	NC	PIN0A_29	NC
PIN0A_14	NC	PIN0A_30	NC
PIN0A_15	NC	PIN0A_31	NC
PIN0A_16	NC	PIN0A_32	NC
PIN0A_33	NC	PIN0A_34	NC
PIN0A_35	NC	PIN0A_35	NC
PIN0A_36	NC	PIN0A_36	NC
PIN0A_37	NC	PIN0A_37	NC
PIN0A_38	NC	PIN0A_38	NC
PIN0A_39	NC	PIN0A_39	NC
PIN0A_40	NC	PIN0A_40	NC
PIN0A_41	NC	PIN0A_41	NC
PIN0A_42	NC	PIN0A_42	NC
PIN0A_43	NC	PIN0A_43	NC
PIN0A_44	NC	PIN0A_44	NC
PIN0A_45	NC	PIN0A_45	NC
PIN0A_46	NC	PIN0A_46	NC
PIN0A_47	NC	PIN0A_47	NC
PIN0A_48	NC	PIN0A_48	NC
PIN0A_49	NC	PIN0A_49	NC
PIN0A_50	NC	PIN0A_50	NC
PIN0A_51	NC	PIN0A_51	NC
PIN0A_52	NC	PIN0A_52	NC
PIN0A_53	NC	PIN0A_53	NC
PIN0A_54	NC	PIN0A_54	NC
PIN0A_55	NC	PIN0A_55	NC
PIN0A_56	NC	PIN0A_56	NC
PIN0A_57	NC	PIN0A_57	NC
PIN0A_58	NC	PIN0A_58	NC
PIN0A_59	NC	PIN0A_59	NC
PIN0A_60	NC	PIN0A_60	NC
PIN0A_61	NC	PIN0A_61	NC
PIN0A_62	NC	PIN0A_62	NC
PIN0A_63	NC	PIN0A_63	NC
PIN0A_64	NC	PIN0A_64	NC
PIN0A_65	NC	PIN0A_65	NC
PIN0A_66	NC	PIN0A_66	NC
PIN0A_67	NC	PIN0A_67	NC
PIN0A_68	NC	PIN0A_68	NC
PIN0A_69	NC	PIN0A_69	NC
PIN0A_70	NC	PIN0A_70	NC
PIN0A_71	NC	PIN0A_71	NC
PIN0A_72	NC	PIN0A_72	NC
PIN0A_73	NC	PIN0A_73	NC
PIN0A_74	NC	PIN0A_74	NC
PIN0A_75	NC	PIN0A_75	NC
PIN0A_76	NC	PIN0A_76	NC
PIN0A_77	NC	PIN0A_77	NC
PIN0A_78	NC	PIN0A_78	NC
PIN0A_79	NC	PIN0A_79	NC
PIN0A_80	NC	PIN0A_80	NC
PIN0A_81	NC	PIN0A_81	NC
PIN0A_82	NC	PIN0A_82	NC
PIN0A_83	NC	PIN0A_83	NC
PIN0A_84	NC	PIN0A_84	NC
PIN0A_85	NC	PIN0A_85	NC
PIN0A_86	NC	PIN0A_86	NC
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PIN0A_88	NC	PIN0A_88	NC
PIN0A_89	NC	PIN0A_89	NC
PIN0A_90	NC	PIN0A_90	NC
PIN0A_91	NC	PIN0A_91	NC
PIN0A_92	NC	PIN0A_92	NC
PIN0A_93	NC	PIN0A_93	NC
PIN0A_94	NC	PIN0A_94	NC
PIN0A_95	NC	PIN0A_95	NC
PIN0A_96	NC	PIN0A_96	NC
PIN0A_97	NC	PIN0A_97	NC
PIN0A_98	NC	PIN0A_98	NC
PIN0A_99	NC	PIN0A_99	NC
PIN0A_100	NC	PIN0A_100	NC



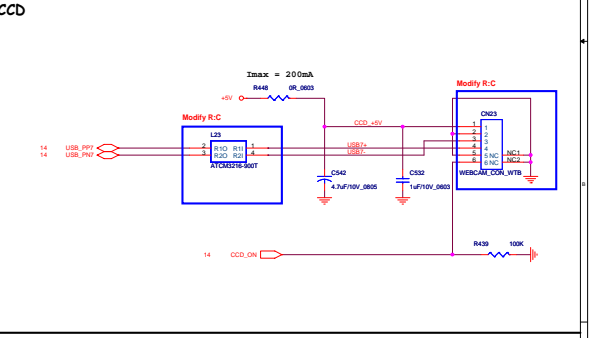
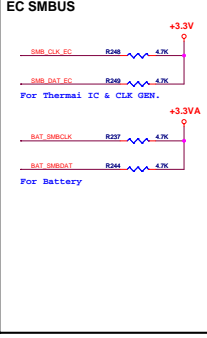
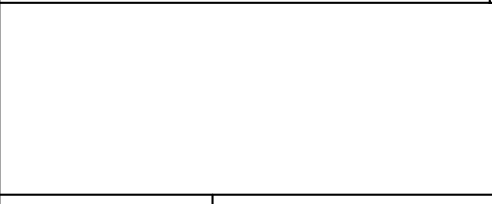
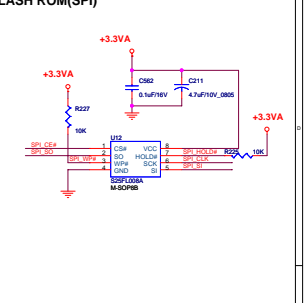
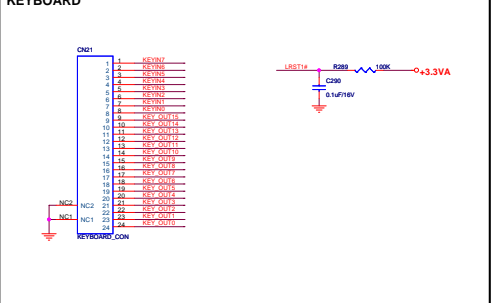
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1	FSB500	1



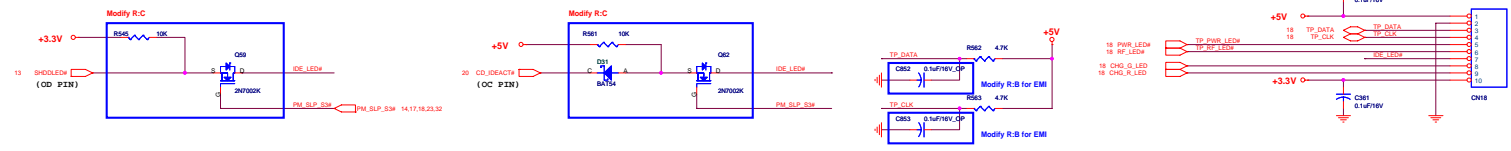
Part		VGA GFX_CORE(OZ827)	
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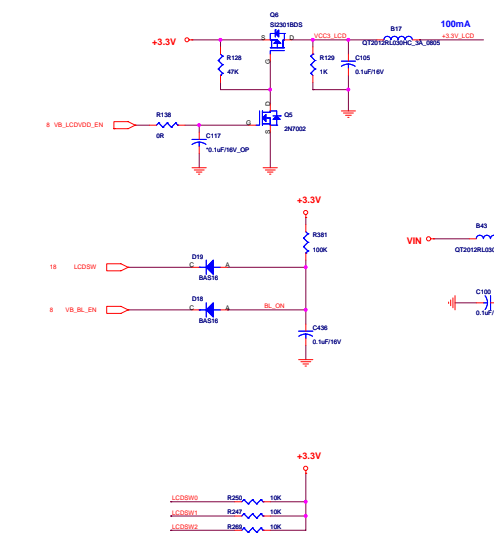
IC	ID	P_ID0	P_ID1
US0110	R:A	0	0
US0110	R:B	0	0
US0110	R:C	1	0



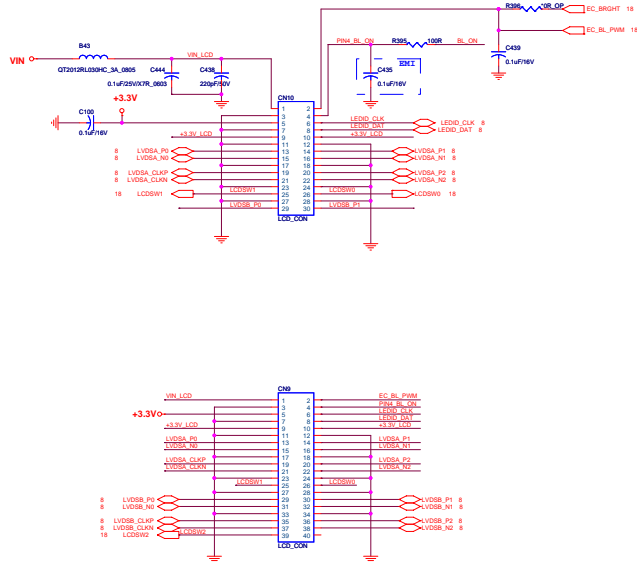
Touch Pad



LCD

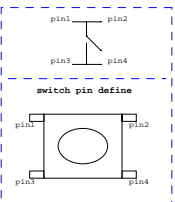
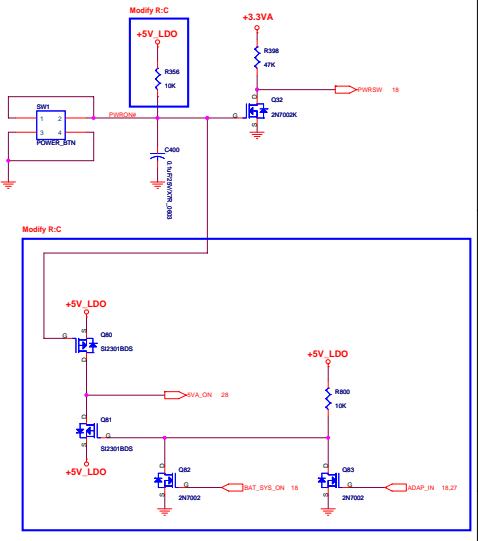


COLAY CN10 & CN11



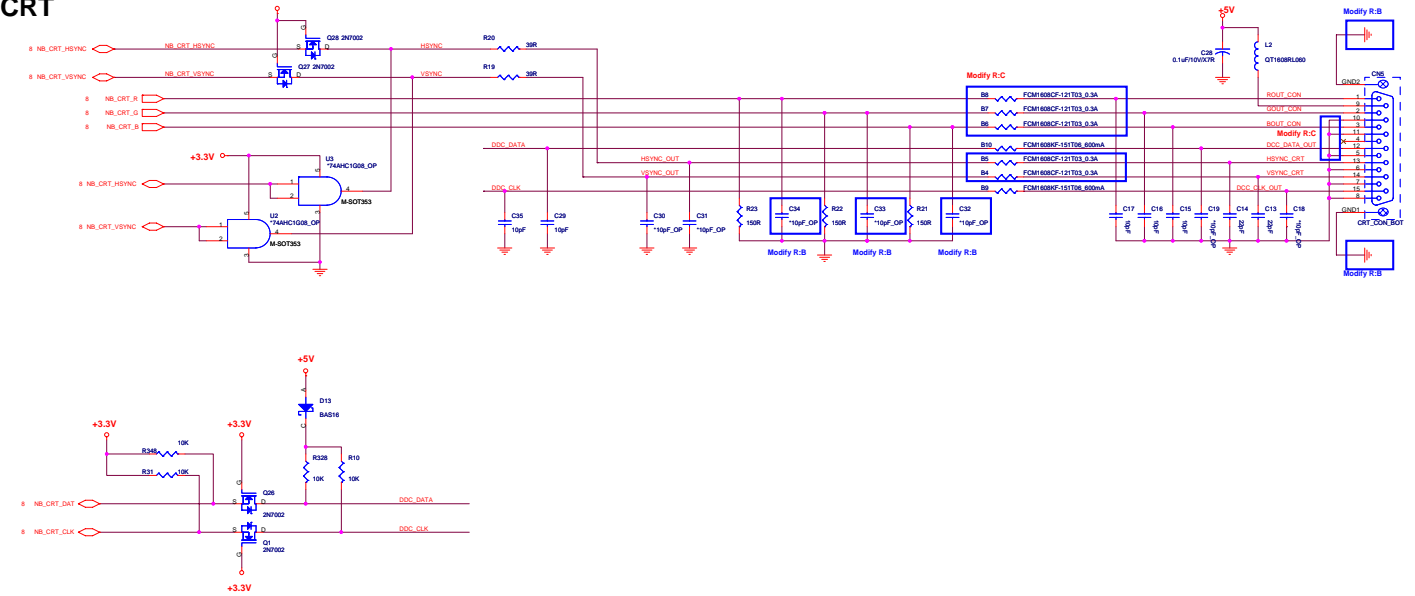
Panel ID	SW0	SW1	SW2
15*	1	1	1
14*	1	0	1
13*	0	0	1

PWR SW

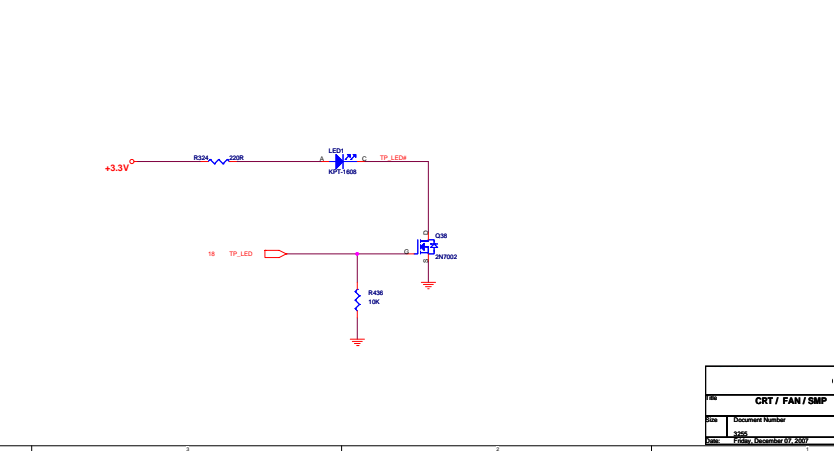
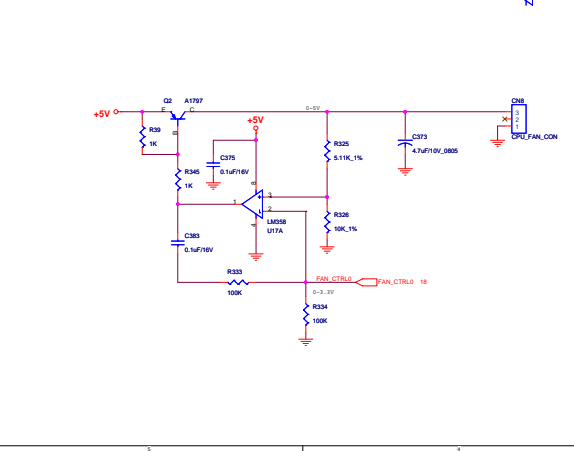


Rev	PWR SW / LED / LCD / TP
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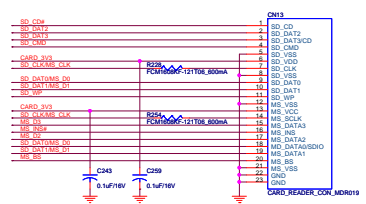
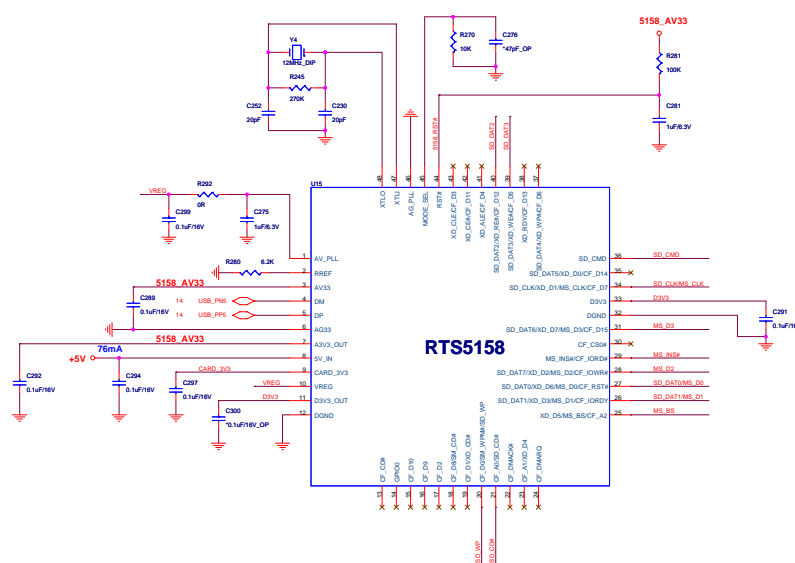
CRT

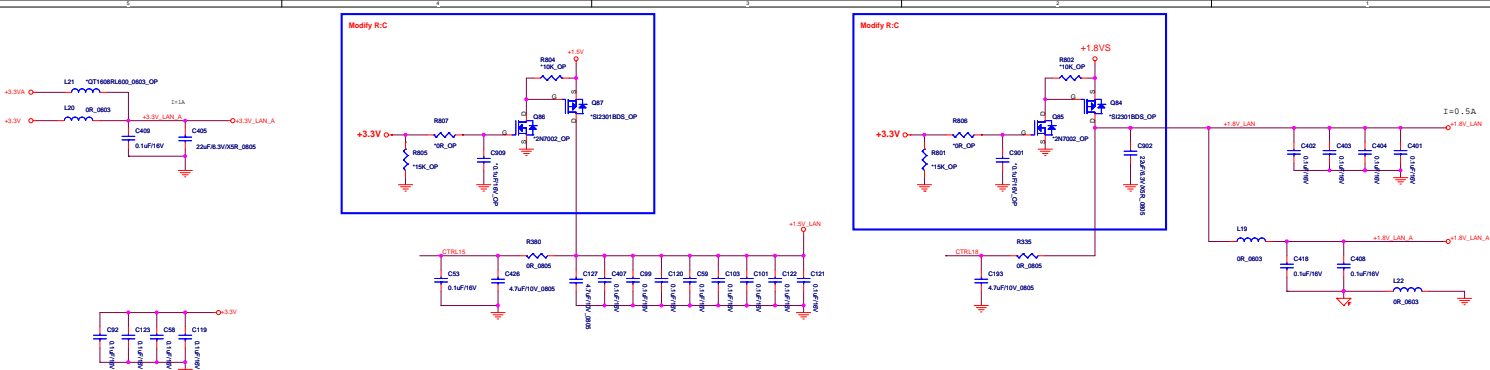


CPU FAN CONTROL

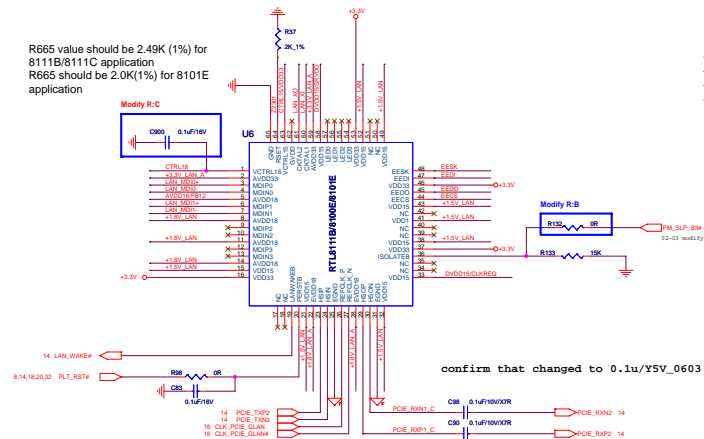


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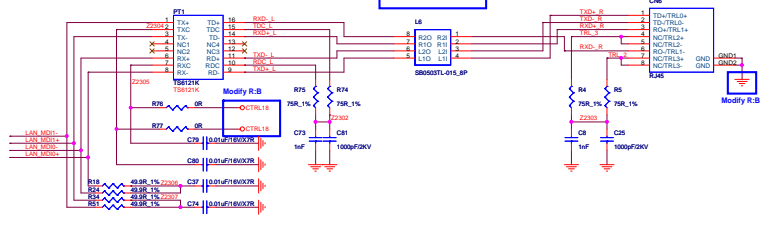


R665 value should be 2.49K (1%) for 8111B/8111C application
 R665 should be 2.0K(1%) for 8101E application



For RTL8111B/8101E application.

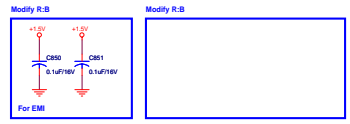
+1.5V LAN	R37A	OR	AVDD15LKB32
+1.5V LAN	R38A	OR	DVDD15LKB00
+1.5V LAN	R39A	OR	DVDD15LKB00
CTRL15	R38	OR	CTRL15VDD03



Power domain chart

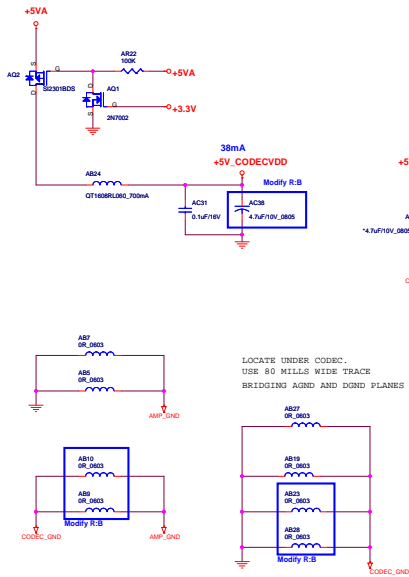
	RTL8111B / RTL8101E	RTL8111C
AVDD33	3.3V	3.3V
AVDD18	1.8V	1.2V
EVDD18	1.8V	1.2V
DVDD15	1.5V	1.2V

	Q76	Q77
RTL8111B	Need	Need
RTL8111C	N/A	N/A
RTL8101E	N/A	N/A

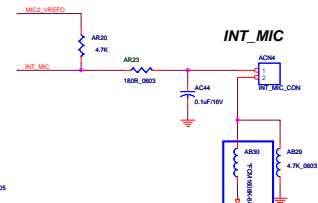


10100M LAN (RTL8101E)		
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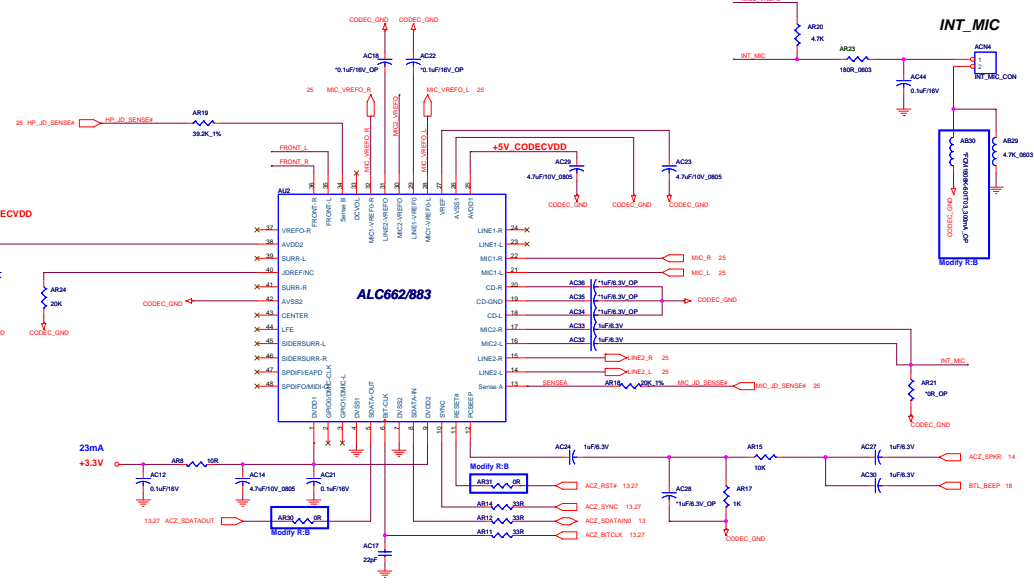
AMP VDD



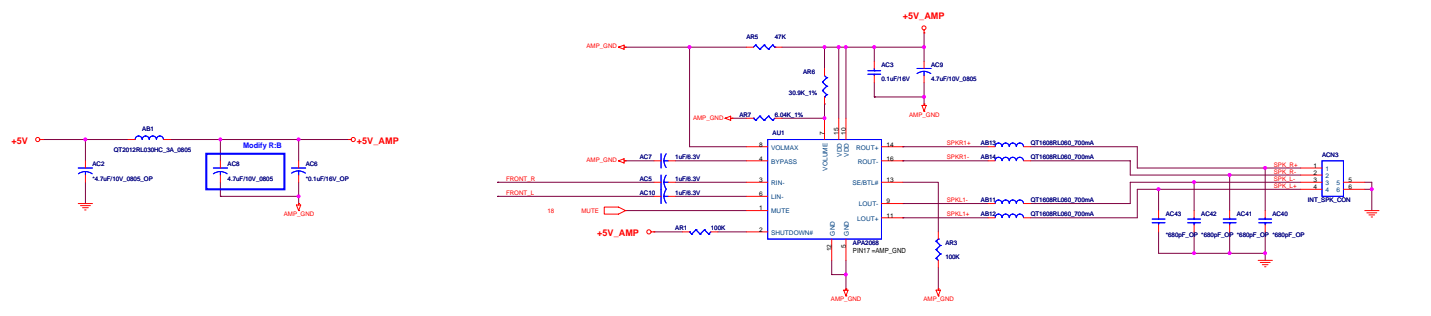
INT_MIC



ALC662/883

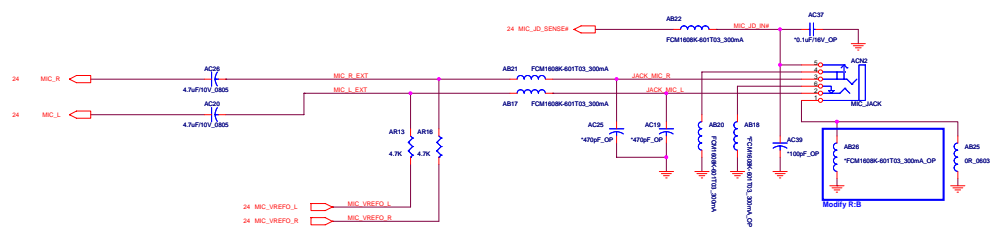


INT_SPK AMP



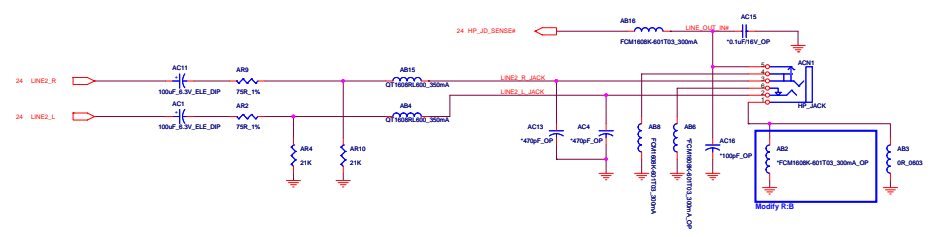
CODEC / AMP / INT_MIC / SPK		
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MIC/Line In JACK



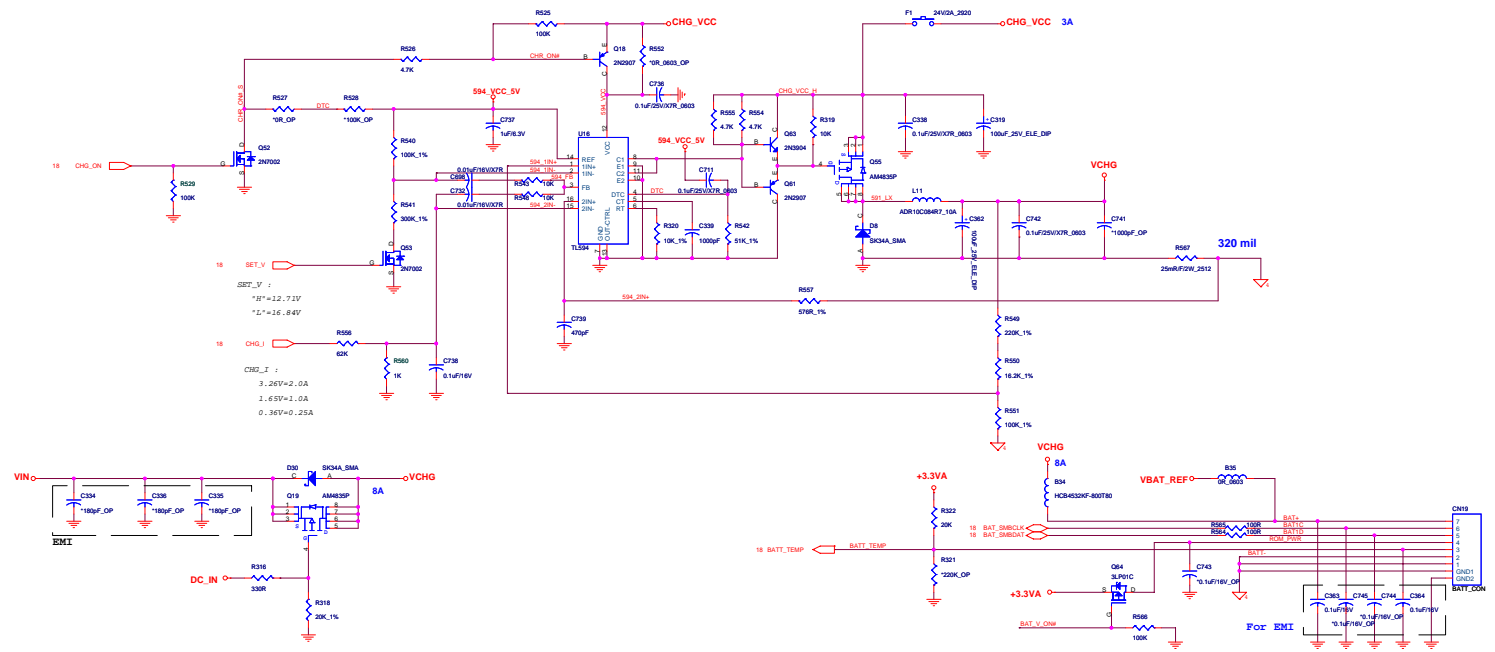
Modify R-C

HeadPhone JACK

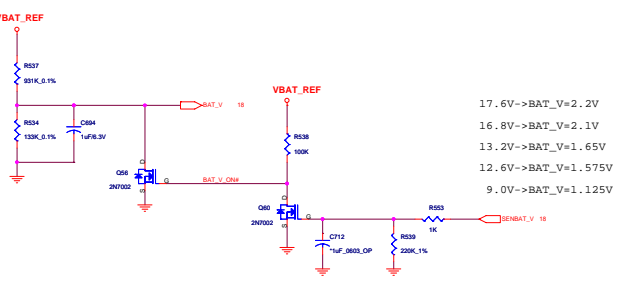


Modify R-C

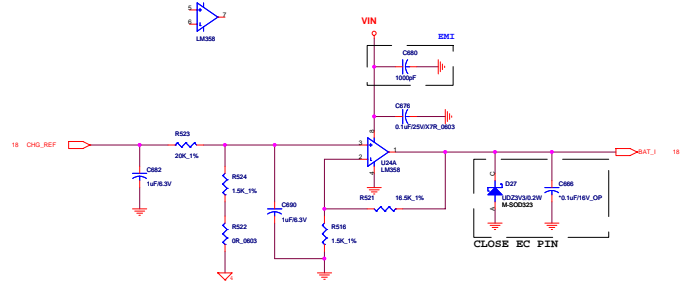
SPK / MIC / HEADPHONE	
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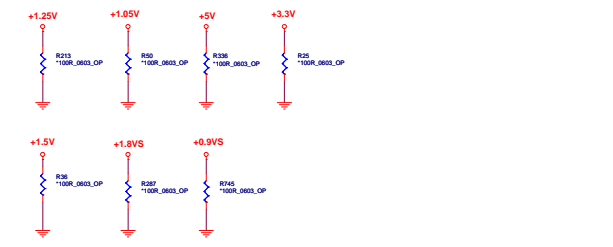
Battery Voltage Detect



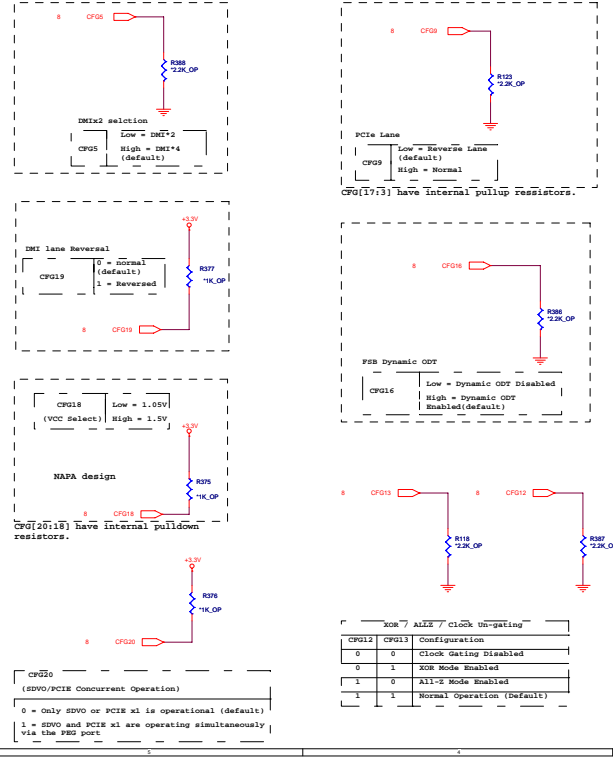
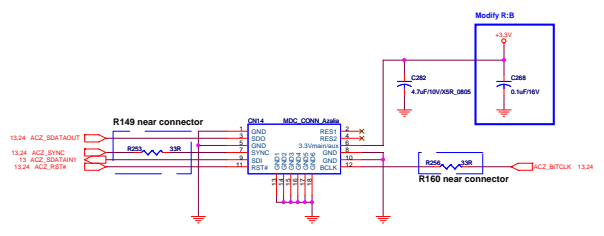
Charge / Discharge Detect



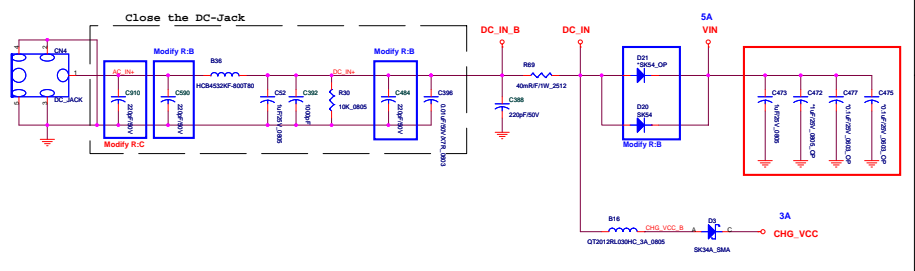
BATT IN / Charger		
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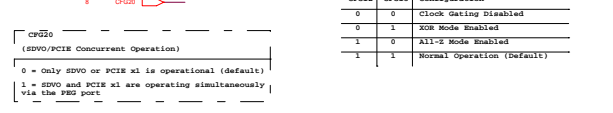
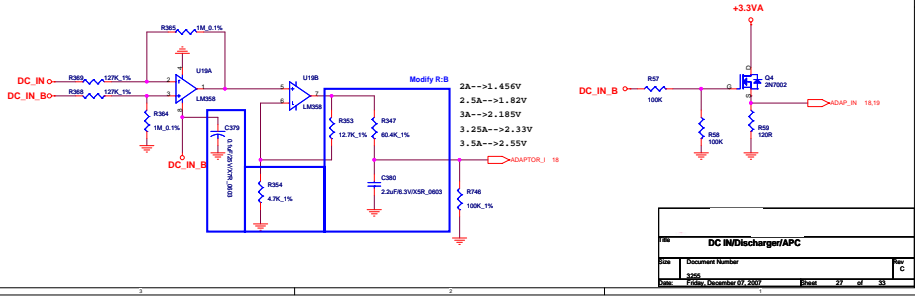
MDC



DC IN



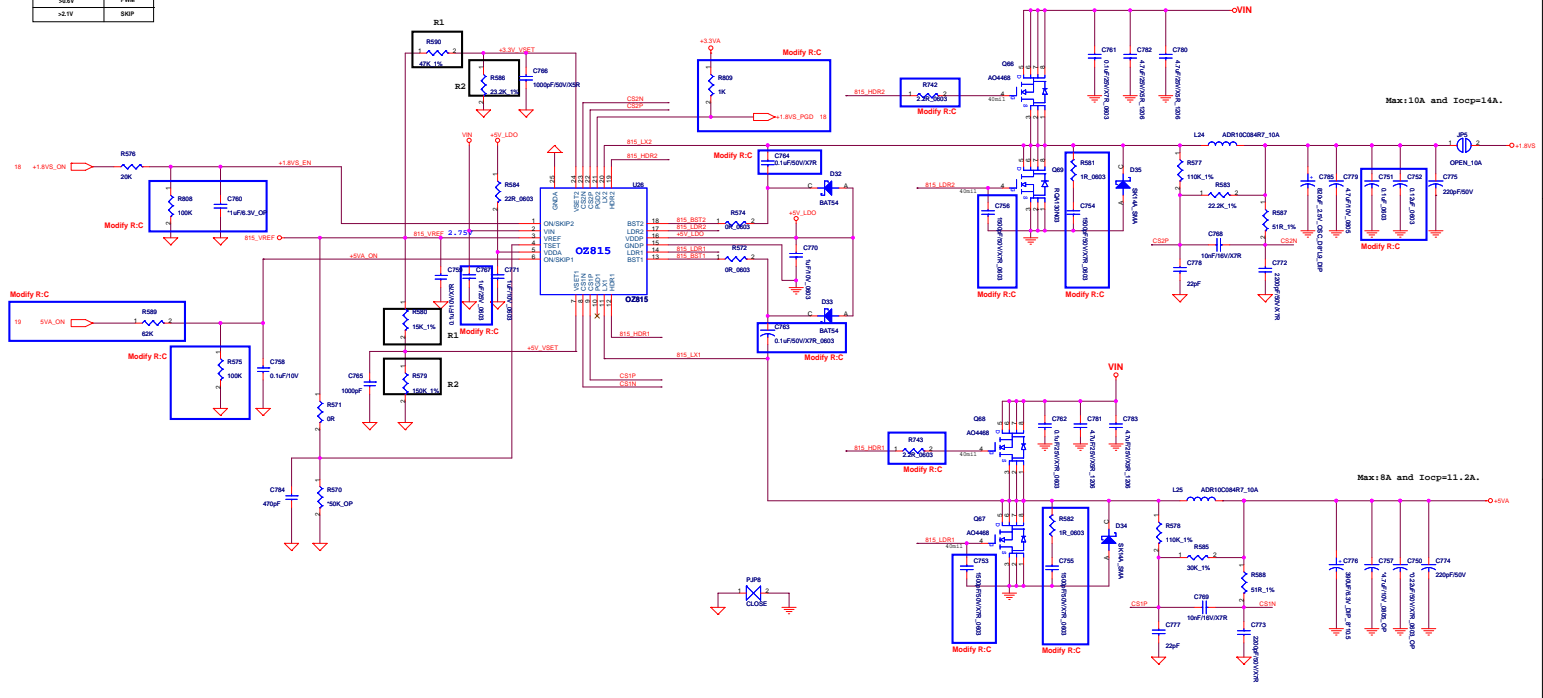
TOTAL POWER



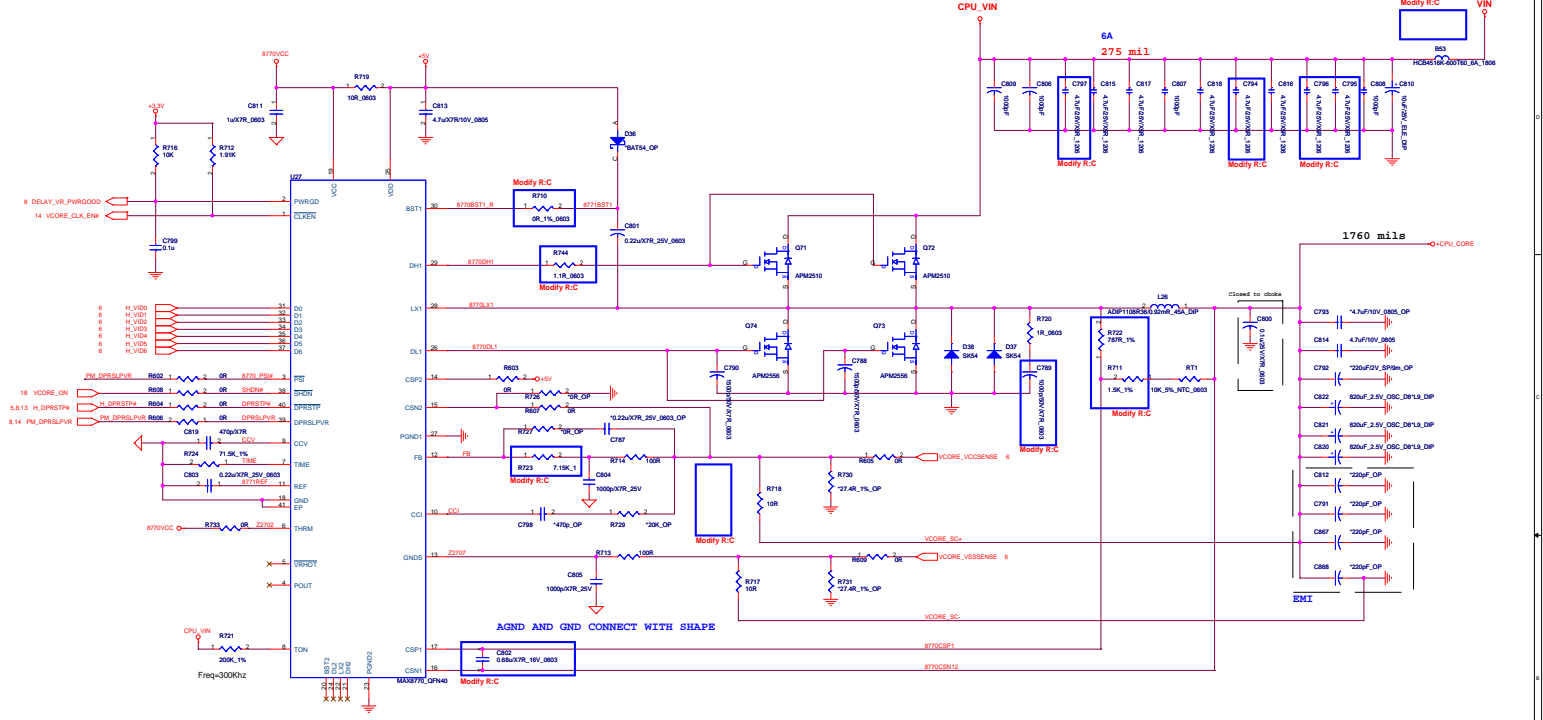
DC IN/Discharger/APC			
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+5VA_V ON Voltage	Mode
$-0.4V$	OFF
$-0.8V$	FRM
$-1.2V$	SKP

Output Voltage = $[V_{ref} \times R2 / (R1 + R2)] \times 2$

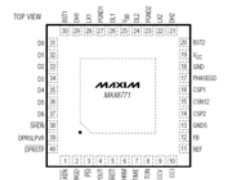
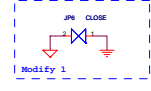


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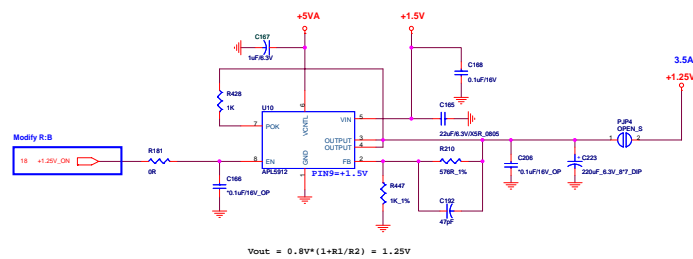
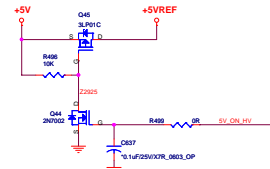
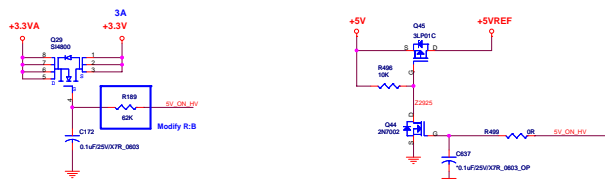
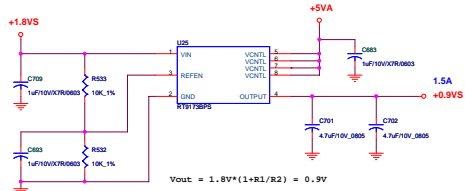


VID TABLE

Bit	5	4	3	2	1	0	Vcore	Status
0	0	1	0	0	0	0	1.2875	(HFM)
0	0	1	2	0	0	0	1.2000	Boot Volt
0	0	1	1	1	0	0	1.2500	Merom(HFM)
0	1	1	0	1	0	1	0.8375	YAM(LFM)
0	1	1	1	0	1	1	0.7625	YAM(Deeper Sleep)
1	1	1	1	1	1	1	0.0000	Shut down

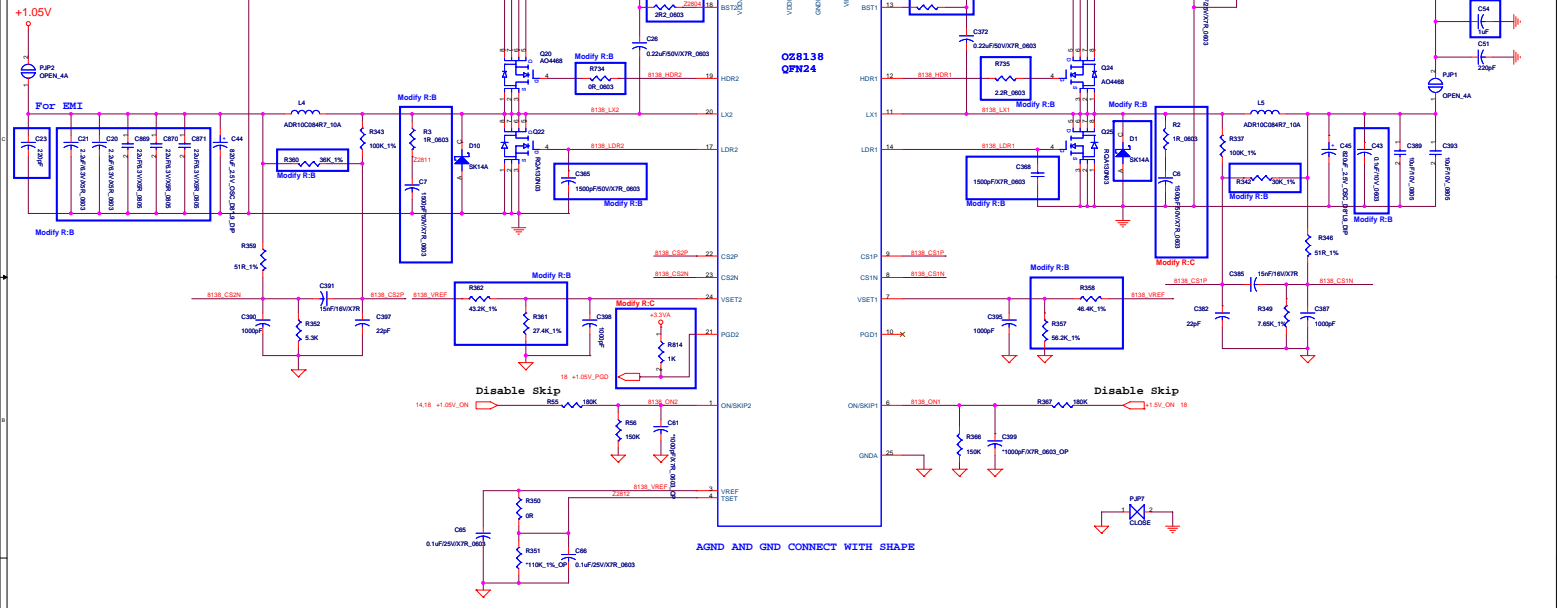


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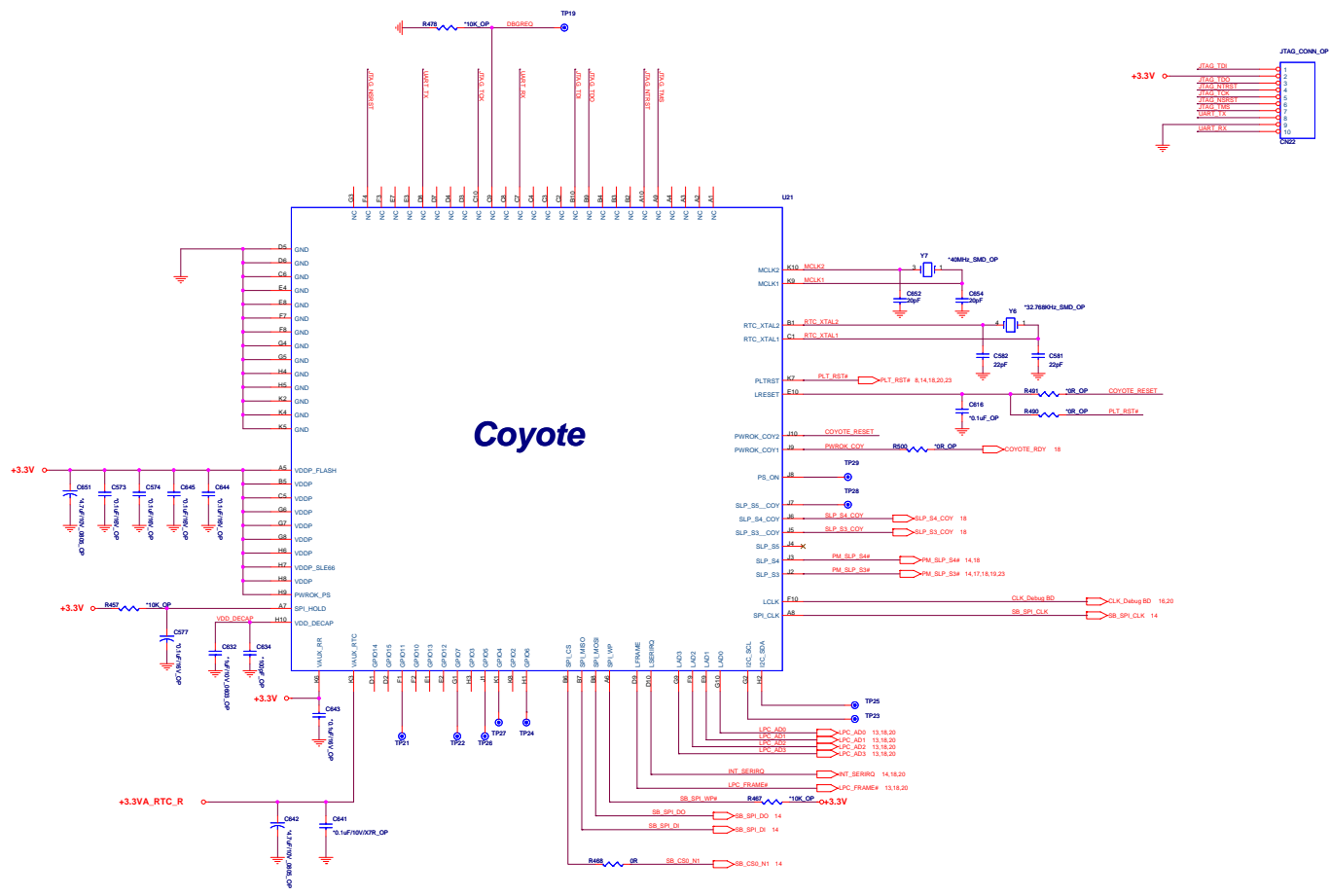
360 mils
Max 8A

160 mils
Max 6A



File	+1.5V+1.05V(OZ8138)	
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Coyote



Part	Coyote
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RA to RB change list:

- Page 5,M1->Add C856 1uF/6.3V for +1.05V
- Page 5,M2->Modify H_THERM# and PM_THERMTRIP# circuit
- Page 6,M3->Add C528/C569/C558/C527/C441/C442/C449/C448(1uF/6.3V) for +CPU_CORE
- Page 6,M4->Add C410/C416/C422/C431/C429/C412/C423/C432(2.2uF/6.3V/XSR_0603) FOR +CPU_CORE
- Page 6,M5->Add C491/C476/C518/C467/C143/C142/C141/C140/C858/C859/C860/C861/C862/C863/C864/C865(2.2uF/6.3V/XSR_0603) FOR +1.05V
- Page 7,M6->Add C855/C854 (0.1uF/16V)
- Page 8,M7->Add R600 (30k ohm) for GFX enable pin pull high to +3.3V
- Page 10,M8->Add C857 (22uF/6.3V/XSR_0805) for +1.05V
- Page 13,M11->C182 change to 6.8pF
- Page 13,M12->C180 change to 8.2pF
- Page 13,M13->Add C470 (10pF) for EMI
- Page 13,M14->C124/C94 change to 22pF
- Page 17,M15->R344 change to 0 ohm
- Page 17,M16->C371 change to 1500pF/50V/X7R_0603
- Page 17,M17->Add R736 2.2R_0603
- Page 17,M18->Original R355 pull high to +3.3VS change to pull high to +3.3V
- Page 17,M19->Add R371/R370 0 ohm
- Page 18,M20->EC_EXTSMI Swarp to U13/Pin 23
- Page 18,M21->Add +1.25V_CN (EC Pin117)
- Page 19,M22->Delete R397/Q31/Q35
- Page 20,M23->Add C710 (4.7uF/10V_0805) For V_CDROM
- Page 20,M24->Remove L3/L7 and Add RP20/RP21
- Page 21,M25->R6/R7/BB change to FCML608CF-600T06 TAI-TECH
- Page 21,M26->Remove C34/C33/C32
- Page 22,M27->CN23 change (53398-0610L)
- Page 23,M28->C48/C49 change to 22pF
- Page 23,M29->Delete R131 and Add R132
- Page 23,M30->R76/R77 change net link to CTRL18
- Page 23,M31->Add R335 and Remove R78
- Page 24,M32->Add AB710/AB9/AB23/AB28
- Page 24,M33->Add AC38/AC9/AB30/AR31/AB30
- Page 25,M34->Remove AB26/AB2
- Page 27,M35->Add R745/C590/C484
- Page 27,M36->Delete R183/R207/R206/Q9/Q10/R407/Q36
- Page 27,M37->R347 change to 60.4K ohm
- Page 27,M38->Delete C386 and add R746 100K ohm
- Page 28,M39->+5VA and +1.8VS PWM solution change to OZ815
- Page 29,M40->CPU_CORE PWM solution change to MAX8770
- Page 30,M41->R181 link to +1.25V_CN
- Page 31,M42->R189 change to 60.4K ohm
- Page 31,M43->R190 change to 0 ohm and C175 OP
- Page 32,M44->R360 change to 36K ohm
- Page 32,M45->R8/R9 change to 2R2 ohm
- Page 32,M46->C365/C368 change to 1500pF
- Page 32,M47->R342 change to 30K ohm 1%
Page 32,M48->R362 change to 43.2K ohm
Page 32,M49->R361 change to 27.4K ohm
Page 32,M50->R357 change to 56.2K ohm
Page 32,M51->R358 change to 46.4K ohm
- Page 32,M52->Add C21/C20/C869/C870/C871/R734/R735
- Page 32,M53->C43 change to 0.1uF/ Add C54 1uF

Change Notes		
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