U40II1 Schematics Rev:C

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Elitegroup Computer Systems

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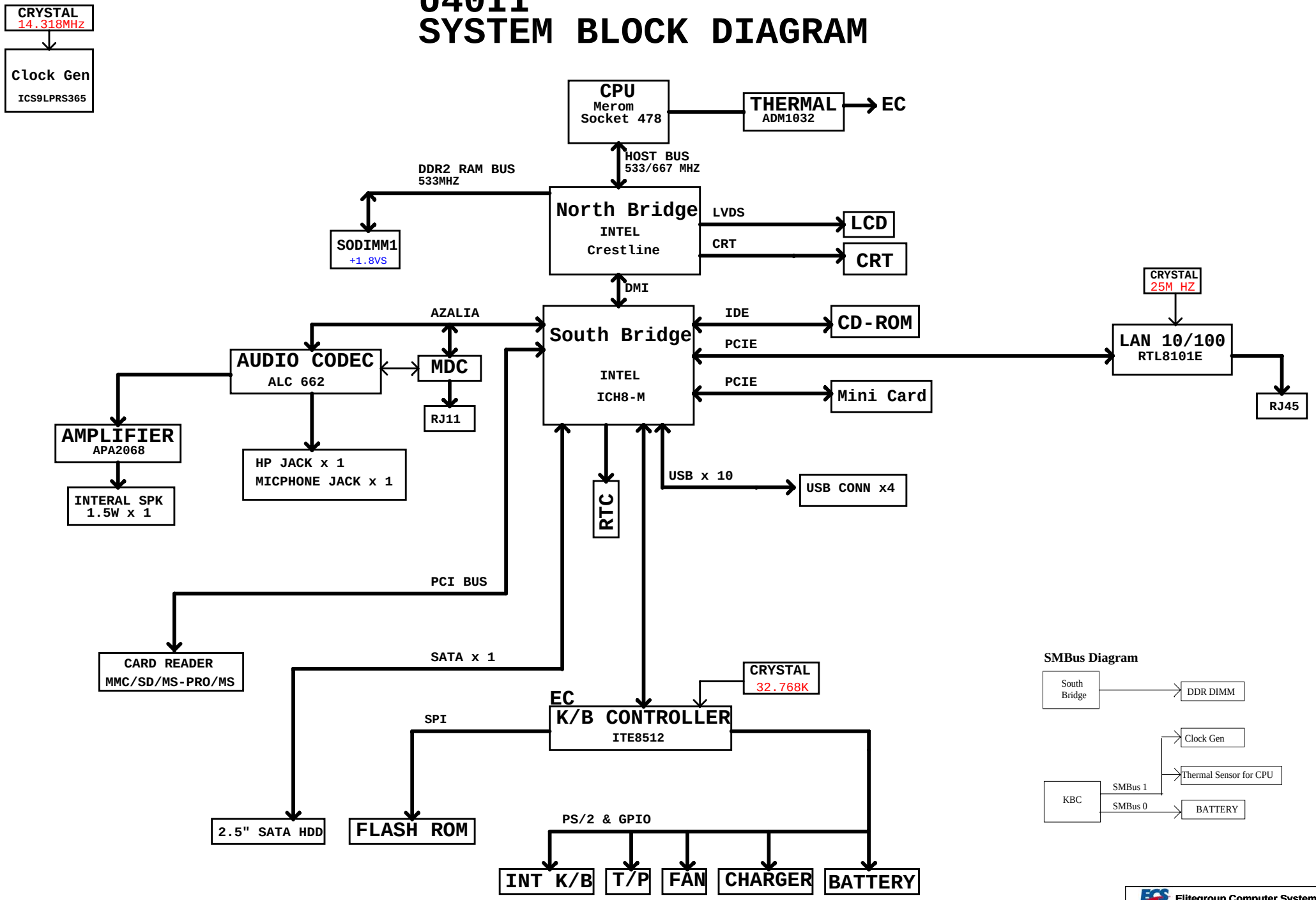
U40II
SYSTEM BLOCK DIAGRAM



SMBus Diagram

Elitegroup Computer Systems

Title	SYSTEM BLOCK DIAGRAM	
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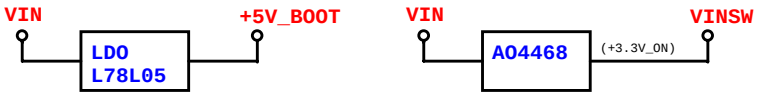
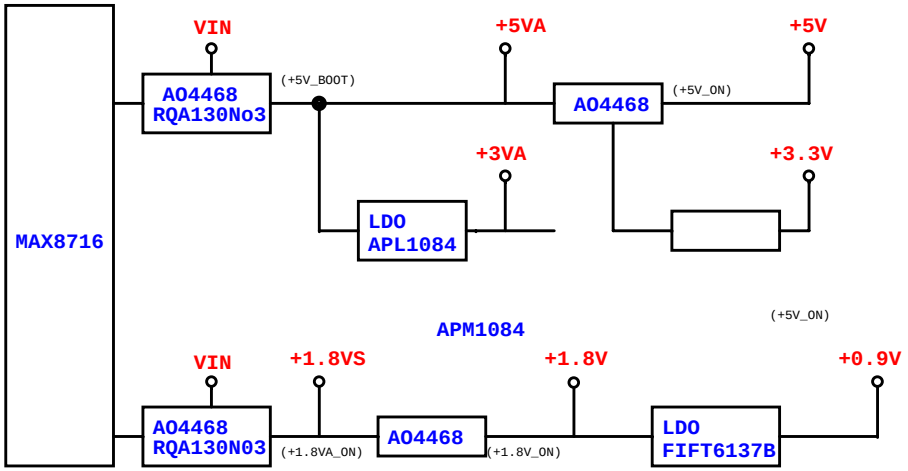
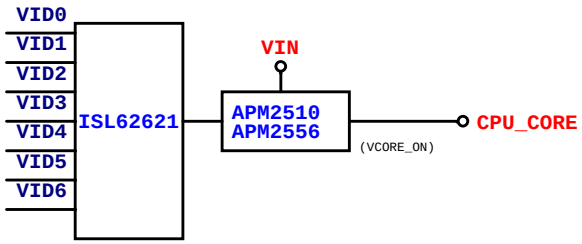
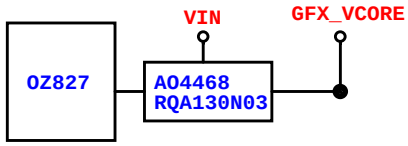
POWER BLOCK DIAGRAM









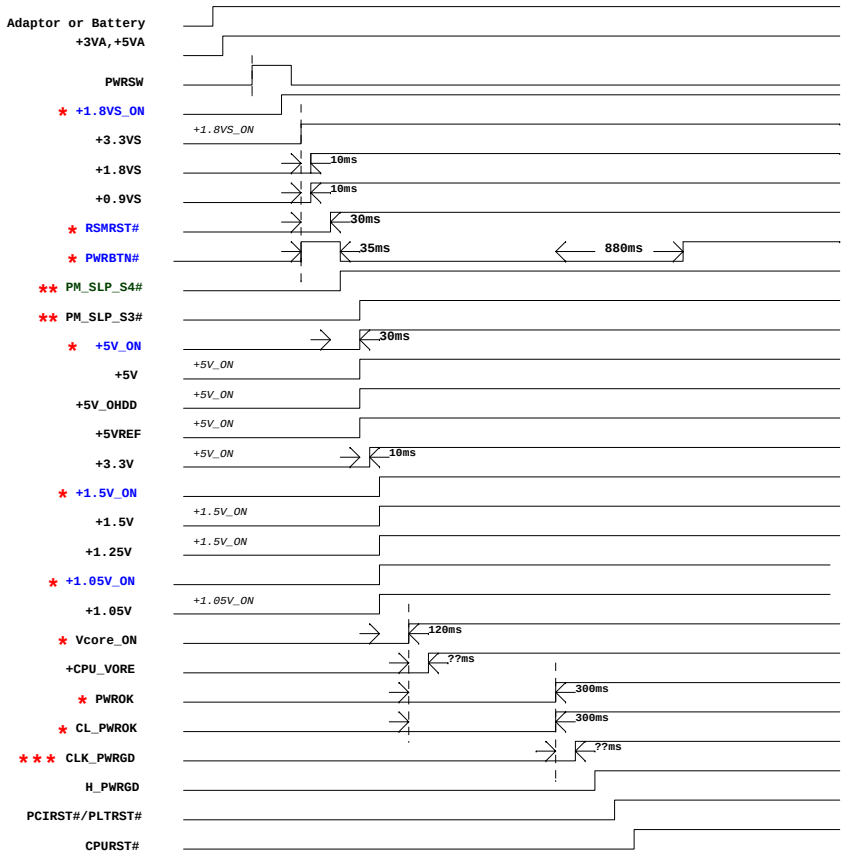
POWER Sequence

EC debouns 100ms

* EC Control Pin (O/P)

* * EC Control Pin(I/P)

* * * SB Control Pin(O/P)



Elitegroup Computer Systems		
Title: POWER DIAGRAM & SEQUENCE		
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SIS968 GPIO	
GPIO0	NC
GPIO1	NC
GPIO2	PM_THROTTING#
GPIO3	EC_EXTSMI#
GPIO4	PM_CLKRUN#
GPIO5	NC
GPIO6	NC
GPIO7	NC
GPIO8	NC
GPIO9	NC
GPIO10	SLP_S5#
GPIO11	AGPSTOP_N
GPIO12	DPSLP#
GPIO13	SB_DPRSLPVR
GPIO14	NC
GPIO15	SLP_S3#
GPIO16	NC
GPIO17	H_A20GATE
GPIO18	H_RCIN#
GPIO19	SB_SMB_CLK
GPIO20	SB_SMB_DATA

ITE8512E GPIO	
GPA0	AUX_PWRGD
GPA1	DDR_V_SW#
GPA2	BTL_BEEP
GPA3	RFLED_ON
GPA4	SCROLL/3G_LED
GPA5	NUM_LED
GPA6	CAPS_LED
GPA7	PWRON_LED
GPB0	PM_SLP_S5#
GPB1	PM_SLP_S3#
GPB2	WEBCAM_ON
GPB3	BAT_SMBCLK
GPB4	BAT_SMBDAT
GPB5	H_A20GATE
GPB6	H_RCIN#
GPB7	BT_ON
GPC0	EC_VID5
GPC1	SMBCLK_EC
GPC2	SMBDAT_EC
GPC3	EC_VID2
GPC4	RF_SW_ON#
GPC5	EC_VID1
GPC6	INTERNET#
GPC7	SILENT#
GPD0	EC_PREST#
GPD1	PWRBTN#
GPD2	EC_LPCRST#
GPD3	EC_EXTSCI#
GPD4	EC_EXTSMI#
GPD5	H_PROCHOT#
GPD6	CHG_ON
GPD7	LCDSW
GPE0	EC_PWR_ON
GPE1	SET_V
GPE2	PWROK
GPE3	VCORE_ON
GPE4	LID#
GPE5	AC_IN/OUT#
GPE6	FAN_SPD# or RTCRST
GPE7	AMP_MUTE#
GPF0	3G_ON
GPF1	EC_BSEL1
GPF2	CHG_G_LED
GPF3	CHG_R_LED
GPF4	TP_CLK
GPF5	TP_DATA
GPF6	VGA_SMBCLK
GPF7	VGA_SMBDAT
GPG0	EC_VID3
GPG1	EC_WDOG OK
GPG2	FLFRAME#
GPG6	NEW_CARD_PWR_ON#
GPH0	+1.8V_ON
GPH1	+1.8VS_ON
GPH2	SENBAT_V
GPH3	+3.3VS_ON
GPH4	+5V_ON
GPH5	VDD_CORE_ON
GPH6	EC_VID4

ITE8512E GPIO	
GPI0	BATT_TEMP
GPI1	ADAPTOR_I
GPI2	BAT_V
GPI3	CPPE#
GPI4	BAT_I
GPI5	EC_CPU_PWR
GPI6	DDR2_TEMP
GPI7	ADAP_IN
GPJ0	EC_BRGHT
GPJ1	CHG_I
GPJ2	FAN_CTRL0
GPJ3	SILENT_LED
GPJ4	SMP1_EN#
GPJ5	PM_THROTTING#

del VGA_TEMP

CPU

	CPU CORE(V)	ICC(mA)	W	TEMP( )
2.0G	1.525	35.7	54.3	69
2.2G	1.525	37.5	57.1	70
2.26G	1.525	38.1	58.0	70
2.4G	1.525	39.3	59.8	71
2.5G	1.525	40	61.0	72
2.53G	1.525	40.4	61.5	72
2.6G	1.525	41.05	62.6	72
2.66G	1.525	43.35	66.1	74
2.8G	1.525	44.86	68.4	75
3.06G	1.525	55.9	85.2	81

VCC	ICC(mA)	W	TEMP( )
+1.5V	120	0.18	
+1.05V	2500	2.625	70

672MX

VCC	ICC(mA)	W	TEMP( )
+1.2V	2303	2.76	
+1.8V	1215	2.18	70
+1.05V	80	0.084	

SIS968

VCC	ICC(mA)	W	TEMP( )
+3.3V	86	0.283	
+1.8V	851	1.531	70
+1.05V	22	0.022	

307LV

VCC	ICC(mA)	W	TEMP( )
+3.3V	236	1.107	
+1.8V	565	0.778	70

CLOCK GENERATOR+BUFFER

VCC	ICC(mA)	W	TEMP( )
+3.3V	400	1.32	70
+1.8V	300	0.54	

ITE8512E

VCC	ICC(mA)	W	TEMP( )
+3.3V	200	0.66	70
+3.3VA	500	1.65	

RTS5158

VCC	ICC(mA)	W	TEMP( )
+5V	76	0.38	85

RTL8201CL

VCC	ICC(mA)	W	TEMP( )
+3.3V	20	0.396	85

ALC662

VCC	ICC(mA)	W	TEMP( )
+3.3V	23	0.075	70
+5VA	38	0.19	

APA2068

VCC	ICC(mA)	W	TEMP( )
5V	20	0.1	85

ADM1032

VCC	ICC	W	TEMP( )
+3.3V	170uA	0.56mW	150

SMART POWER TABLE

VID6	VID5	VID4	VID3	VID2	VID1	VID0	VCORE	+_mV
0	0	0	0	0	0	0	1.5000	-0mV
0	0	0	0	0	0	1	1.4875	-2.5mV
0	0	0	0	0	1	0	1.4750	-5mV
0	0	0	0	1	0	0	1.4500	-50mV
0	0	0	1	0	0	0	1.4000	-100mV
0	0	1	0	0	0	0	1.3000	-200mV
0	1	0	0	0	0	0	1.1000	-400mV
1	0	0	0	0	0	0	0.7000	-800mV
0	0	1	1	0	1	1	1.1625	
0	0	1	0	0	0	1		
0	0	1	0	0	1	0		
0	0	1	0	1	0	0		
0	0	1	0	1	1	0		
0	0	1	1	0	0	1		
0	0	1	1	0	1	0		

Elitegroup Computer Systems

Title: GPIO & POWER Consumption

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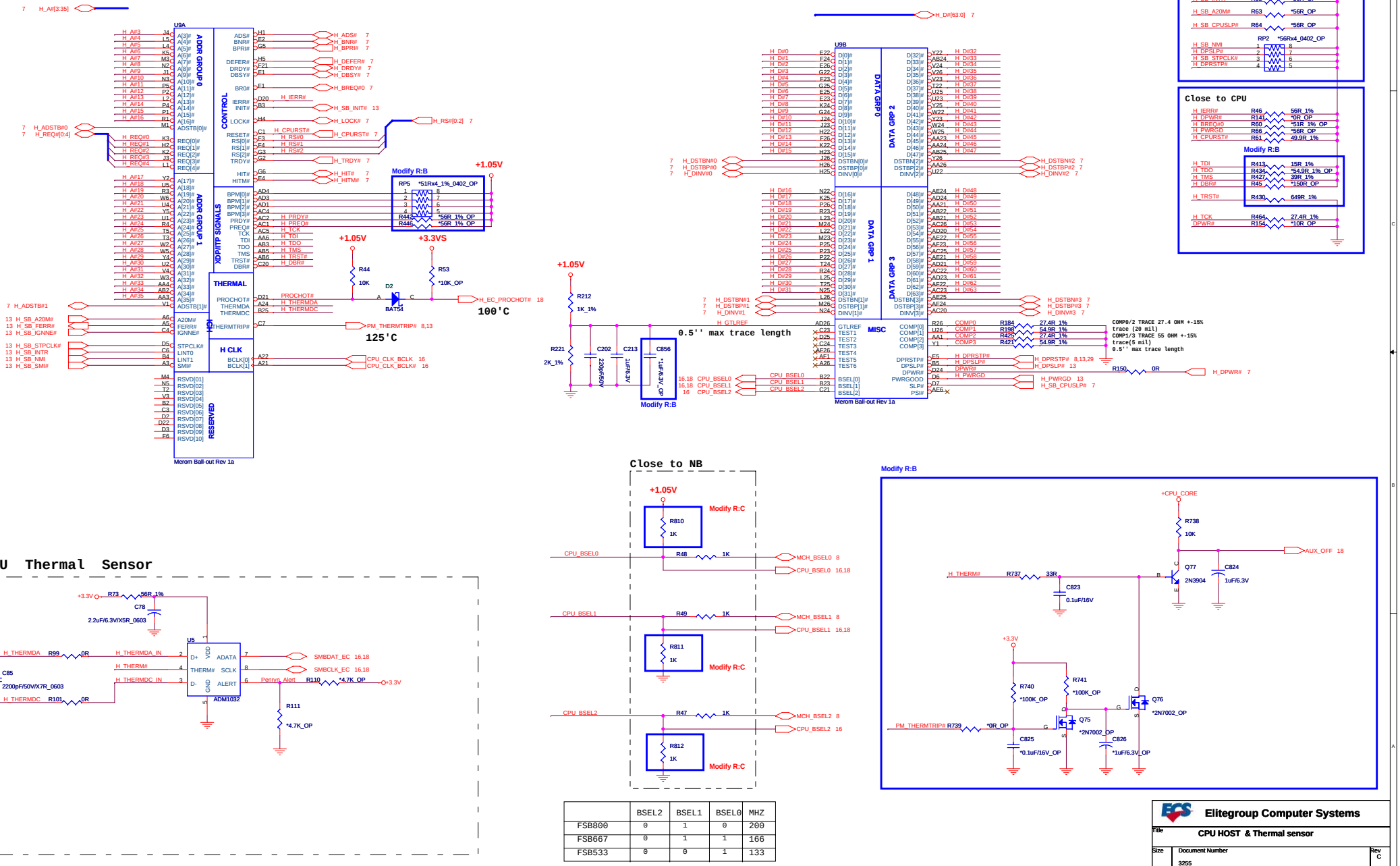

	BSEL2	BSEL1	BSEL0	MHZ
FSB800	0	1	0	200
FSB667	0	1	1	166
FSB533	0	0	1	133

Elitegroup Computer Systems

Title: CPU HOST & Thermal sensor

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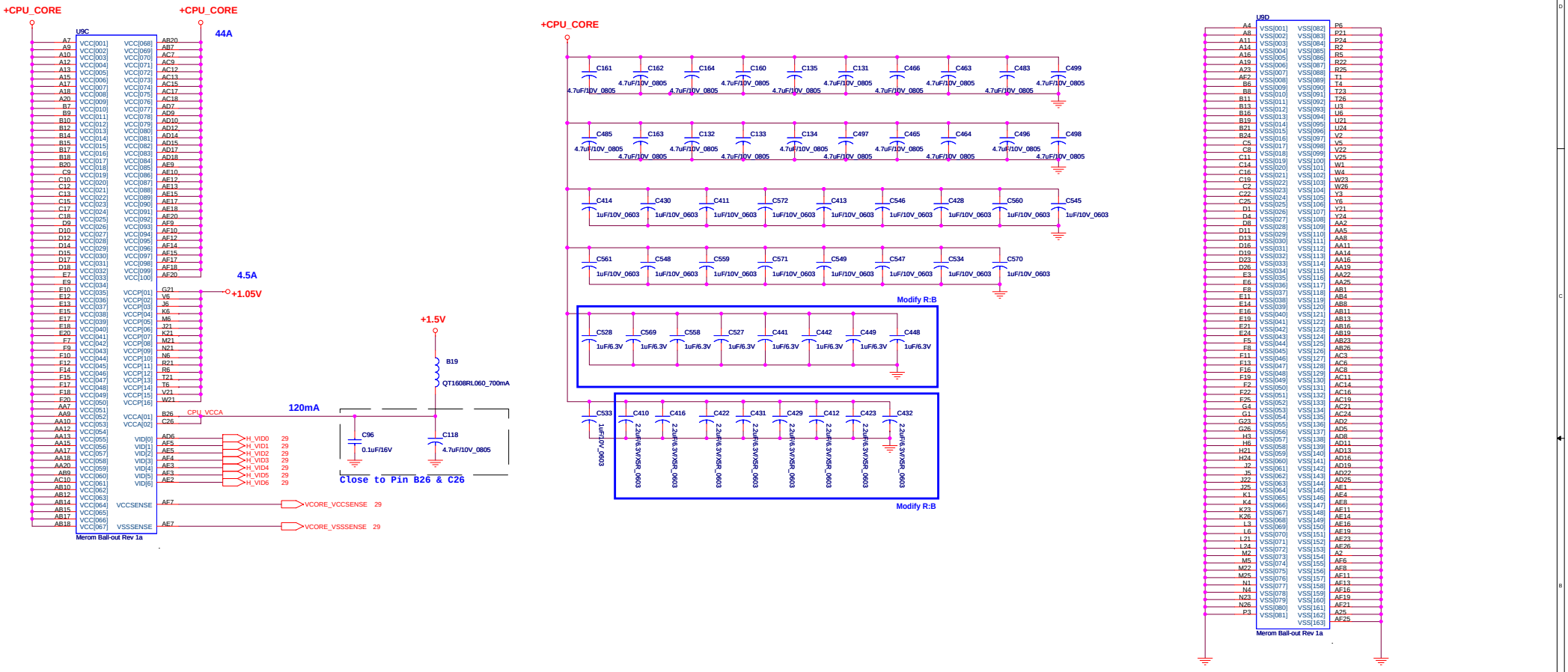

Elitegroup Computer Systems			
Title: CPU Power			
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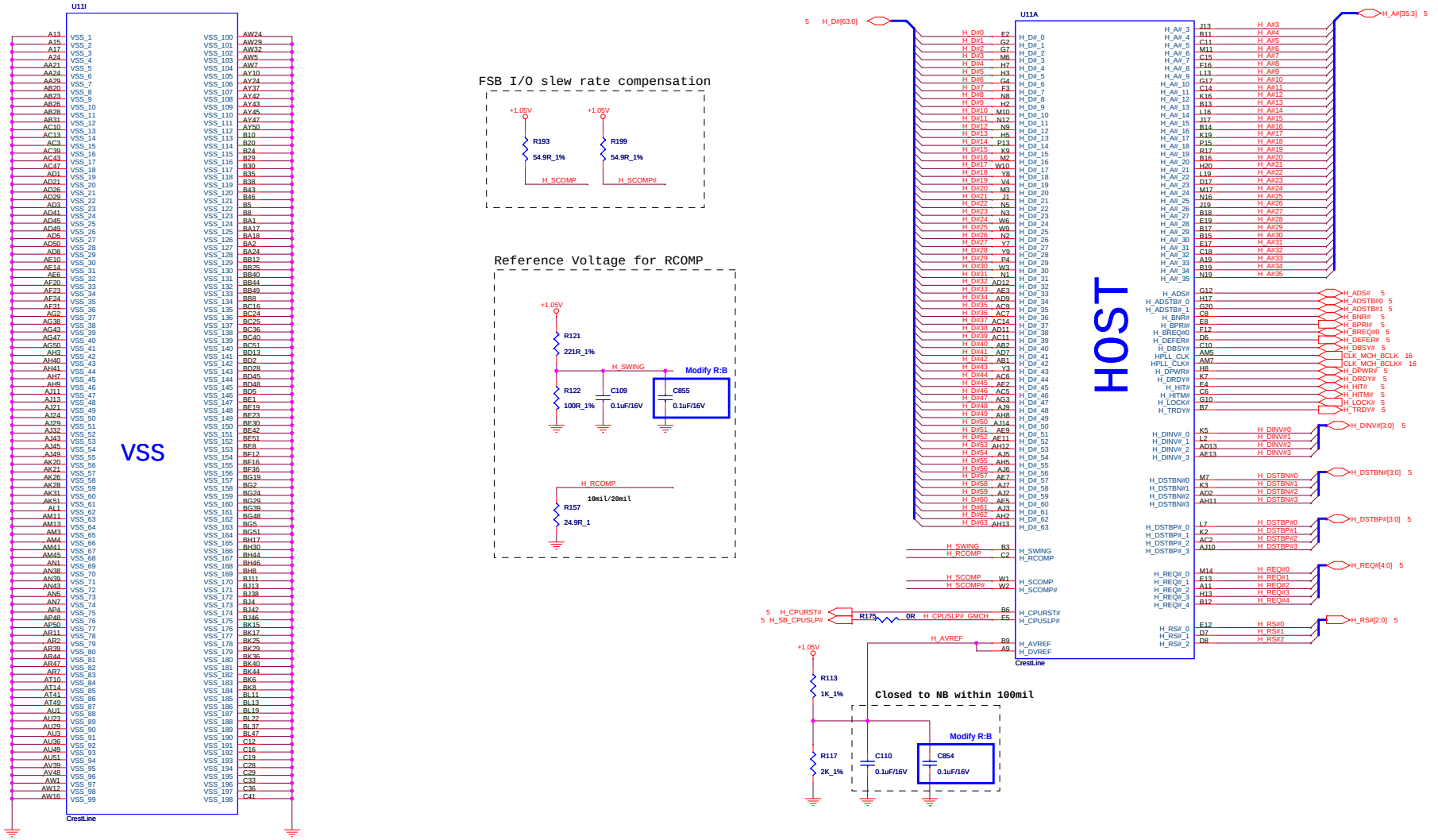

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Title	NB HOST-1/5
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GCLK => PCI-E & DMI (100MHZ)
DREFCLK => Dispaly PLLA (nun- ss 96MHZ)
DRESSFCLK => Display LVDS PLLB (ss 100MHZ)

CFG0	CFG1	CFG2	Host Clock Frequency
0	1	0	800
1	1	0	667

GHCH integrated graphics busy

CLK_REQ:Asserted to control the raw PCI-E clock

SDVOCLK has internal pull down
SDVODATA has internal pull down
0= no DVO device
1= DVO device present

CFG20 (IPD) => 1= Only SDVO or PCI-E
0 = SDVO and PCI-E
CFG7 (IPU) => 1= Mobility CPU
0 = Reverse

Colse to NB

Reserve for NB THRMTRIP# (O/D Vccp)

Checking

For MEN bus throttling
Check with S/W

Modify R:B

CrestLine

Elitegroup Computer Systems	
Title	NB DDRCLK_VGA_PCIEXPR-2/5
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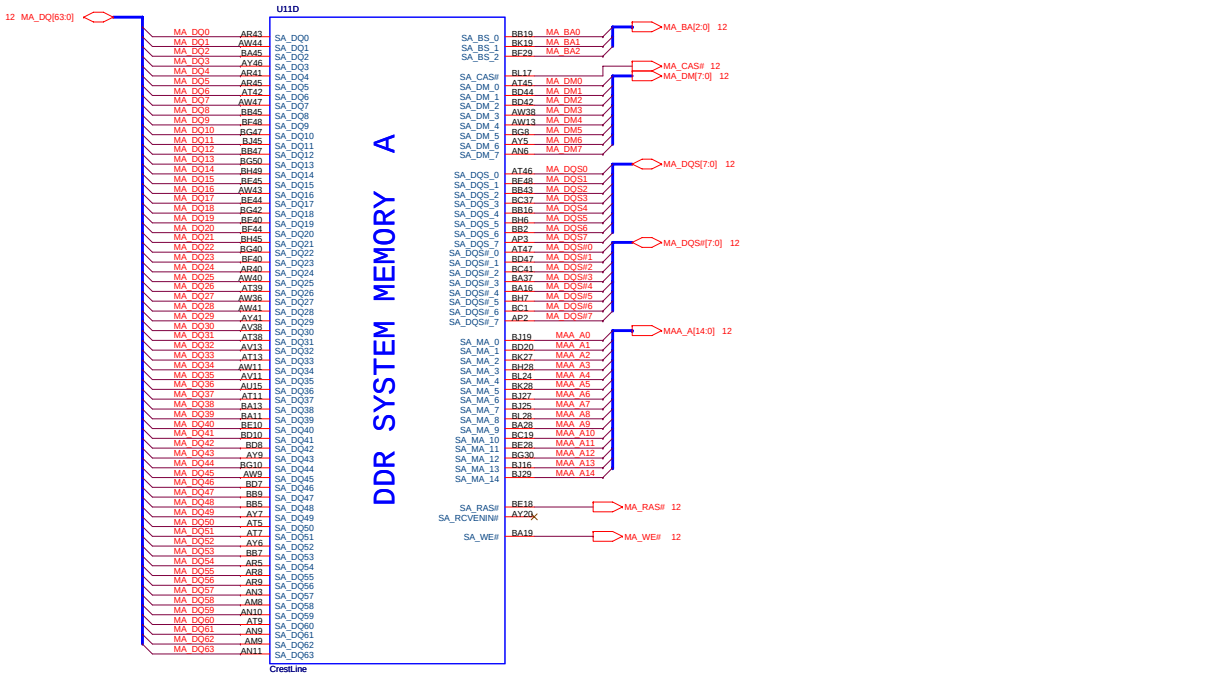

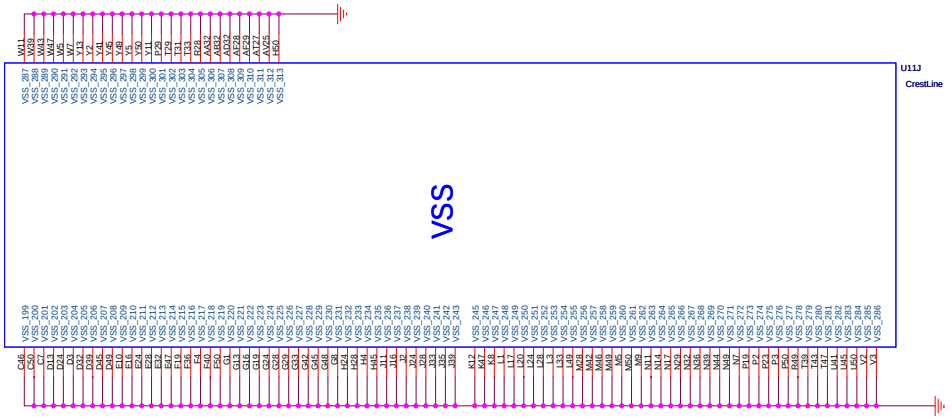

Elitegroup Computer Systems			
Title: NB DDR_MEM SYSTEM-3/5			
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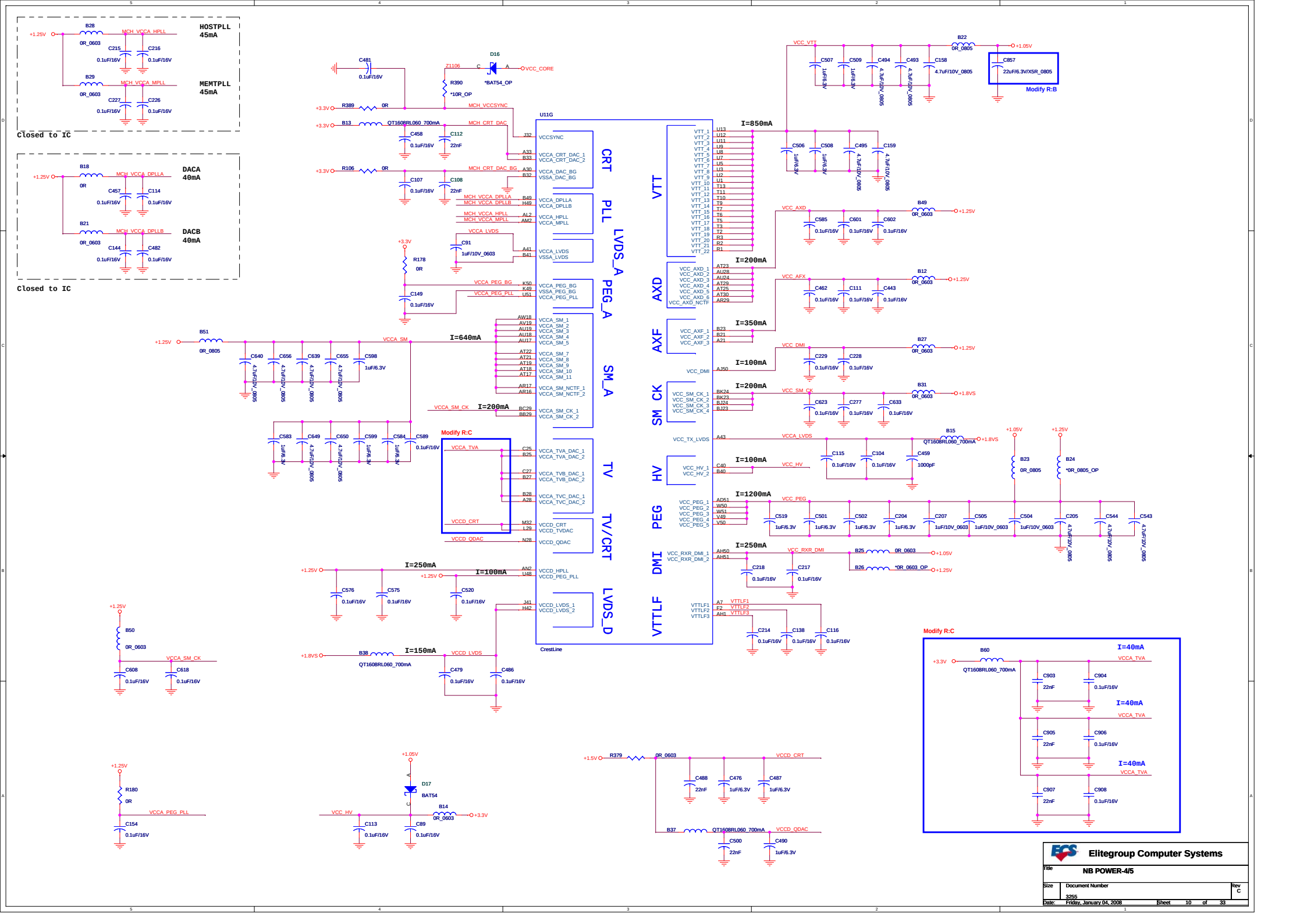
HOSTPLL
45mA
MEMTPLL
45mA
Closed to IC
DACA
40mA
DACB
40mA
Closed to IC
U11G
CRT
PLL
LVDS_A
PEG_A
SM_A
TV
TV/CRT
LVDS_D
VTT
AXD
AXF
SM CK
PEG
HV
DMI
VTTLF
CrestLine
I=850mA
I=200mA
I=350mA
I=100mA
I=200mA
I=640mA
I=200mA
I=100mA
I=1200mA
I=250mA
I=250mA
I=100mA
I=150mA
Modify R:B
Modify R:C
Modify R:C
I=40mA
I=40mA
I=40mA
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NB POWER-4/5
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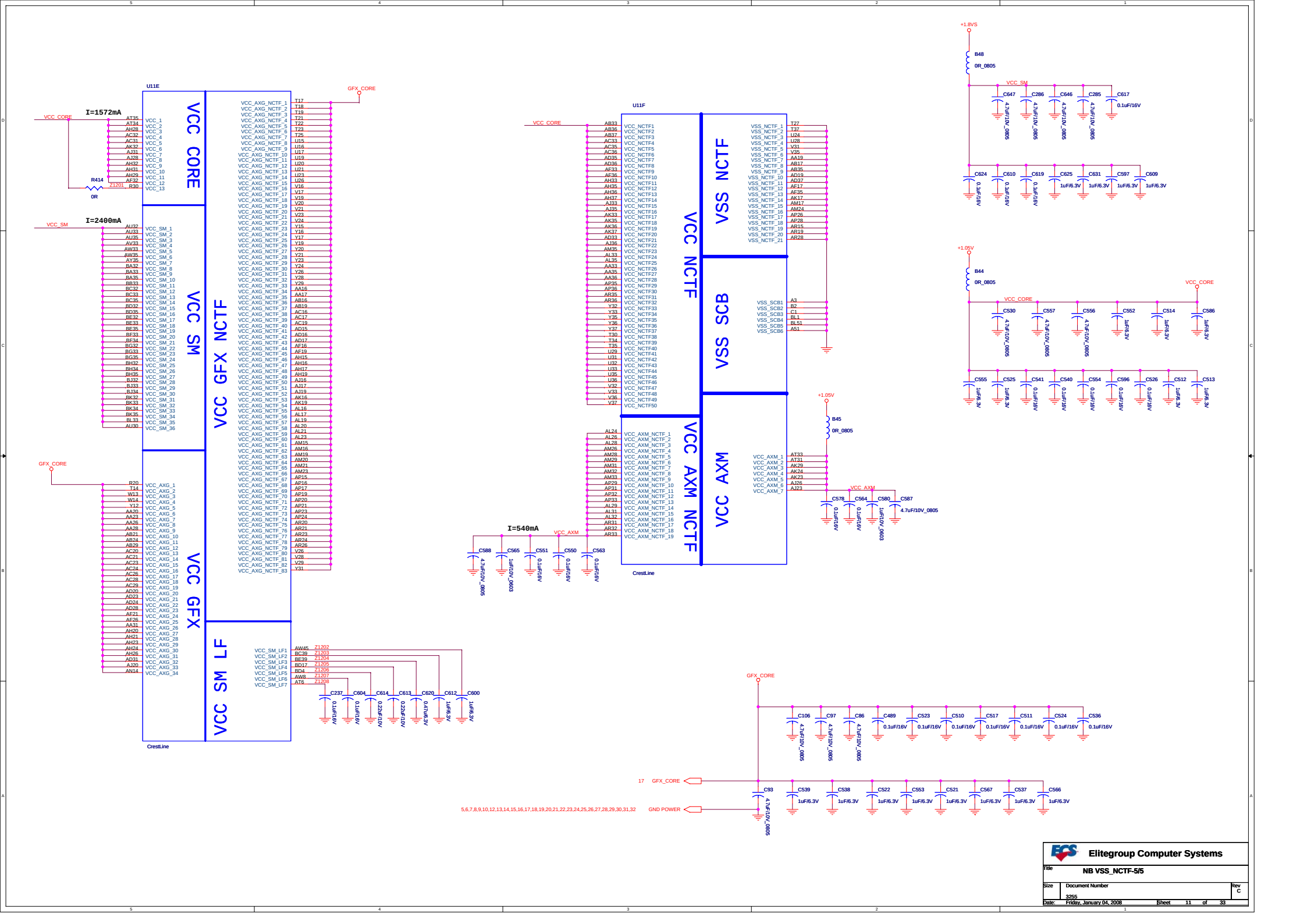

Elitegroup Computer Systems	
Title	NB VSS_NCTF-5/5
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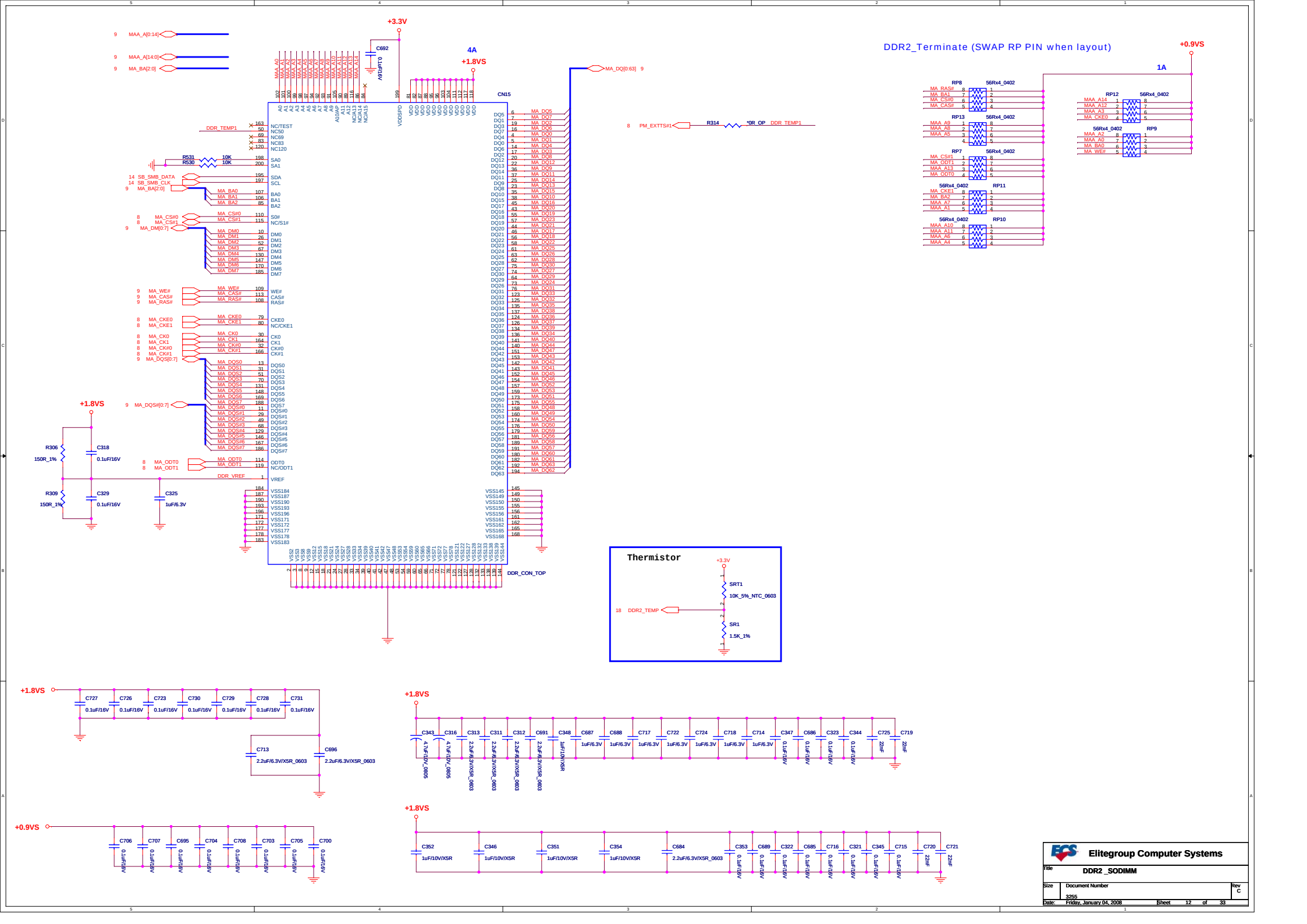
DDR2_Terminate (SWAP RP PIN when layout)
+3.3V
+1.8VS
+0.9VS
4A
1A
CN15
DDR_CON_TOP
Thermistor
SRT1
10K_5%_NTC_0603
SR1
1.5K_1%
DDR2_TEMP
DDR_TEMP1
R314
*0R_OP
PM_EXTTS#1
MA_DQ[0:63]
MAA_A[0:14]
MA_BA[2:0]
MA_DM[0:7]
MA_DQS[0:7]
MA_DQS#[0:7]
RP8
RP12
RP13
RP9
RP7
RP11
RP10
56Rx4_0402
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DDR2 _SODIMM
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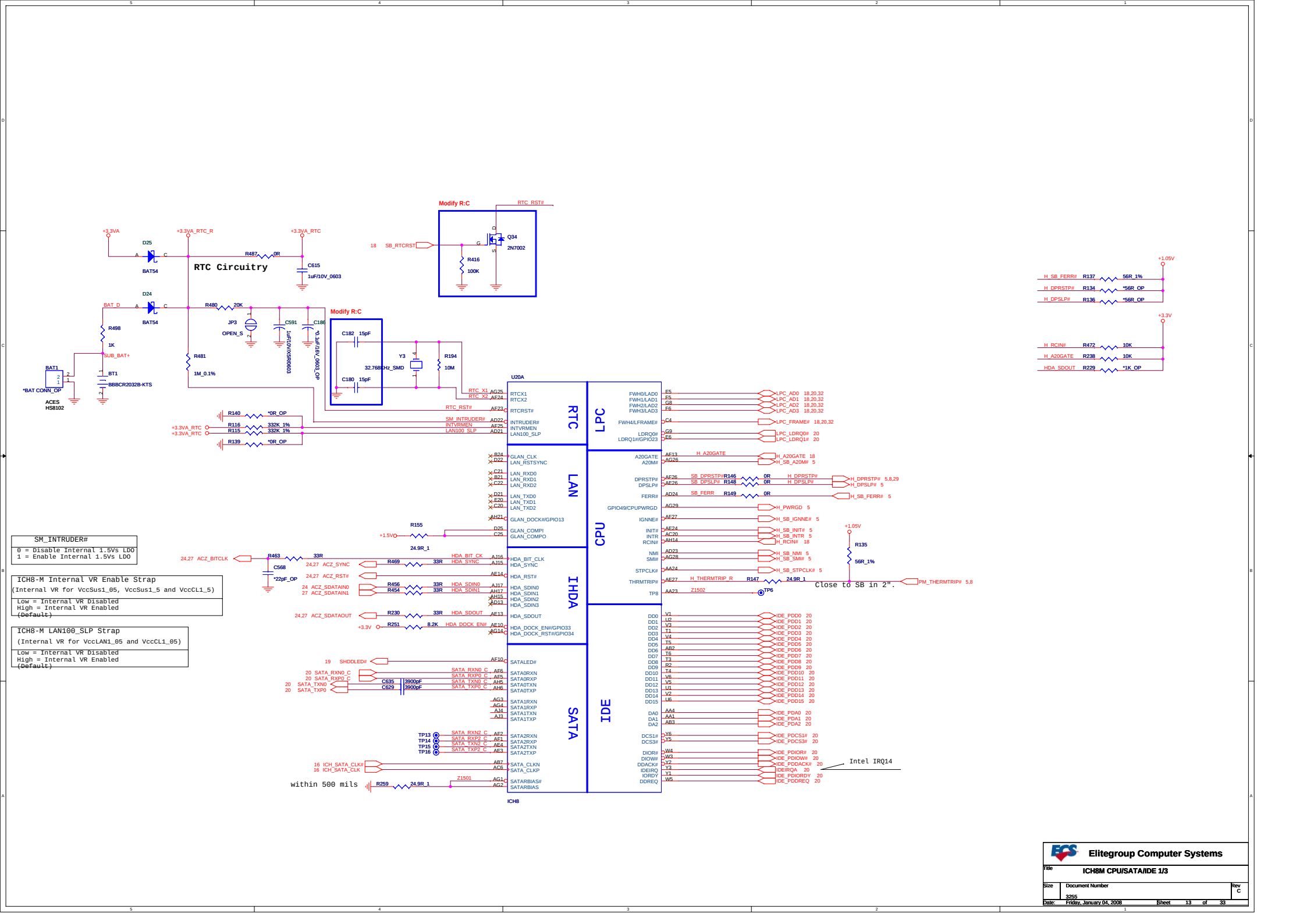
RTC Circuitry
Modify R:C
Modify R:C
U20A
RTC
LPC
LAN
CPU
IHDA
IDE
SATA
ICH8
SM_INTRUDER#
0 = Disable Internal 1.5Vs LDO
1 = Enable Internal 1.5Vs LDO
ICH8-M Internal VR Enable Strap
(Internal VR for VccSus1_05, VccSus1_5 and VccCL1_5)
Low = Internal VR Disabled
High = Internal VR Enabled
(Default)
ICH8-M LAN100_SLP Strap
(Internal VR for VccLAN1_05 and VccCL1_05)
Low = Internal VR Disabled
High = Internal VR Enabled
(Default)
Close to SB in 2".
Intel IRQ14
within 500 mils
Elitegroup Computer Systems
ICH8M CPU/SATA/IDE 1/3
Friday, January 04, 2008

(3.3V , 1.5V , PCI CLK 99mS)

INTA# : NC	PREQ#0 : NC
INTB# : NC	PREQ#1 : 1394
INTC# : 1394	PREQ#2 : NC
INTD# : (1394)	PREQ#3 : NC
	PREQ#4 : NC

Sample on PWROK rise edge

01 = SPI
10 = PCI
11 = LPC
(Internal PU)

Strap SPKR 1:Normal
0:No Reboot Mode

close SB

Modify R:C

INTEL RECOMMAND 10K

Resume Power GPIO [8:10] [12:15] [25:28]

	U40II1
USB0	Mini Card
USB1	USB
USB2	USB
USB3	USB
USB4	USB
USB5	Card Reader
USB6	NC
USB7	CCD
USB8	NC
USB9	NC

	U40II1
PCIE 1	NC
PCIE 2	10/100 LAN
PCIE 3	NC
PCIE 4	NC
PCIE 5	NC

CLOSE TO SB

Close to SB (D<500mil)

Elitegroup Computer Systems	
Title	ICH8M IO/GPIO/SYS-2/3
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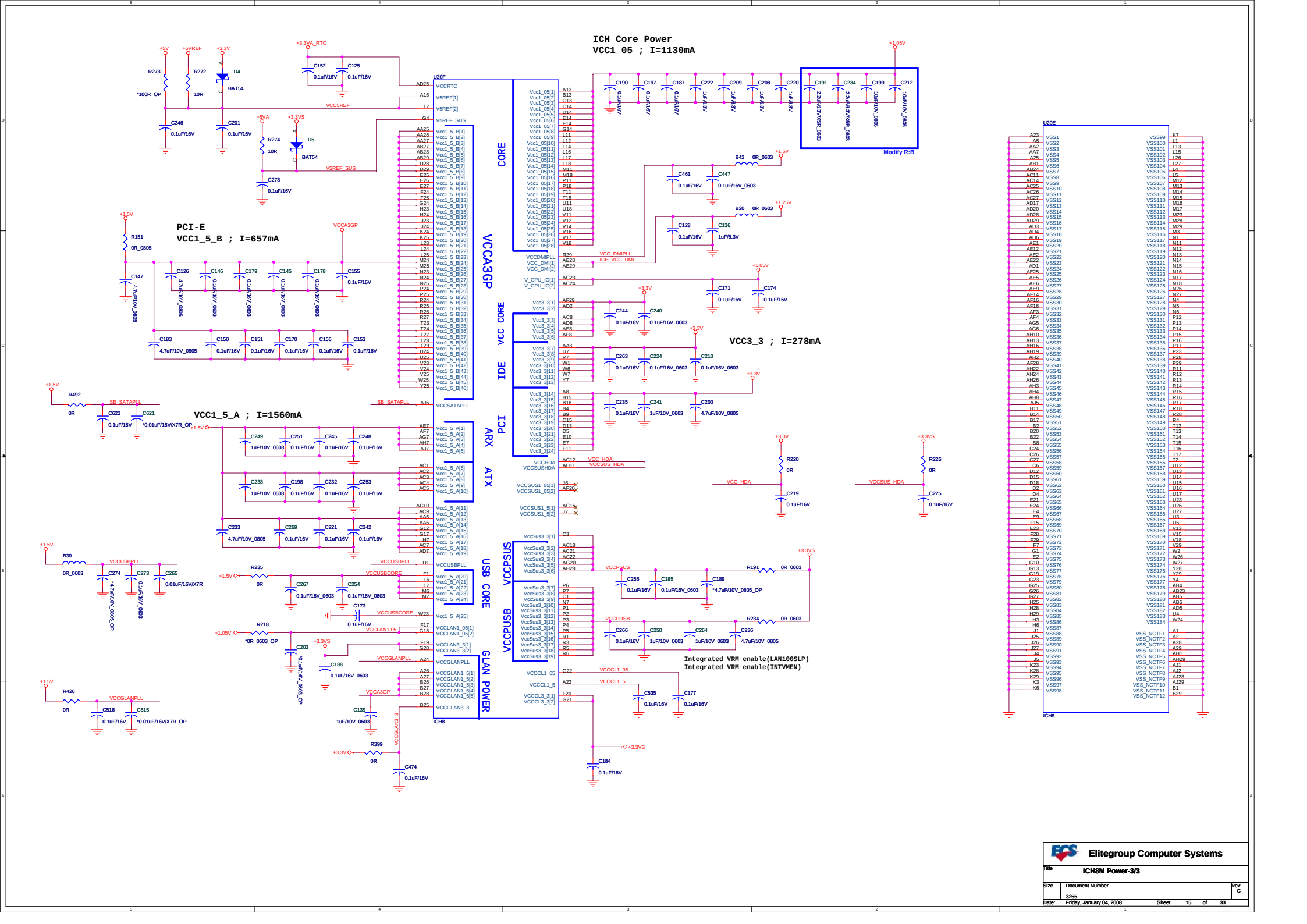
ICH Core Power
VCC1_05 ; I=1130mA
PCI-E
VCC1_5_B ; I=657mA
VCC1_5_A ; I=1560mA
VCC3_3 ; I=278mA
Modify R:B
Integrated VRM enable(LAN100SLP)
Integrated VRM enable(INTVMEN)
U20F
ICH8
U20E
CORE
VCCA3GP
VCC CORE
IDE
PCI
ARX
ATX
USB CORE
VCCPSUS
VCCPUSB
GLAN POWER
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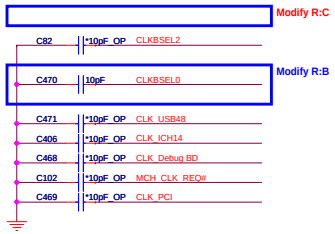
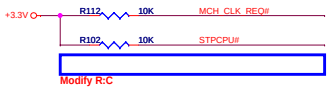
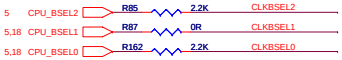
Reserved FOR EMI

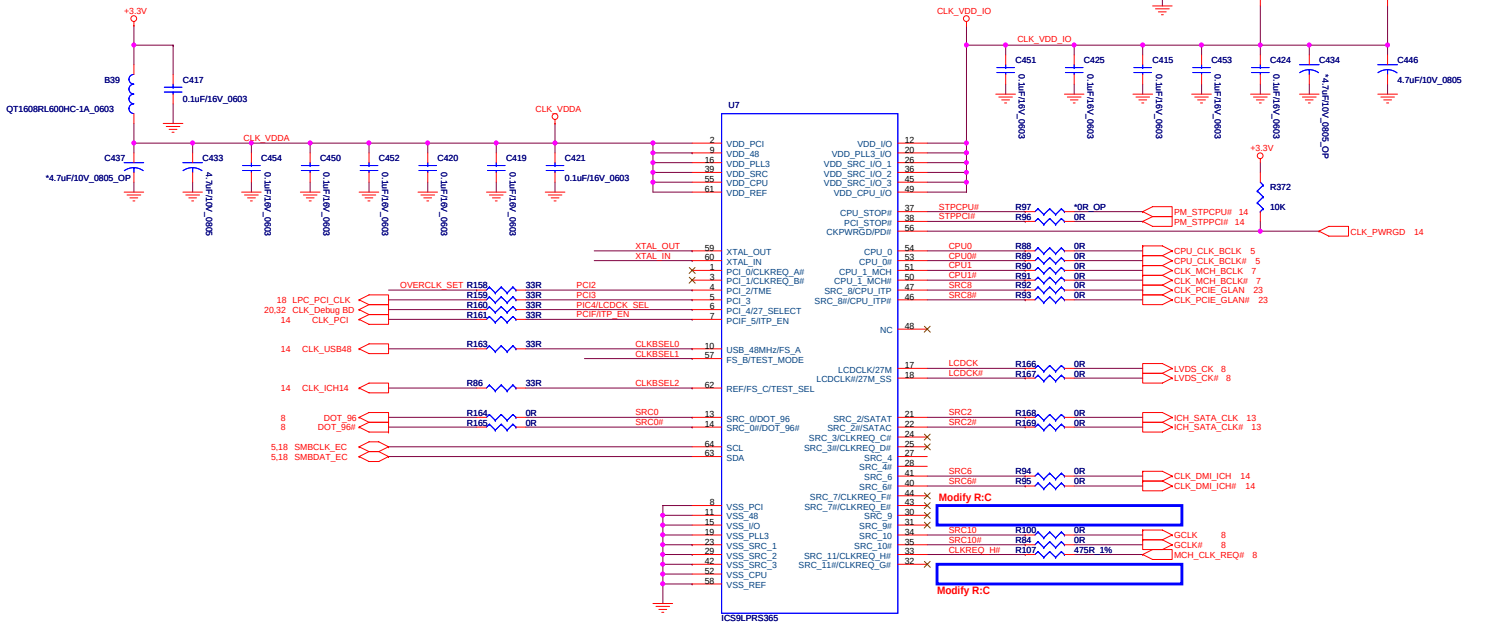


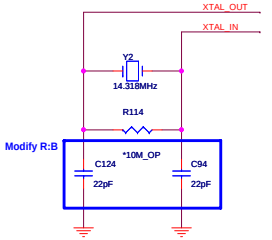




Bsel [0,2] Vil = 0.3 Vih = 0.7

	BSEL2 FSLC	BSEL1 FSLB	BSEL0 FSLA	CPU MHZ	PCI MHZ	PCI-E MHZ
PSB800	0	1	0	200		
PSB667	0	1	1	166	33	100
PSB533	0	0	1	133		

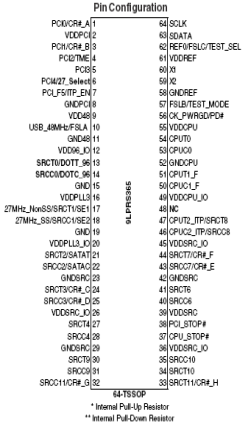




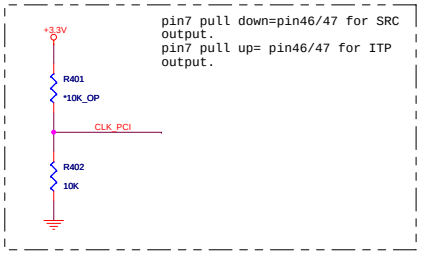

```
Ce =  2*CL - ( Cs + Ci )
CL =  Crystal Load Cap = 20P
Ci =  IC internal Cap = 5P
Cs =  2P
Ce =  Crystal external Cap = 33P
```

CLK REQ	From	SRC BUS
C	ICH_SATA	SRC2
G	MINICARD	SRC9
B	LAN 10/100	SRC4
H	MCH_GCLK	SRC10

Pin Configuration

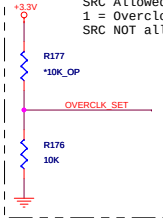


* Internal Pull-Up Resistor
** Internal Pull-Down Resistor



pin7 pull down=pin46/47 for SRC output.
pin7 pull up= pin46/47 for ITP output.

0 = LVDSCLK & DOT_96 for internal graphic controller support
1 = 27M & 27M_SS & SRC_0 for external graphic controller support



0 = Overclocking of CPU and SRC Allowed
1 = Overclocking of CPU and SRC NOT allowed

Elitegroup Computer Systems		
Title: CLK_GEN (ICS9LPRS365)		
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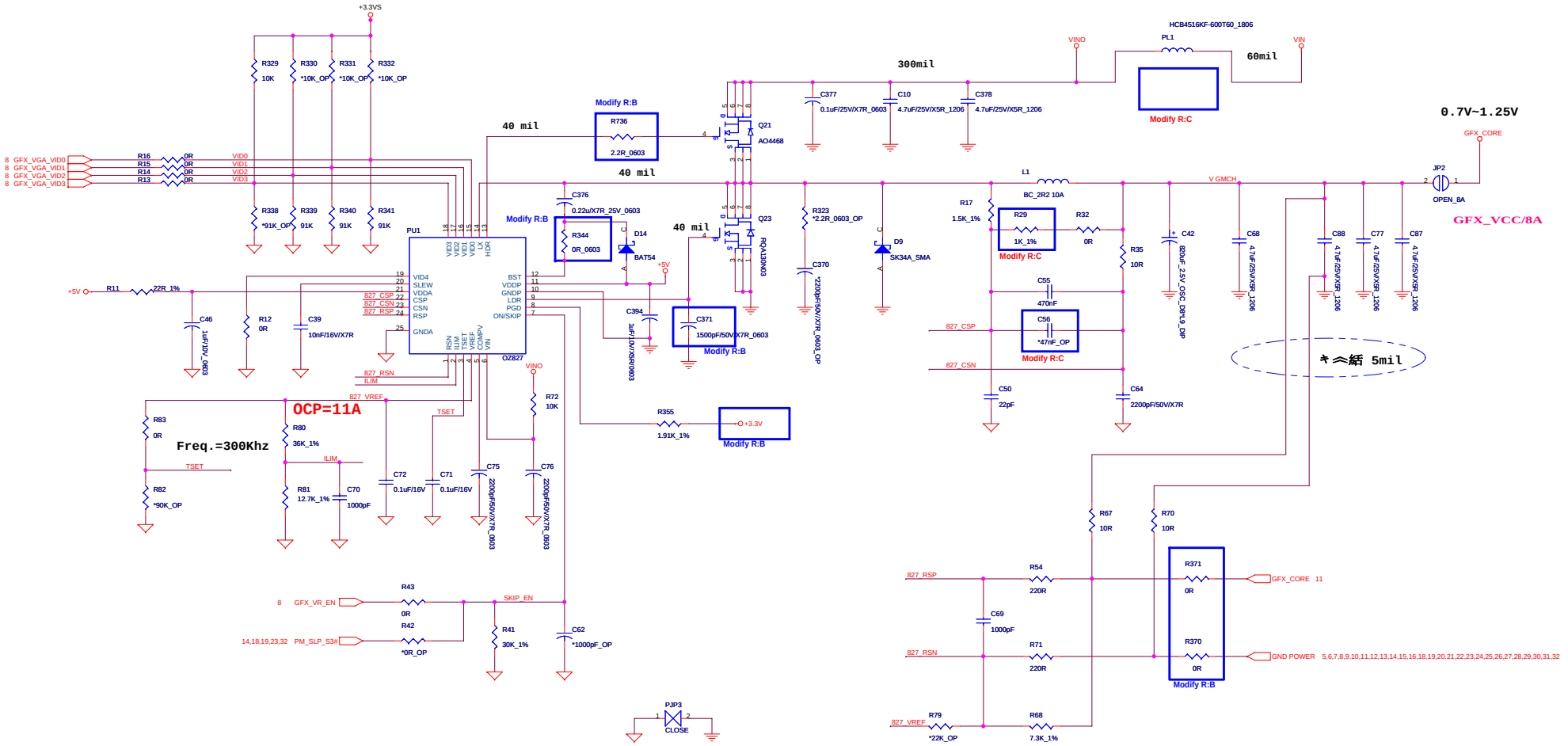

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Title	VGA GFX_CORE(OZ827)	
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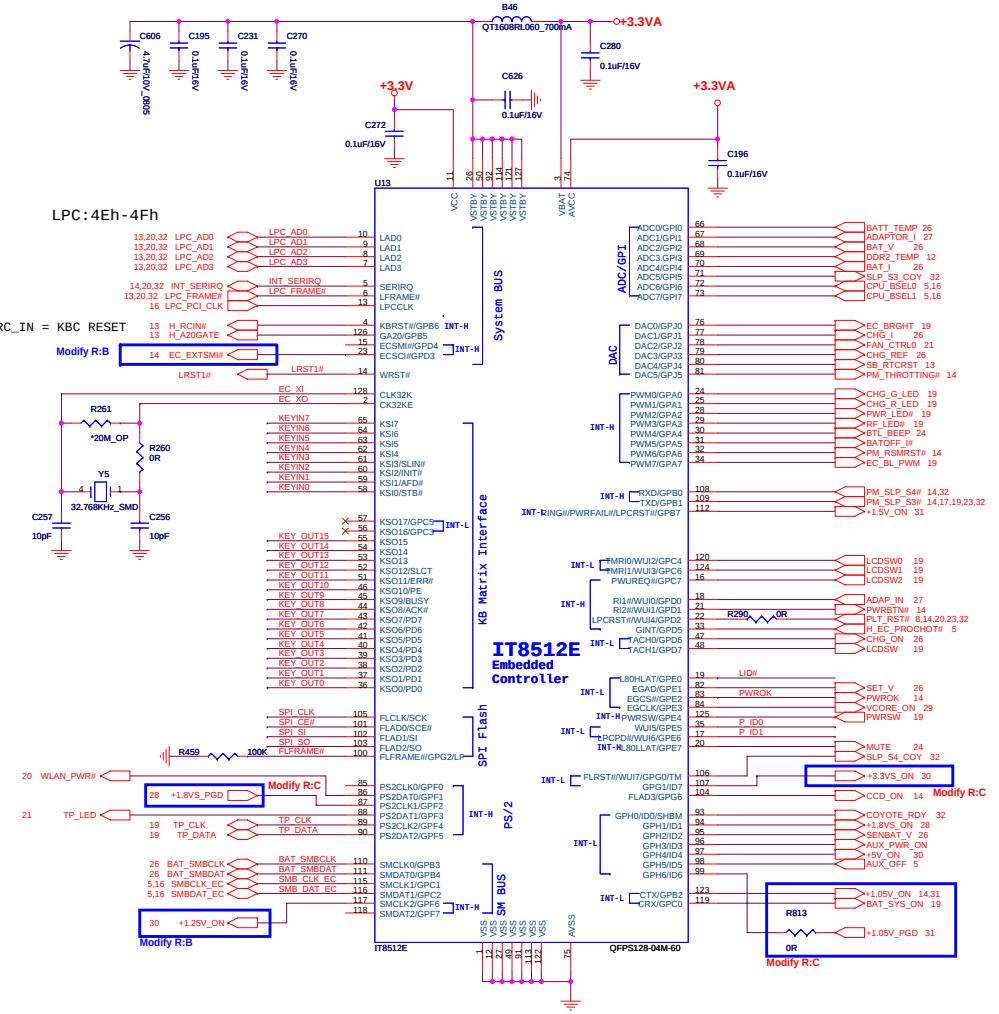
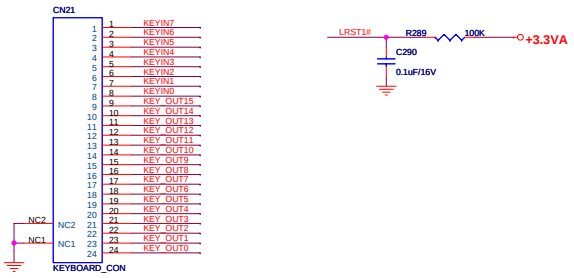
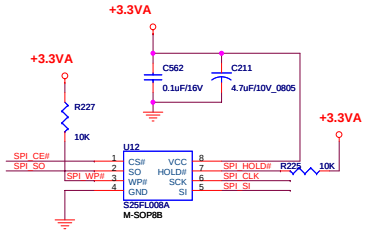


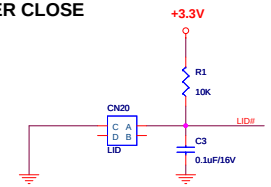
KEYBOARD



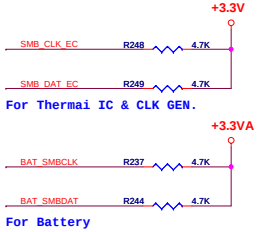
FLASH ROM(SPI)



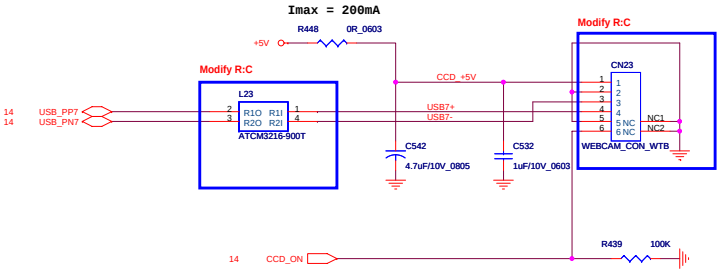
COVER CLOSE



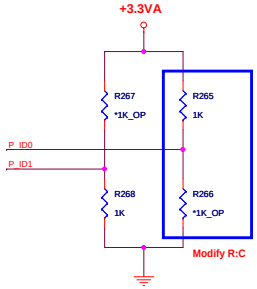
EC SMBUS



CCD



MODEL \ ID	P_ID0	P_ID1
U50II0 R:A	0	0
U50II0 R:B	0	0
U50II0 R:C	1	0



Elitegroup Computer Systems	
Title	EC IT8512 / BIOS / KB CONN / CCD
Size	Document Number: 3255 — Rev: C
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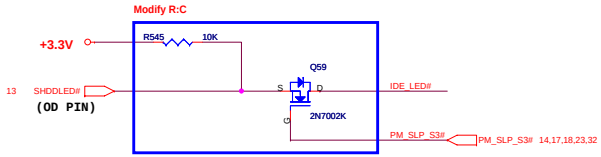
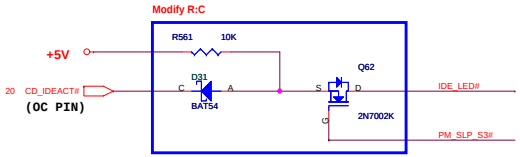
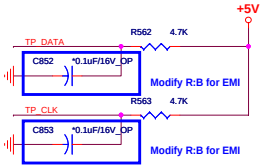
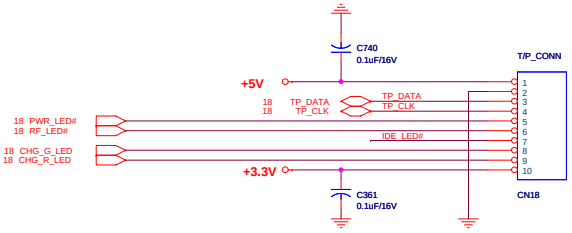
Touch Pad

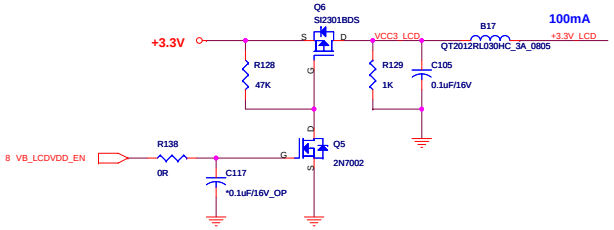


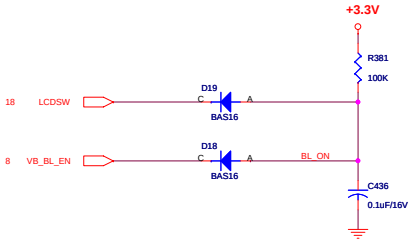




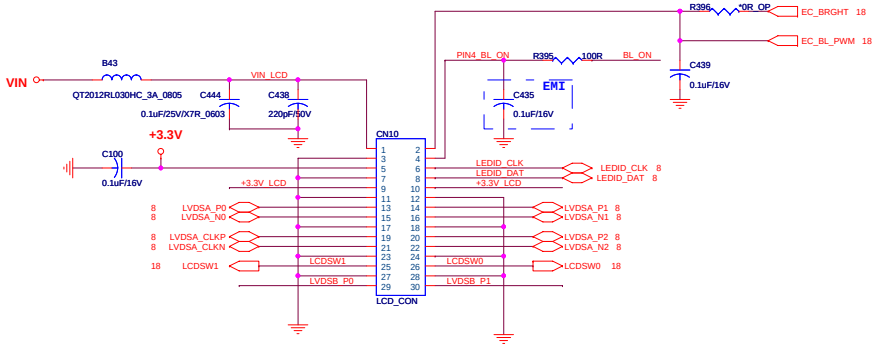


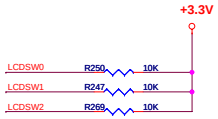
LCD



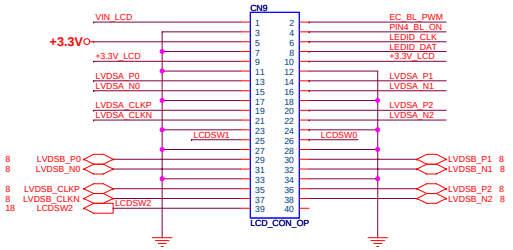


COLAY CN10 & CN11

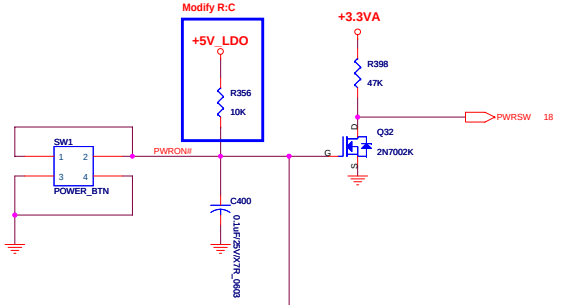


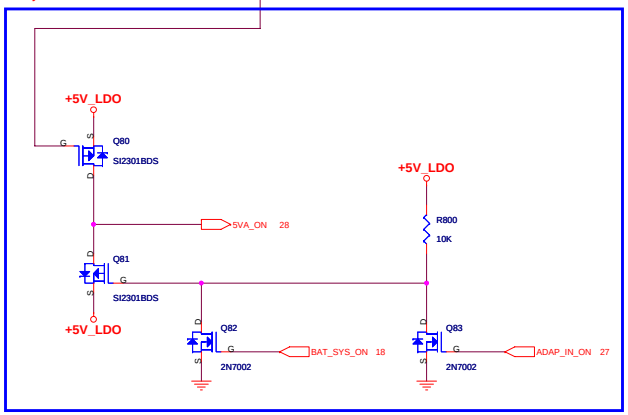


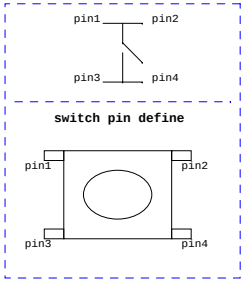
Panel ID		SW0	SW1	SW2
	15"	1	1	1
	14"	1	0	1
	13"	0	0	1



PWR SW







Elitegroup Computer Systems

Title: PWR SW / LED / LCD / TP

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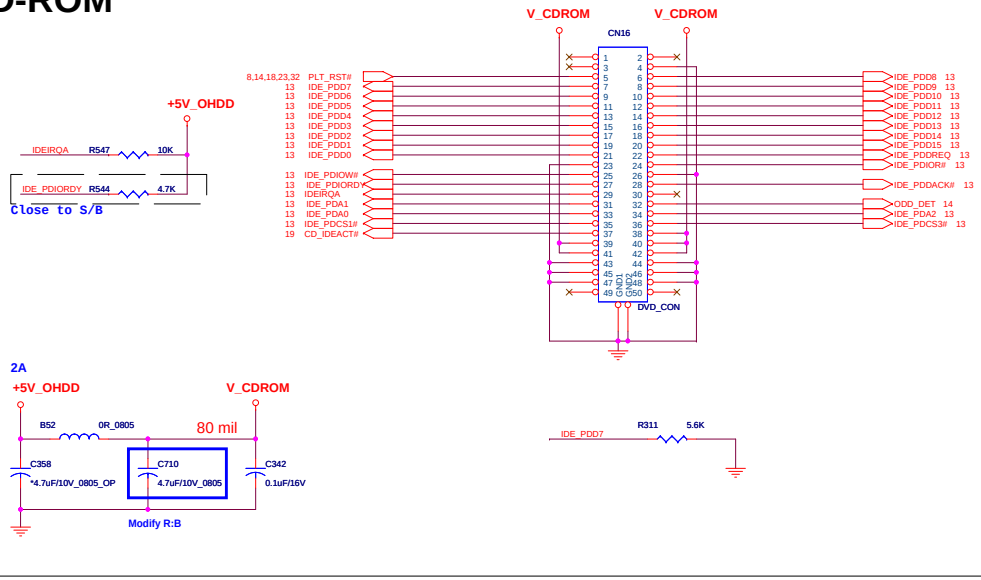
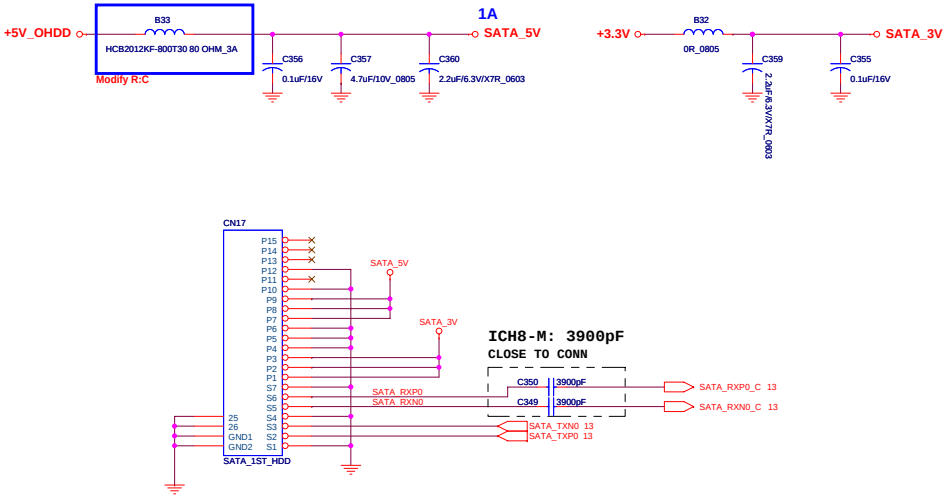
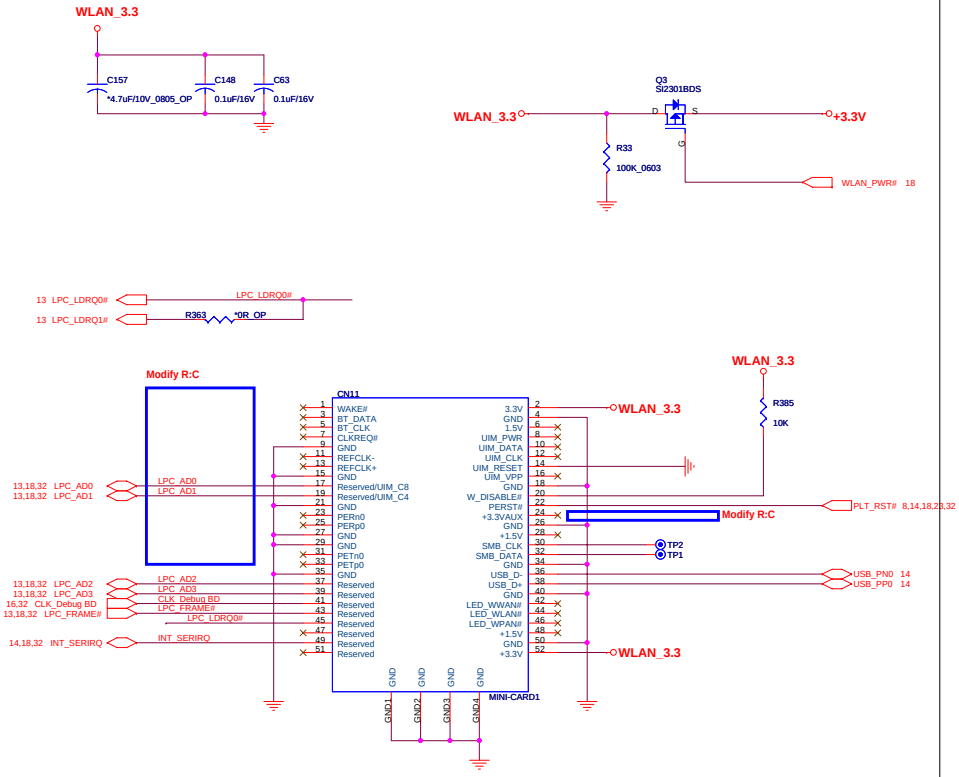
CD-ROM



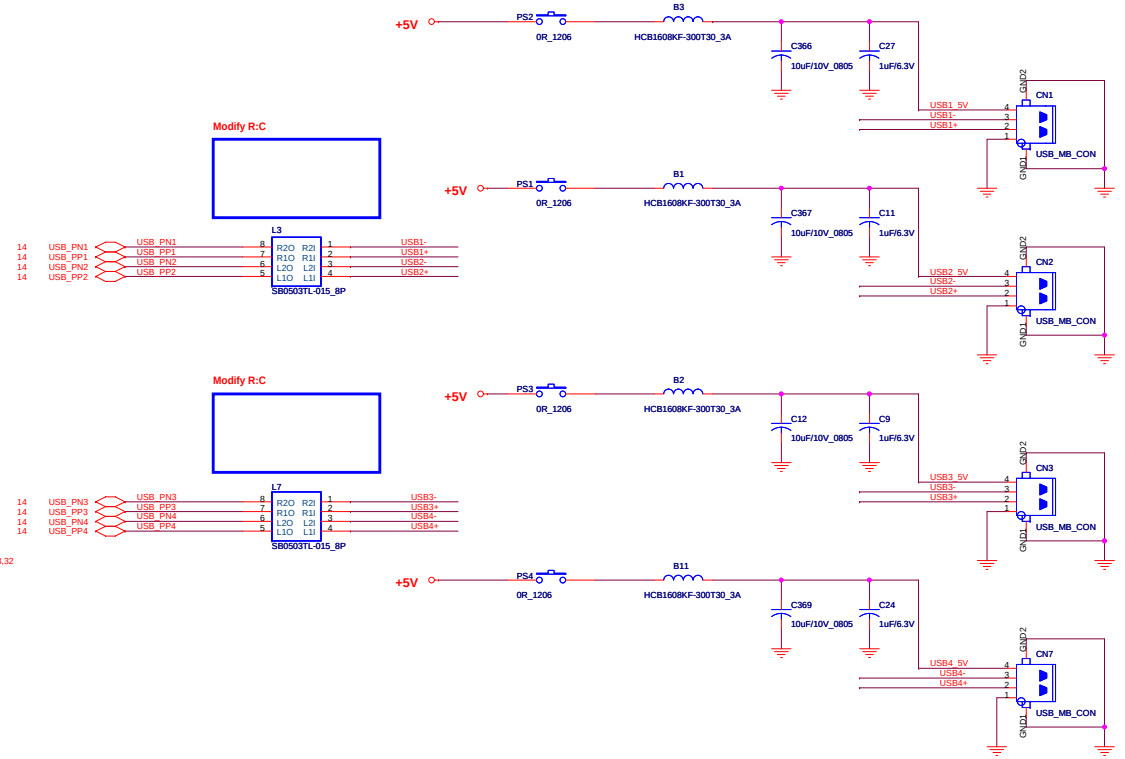
SATA-HDD



MINI CARD CONN



USB Port



Elitegroup Computer Systems

Title: HDD / ODD / USB / Mini Card

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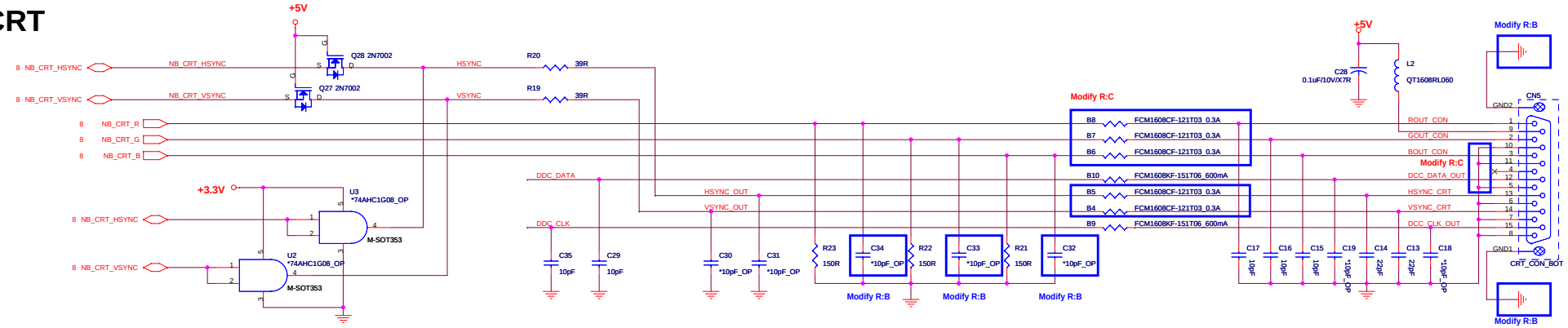
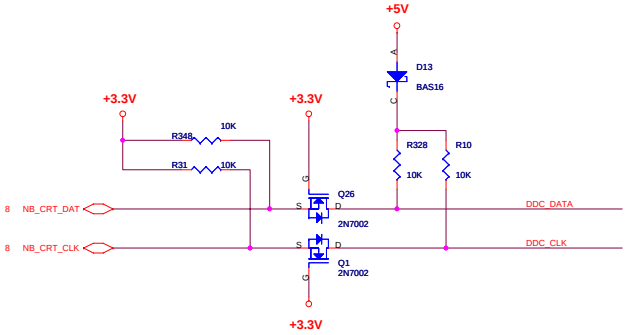
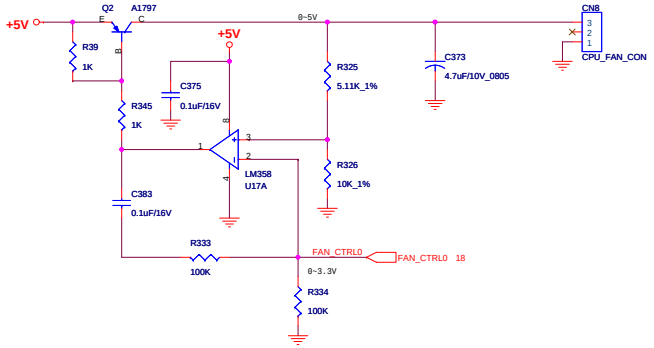
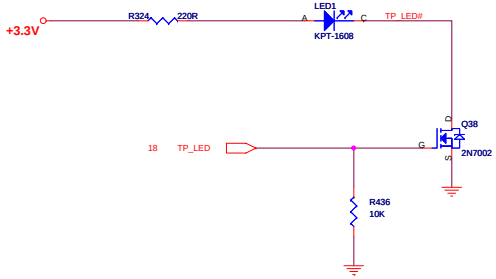
CRT





CPU FAN CONTROL





Elitegroup Computer Systems		
Title: CRT / FAN / SMP		
Size	Document Number: 3255	Rev: C
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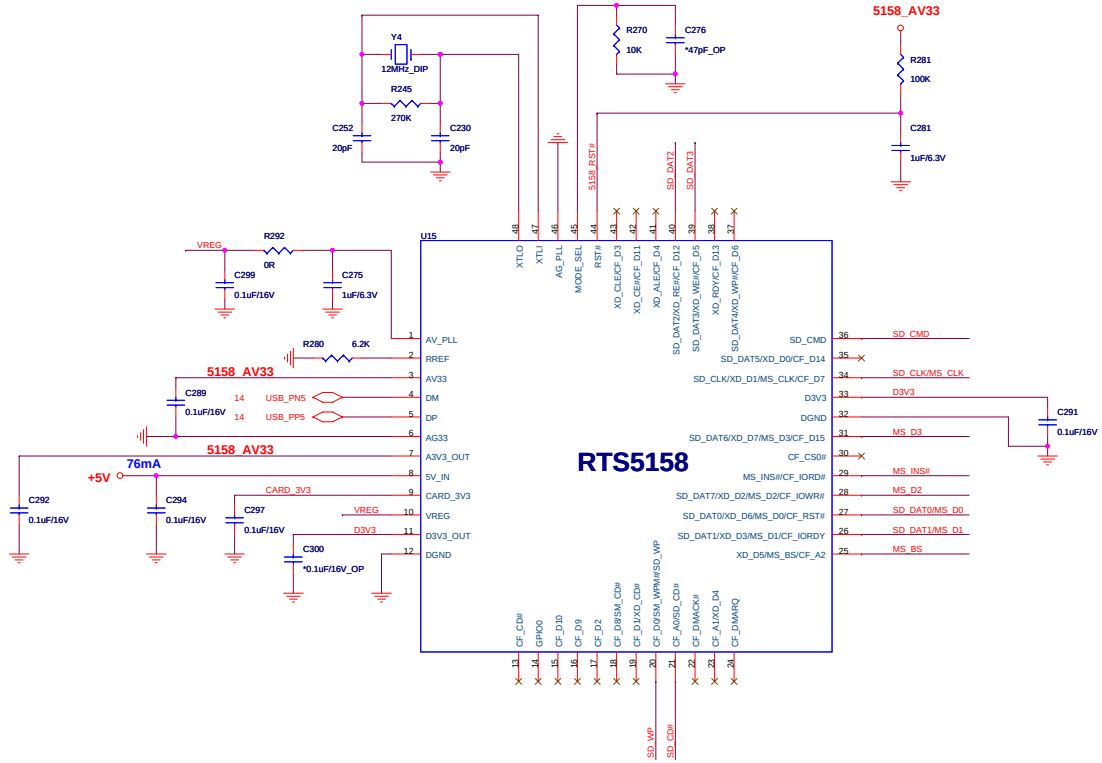

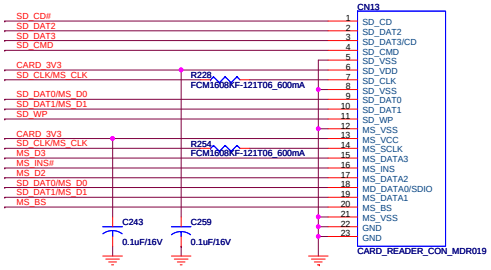

Elitegroup Computer Systems		
Title: CARD READER(RTS5158)		
Size	Document Number: 3255	Rev: C
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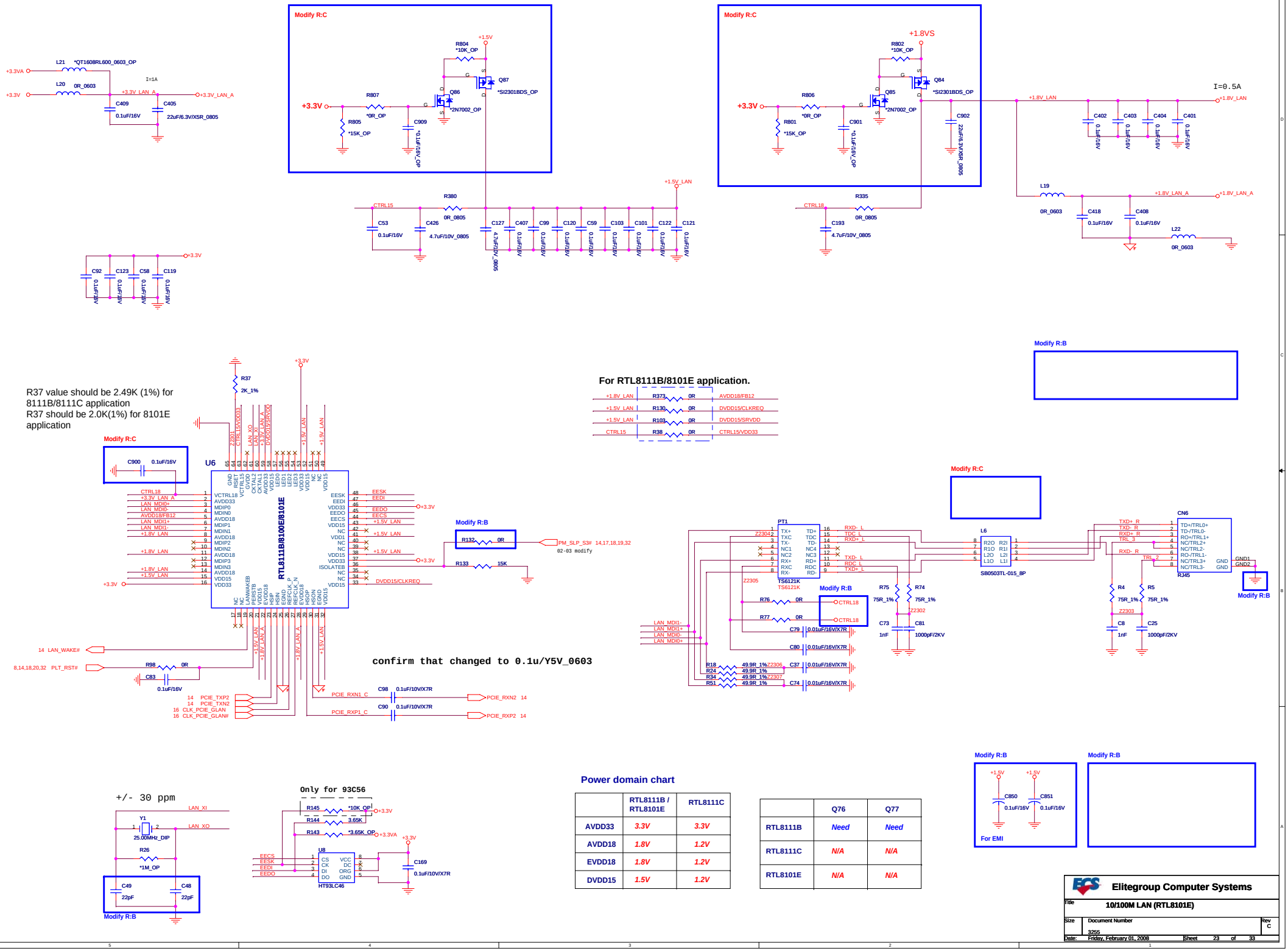

	RTL8111B / RTL8101E	RTL8111C
AVDD33	3.3V	3.3V
AVDD18	1.8V	1.2V
EVDD18	1.8V	1.2V
DVDD15	1.5V	1.2V

	Q76	Q77
RTL8111B	Need	Need
RTL8111C	N/A	N/A
RTL8101E	N/A	N/A

AMP VDD

+5VA

AQ2 SI2301BDS

AR22 100K

AQ1 2N7002

+3.3V

38mA

+5V_CODECVDD

AB24 QT1608RL060_700mA

AC31 0.1uF/16V

AC38 4.7uF/10V_0805

Modify R:B

AB7 0R_0603

AB5 0R_0603

AMP_GND

AB10 0R_0603

AB9 0R_0603

Modify R:B

CODEC_GND

AB27 0R_0603

AB19 0R_0603

AB23 0R_0603

AB28 0R_0603

Modify R:B

CODEC_GND

LOCATE UNDER CODEC.
USE 80 MILLS WIDE TRACE
BRIDGING AGND AND DGND PLANES

+5V_CODECVDD

AC45 *4.7uF/10V_0805_OP

AR24 20K

CODEC_GND

CODEC_GND CODEC_GND

AC18 *0.1uF/16V_OP

AC22 *0.1uF/16V_OP

25 MIC_VREFO_R

MIC_VREFO_L 25

25 HP_JD_SENSE#

HP_JD_SENSE#

AR19 39.2K_1%

FRONT_L

FRONT_R

+5V_CODECVDD

AC29 4.7uF/10V_0805

AC23 4.7uF/10V_0805

CODEC_GND CODEC_GND CODEC_GND

AU2 ALC662/883

37	VREFO-R	LINE1-R	24
38	AVDD2	LINE1-L	23
39	SURR-L	MIC1-R	22
40	JDREF/NC	MIC1-L	21
41	SURR-R	CD-R	20
42	AVSS2	CD-GND	19
43	CENTER	CD-L	18
44	LFE	MIC2-R	17
45	SIDESURR-L	MIC2-L	16
46	SIDESURR-R	LINE2-R	15
47	SPDIFI/EAPD	LINE2-L	14
48	SPDIFO/MIDI-CLK	Sense A	13

MIC_R 25

MIC_L 25

AC36 *1uF/6.3V_OP

AC35 *1uF/6.3V_OP

AC34 *1uF/6.3V_OP

CODEC_GND

AC33 1uF/6.3V

AC32 1uF/6.3V

LINE2_R 25

LINE2_L 25

SENSEA

AR18 20K_1%

MIC_JD_SENSE#

MIC_JD_SENSE# 25

MIC2_VREFO

AR20 4.7K

INT_MIC

AR23 180R_0603

ACN4 INT_MIC_CON

AC44 0.1uF/16V

INT_MIC

AB30 *FCM1608K-601T03_300mA_OP

AB29 4.7K_0603

CODEC_GND

Modify R:B

INT_MIC

AR21 *0R_OP

CODEC_GND

23mA

+3.3V

AR8 10R

AC12 0.1uF/16V

AC14 4.7uF/10V_0805

AC21 0.1uF/16V

13,27 ACZ_SDATAOUT

AR30 0R

Modify R:B

AC17 22pF

AC24 1uF/6.3V

Modify R:B

AR31 0R

ACZ_RST# 13,27

AR14 33R

ACZ_SYNC 13,27

AR12 33R

ACZ_SDATAIN0 13

AR11 33R

ACZ_BITCLK 13,27

AC28 *1uF/6.3V_OP

AR15 10K

AR17 1K

CODEC_GND

AC27 1uF/6.3V

ACZ_SPKR 14

AC30 1uF/6.3V

BTL_BEEP 18

INT SPK AMP

+5V

AB1 QT2012RL030HC_3A_0805

AC2 *4.7uF/10V_0805_OP

Modify R:B

AC8 4.7uF/10V_0805

AC6 *0.1uF/16V_OP

AMP_GND

+5V_AMP

+5V_AMP

AR5 47K

AMP_GND

AR6 30.9K_1%

AC3 0.1uF/16V

AC9 4.7uF/10V_0805

AMP_GND

AMP_GND AR7 6.04K_1%

AU1 APA2068 PIN17 =AMP_GND

8	VOLMAX	ROUT+	14
4	BYPASS	ROUT-	16
3	RIN-	SE/BTL#	13
6	LIN-	LOUT-	9
1	MUTE	LOUT+	11
2	SHUTDOWN#		

VOLUME 7 · VDD 15 · VDD 10 · GND 12 · GND 5

AMP_GND AC7 1uF/6.3V

FRONT_R AC5 1uF/6.3V

FRONT_L AC10 1uF/6.3V

18 MUTE

+5V_AMP AR1 100K

SPKR1+ AB13 QT1608RL060_700mA

SPKR1- AB14 QT1608RL060_700mA

SPKL1- AB11 QT1608RL060_700mA

SPKL1+ AB12 QT1608RL060_700mA

AR3 100K

AMP_GND AMP_GND

ACN3 INT_SPK_CON

SPK_R+ SPK_R- SPK_L- SPK_L+

AC43 AC42 AC41 AC40

*680pF_OP *680pF_OP *680pF_OP *680pF_OP

Elitegroup Computer Systems

Title: CODEC / AMP / INT_MIC / SPK

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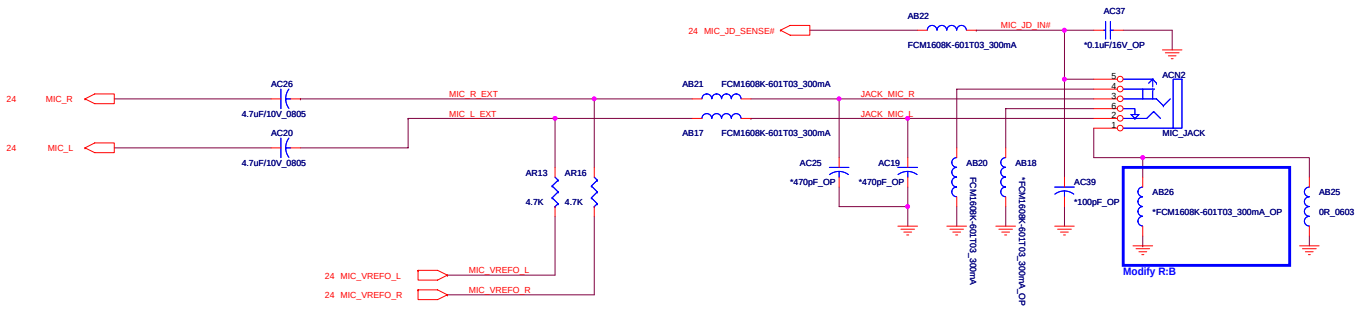
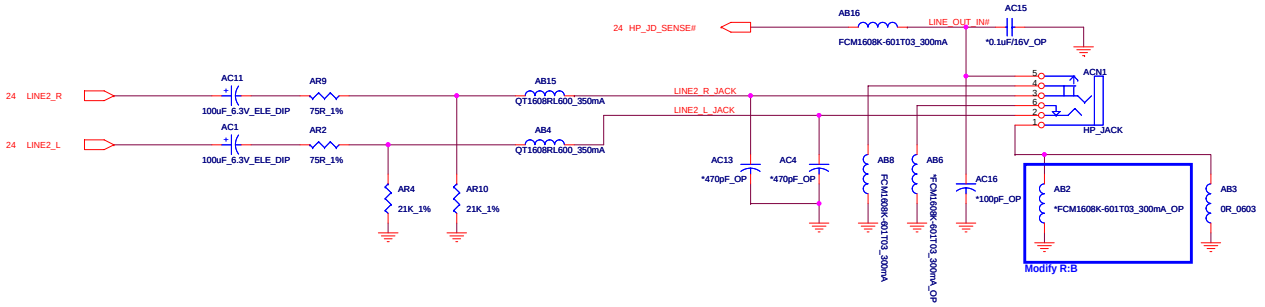
MIC/Line In JACK



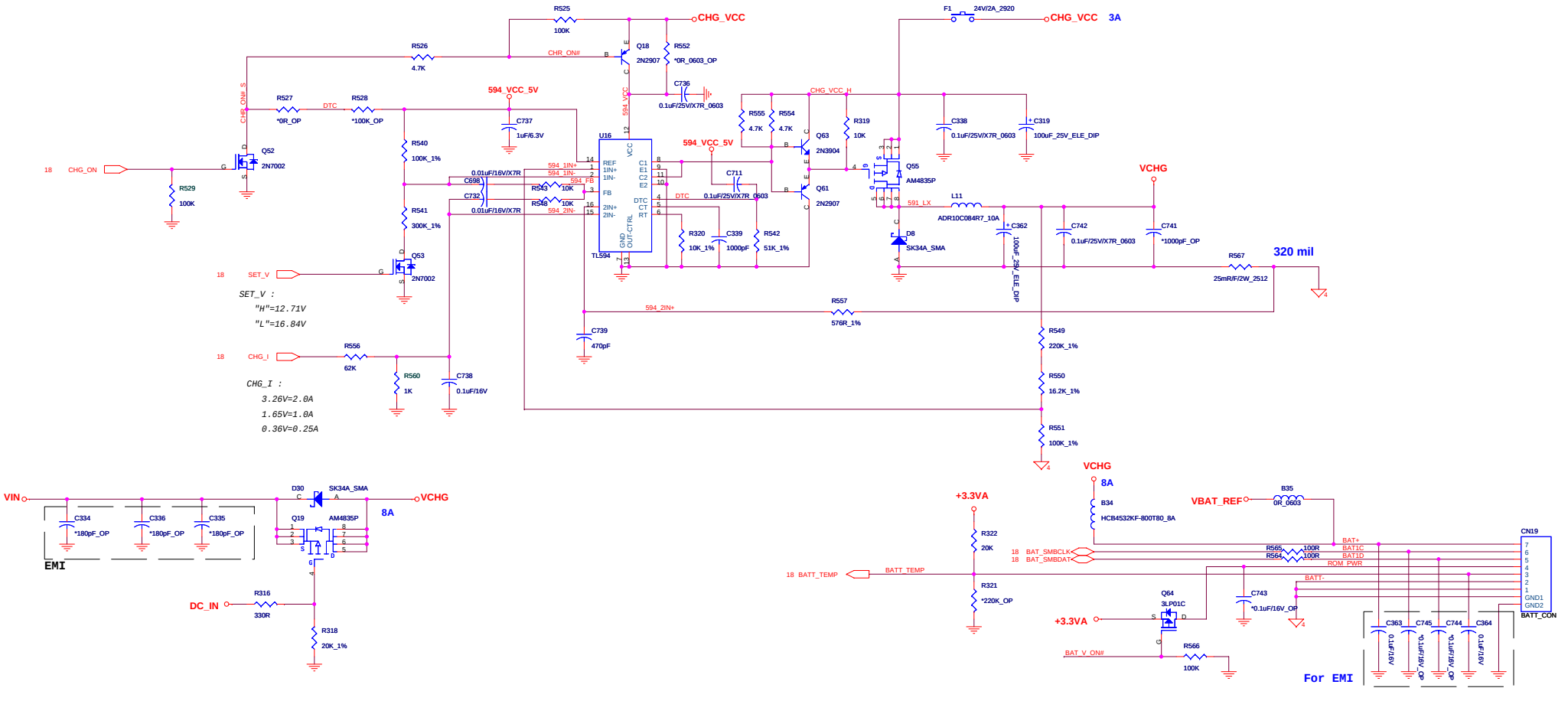
Modify R:C

HeadPhone JACK



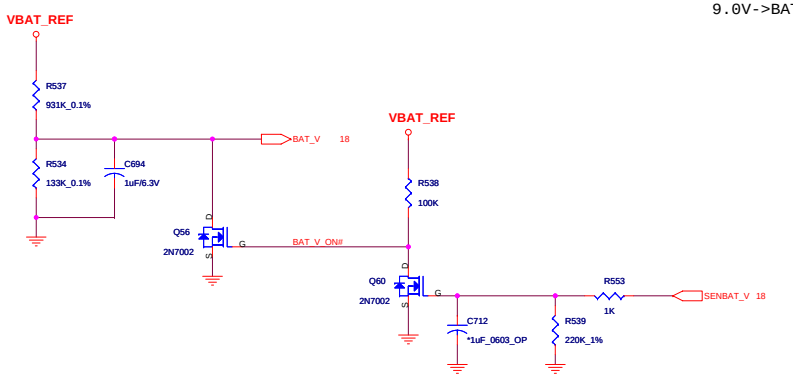
Modify R:C

Elitegroup Computer Systems		
Title: SPK / MIC / HEADPHONE		
Size	Document Number: 3255	Rev: C
Date: Friday, January 04, 2008	Sheet 25 of 33	

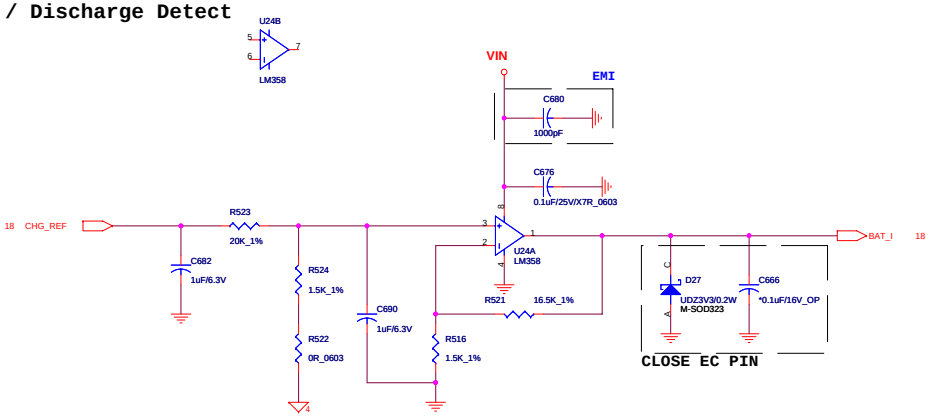


Battery Voltage Detect

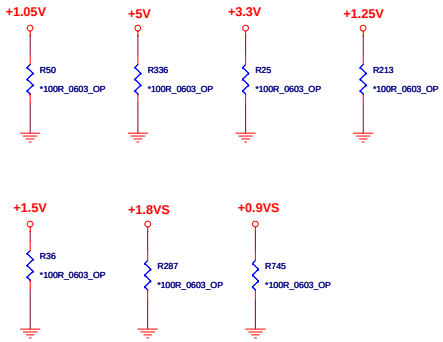
17.6V->BAT_V=2.2V
16.8V->BAT_V=2.1V
13.2V->BAT_V=1.65V
12.6V->BAT_V=1.575V
9.0V->BAT_V=1.125V



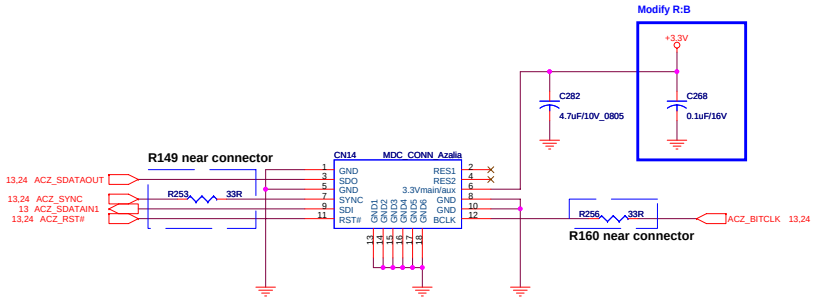
Charge / Discharge Detect



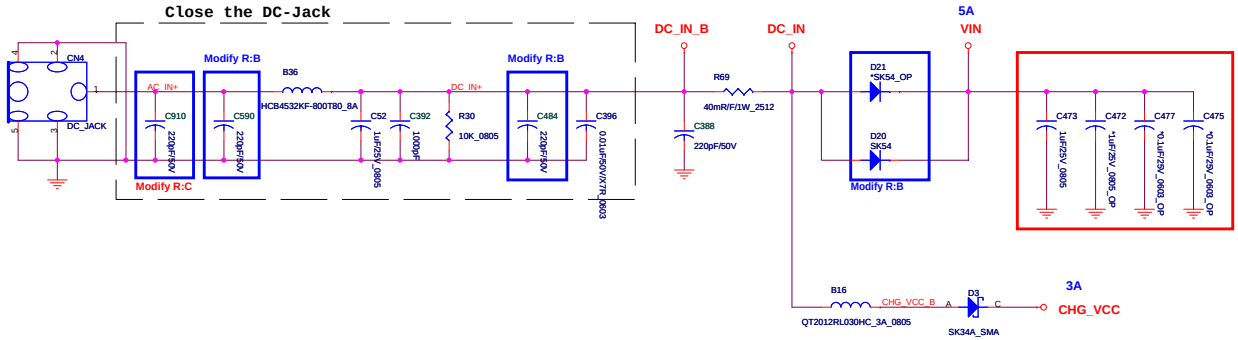
Elitegroup Computer Systems			
Title: BATT IN / Charger			
Size	Document Number		Rev C
	3255		
Date: Friday, January 04, 2008	Sheet 26 of 33		



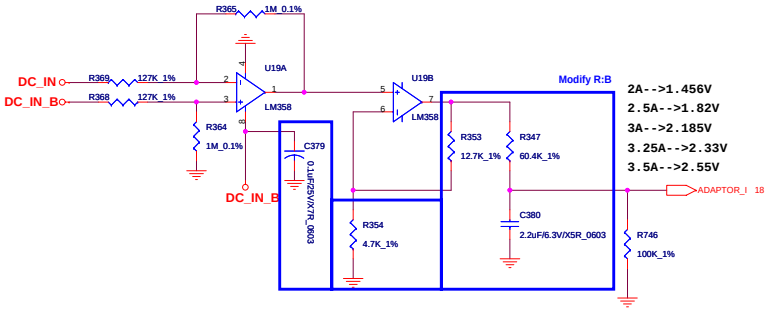
MDC

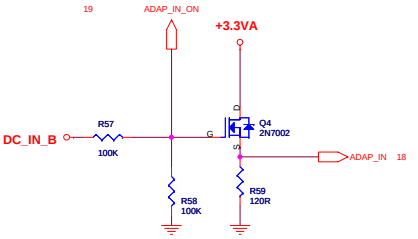


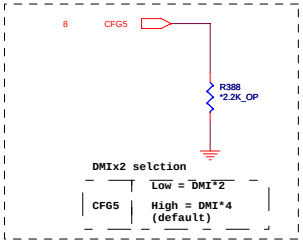
DC IN

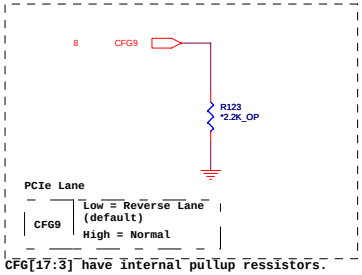


TOTAL POWER

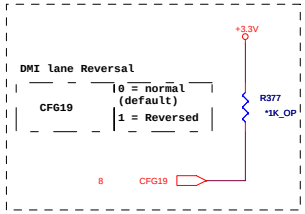


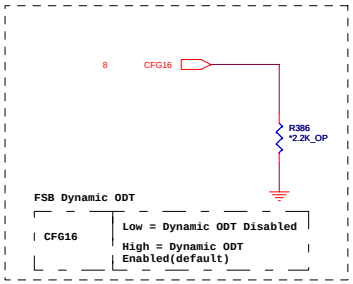


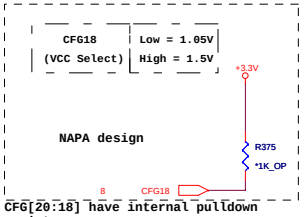




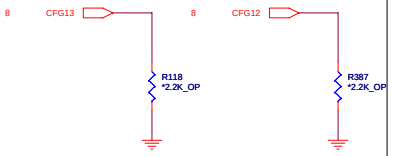
CFG[17:3] have internal pullup ressistors.

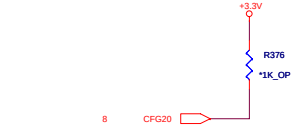






CFG[20:18] have internal pulldown resistors.





XOR / ALLZ / Clock Un-gating		
CFG12	CFG13	Configuration
0	0	Clock Gating Disabled
0	1	XOR Mode Enabled
1	0	All-Z Mode Enabled
1	1	Normal Operation (Default)

CFG20
(SDVO/PCIE Concurrent Operation)

0 = Only SDVO or PCIE x1 is operational (default)

1 = SDVO and PCIE x1 are operating simultaneously via the PEG port

Elitegroup Computer Systems

Title: DC IN/Discharger/APC

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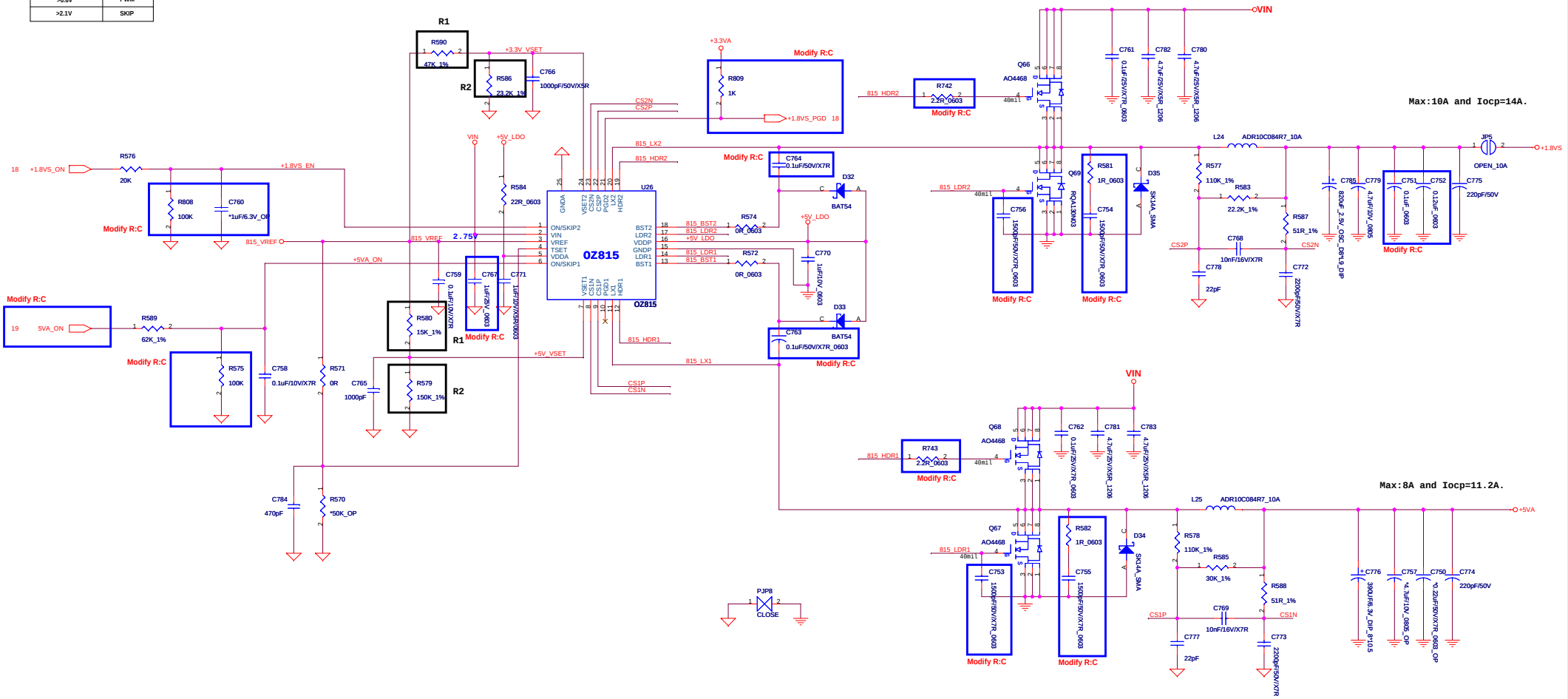
+3V/+5V_ON Voltage	Mode
<0.4V	OFF
>0.6V	PWM
>2.1V	SKIP

Output Voltage =[Vref x R2/(R1+R2)] x 2



Elitegroup Computer Systems			
Title	+5VA/+1.8VS(OZ815)		
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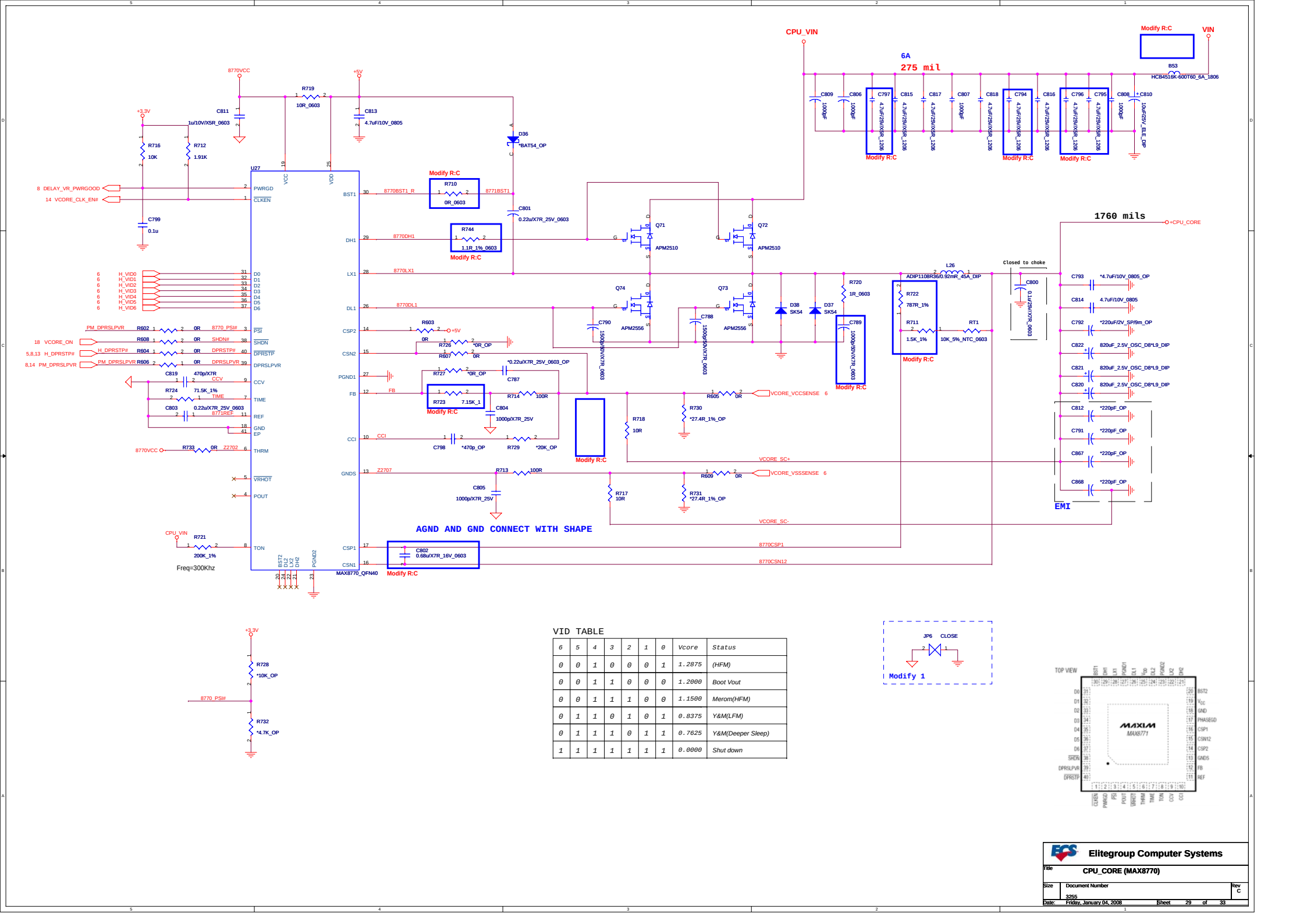

VID TABLE

6	5	4	3	2	1	0	Vcore	Status
0	0	1	0	0	0	1	1.2875	(HFM)
0	0	1	1	0	0	0	1.2000	Boot Vout
0	0	1	1	1	0	0	1.1500	Merom(HFM)
0	1	1	0	1	0	1	0.8375	Y&M(LFM)
0	1	1	1	0	1	1	0.7625	Y&M(Deeper Sleep)
1	1	1	1	1	1	1	0.0000	Shut down

Elitegroup Computer Systems

Title: CPU_CORE (MAX8770)

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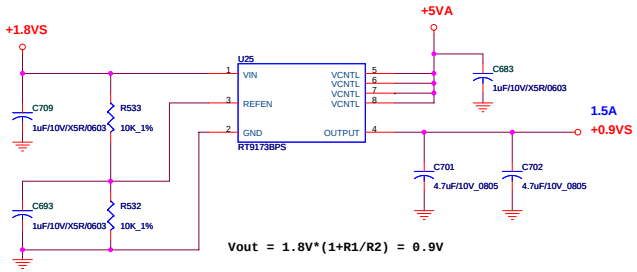

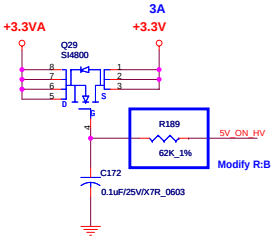

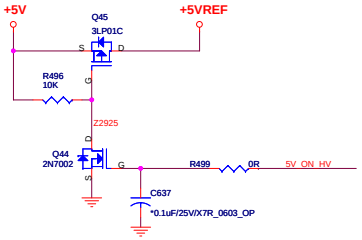

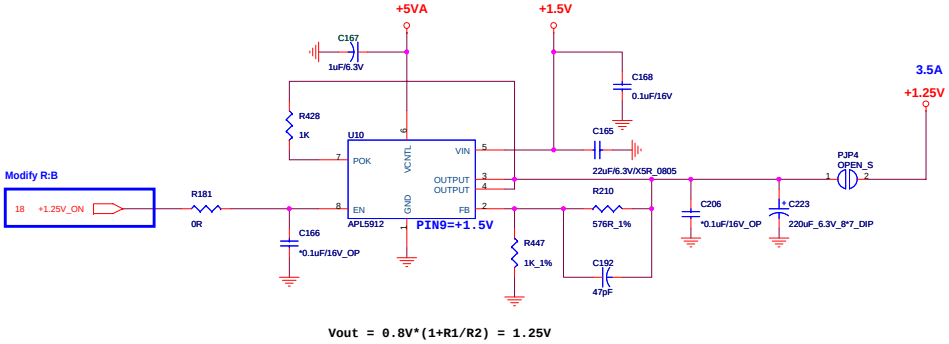

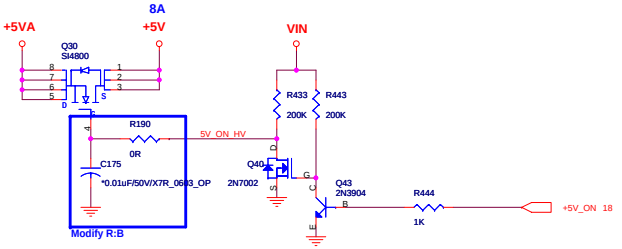

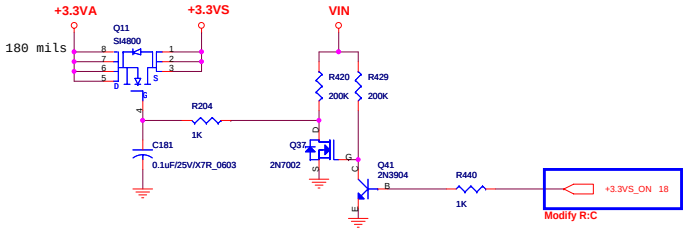

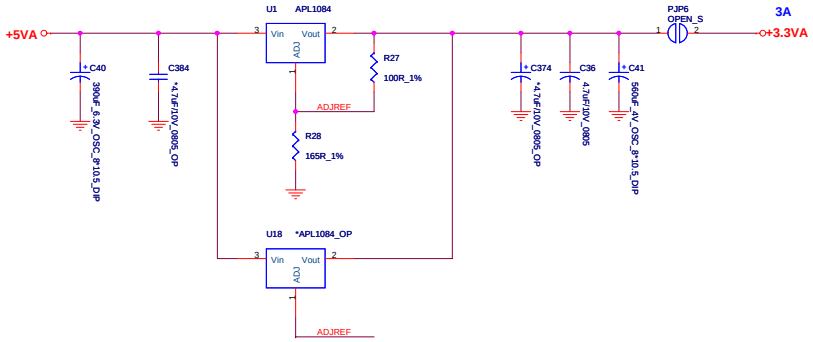

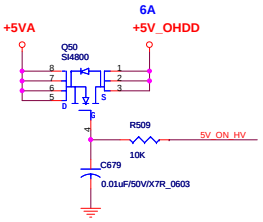

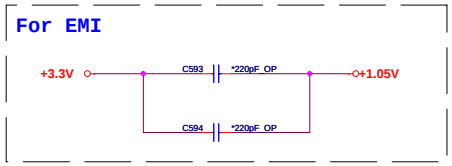

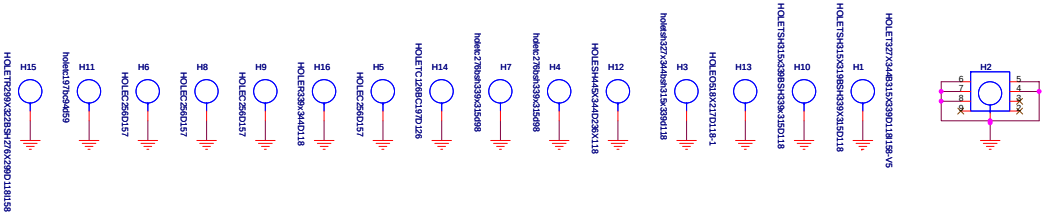

Elitegroup Computer Systems			
Title	LDO 3.3VA/1.8V/1.25V/0.9VS / VCC SW		
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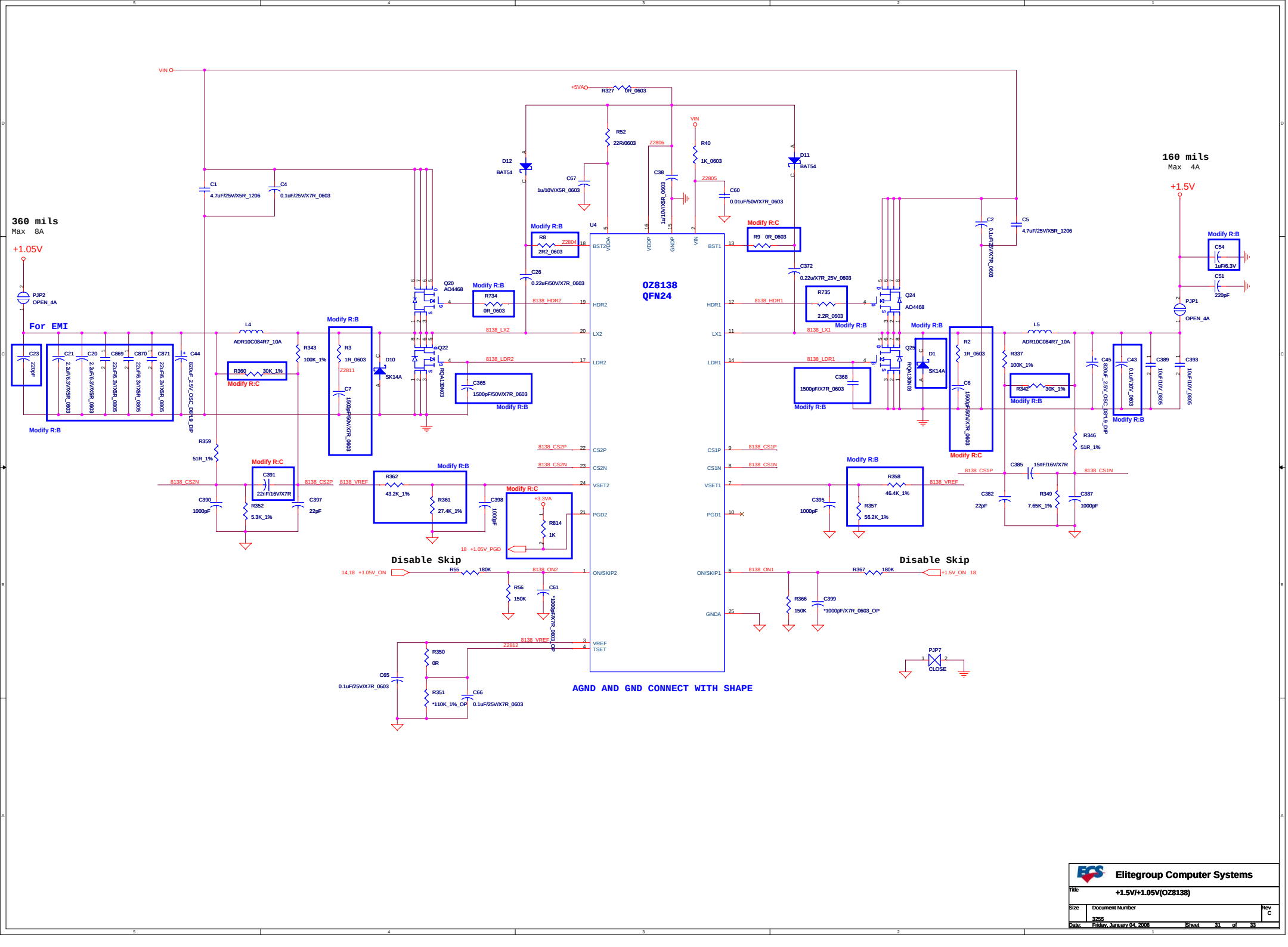

Elitegroup Computer Systems		
Title	+1.5V/+1.05V(OZ8138)	
Size	Document Number	Rev C
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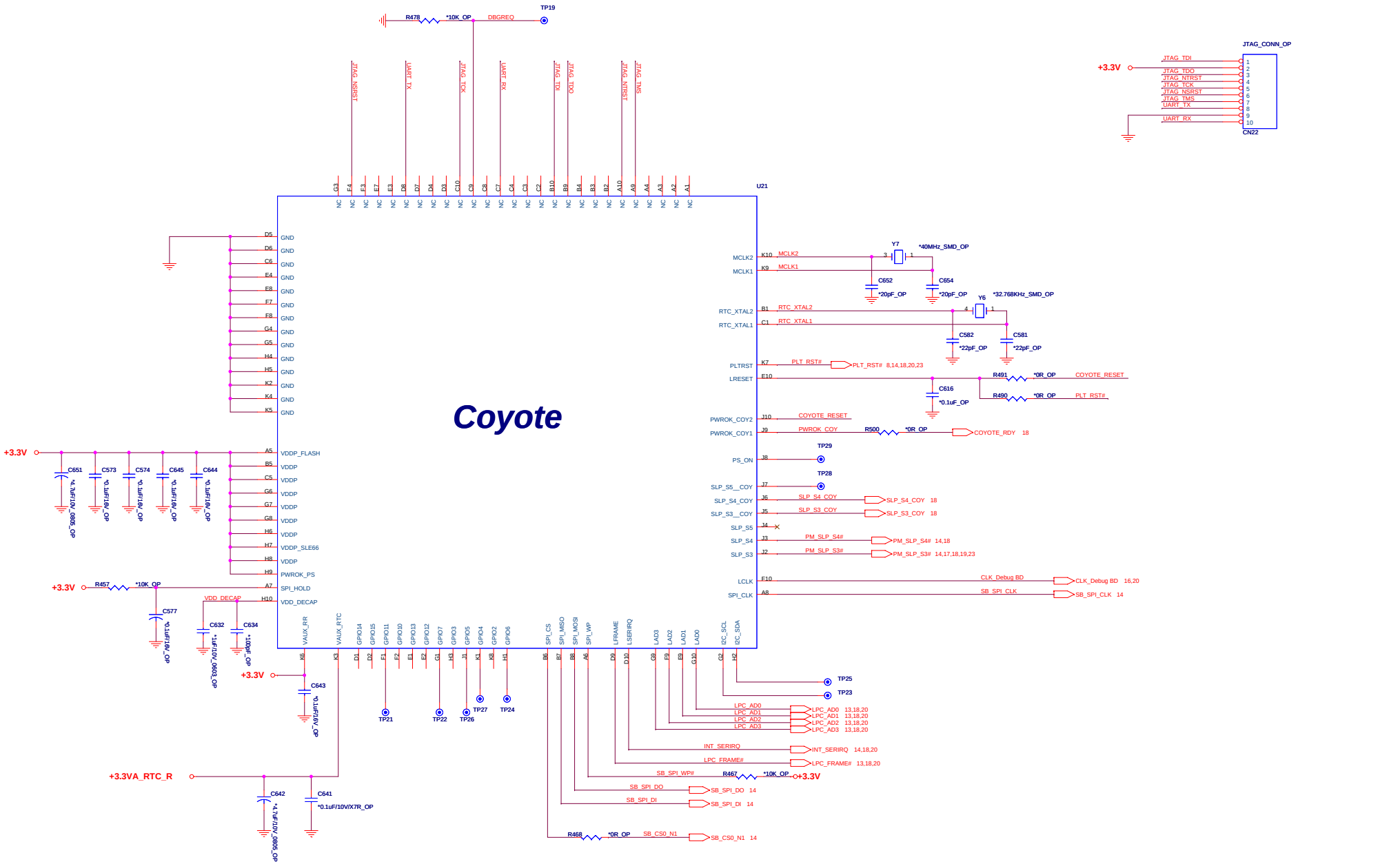

Elitegroup Computer Systems	
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RA to RB change list:

Page 5,M1->Add C856 1uF/6.3V for +1.05V
Page 5,M2->Modify H_THERM# and PM_THERMTRIP# circuit
Page 6,M3->Add C528/C569/C558/C527/C441/C442/C449/C448(1uF/6.3V) for +CPU_CORE
Page 6,M4->Add C410/C416/C422/C431/C429/C412/C423/C432(2.2UF/6.3V/X5R_0603) FOR +CPU_CORE
Page 6,M5->Add C491/C478/C518/C467/C143/C142/C141/C140/C858/C859/C860/C861/C862/C863/C864/C865(2.2uF/6.3V/X5R_0603) FOR +1.05V
Page 7,M6->Add C855/C854 (0.1uF/16V)
Page 8,M7->Add R600 (30k ohm) for GFX enable pin pull high to +3.3V
Page 10,M8->Add C857 (22uF/6.3V/X5R_0805) for +1.05V
Page 13,M11->C182 change to 6.8pF
Page 13,M12->C180 change to 8.2pF
Page 13,M13->Add C470 (10pF) for EMI
Page 13,M14->C124/C94 change to 22pF
Page 17,M15->R344 change to 0 ohm
Page 17,M16->C371 change to 1500pF/50V/X7R_0603
Page 17,M17->Add R736 2.2R_0603
Page 17,M18->Original R355 pull high to +3.3VS change to pull high to +3.3V
Page 17,M19->Add R371/R370 0 ohm
Page 18,M20->EC_EXTSMI Swarp to U13/Pin 23
Page 18,M21->Add +1.25V_ON (EC Pin117)
Page 19,M22->Delete R397/Q31/Q35
Page 20,M23->Add C710 (4.7uF/10V_0805) for V_CDROM
Page 20,M24->Remove L3/L7 and Add RP20/RP21
Page 21,M25->B6/B7/B8 change to FCM1608CF-600T06 TAI-TECH
Page 21,M26->Remove C34/C33/C32
Page 22,M27->CN23 change (53398-0610L)
Page 23,M28->C48/C49 change to 22pF
Page 23,M29->Delete R131 and Add R132
Page 23,M30->R76/R77 change net link to CTRL18
Page 23,M31->Add R335 and Remove R78
Page 24,M32->Add AB710/AB9/AB23/AB28
Page 24,M33->Add AC38/AC8/AR30/AR31/AB30
Page 25,M34->Remove AB26/AB2
Page 27,M35->Add R745/C590/C484
Page 27,M36->Delete R183/R207/R206/Q9/Q10/R407/Q36
Page 27,M37->R347 change to 60.4K ohm
Page 27,M38->Delete C386 and add R746 100k ohm
Page 28,M39->+5VA and +1.8VS PWM solution change to OZ815
Page 29,M40->CPU_CORE PWM solution change to MAX8770
Page 30,M41->R181 link to +1.25V_ON
Page 31,M42->R189 change to 60.4K ohm
Page 31,M43->R190 change to 0 ohm and C175 OP
Page 32,M44->R360 change to 36K ohm
Page 32,M45->R8/R9 change to 2R2 ohm
Page 32,M46->C365/C368 change to 1500pF
Page 32,M47->R342 change to 30K ohm 1%
Page 32,M48->R362 change to 43.2K ohm
Page 32,M49->R361 change to 27.4K ohm
Page 32,M50->R357 change to 56.2K ohm
Page 32,M51->R358 change to 46.4K ohm
Page 32,M52->Add C21/C20/C869/C870/C871/R734/R735
Page 32,M53->C43 change to 0.1UF/ Add C54 1uF

Elitegroup Computer Systems		
Title: Change Notes		
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