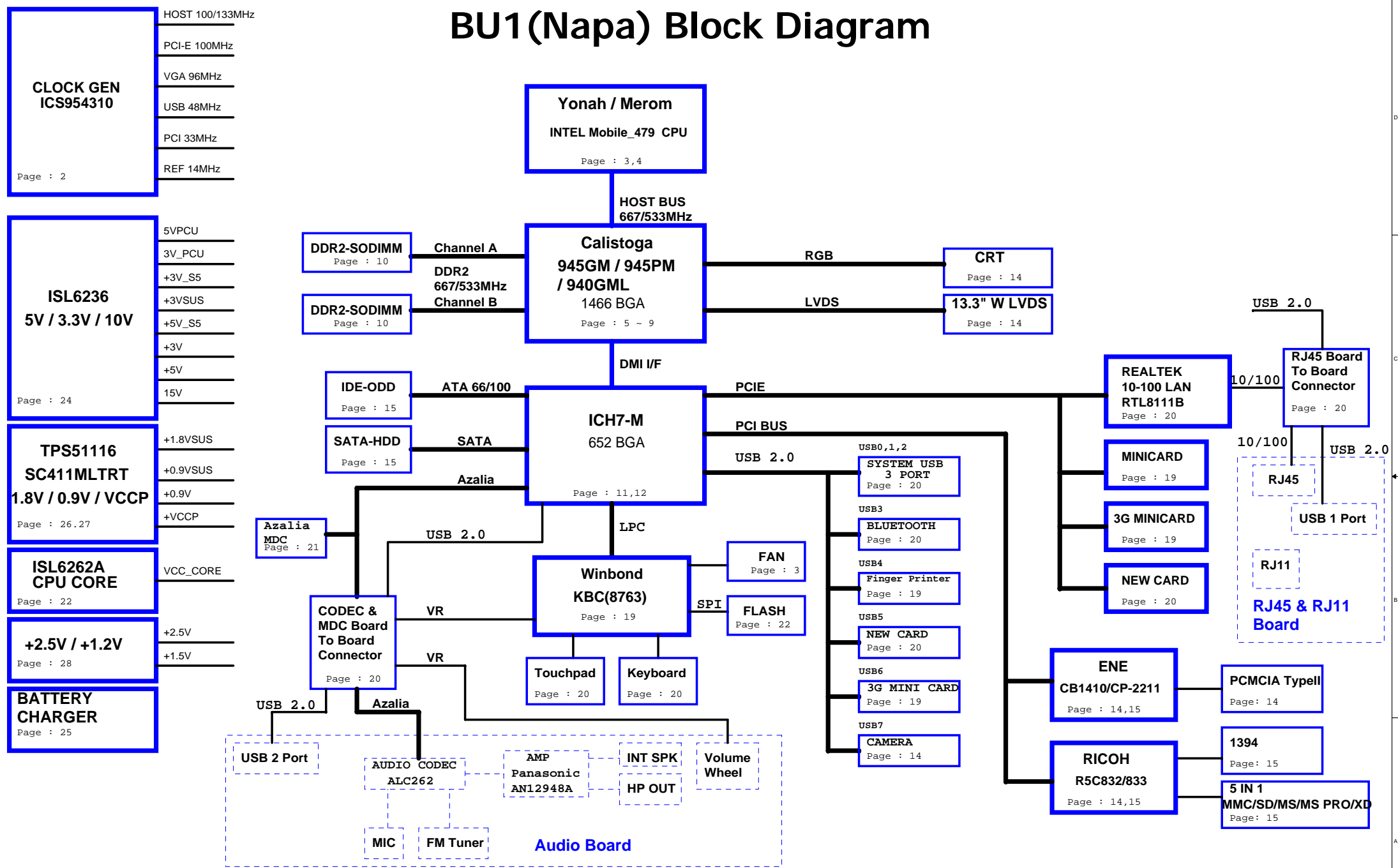
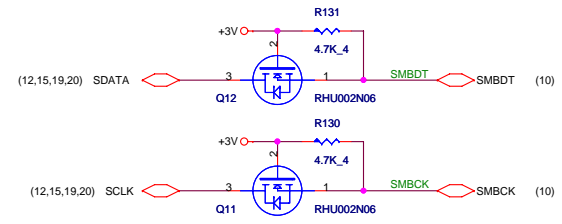
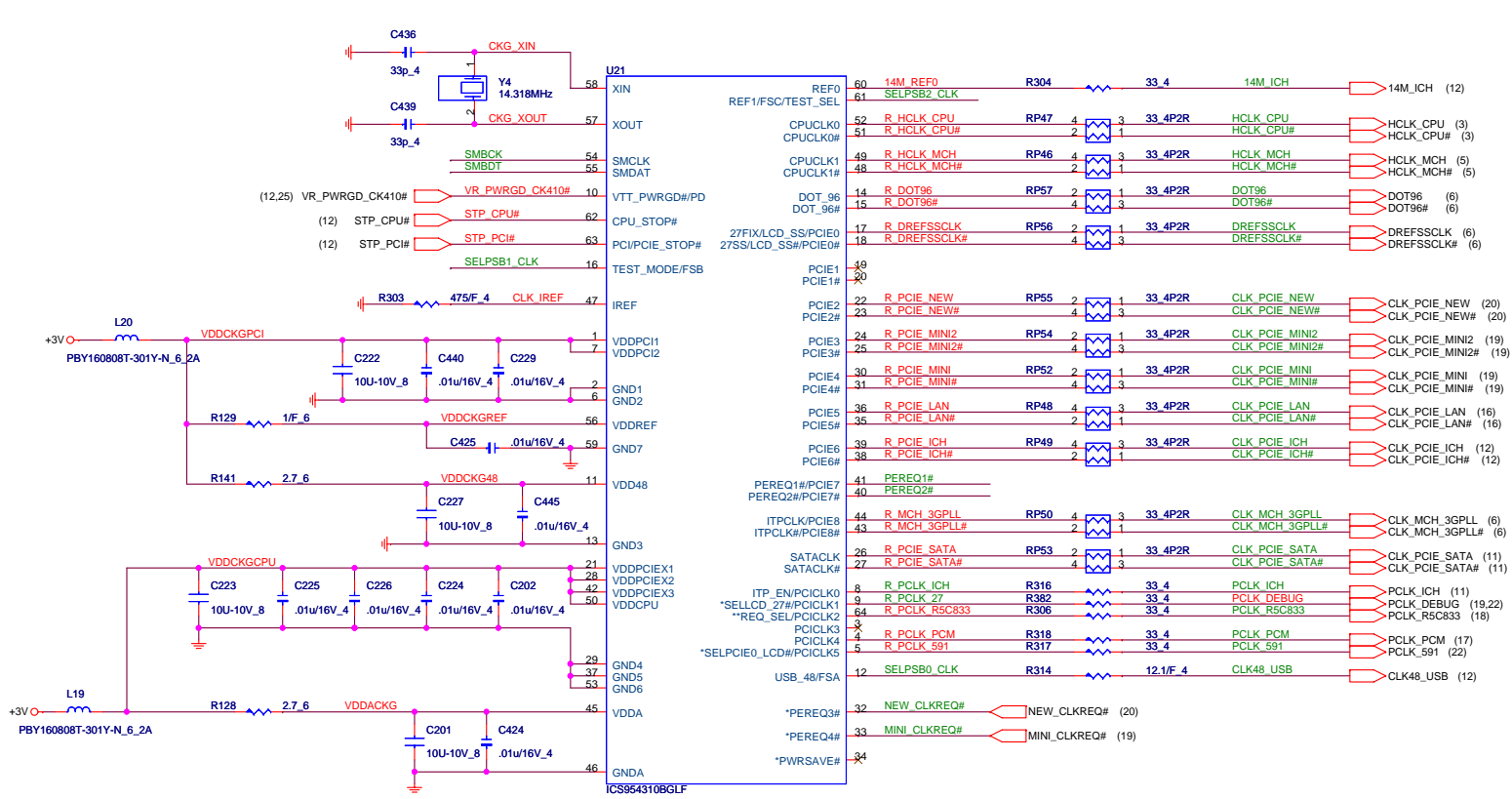


# BU1(Napa) Block Diagram



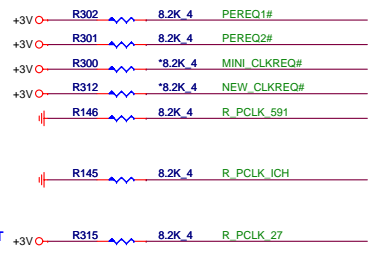
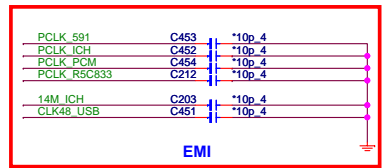
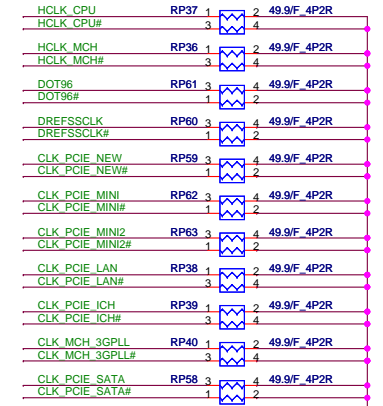
PCI ROUTING TABLE	IDSEL	INTERRUPT	DEVICE
REQ0# / GNT0#	AD17	INTA#, INTB#	Card Reader Controller
REQ1# / GNT1#	AD20	INTC#	Card Bus Controller

# CLOCK GENERATOR



PEREQ1# --> PCIE0 & PCIE6  
 PEREQ2# --> PCIE1 & PCIE8  
 PEREQ3# --> PCIE2 & PCIE4  
 PEREQ4# --> PCIE3 & PCIE5 & PCIE7

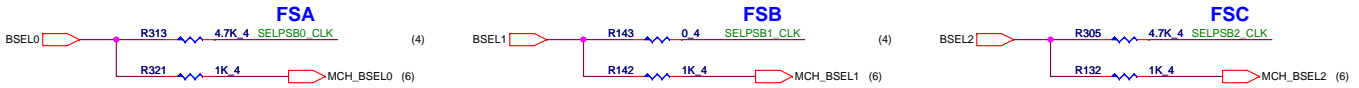
SMBUS Address : D2 (Read) , D3 (Write)



LCD / PCI SELECT  
 0: LCD 1: PCI

ITP/SRC7 SELECT  
 0: SRC7 1: ITP

SELLCD 27# SELECT  
 0: 27MSS 1: LCD CLK



**FSA**      **FSB**      **FSC**

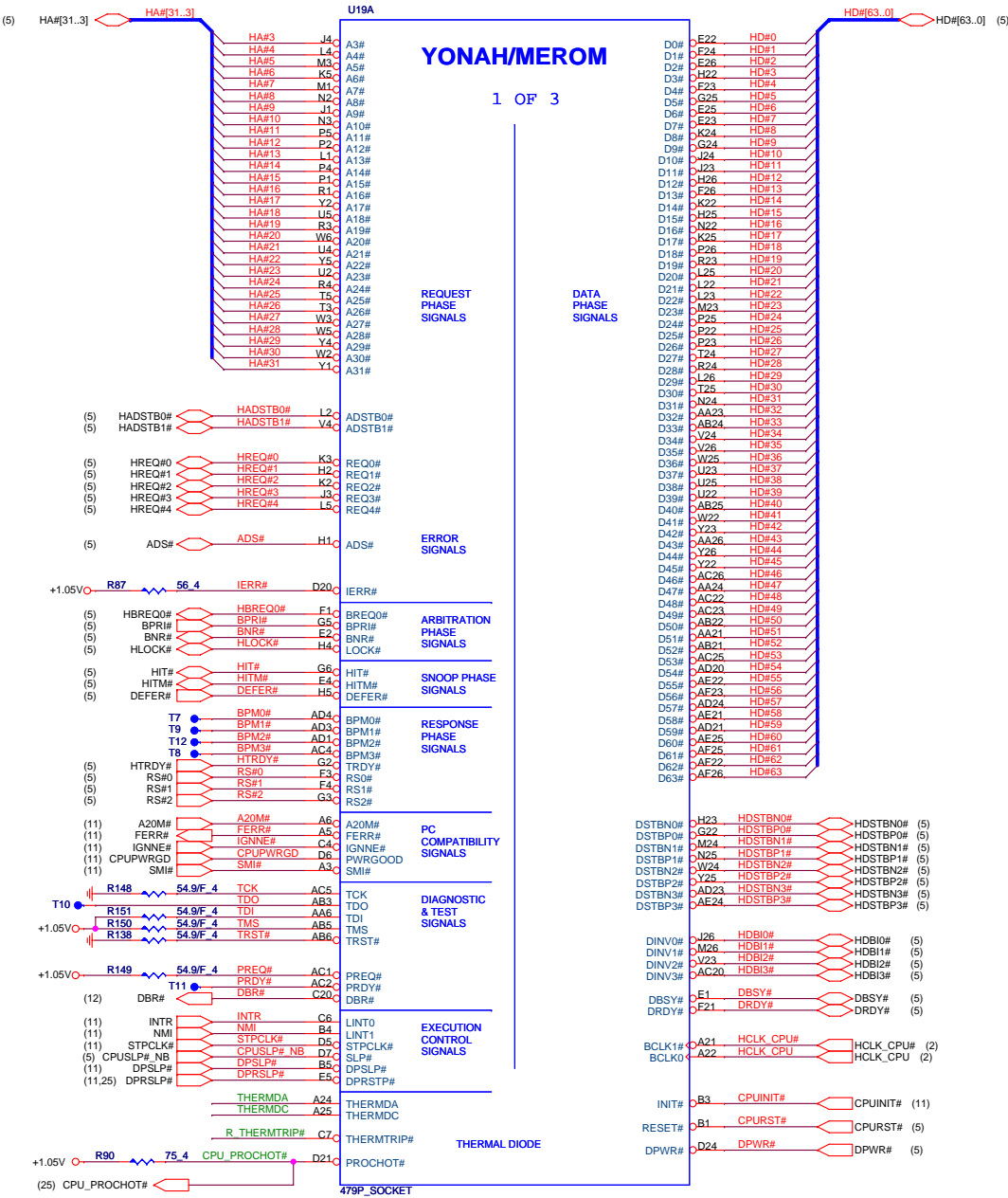
**FSB SETTING**

FSC	FSB	FSA	CPU	PCIE	PCI
0	0	0	266	100	33
0	0	1	133	100	33
0	1	0	200	100	33
0	1	1	166	100	33
1	0	0	333	100	33
1	0	1	100	100	33
1	1	0	400	100	33
1	1	1	200	100	33

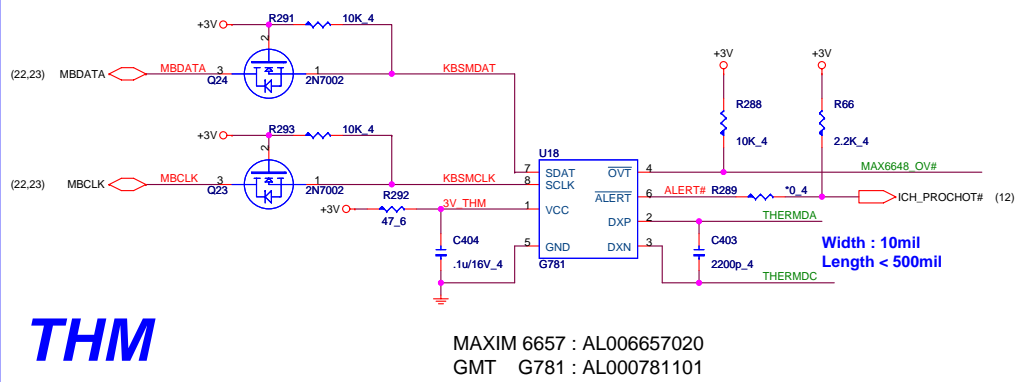
**PROJECT : BU1(NAPA)**  
**Quanta Computer Inc.**

Size: Document Number: **CLOCK GENERATOR** Rev: 1C

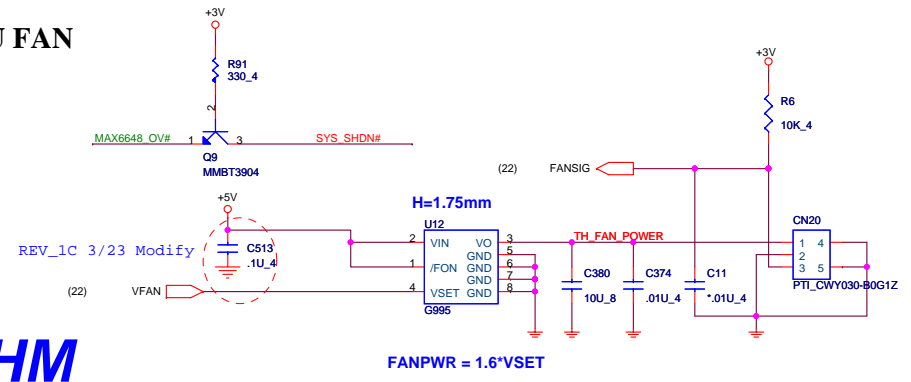
Date: Thursday, March 29, 2007 Sheet: 2 of 28



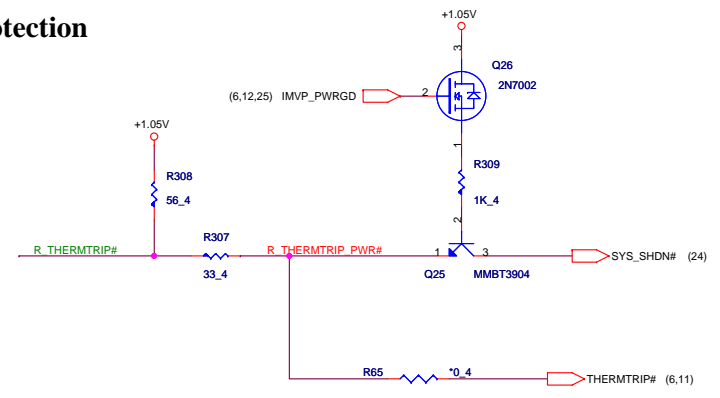
### SMBUS Address : 98



### CPU FAN



### 125 Degree Protection



# CPU

PROCHOT# Active: CPU reached it's max. soft operating temp  
Thermtrip# Active: CPU junction temp exceeds 125 degree

**PROJECT : BU1(NAPA)**  
**Quanta Computer Inc.**

Size	Document Number	Rev
	<b>CPU (HOST) / FAN</b>	1C
Date:	Friday, March 30, 2007	Sheet 3 of 28

COMP0 - COMP3  
Width : 20mil  
Length < 500mil

GTLREF  
Width : 5mil  
Length < 500mil

## YONAH/ MEROM

2 OF 3  
POWER,  
RESERVED  
SIGNALS

V6  
G21  
J6  
K6  
M6  
J21  
K21  
M21  
N21  
N6  
R21  
R6  
T21  
T6  
V21  
W21

AA1  
AA4  
AA2  
AA3  
M4  
N5  
T2  
V3  
B2  
C3  
B25  
T22  
D2  
F6  
D3  
C1  
AF1  
D22  
C23  
C24

(2) BSEL0 BSEL0 B22 BSEL0  
(2) BSEL1 BSEL1 B23 BSEL1  
(2) BSEL2 BSEL2 C21 BSEL2

(25) PSI# PSI# AE6 PSI#

(25) CPU\_VID0 CPU\_VID0 AD6 VID0  
(25) CPU\_VID1 CPU\_VID1 AE5 VID1  
(25) CPU\_VID2 CPU\_VID2 AE5 VID2  
(25) CPU\_VID3 CPU\_VID3 AE4 VID3  
(25) CPU\_VID4 CPU\_VID4 AE3 VID4  
(25) CPU\_VID5 CPU\_VID5 AE2 VID5  
(25) CPU\_VID6 CPU\_VID6 AE2 VID6

(25) VCCSENSE VCCSENSE AF7 VCCSENSE  
(25) VSSSENSE VSSSENSE AE7 VSSSENSE

479P\_SOCKET

VCC\_CORE


CAPACITOR HEIGHT: 1.25 +/- 0.2 mm

U19C

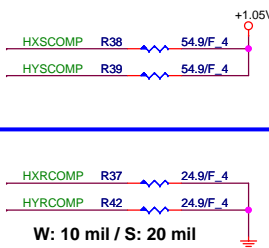
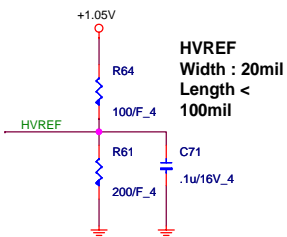
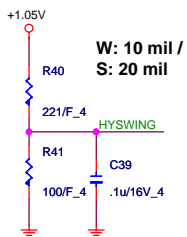
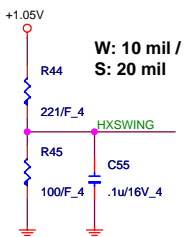
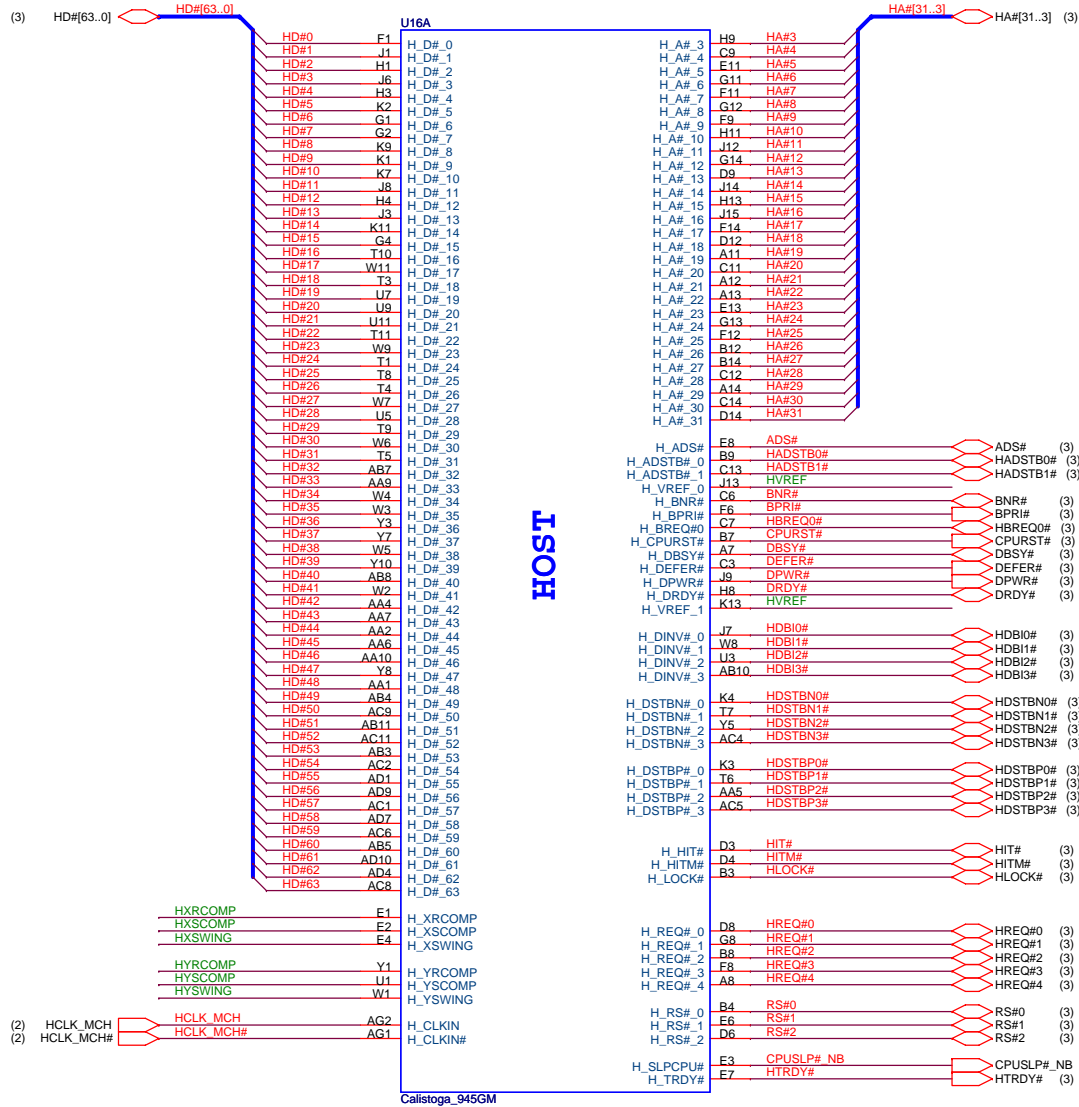
## YONAH/MEROM

3 OF 3  
GROUND

479P\_SOCKET

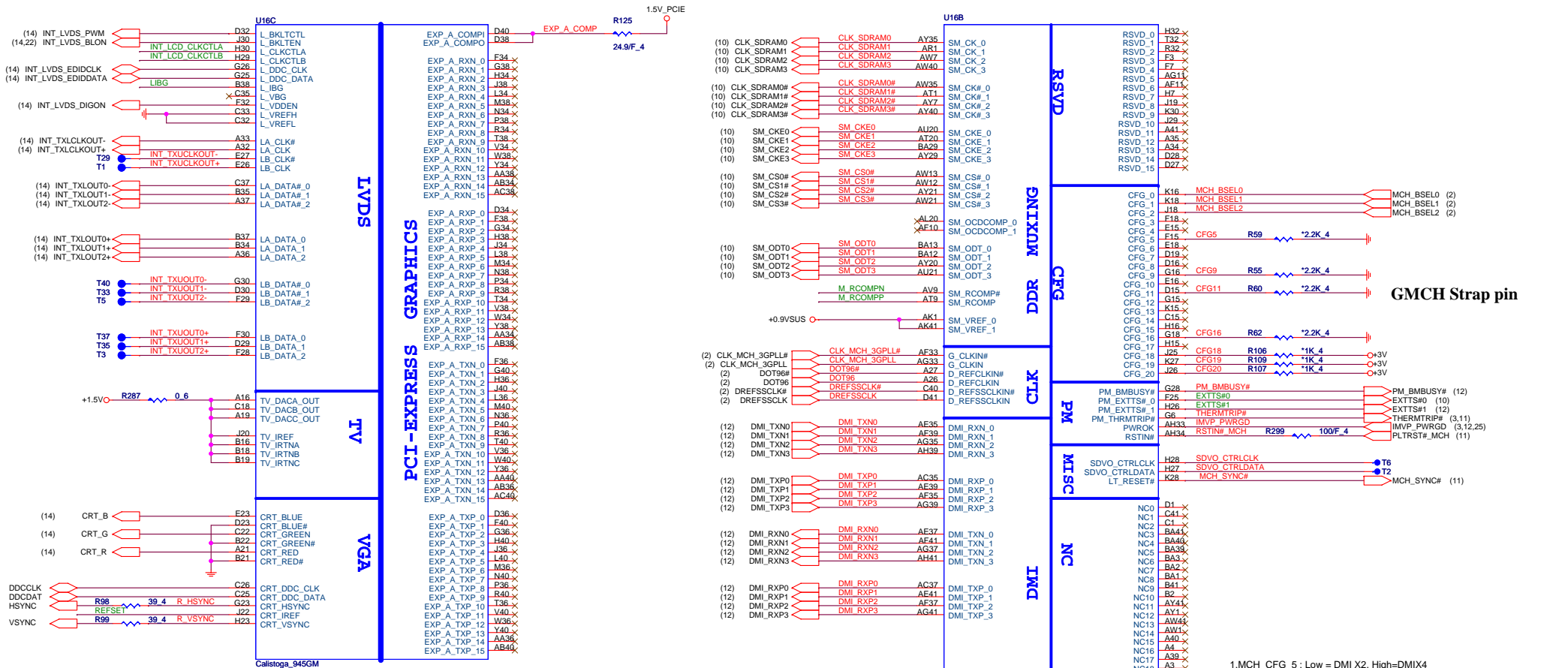
 PROJECT : BU1(NAPA)  
Quanta Computer Inc.

Size	Document Number	Rev
	CPU (POWER/GND)	1C
Date:	Thursday, March 29, 2007	Sheet 4 of 28

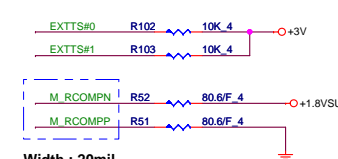
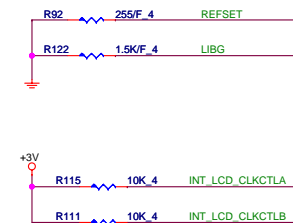
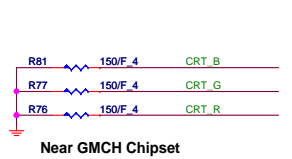


**PROJECT : BU1(NAPA)**  
**Quanta Computer Inc.**


# CLG



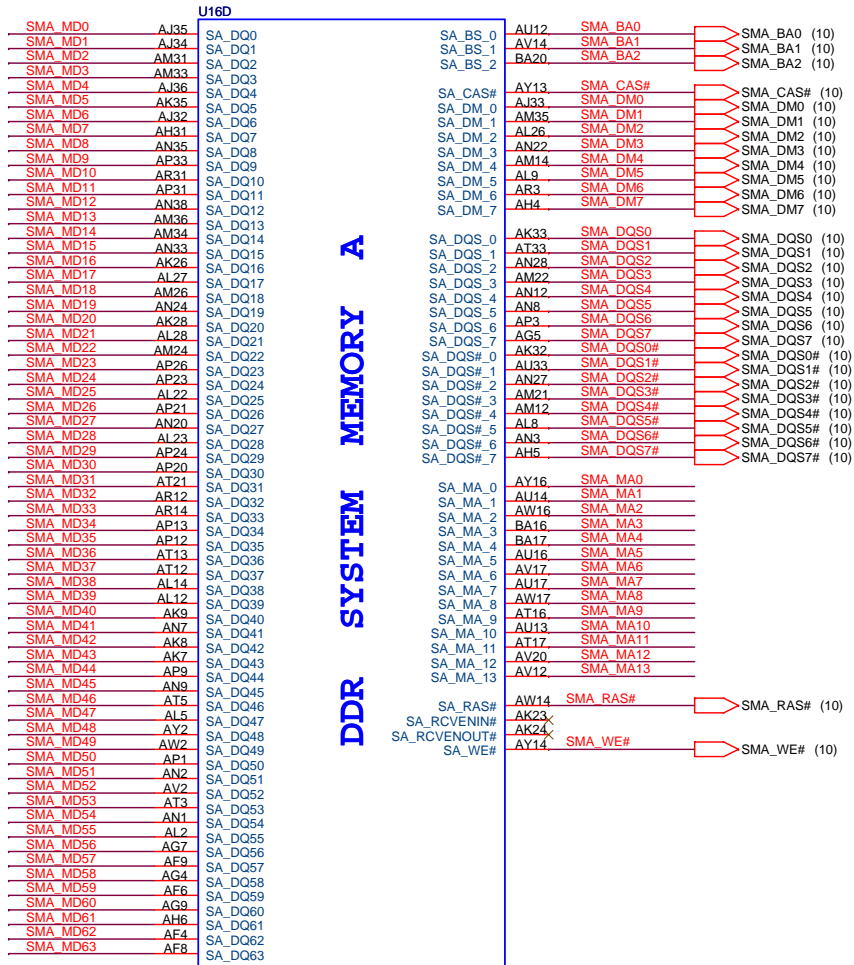
- MCH\_CFG\_5 : Low = DMI X2, High=DMIX4
- MCH\_CFG\_6 : Low = Moby Dick, High = Calistoga (Default)
- MCH\_CFG\_7 : Low = RSVD, High = Mobile CPU
- MCH\_CFG\_9 PCI Exp Graphics Lane: Low =Reverse lane ,High=Normal
- MCH\_CFG\_10 Host PLL VCC Select: Low=Reserved, High=Mobility
- MCH\_CFG\_11: PSB 4x Enable : Low=Rsvd, High=Calistoga.
- MCH\_CFG\_16 FSB Dymnic ODT: Low = Dynamic ODT Disabled, High= Dynamic ODT Enabled.
- MCH\_CFG\_18 VCC Select: LOW=1.05V, High=1.5V
- MCH\_CFG\_19 DMI Lane Reversal:Low=Normal,High=LANES Reversed.
- MCH\_CFG\_20 PCIe Backward interoperability mode: Low= only SDVO or PCIe x1 is operational (defaults) , High=SDVO and PCIe x1 are operation simultaneously via the PEG port.



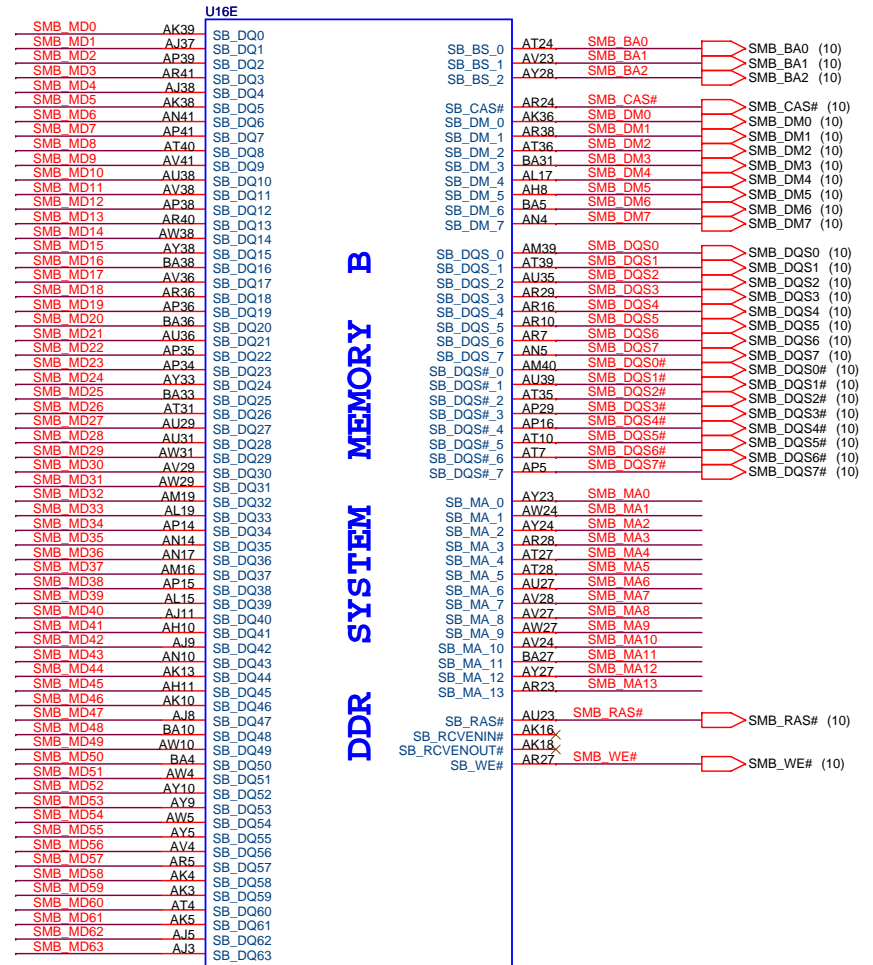
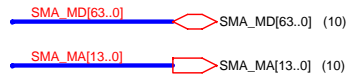
Width : 20mil  
Length < 500mil



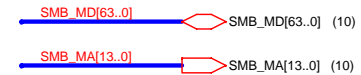
Size	Document Number	Rev
	<b>GMCH DMI &amp; VEDIO</b>	1C
Date:	Thursday, March 29, 2007	Sheet 6 of 28



Calistoga\_945GM



Calistoga\_945GM



**CLG**



Size	Document Number <b>GMCH (MEMORY)</b>	Rev 1C
Date:	Thursday, March 29, 2007	Sheet 7 of 28



Calistoga\_945GM

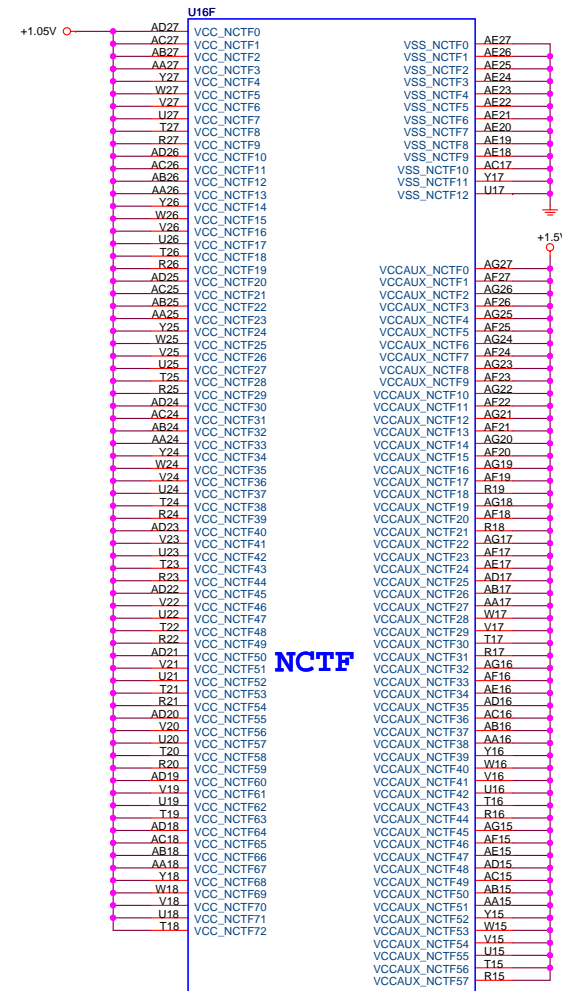
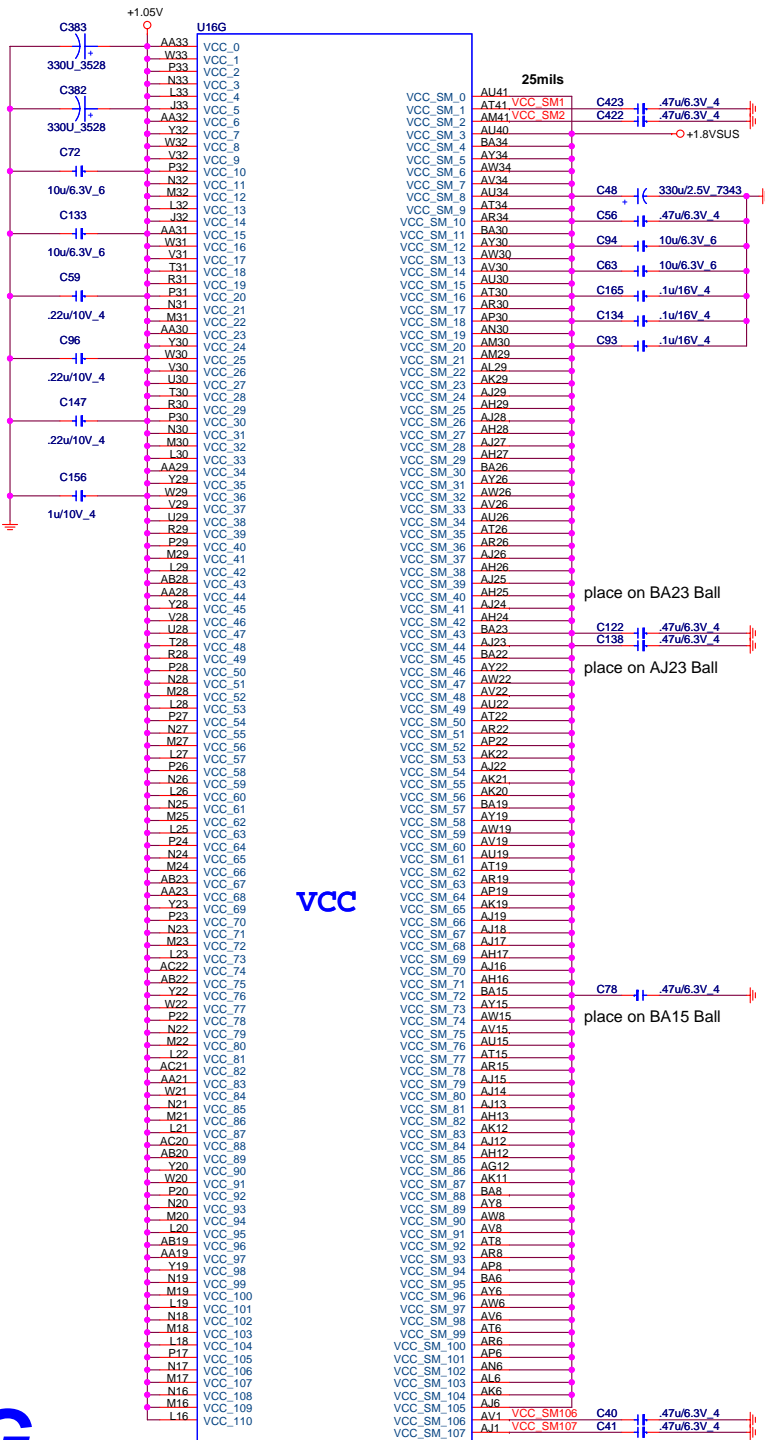
25mils

Calistoga\_945GM



PROJECT : BU1(NAPA)  
Quanta Computer Inc.

Size	Document Number	Rev
	<b>GMCH (POWER/NCTF)</b>	1C
Date:	Tuesday, March 20, 2007	Sheet 8 of 28



NCTF

VCC

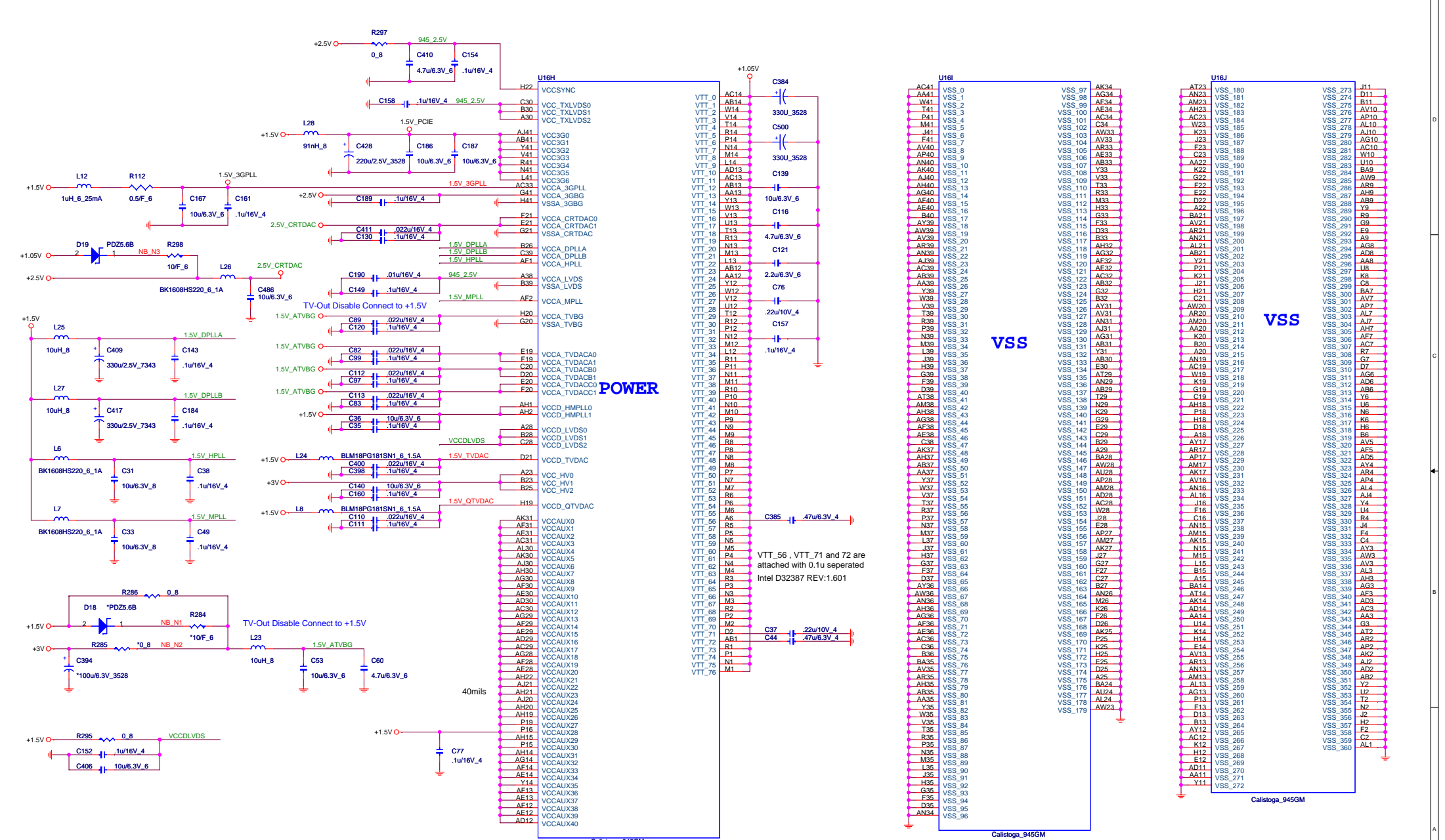
place on BA23 Ball

place on AJ23 Ball

place on BA15 Ball

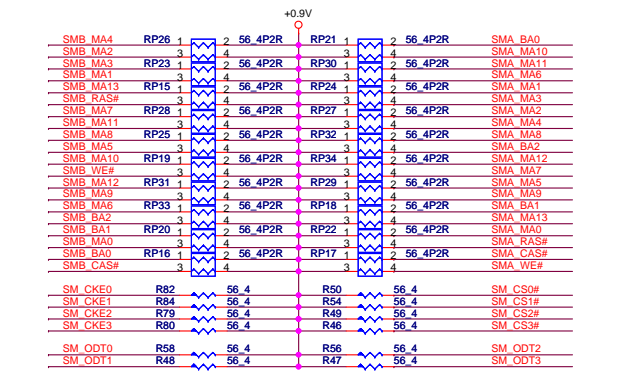
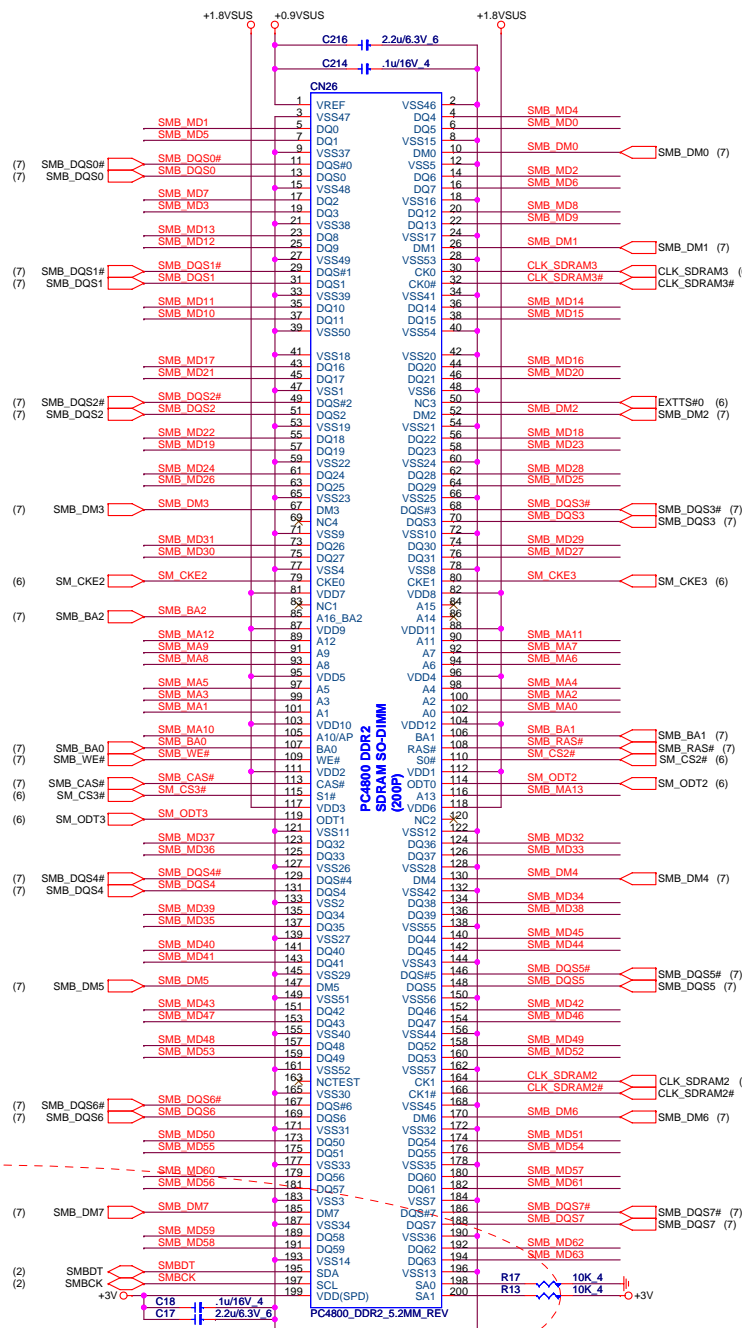
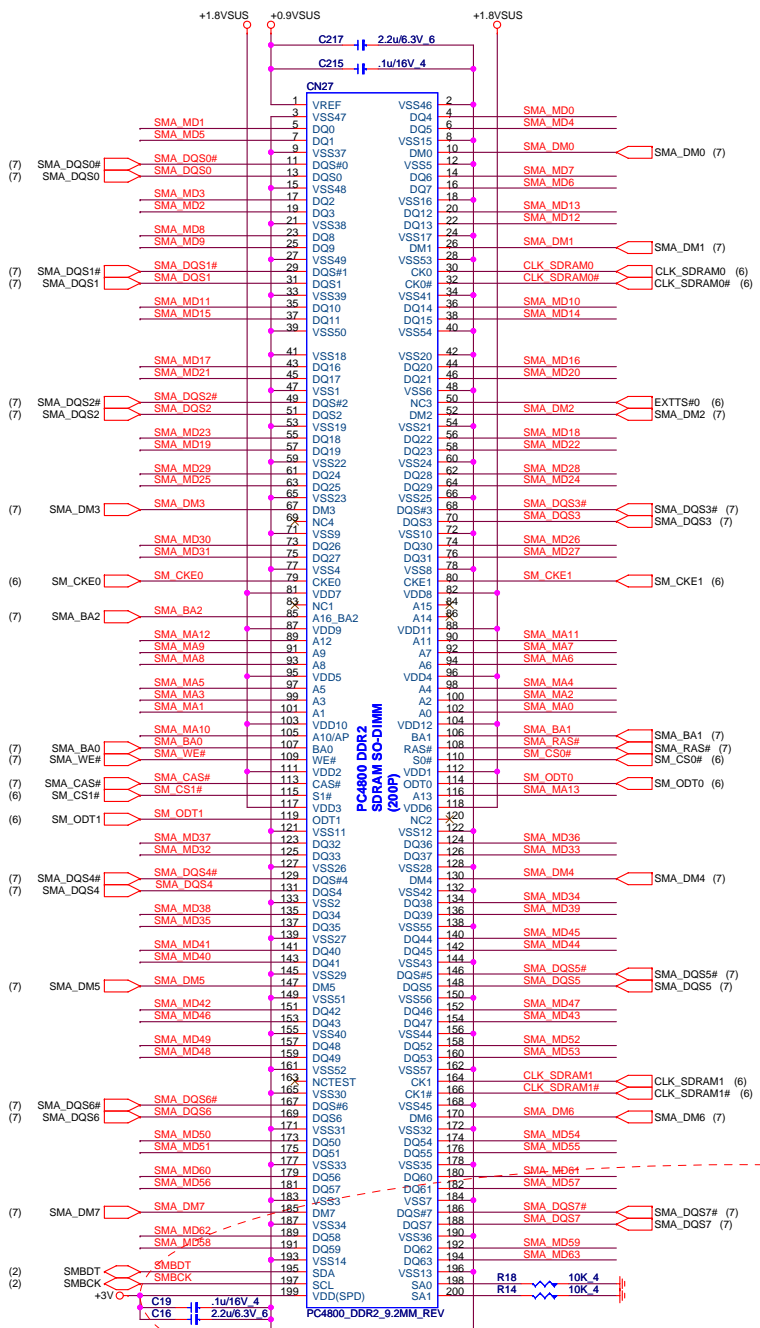
+1.5V



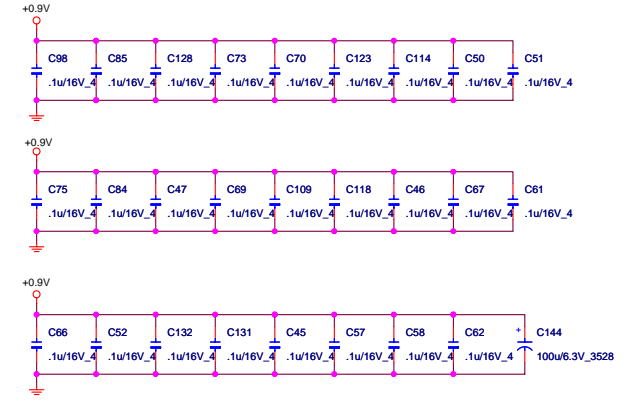


# DDR2 SO-DIMM SOCKET

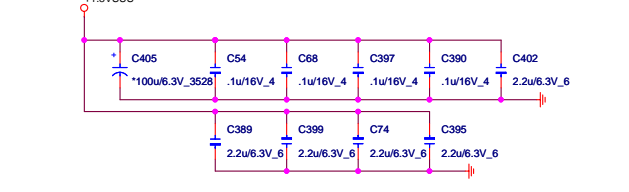
# DDR2 TERMINATOR



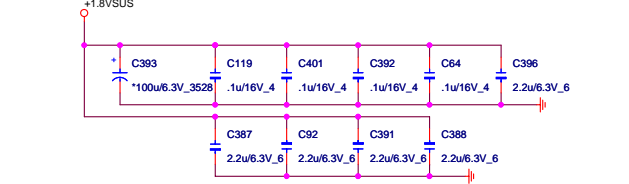
## TERMINATOR DECOUPLING CAPACITOR



## CLOSE SO-DIMM SOCKET CAPACITORS



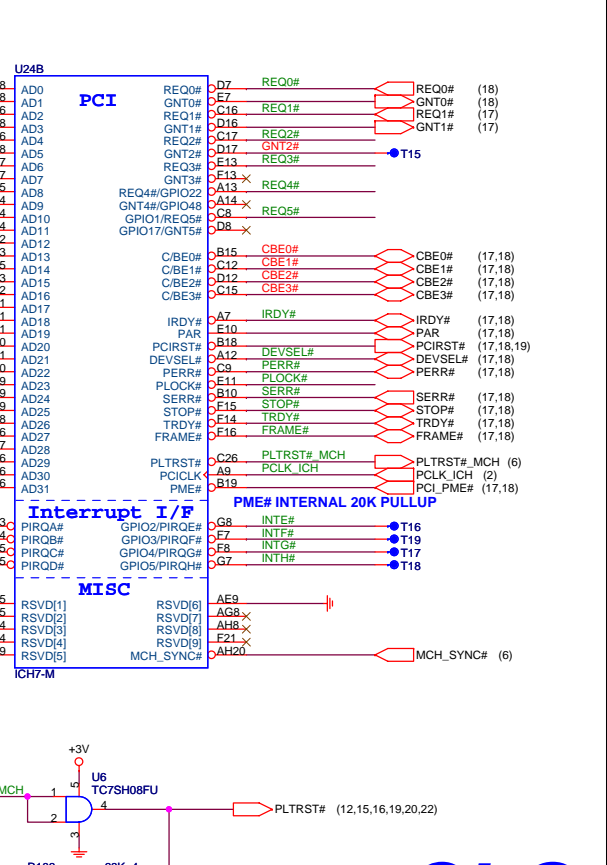
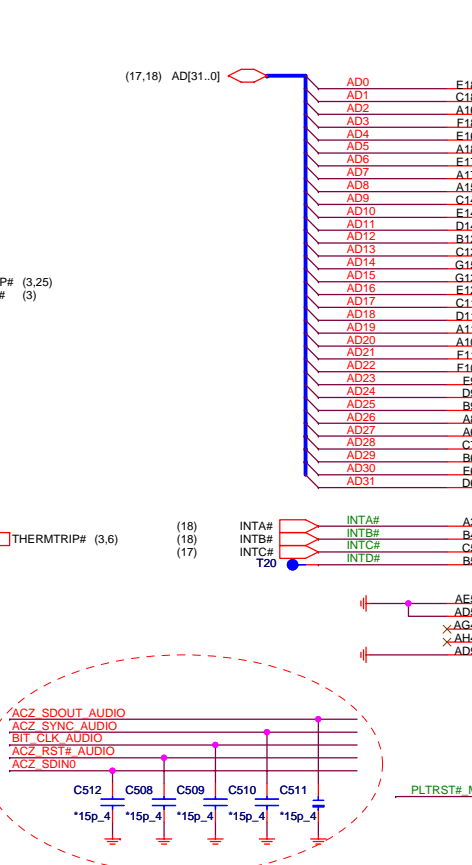
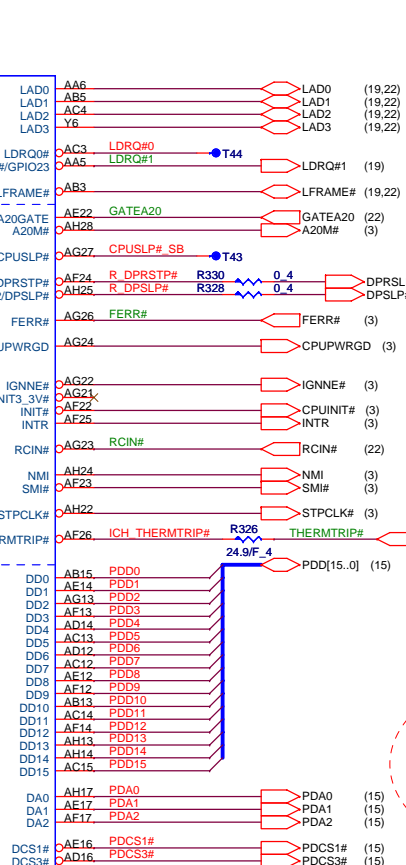
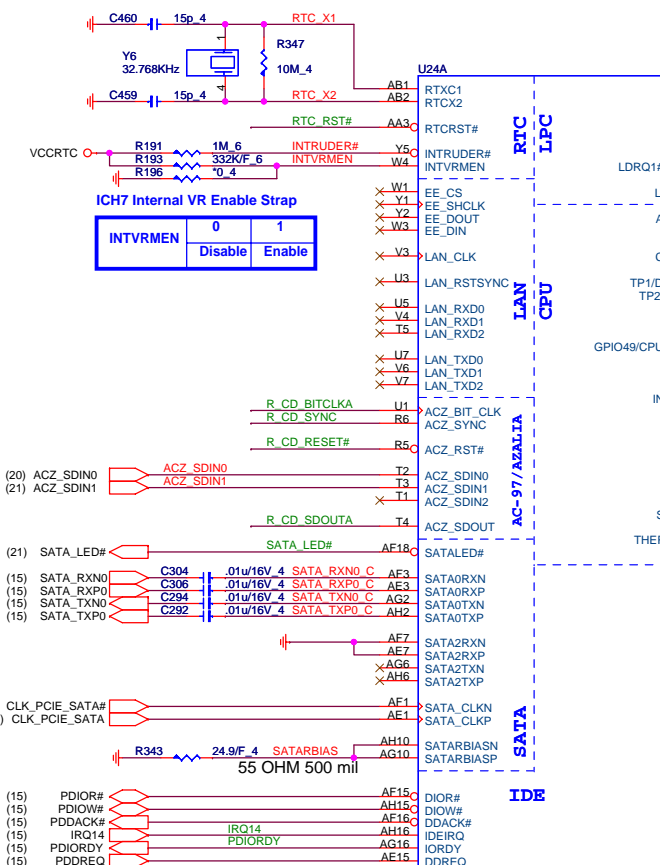
## CLOSE SO-DIMM SOCKET CAPACITORS



**DDR**

SMbus address A0  
**CLOCK 0,1,2  
 CKE 0,1**  
 SMA\_MD[63..0] SMA\_MD[63..0]  
 SMA\_MA[13..0] SMA\_MA[13..0]  
 REV\_IC 3/23 Modify SO-DIMM Footprint

SMbus address A1  
**CLOCK 3,4,5  
 CKE 2,3**  
 SMB\_MD[63..0] SMB\_MD[63..0]  
 SMB\_MA[13..0] SMB\_MA[13..0]

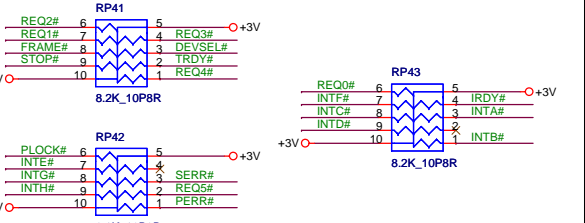
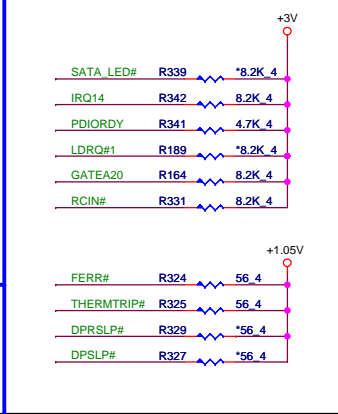
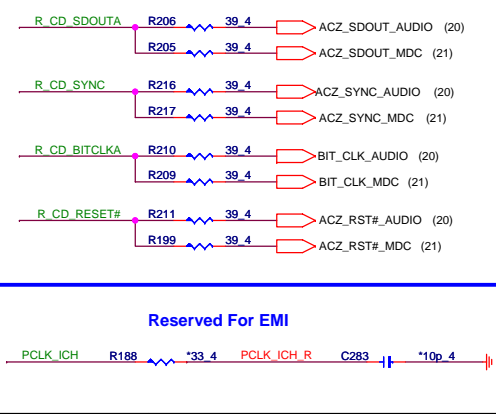
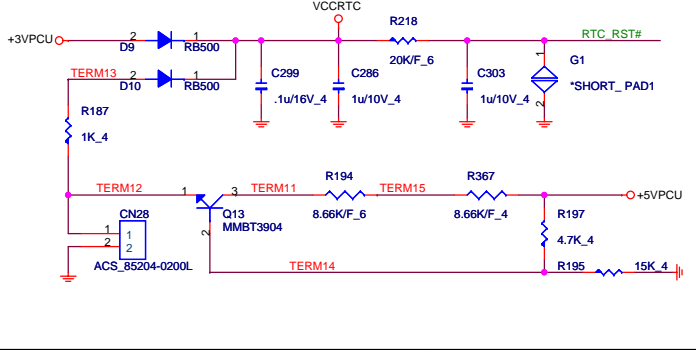


DIOR#: Write Strobe  
DIORDY#: Read Strobe

REV\_IC 3/23 Reserve for EMI  
Placement no rule

# CLG

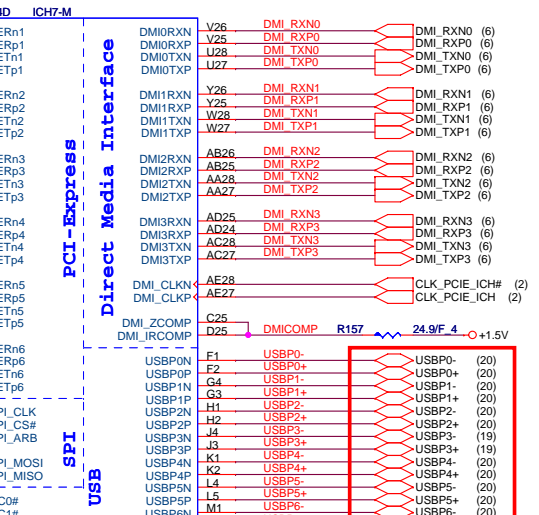
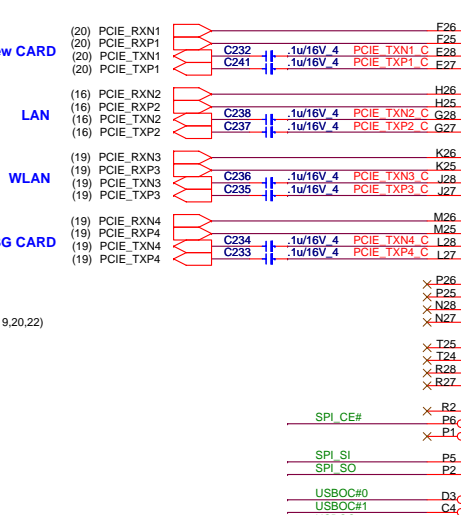
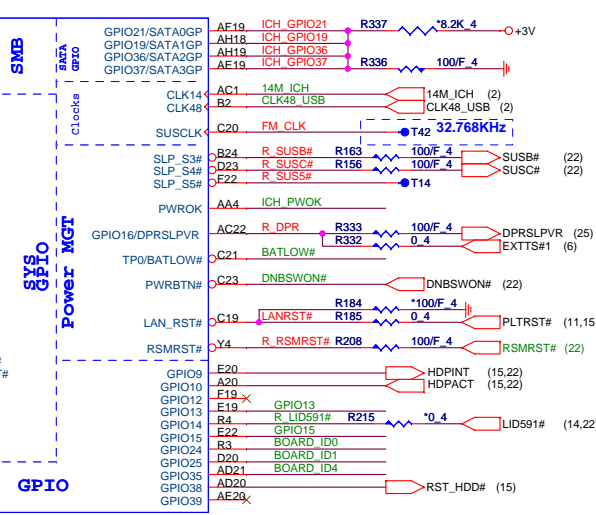
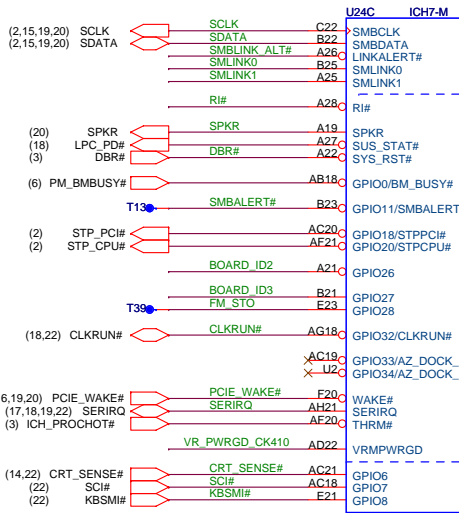
## RTC



Reserved For EMI

**PROJECT : BU1(NAPA)**  
**Quanta Computer Inc.**

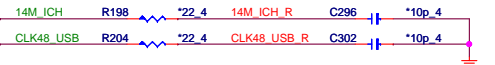
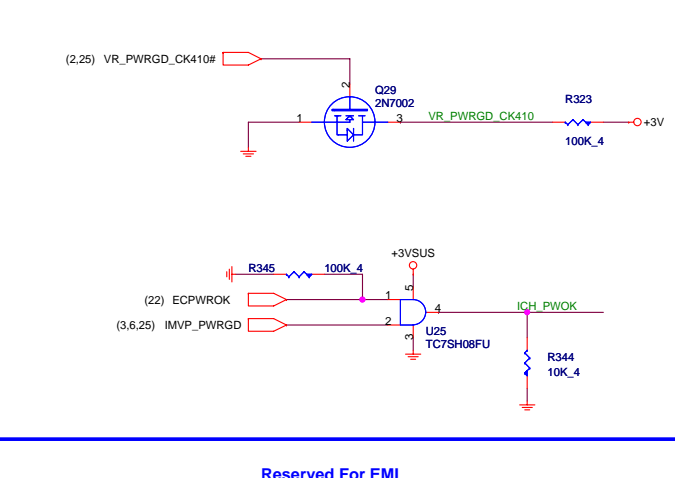
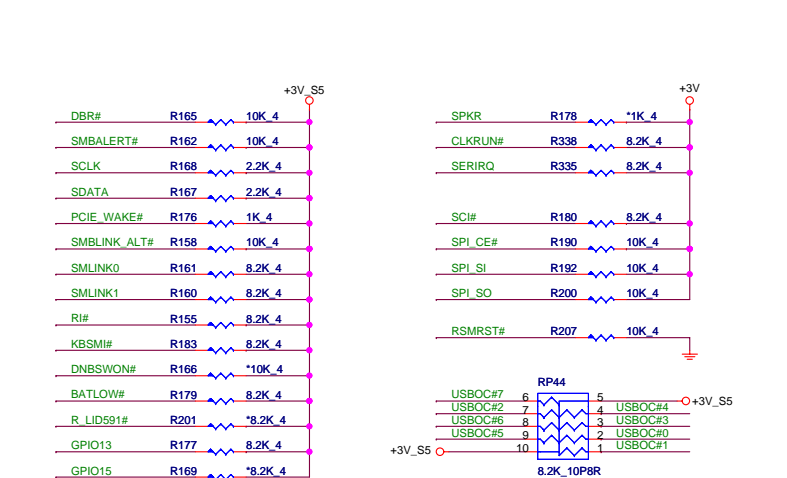
Size	Document Number	Rev
ICH7 (CPU/PCI/IDE)		1C
Date: Friday, March 30, 2007		Sheet 11 of 28



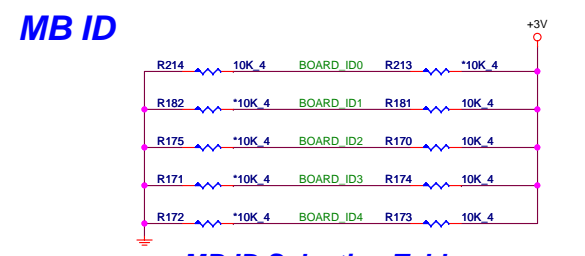
NAME	Tolerance	Power Well
GPIO6,7	3.3V	Core
GPIO8,9,10	3.3V	Resume
GPIO11	3.3V	Resume
GPIO12,13,14,15	3.3V	Resume
GPIO24,25,28	3.3V	Resume
GPIO35,38,39	3.3V	Core

USB Pair 0:	Audio Board Con Pin2,3	USB Pair 4:	Finger Printer
USB Pair 1:	RJ45/USB Con	USB Pair 5:	Blue Tooth
USB Pair 2:	Audio Board Pin4,5	USB Pair 6:	New Card
USB Pair 3:	GPS or Relatek WLAN	USB Pair 7:	Camera

**CLG**



Reserved For EMI



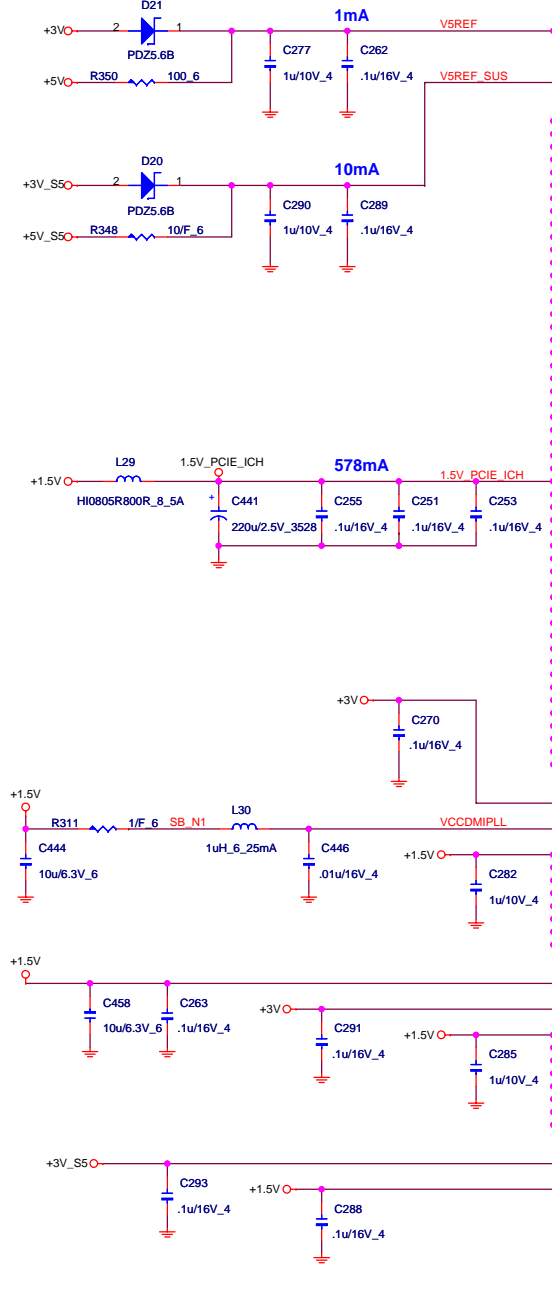
**MB ID Selection Table**

	BOARD_ID0	BOARD_ID1	BOARD_ID2	BOARD_ID3
W/ New Crad	H			
W/O New Crad	L			
W/O Crad Bus		H		
W/ G-Sensor		L	H	
W/O G-Sensor			L	
W/ CCD				H
W/O CCD				L

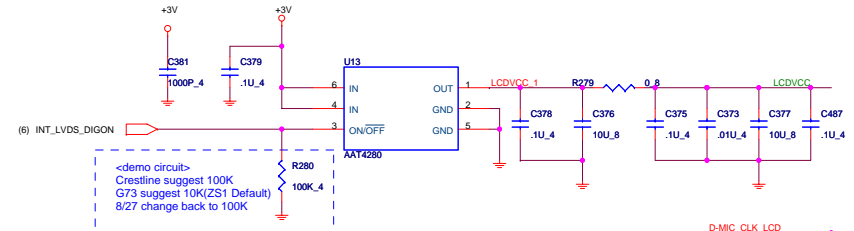
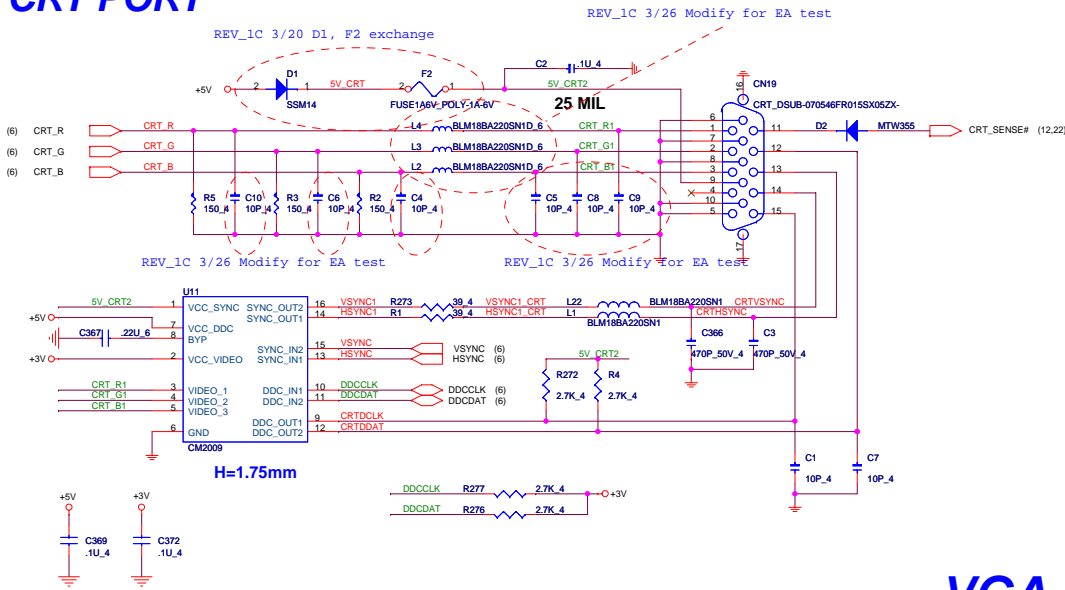
**PROJECT : BU1(NAPA)  
Quanta Computer Inc.**

Size	Document Number	Rev
	<b>ICH7 (USB/GPIO)</b>	<b>1C</b>
Date:	Thursday, March 29, 2007	Sheet 12 of 28

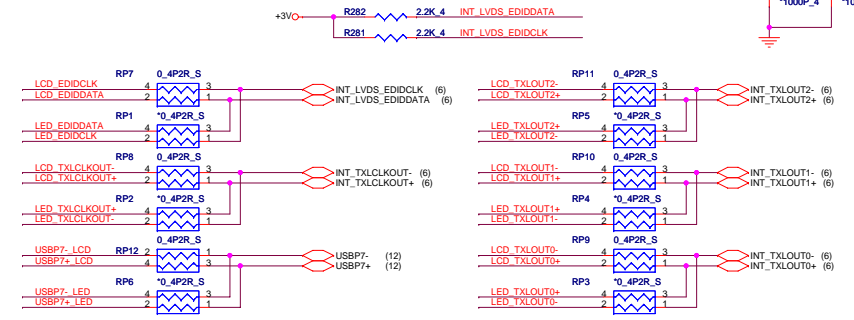
U24E		
A4	VSS[1]	P28
A23	VSS[2]	R1
B1	VSS[3]	R11
B8	VSS[100]	R12
B11	VSS[4]	R13
B14	VSS[5]	R14
B17	VSS[7]	R15
B20	VSS[8]	R16
B26	VSS[105]	R17
B28	VSS[9]	R18
B30	VSS[10]	T6
C2	VSS[11]	T12
C6	VSS[12]	T13
C7	VSS[13]	T14
D10	VSS[14]	T15
D13	VSS[15]	T16
D18	VSS[16]	T17
D21	VSS[17]	U4
D24	VSS[18]	U13
E1	VSS[19]	U14
E2	VSS[20]	U15
E4	VSS[21]	U16
E8	VSS[22]	U17
F3	VSS[23]	U18
F4	VSS[24]	U19
F5	VSS[25]	U20
F12	VSS[27]	U26
F27	VSS[28]	U25
F28	VSS[28]	U25
G1	VSS[29]	U26
G2	VSS[30]	V2
G5	VSS[31]	V13
G6	VSS[32]	V15
G9	VSS[33]	V28
G14	VSS[34]	W6
G18	VSS[35]	W25
G21	VSS[36]	W26
G24	VSS[37]	Y3
G25	VSS[39]	Y24
G26	VSS[40]	Y28
H3	VSS[41]	AA1
H4	VSS[42]	AA24
H5	VSS[43]	AA25
H24	VSS[44]	AA26
H27	VSS[45]	AB4
H28	VSS[45]	AB4
J1	VSS[46]	AB6
J2	VSS[48]	AB11
J5	VSS[49]	AB14
J24	VSS[50]	AB16
J26	VSS[51]	AB19
J26	VSS[51]	AB21
K24	VSS[52]	AB24
K27	VSS[54]	AB27
K28	VSS[55]	AB28
L13	VSS[56]	AC2
L15	VSS[57]	AC5
L24	VSS[58]	AC9
L25	VSS[59]	AC11
L26	VSS[60]	AD1
M3	VSS[61]	AD3
M4	VSS[62]	AD4
M5	VSS[63]	AD7
M12	VSS[64]	AD8
M13	VSS[65]	AD11
M14	VSS[66]	AD15
M15	VSS[66]	AD19
M16	VSS[67]	AD23
M17	VSS[68]	AE2
M24	VSS[69]	AE4
M27	VSS[71]	AE8
M28	VSS[72]	AE13
N1	VSS[73]	AE18
N2	VSS[74]	AE24
N5	VSS[75]	AE24
N6	VSS[76]	AE24
N11	VSS[77]	AE2
N12	VSS[78]	AE4
N13	VSS[79]	AE8
N14	VSS[80]	AE8
N15	VSS[81]	AF11
N16	VSS[82]	AF28
N17	VSS[82]	AF28
N18	VSS[83]	AG1
N24	VSS[84]	AG3
N25	VSS[85]	AG7
N26	VSS[86]	AG7
P3	VSS[87]	AG14
P4	VSS[88]	AG17
P12	VSS[89]	AG20
P13	VSS[91]	AG25
P14	VSS[92]	AH1
P15	VSS[92]	AH3
P16	VSS[93]	AH7
P17	VSS[94]	AH12
P24	VSS[95]	AH23
P27	VSS[96]	AH27
ICH7-M	VSS[97]	VSS[194]



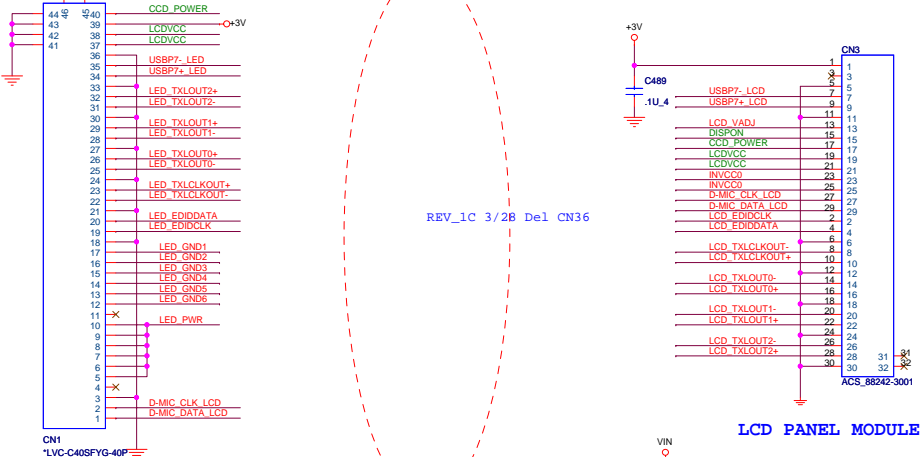
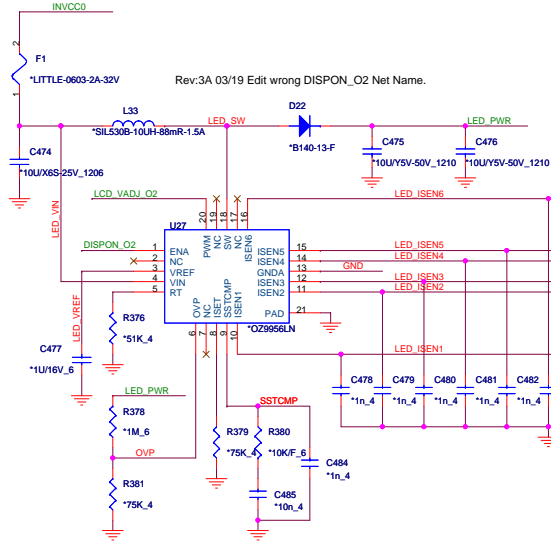
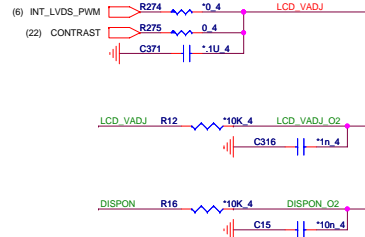
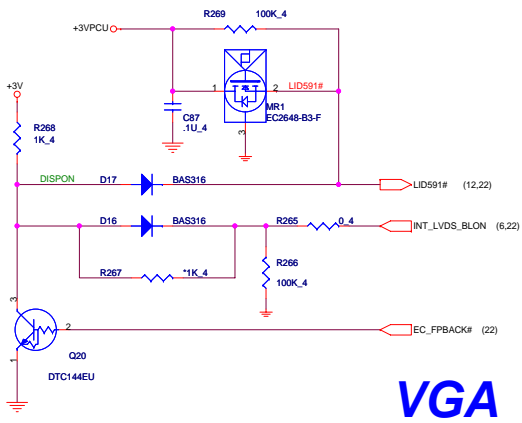
# CRT PORT



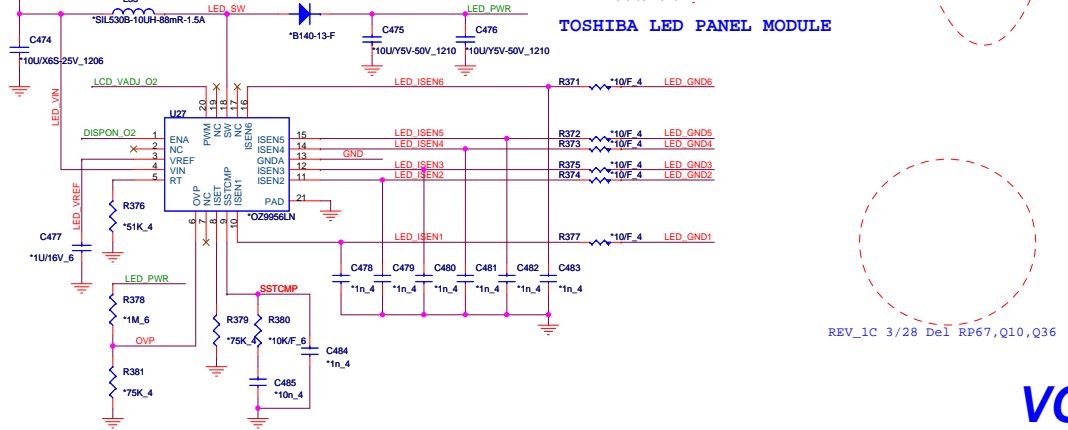
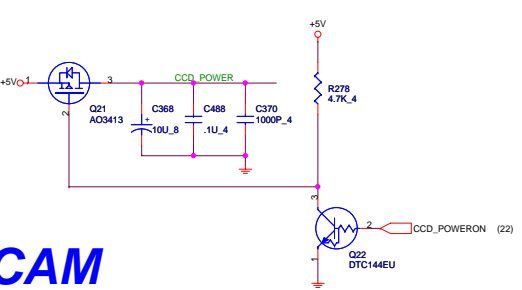
# LCD/LED TYPE CONNECTOR



# LID SWITCH

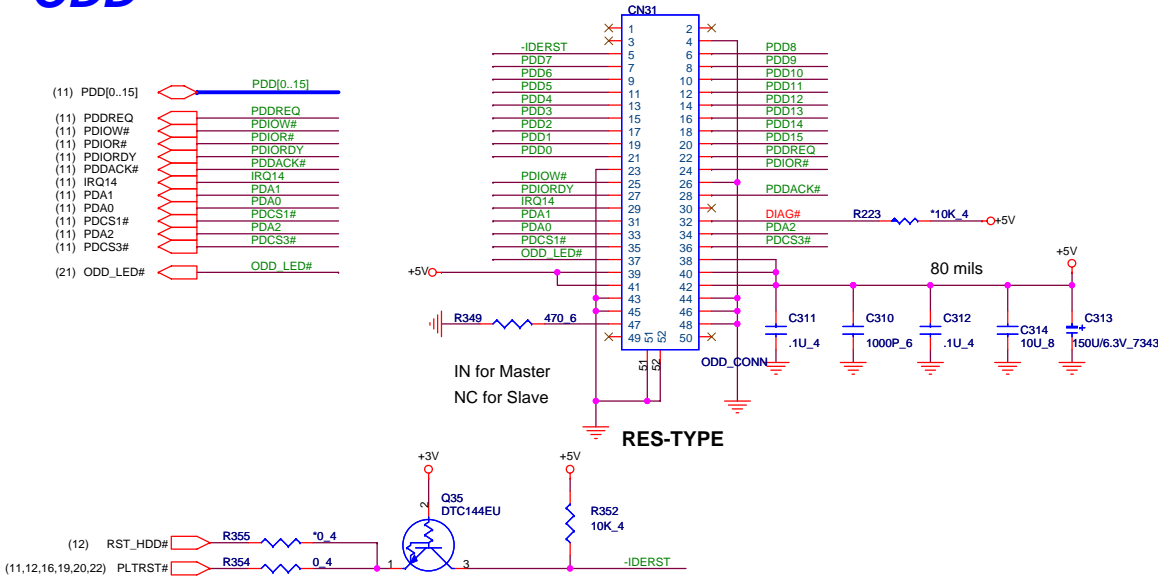


# CAMERA MODULE Power

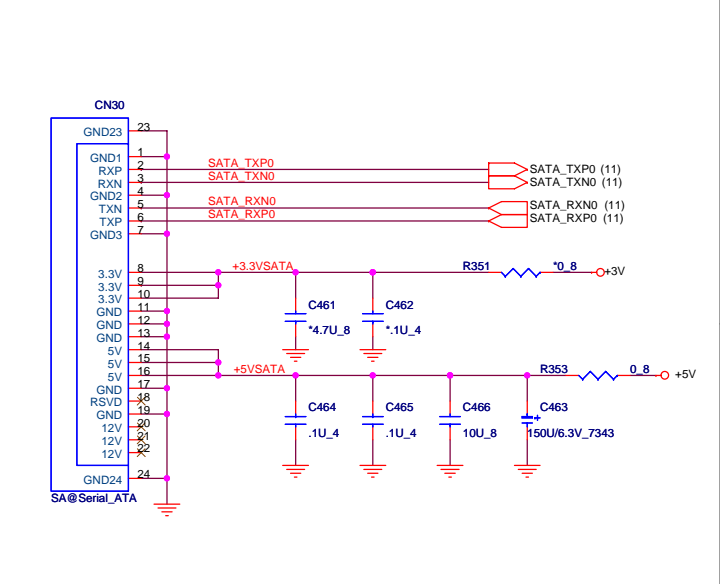


PROJECT : BU1(NAPA)  
Quanta Computer Inc.

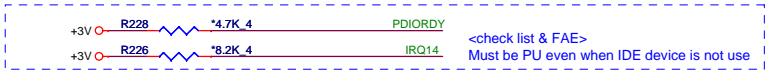
# ODD



# SATA HDD



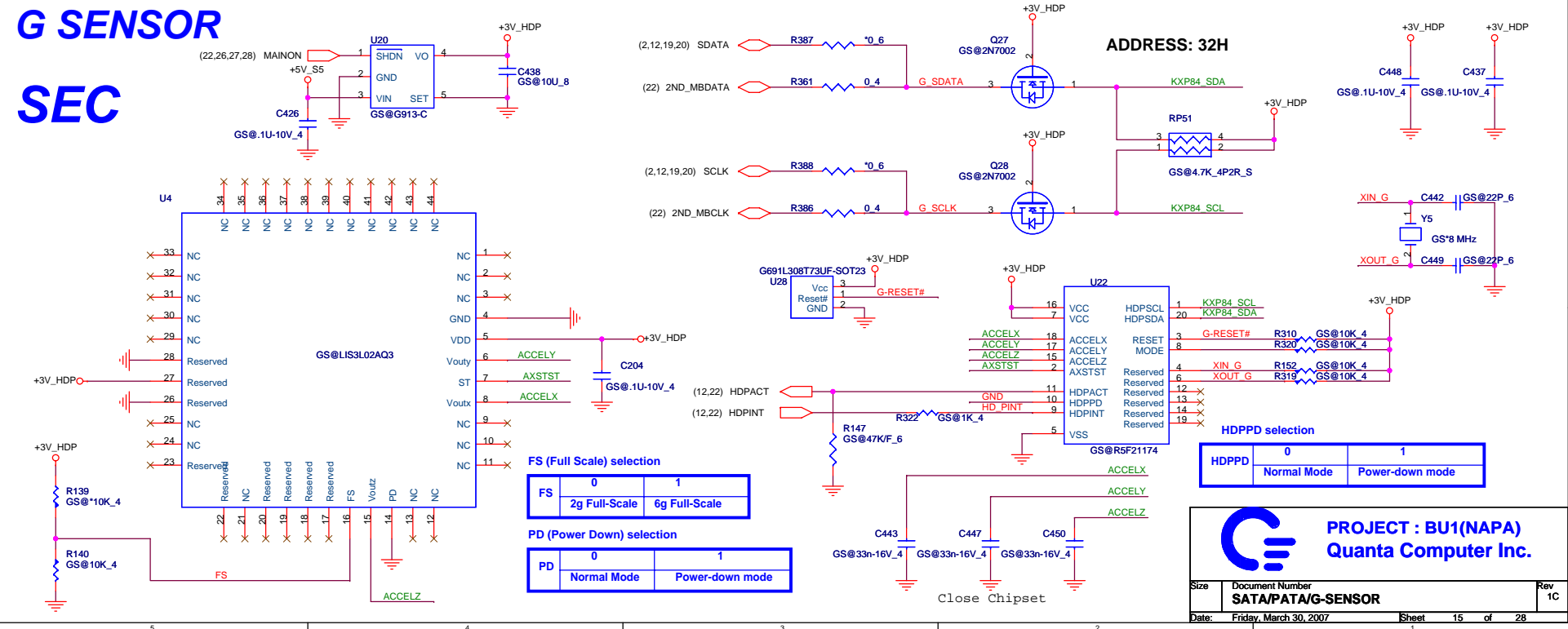
# IDE



# IDE

# G SENSOR

# SEC

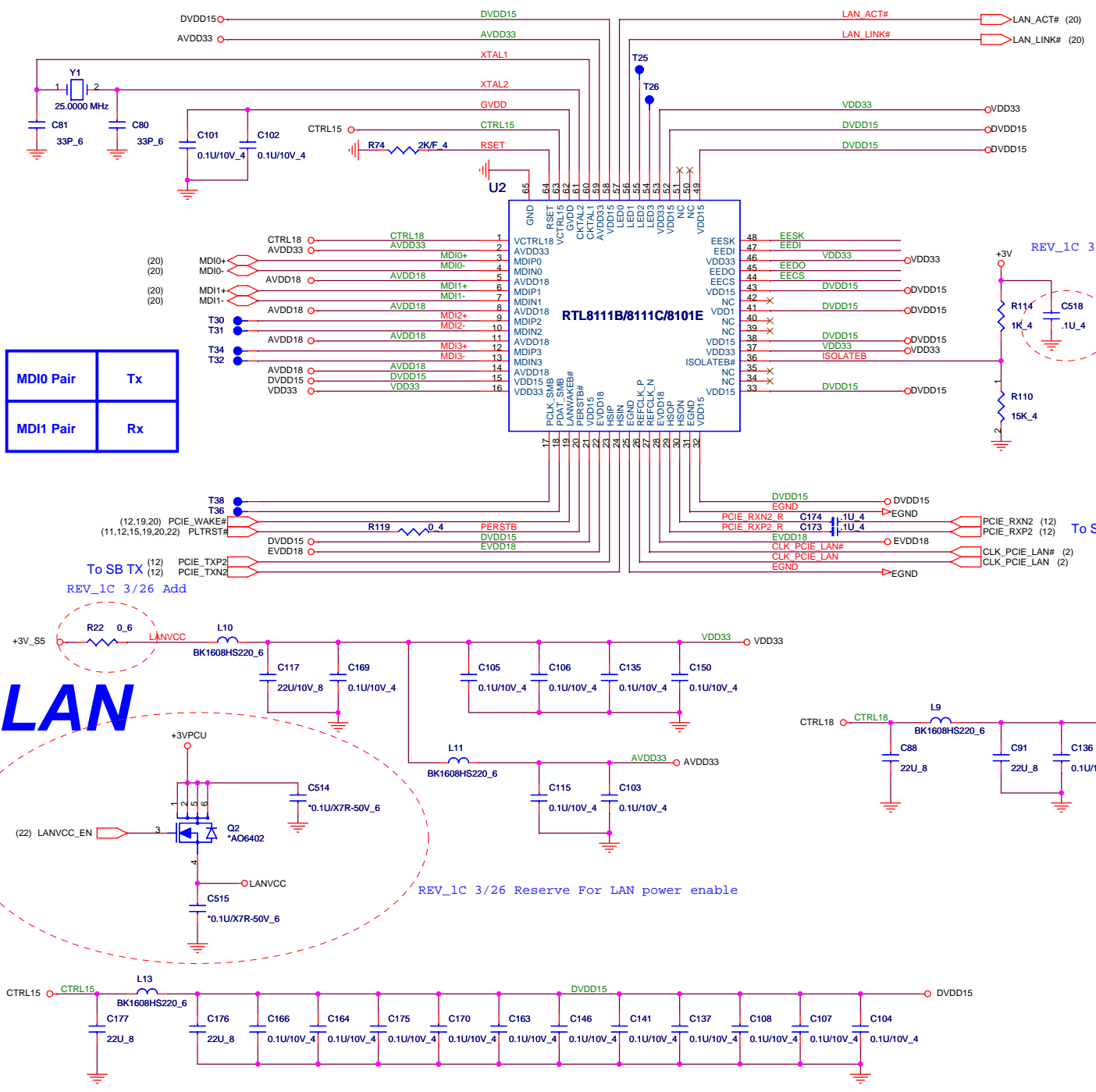


**PROJECT : BU1(NAPA)**  
**Quanta Computer Inc.**

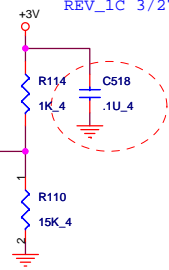
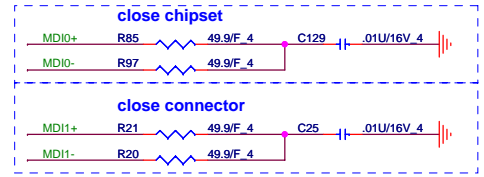
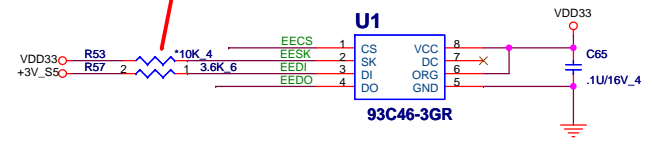
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MDI0 Pair	Tx
MDI1 Pair	Rx

# LAN

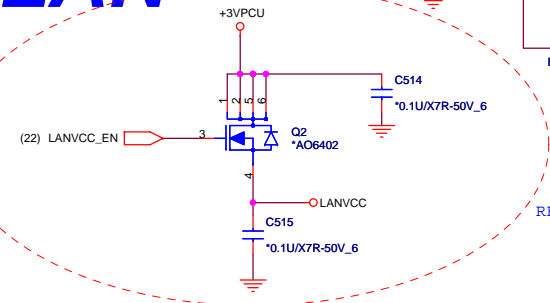
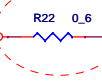


93C56: STUFF  
93C46: NOSTUFF



To SB TX (12)  
To SB RX (12)

REV\_1C 3/26 Add

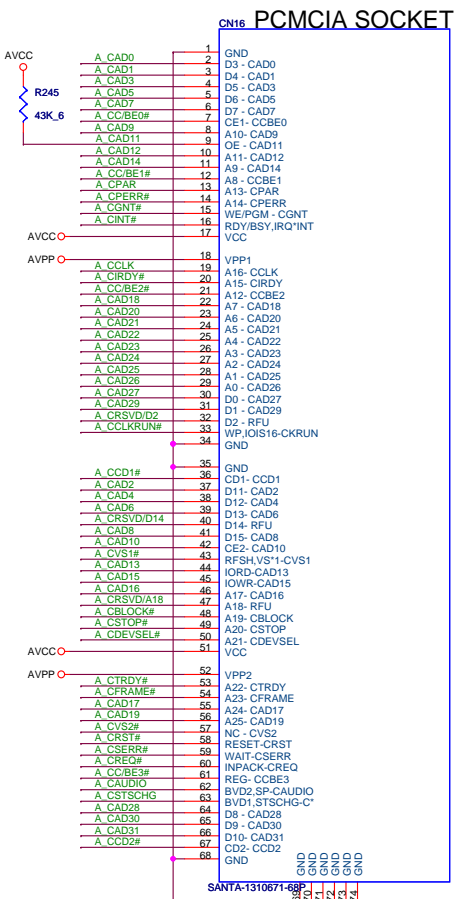
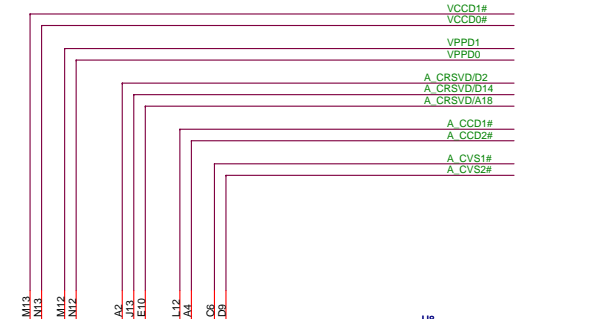
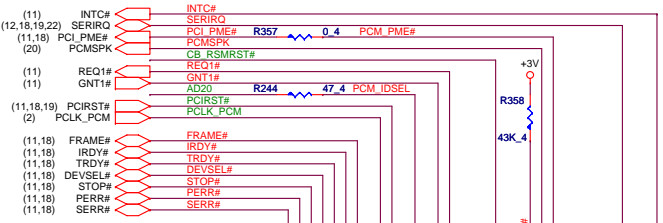
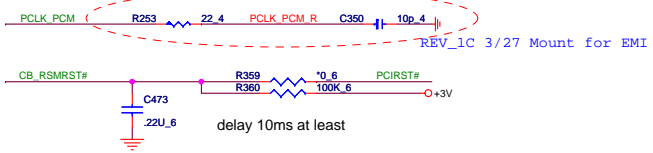
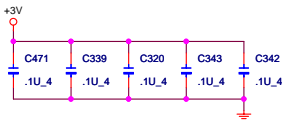
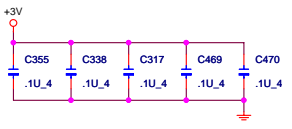


REV\_1C 3/26 Reserve For LAN power enable

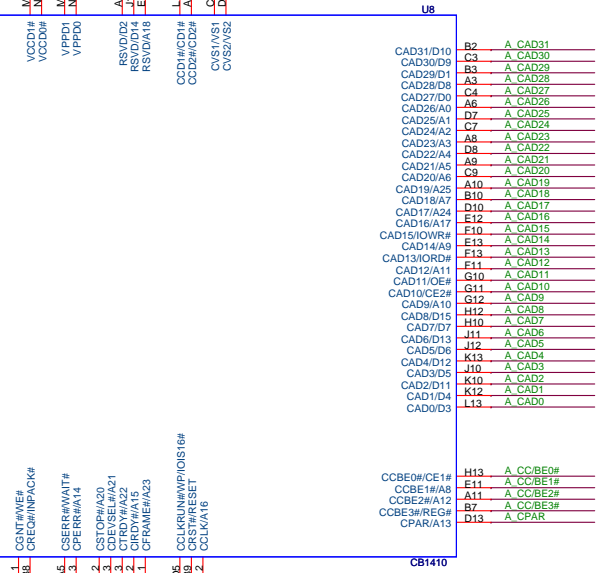
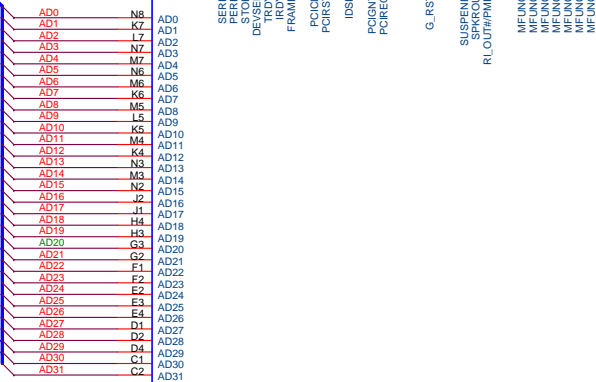
**PROJECT : BU1(NAPA)**  
**Quanta Computer Inc.**

Size	Document Number	Rev
	<b>RTL8101E 10/100 LAN</b>	<b>1C</b>
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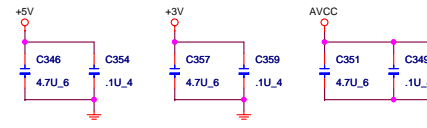
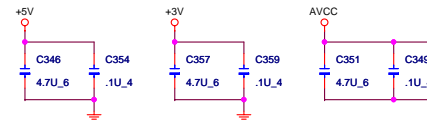
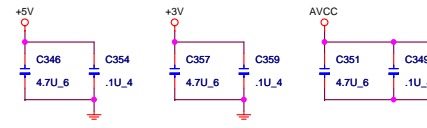
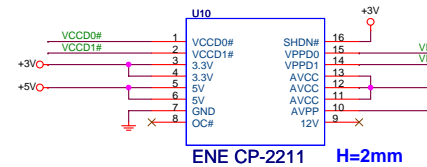
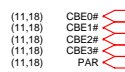
(11,18) AD[31..0]



ENE1410 AJ014100T41

ID Select : AD20  
 Interrupt Pin : INTC#  
 Request Indicate : REQ1#  
 Grant Indicate : GNT1#

**CBS**



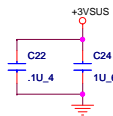
H=1.4mm

H=2mm

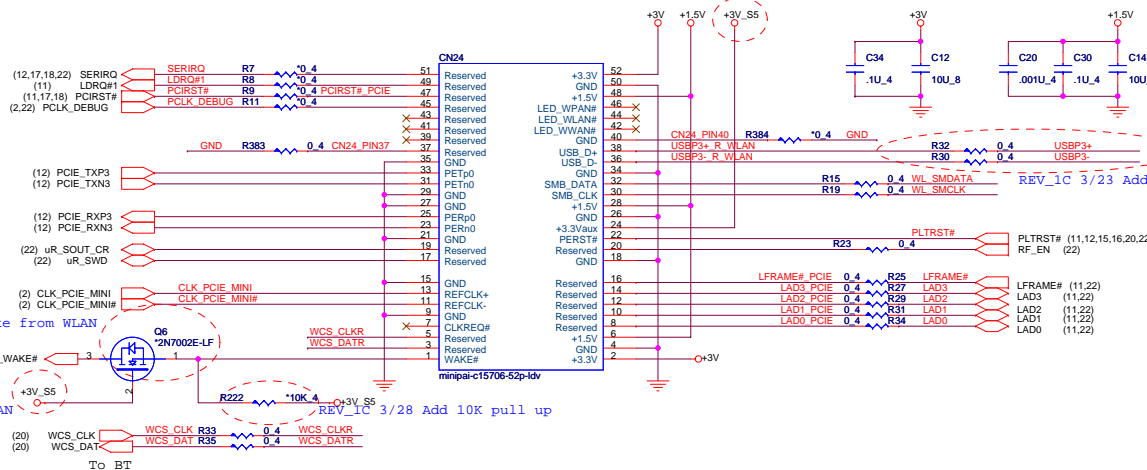
CB1410







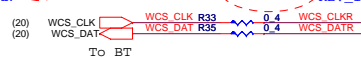
### Mini PCI-E Card WLAN



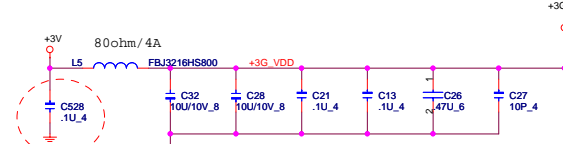
REV\_1C 3/23 Mount Q6 for wake from WLAN



REV\_1C 3/23 Change to +3V\_S5 for wake from WLAN

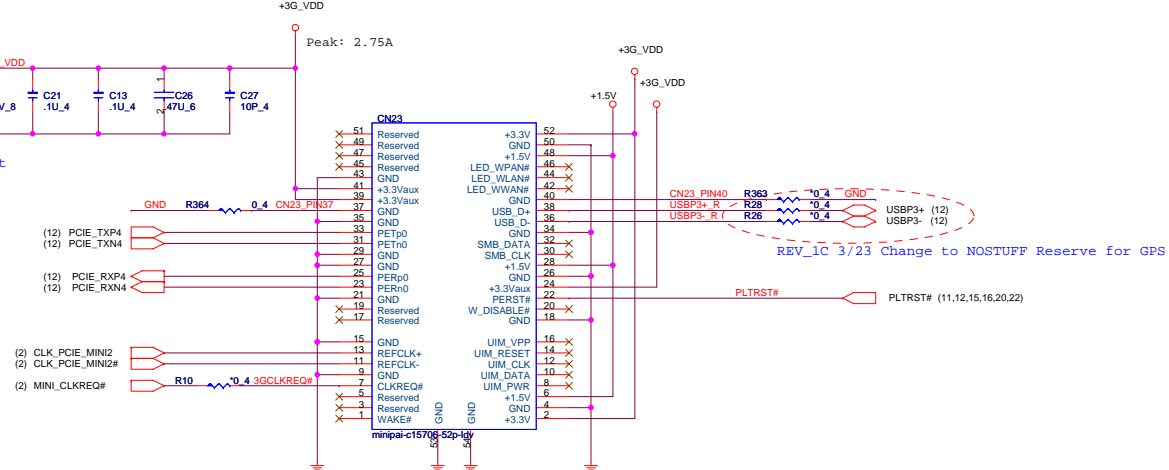


REV\_1C 3/27 Add for EMI requirement

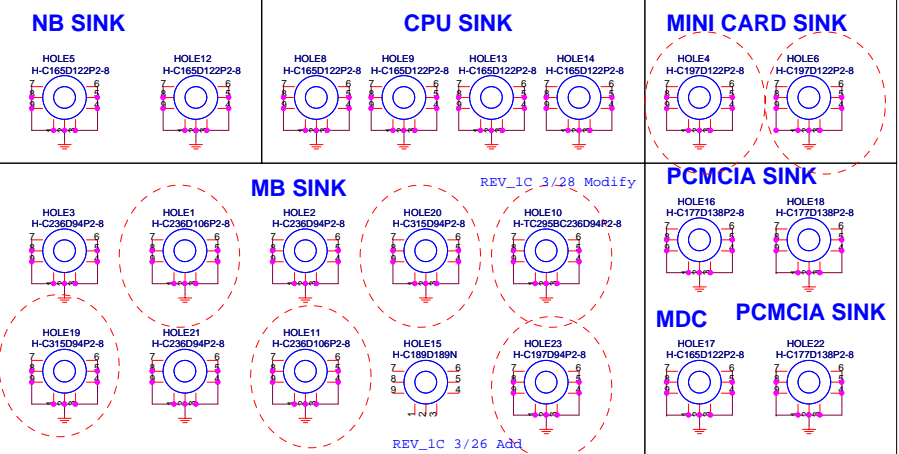


### Mini PCI-E Card

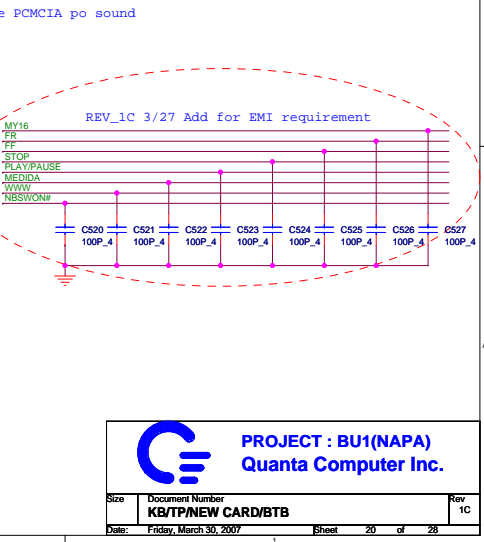
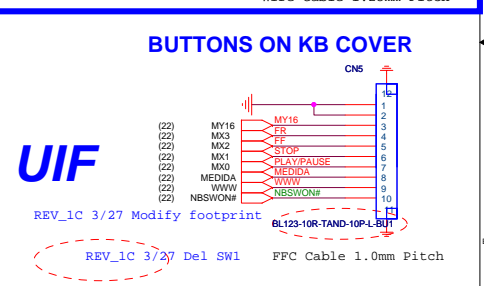
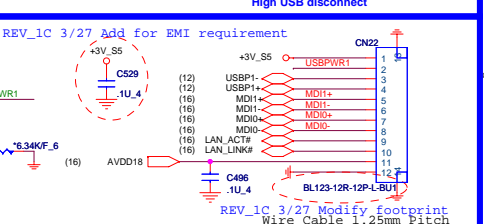
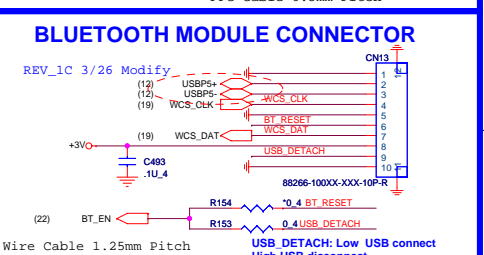
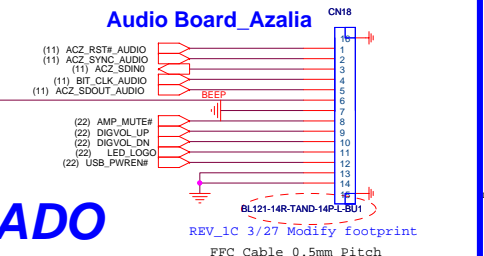
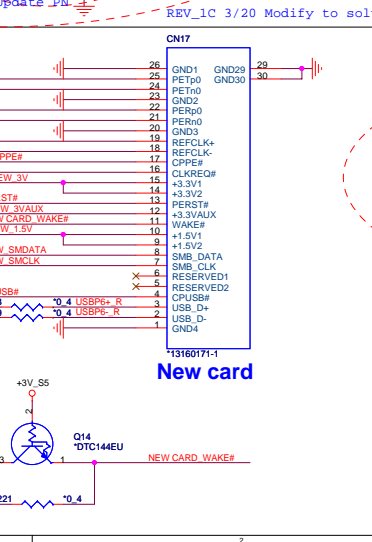
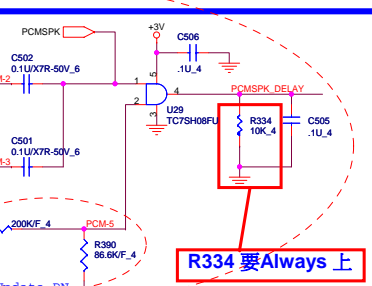
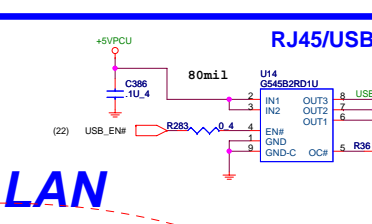
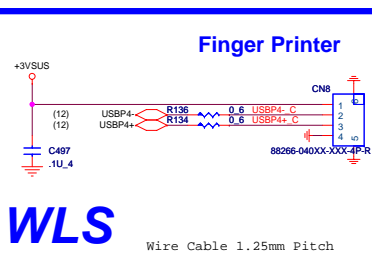
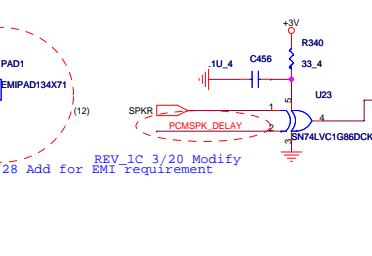
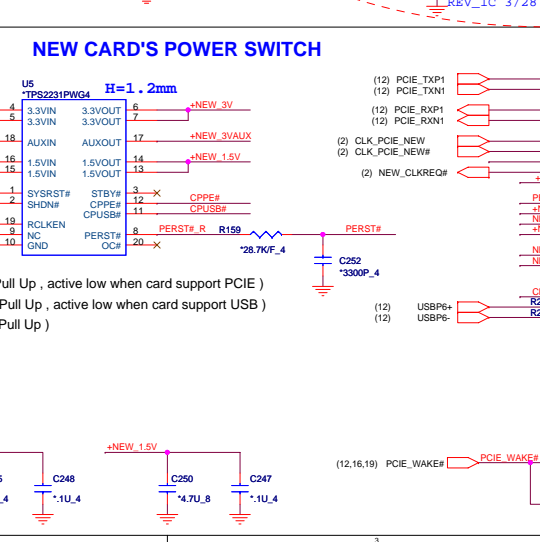
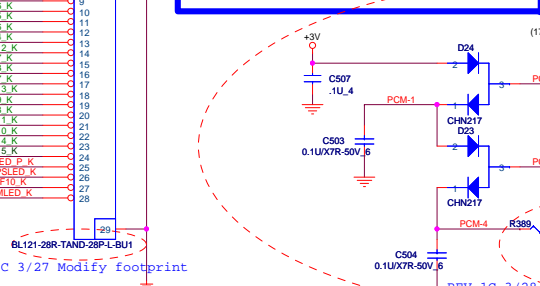
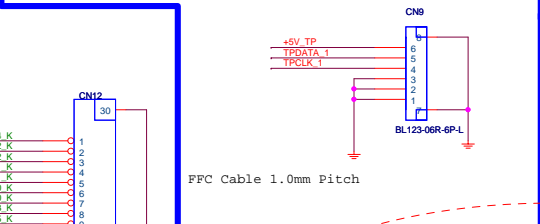
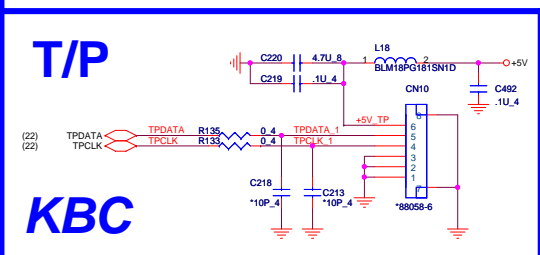
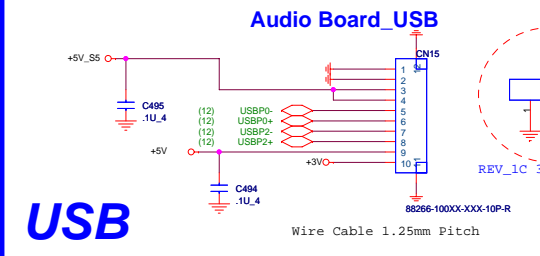
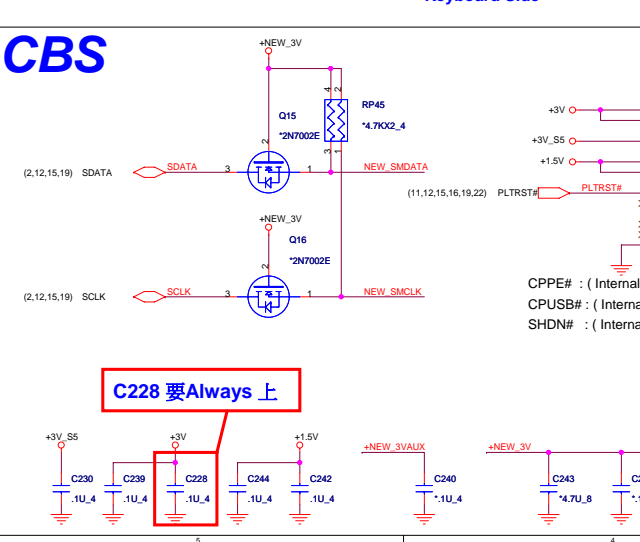
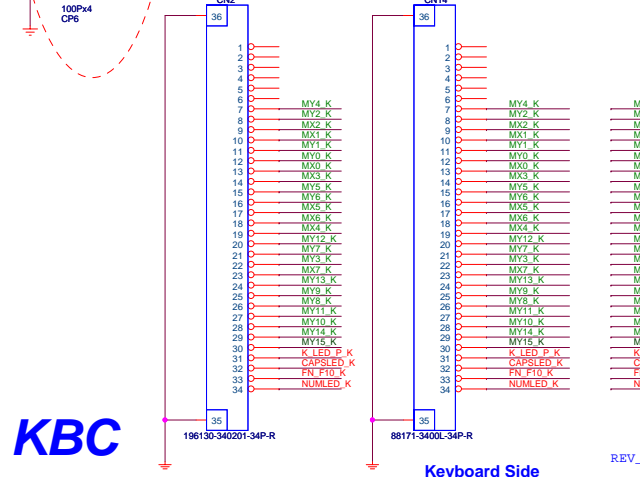
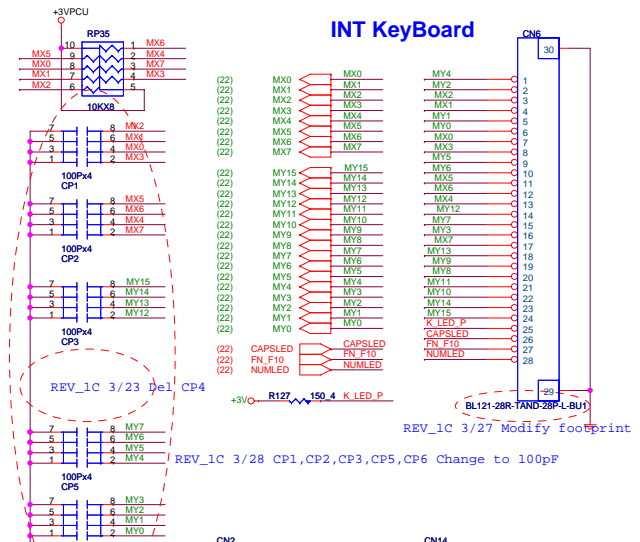
# RFM

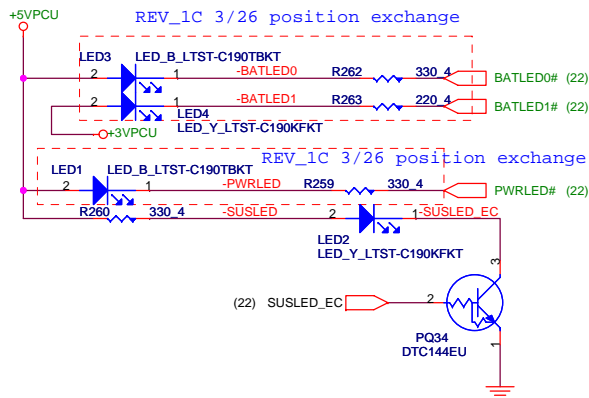


REV\_1C 3/23 Change to NOSTUFF Reserve for GPS



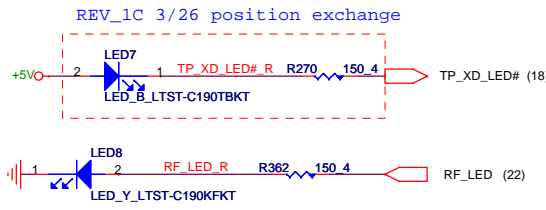
# OTH





**BATTERY**  
Full Charge --> Blue  
Charging --> Orange

**POWER**  
Power On --> Blue  
S3 --> Orange

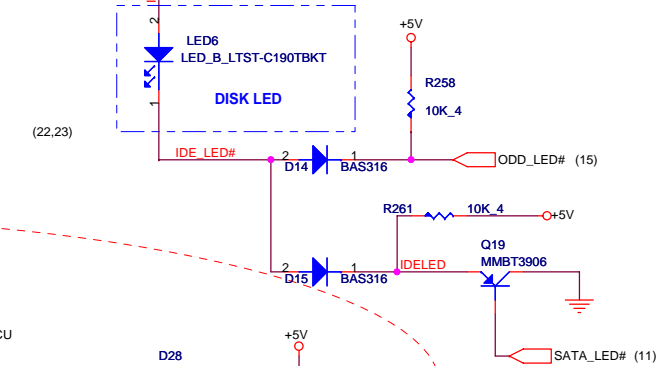
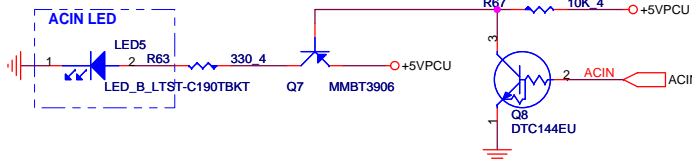


**CARDREADER**  
Blue

**W-LAN&BT**  
Amber

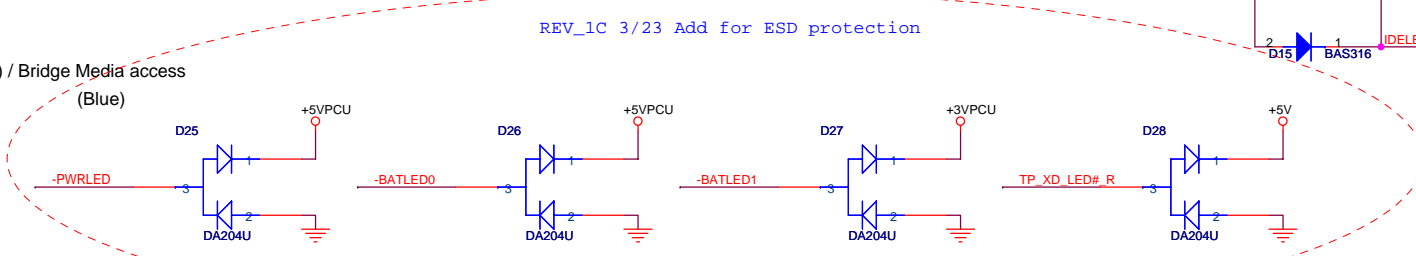
**ODD / HDD**

Blue

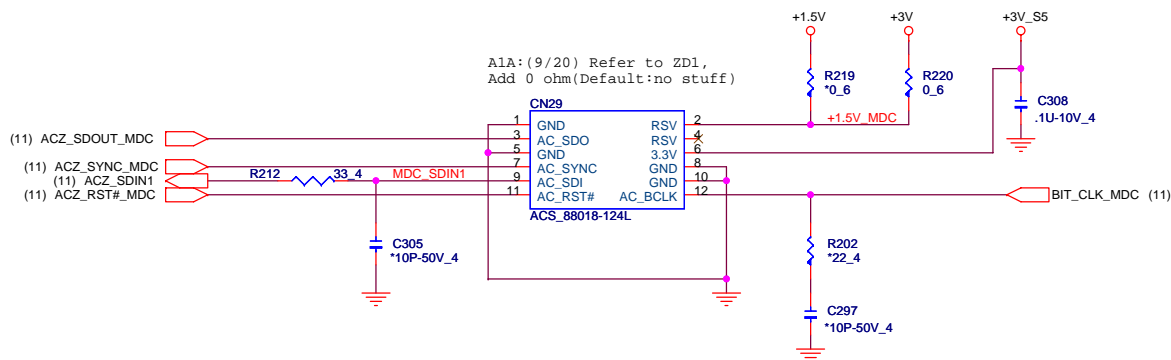


W\_LAN&BT / 3G (Amber) (Blue)  
DC-IN / Power / Battery / HDD(ODD) / Bridge Media access (Blue) (Blue) (Blue) (Blue) (Amber) (Amber)

**UIF**

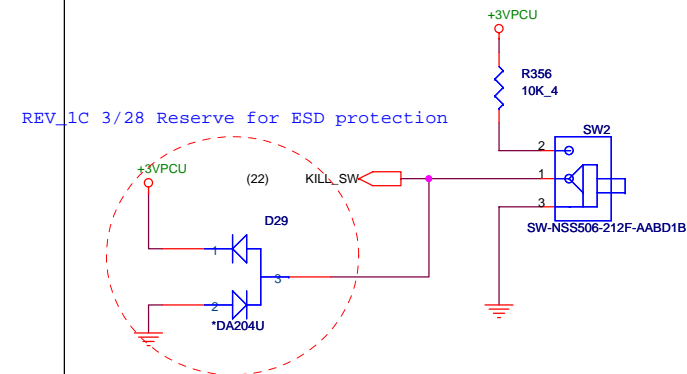


**MDC (MODULE)**




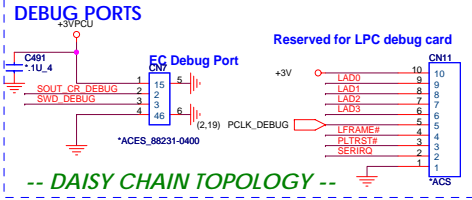
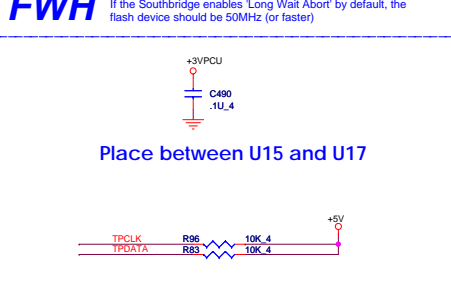
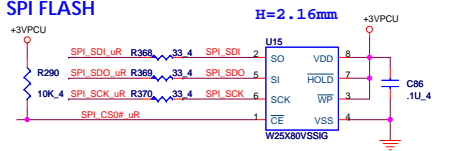
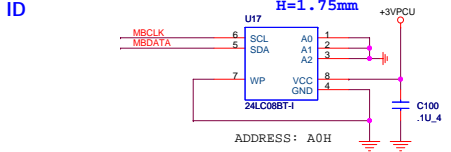
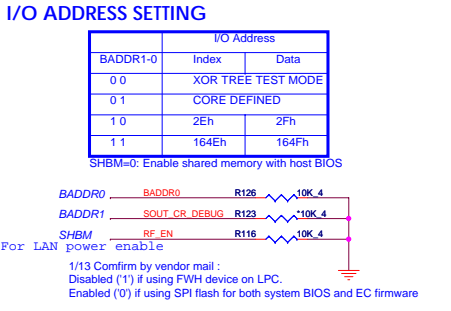
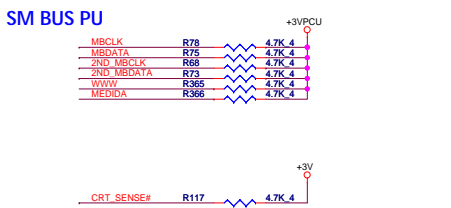
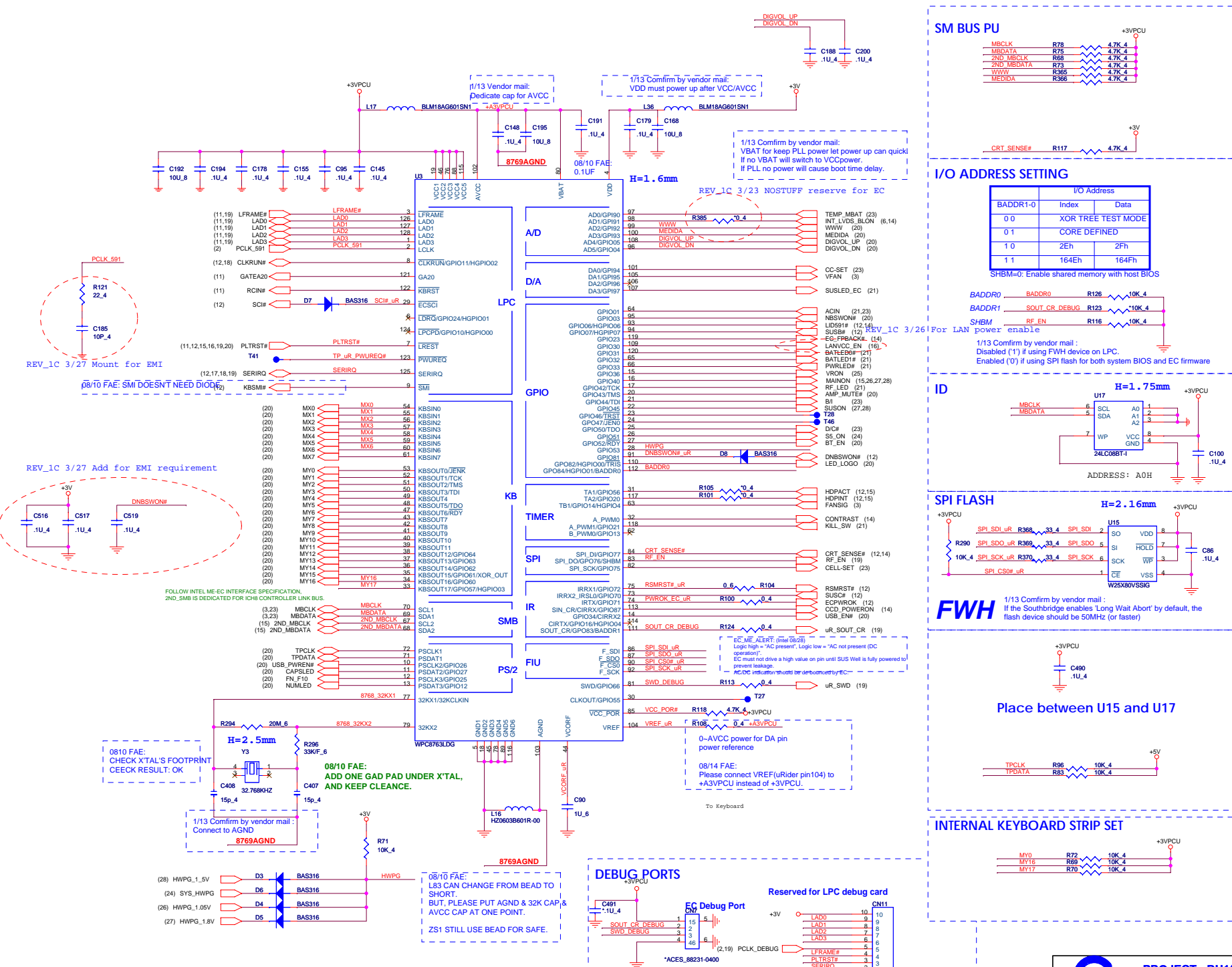
**MDM**

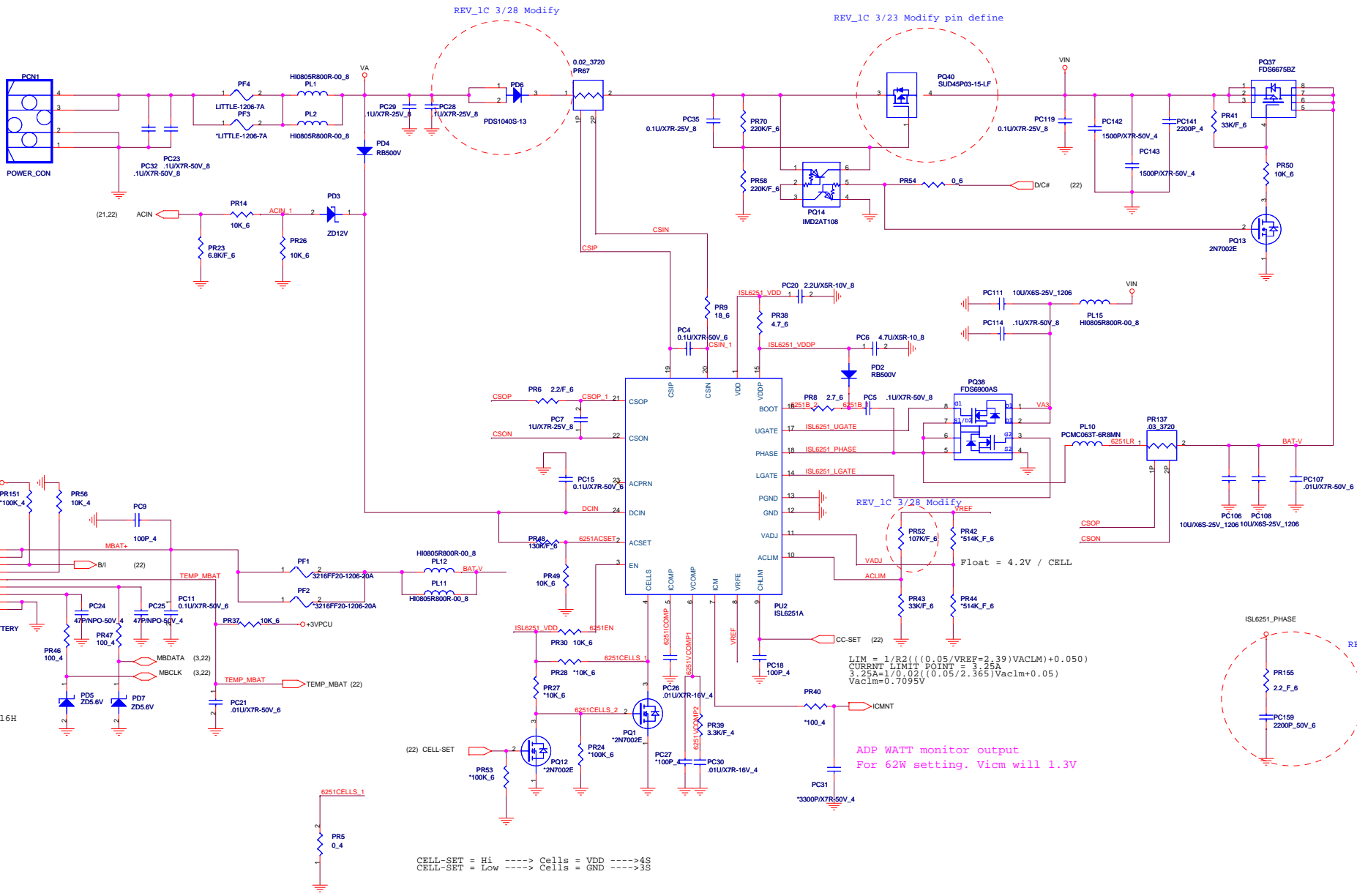
**W-LAN SWITCH**



**UIF**

		PROJECT : BU1(NAPA)	
		Quanta Computer Inc.	
Size	Document Number	Rev 1C	
	SW/LED/MDC		
Date:	Friday, March 30, 2007	Sheet	21 of 28





$$LIM = 1/R2(((0.05/VREF=2.39)VACLM)+0.050)$$

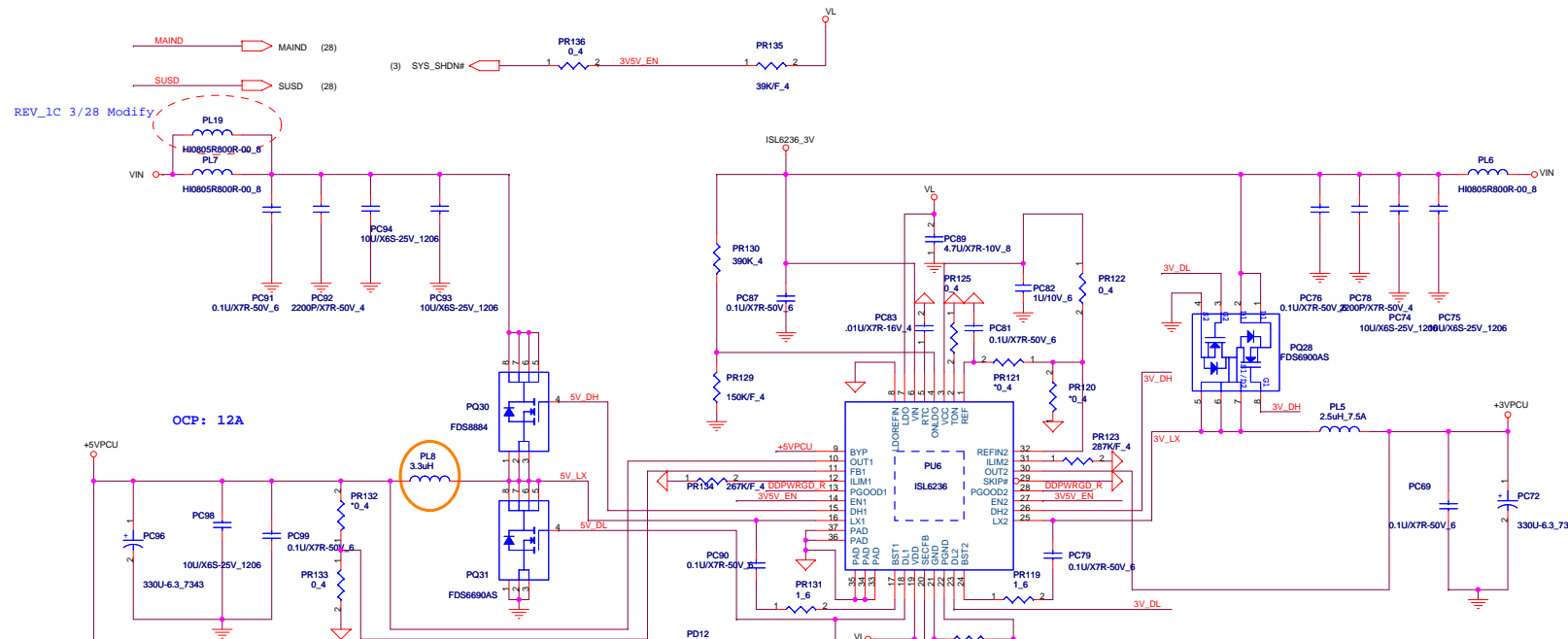
$$CURRENT\_LIMIT\_POINT = 3.25A$$

$$3.25A = 1/0.021((0.05/2.365)Vac1m+0.05)$$

$$Vac1m = 0.7095V$$

ADP WATT monitor output  
For 62W setting, Vicm will 1.3V

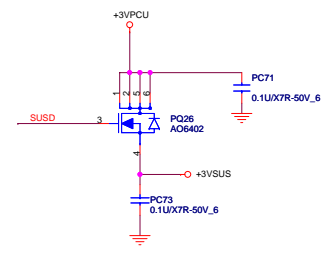
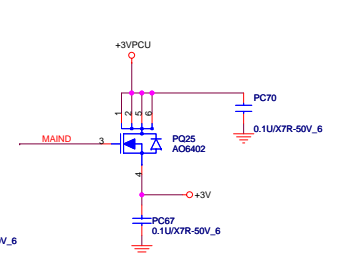
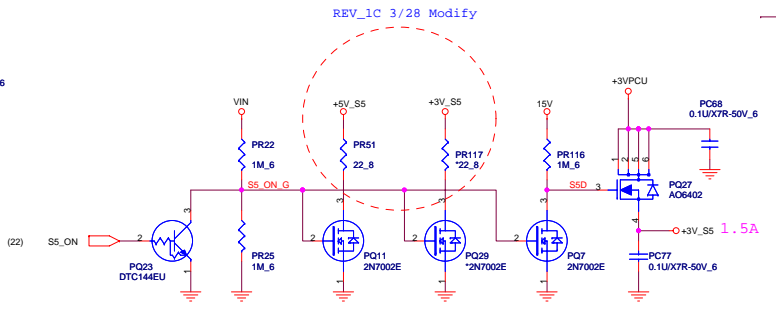
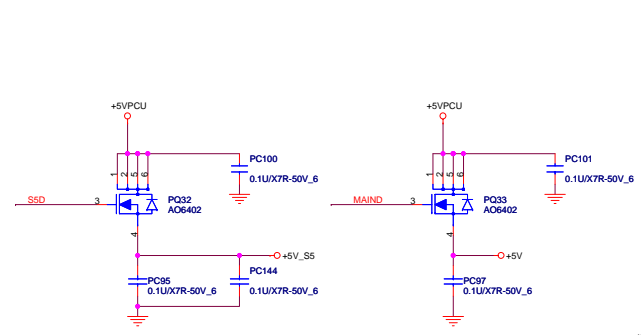
CELL-SET = Hi ----> Cells = VDD ----> 4S  
CELL-SET = Low ----> Cells = GND ----> 3S



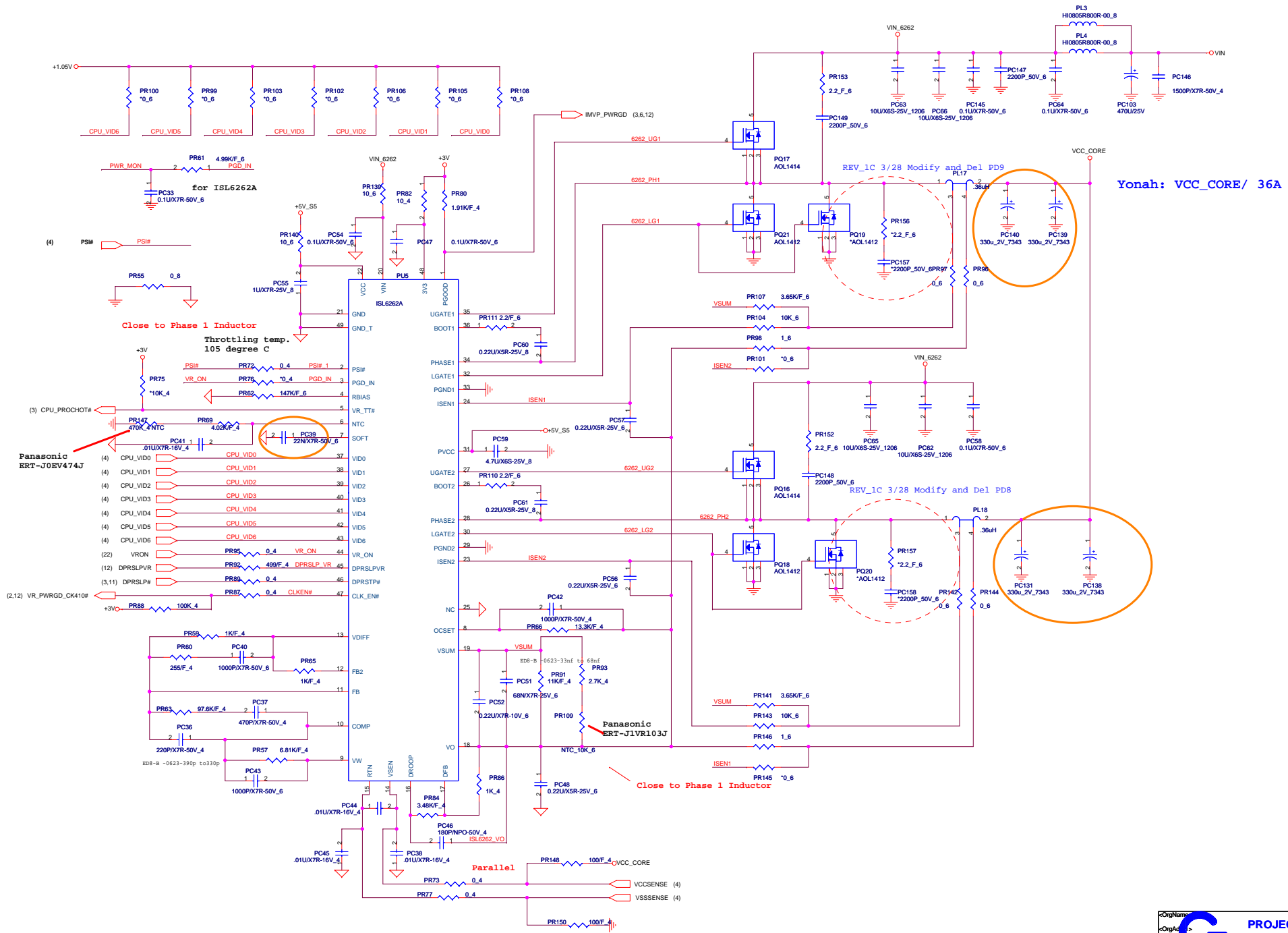
OCP : 6.25A

**OCP:12A**  
 $L(\text{ripple current}) = (19-5) * 5 / (1.5u * 0.4M * 19) \sim 6A$   
 $I_{ocp} = 12 - (6/2) = 9A$   
 $V_{th} = 9A * 15m\Omega = 135mV$   
 $R(I_{lim}) = (135mV * 10) / 5uA \sim 270K$

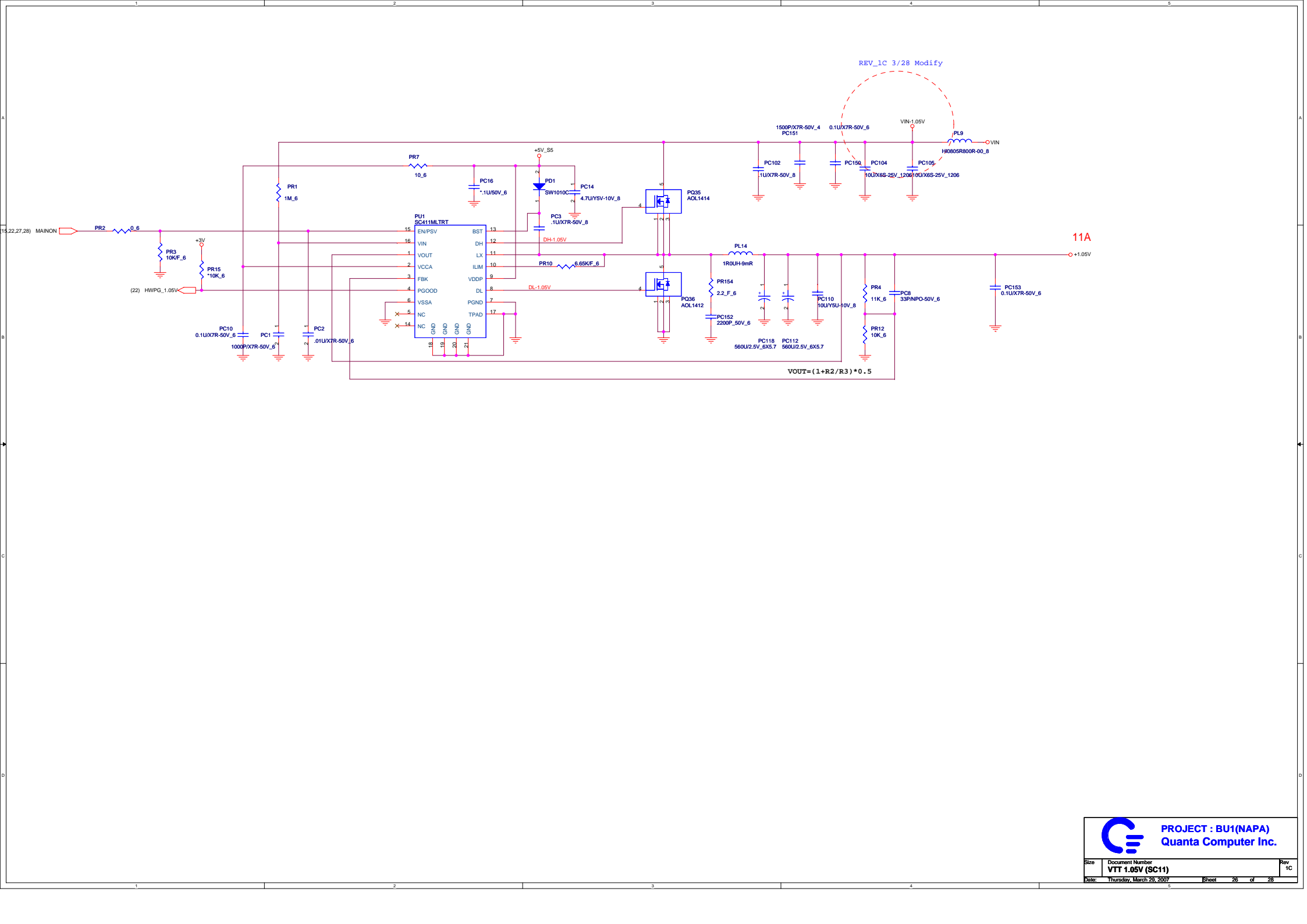
**OCP:6.25A**  
 $L(\text{ripple current}) = (19-3.3) * 3.3 / (2.5u * 0.5M * 19) \sim 2.18A$   
 $I_{ocp} = 6.25 - (2.18/2) = 5.16A$   
 $V_{th} = 5.16A * 28m\Omega = 145mV$   
 $R(I_{lim}) = (145mV * 10) / 5uA \sim 294K$

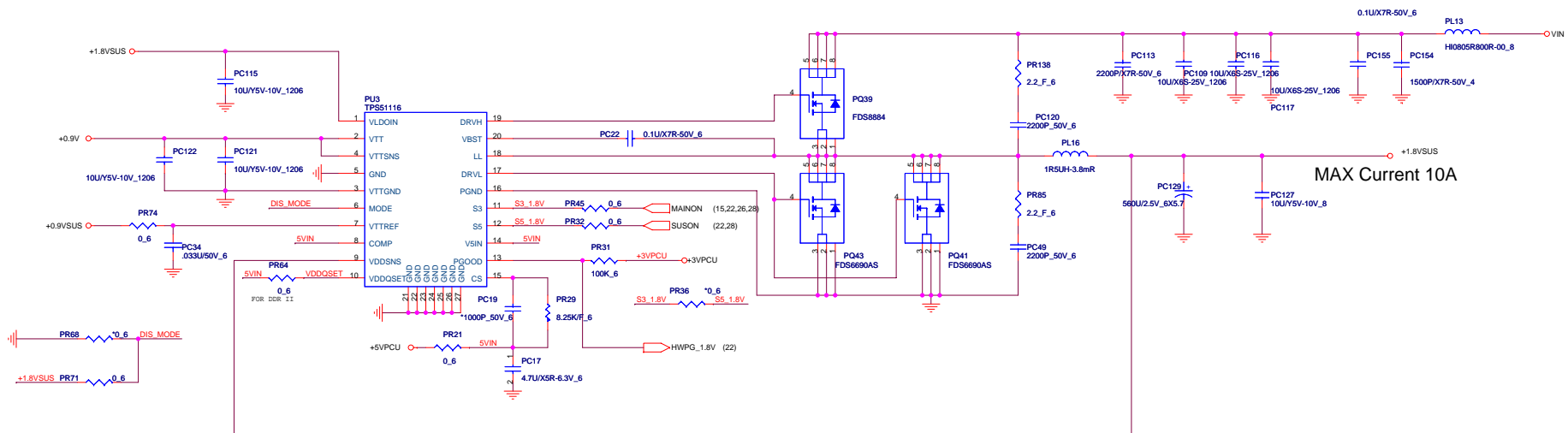




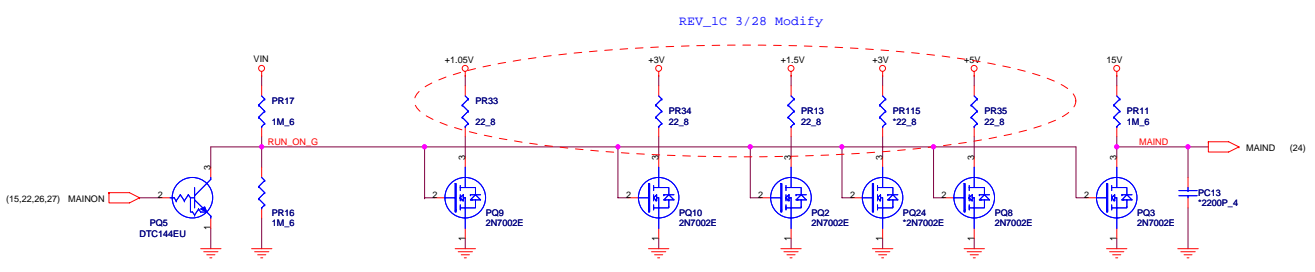
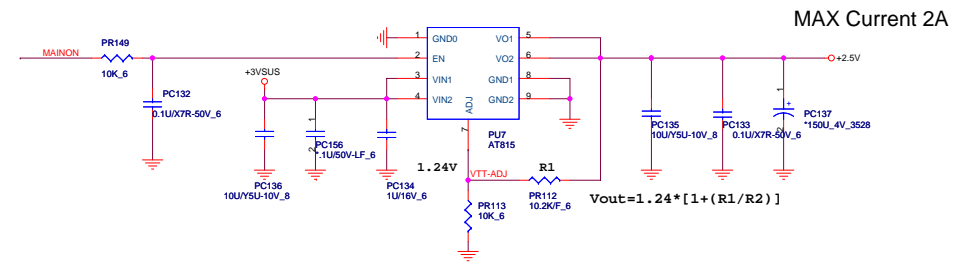
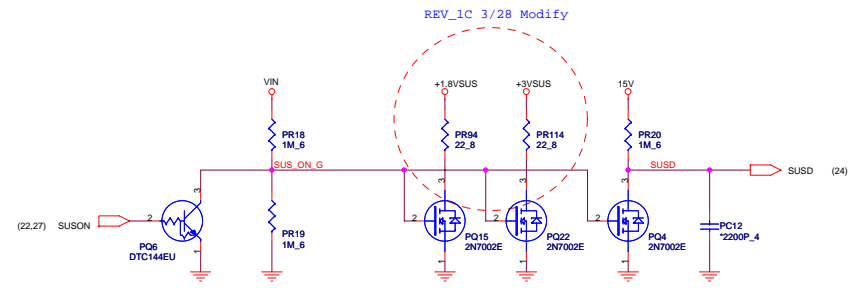
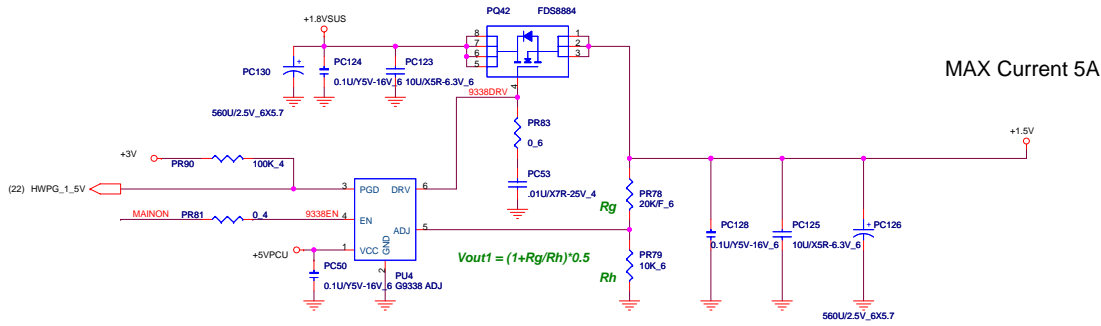


Yonah: VCC\_CORE/ 36A





MAX Current 10A




Model	REV	DATE	CHANGE LIST	NOTE	MODEL		
					Page	FROM	To
<b>BU1 (NAPA)</b>	<b>A</b>	20061218	<b>FIRST RELEASED: E20061218</b>				
			Page02: U1001 PCIE3 pair connect change to WLAN	Circuit modify	1	1A	
			Page02: U1001 PCIE4 pair connect change to 3G module	Circuit modify	2	1A	
			Page02: U1001 pin33 connect to 3G module CLKREQ# pin	Circuit modify	3	1A	
			Page03: Del R1038	Q1006 pin3 already has pull-up resistor	4	1A	
			Page03: Del R382,R383,Q60 and D39	Circuit modify	5	1A	
			Page03: U28 pin1 connect to MAX6648_OV# directly	Circuit modify	6	1A	
			Page06: U1003 pin"D32" connect to CN43 to control LCD backlight (Back-up solution)	Circuit modify	7	1A	
			Page12: Reserve R1222 connect to GND		8	1A	
			Page12: Del R1189	Already pull-up in the Page22	9	1A	
		2006/12/22	Page12: Add signal HDPACT connect to G-Sensor controller		10	1A	
			Page12: Add signal HDPINT connect to G-Sensor controller		11	1A	
			Page14: Add second (LED backlight) solution	Circuit modify	12	1A	
			Page15: Change G-Sensor solution	Circuit modify	13	1A	
			Page19: Modify screw holes to make the same with BU1		14	1A	
			Page20: Add R1223 and C1273	Reserve for EMI	15	1A	
			Page20: Add C810		16	1A	
			Page21: Change the connection of the ODD/HDD LED circuit	To prevent the voltage leakage	17	1A	
			Page22: Signal HWPG_CPUIO change name to HWPG_1.5V		18	1A	
			Page22: Del D64	We don't have HWPG_1.5V signal	19	1A	
			Page26: Mount PR109	HWPG_1.05V is open-drain signal	20	1A	
		2006/12/25	Page14: Change LCD connector pin define	Circuit modify	21	1A	
			Page15: Add C97 for ODD power and C98 for SATA power	Circuit modify	22	1A	
			Page18: Reserve D1006 for LPC_PD# function	Circuit modify	23	1A	
			Page19: Del R729, R730	WLAN don't support USB signals	24	1A	
			Page22: Add R765 and R766 for reserve the G-sensor	Circuit modify	25	1A	
			Page23: Del PL2,3,8,9 and add PF1~4	Power circuit modify	26	1A	
			Page24: Del power jump of +3VPCU and +5VPCU	Power circuit modify	27	1A	
	Page26: Add PR156	Power circuit modify	28	1A			
	Page28: Del power jump of +1.5V and +2.5V	Power circuit modify					
2006/12/28	Page14: Add C1284	Circuit modify					
	Page15: Del R1226	Circuit modify					
2007/01/03	<b>FIRST RELEASED GERBER FILE(RE-NAME)</b>						
2007/01/24							



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Model	REV	DATE	CHANGE LIST	NOTE	MODEL		
					Page	FROM	To
<b>BU1 (NAPA)</b>	<b>B</b>	20061218	<b>FIRST RELEASED: E20061218</b>				
		2007/01/24	Page02: Add R382 and connect to PCLK_DEBUG	Circuit modify	1	1A	1B
			Page03: Change Thermal Protect Schematic	Circuit modify	2	1A	1B
			Page09: Add C486	Circuit modify	3	1A	1B
			Page11: Add R367 and R194 change to 8.66K	Circuit modify	4	1A	1B
			Page14: Change LED type panel schematic	Circuit modify	5	1A	1B
			Page20: CN18 change to 16 pin and add a LED_LOGO signal	Circuit modify	6	1A	1B
			Page22: Add R365, R366	Circuit modify	7	1A	1B
			Page22: Add R368~R370	Circuit modify	8	1A	1B
		2007/01/29	Page14: Add F2	Customer require	9	1A	1B
			Page14: Add C487,C488,C489,C498,L34 and L35	EMI suggestion	10	1A	1B
			Page19: Add R363,R364,R383,R384	Circuit modify	11	1A	1B
			Page20: Add CP3~CP6	EMI suggestion	12	1A	1B
			Page20: Add CN2	Add second source	13	1A	1B
			Page20: Add C492~C497	EMI suggestion	14	1A	1B
			Page20: CN15 change to 10pin and connect one more +5V_S5 and GND	Circuit modify	15	1A	1B
			Page20: CN18 change to 16pin and connect one more LED_LOGO and USB_PWREN# signal to EC	Circuit modify	16	1A	1B
			Page21: R263 and R356 connect change to +3VPCU	Circuit modify	17	1A	1B
			Page22: Add R490,R491 and L36	EMI suggestion	18	1A	1B
			Page22: Add R385 and connect to INT_LVDS_BLON signal	Circuit modify	19	1A	1B
			Page23: Add PR151 and PR56 connect to B/I signal	Circuit modify	20	1A	1B
			Page23: PQ40 change type	Circuit modify	21	1A	1B
			Page23: Add PC141~PC143	Circuit modify	22	1A	1B
			Page24: Add PC144	EMI suggestion	23	1A	1B
			Page25: Add PC145~PC149 and PR152,PR153	EMI suggestion	24	1A	1B
			Page26: Add PC150~PC153 and PR154	EMI suggestion	25	1A	1B
			Page27: Add PC154,PC155	EMI suggestion	26	1A	1B
		Page28: Add PC156	EMI suggestion	27	1A	1B	
2007/01/30	Page09: Add C500	Circuit modify	28	1A	1B		
	Page09: Add U28 for G-Sensor controller reset	Circuit modify	29	1A	1B		
2007/01/31	Page19: Del SIM card function	Circuit modify					
	Page20: Del FM tuner interface	Circuit modify					
	Page21: Del 3G LED	Circuit modify					
2007/02/01	Page14: Add R12,R16,C15,C316	Circuit modify					
	Page15: Add R361,R386,R387,R388 Page19: Add HOLE22	Circuit modify Circuit modify					

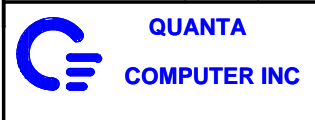


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<b>BU1 (NAPA)</b>	<b>C</b>	20061218	<b>FIRST RELEASED: E20061218</b>		Page	FROM	To	
		2007/03/20	Page14: D1 and F2 exchange position Page20: Add U29, D23, D24, C501-C507, R389, R390	Circuit modify To solve insert PCMCIA Card speaker has no sound	1	1B	1C	
		2007/03/23	Page03: Add C513	Circuit modify	2	1B	1C	
			Page11: Add C508-C512	Reserve for EMI	3	1B	1C	
			Page18: Del CN33, CN34	Can't placement in the latest ME drawing	4	1B	1C	
			Page19: Add R30, R32	Reserve for Realtek WLAN card	5	1B	1C	
			Page19: Mount Q6	Circuit modify	6	1B	1C	
			Page20: Del CP4	Circuit modify	7	1B	1C	
			Page21: Add D25-D28	For ESD protection	8	1B	1C	
			Page22: R385 change to NOSTUFF	EC don't need this function	9	1B	1C	
		2007/03/25	Page16: Add Q2, C514, C515	Circuit modify	10	1B	1C	
			Page23: PQ40 change footprint	Circuit modify	11	1B	1C	
		2007/03/26	Page14: C4,C5,C6,C8,C9,C10 modify value to 10pF	Circuit modify	12	1B	1C	
			Page14: L2,L3,L4 modify value	Circuit modify	13	1B	1C	
			Page21: LED1 & R259, LED3 & R262, LED4 & R263, LED7 & R270 position exchange	Circuit modify	14	1B	1C	
		2007/03/27	Page16: Add C518 0.1uF	For EMI requirement	15	1B	1C	
			Page19: Add C528 0.1uF	For EMI requirement	16	1B	1C	
			Page20: CN6,CN12,CN18,CN22 modify footprint	Circuit modify	17	1B	1C	
			Page20: Add C520-C527 100pF and C529 0.1uF	For EMI requirement	18	1B	1C	
			Page20: Del SW1	Don't need this function	19	1B	1C	
		2007/03/28	Page22: Add C516,C517,C519 0.1uF	For EMI requirement	20	1B	1C	
			Page18: Del U26 and C352	Circuit modify	21	1B	1C	
			Page19: HOLE12 modify footprint	Circuit modify	22	1B	1C	
			Page19: HOLE12 modify footprint	Circuit modify	23	1B	1C	
			Page20: Add PAD1	For EMI requirement	24	1B	1C	
						25	1B	1C
						26	1B	1C
						27	1B	1C
						28	1B	1C
				29	1B	1C		



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