

9. Schematic Diagram

9-1 MAIN BOARD

9-1-1 Schematic Diagrams

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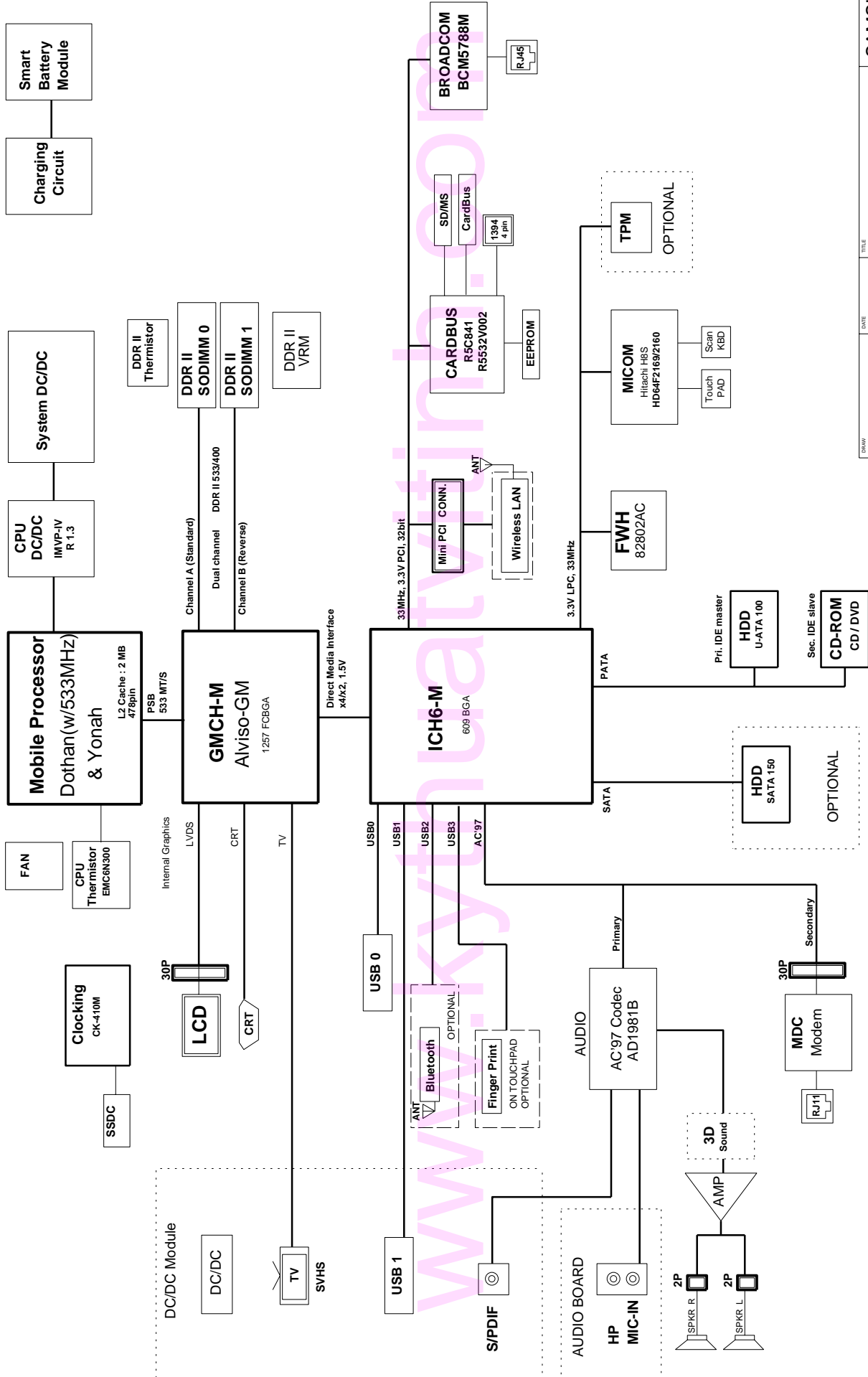
Aquila-So

CPU : Dothan
 Chip Set : Intel Alviso + ICH6-M
 Remarks : Internal Gfx.

Model Name : X06
 PBA Name : BA92-03859A
 PCB Code : BA41-00529A
 Dev. Step : MP
 Revision : 1.0
 T.R. Date : 2005-6-25

DRAW	CHECK	APPROVAL
ZHOU JUN GUO LEI	CHEN TAO	KEVIN LEE

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DATE	6/25/2005	TITLE	AQUILA-SONOMA MAIN
DRAWN	ZHOU & GUO	OPERATION BLOCK DIAGM	
CHECK	ANTONIO	MP	
APPROVAL	KEVIN LEE	REV	1.0
MODULE CODE		LAST EDIT	June 25, 2005 12:21:39 PM
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			SAMSUNG ELECTRONICS
			BA41-00529A

SCHEMATIC ANNOTATIONS AND BOARD INFORMATION

External PCI Devices

Devices	IDSEL#	REQ/GNT#	Interrupts
Cardbus	AD25	0	A, B, C
MiniPCI SLOT	AD23	1	E, F
LOM	AD21	2	G

i²C / SMBus Address

Devices	Address	Hex	Bus
ICH6M	Master	-	SMBUS Master
CK-410 (Clock Generator)	1101 001x	D2h	Clock, Unused, Clock Output Disable
CY25823ZC (Spread Spectrum)	1101 010x	D4h	-
SODIMM0	1010 0000	A0h	-
SODIMM1	1010 010X	A4h	-
MICOM	Master	-	SMBUS Master
EMC6N300 (Thermal Sensor)	0101 111x	5Eh	Thermal Sensor
BATTERY	0001 011X	16h	BATTERY INFORMATION

USB PORT Assign

PORT NUMBER	ASSIGNED TO
0	SYSTEM PORT A
1	SYSTEM PORT B
2 (OPTION)	BIOMETRIC FINGER PRINTER (OPTION)
3 (OPTION)	FINGER PRINTER (OPTION)

Voltage Rails

VDC	Primary DC system power supply (7 to 21V)
VCC_CORE	Core voltage for DOTHAN CPU (1.356 - 0.844V)
VTT	Processor System Bus (PSB) Termination (1.05V) / MCH-M Core Voltage (1.05V)
P1.5V_AUX	1.5V switched power rail (off in S3-S5)
P0.9V	0.9V power rail (off in S3-S5)
P1.8V_AUX	1.8V power rail (off in S3-S5)
MICOM_P3V	3.3V always on power rail for MICOM
P3.3V	3.3V switched power rail (off in S3-S5)
P3.3V_AUX	3.3V power rail (off in S4-S5)
P3.3V_ALWAYS	3.3V power rail (Always on @ AC-IN mode & off in S4-S5 @ BATT mode)
P5V	5.0V switched power rail (off in S3-S5)
P5V_AUX	5V power rail (off in S4-S5)
P2.5V	2.5V switched power rail (off in S3-S5)

REVISION HISTORY

See revision notes in the changes file for more information.

CPU Core Voltage Table

VID5	VID4	VID3	VID2	VID1	VID0	Voltage	VID Hex	VID5	VID4	VID3	VID2	VID1	VID0	Voltage	VID Hex
0	0	0	0	0	0	1.708 V	3Fh	1	0	0	0	0	0	1.086 V	1Fh
0	0	0	0	0	0	1.692 V	3Eh	1	0	0	0	0	0	1.188 V	1Eh
0	0	0	0	0	0	1.676 V	3Dh	1	0	0	0	0	0	1.164 V	1Dh
0	0	0	0	0	0	1.660 V	3Ch	1	0	0	0	0	0	1.148 V	1Ch
0	0	0	0	0	0	1.644 V	3Bh	1	0	0	0	0	0	1.132 V	1Bh
0	0	0	0	0	0	1.628 V	3Ah	1	0	0	0	0	0	1.116 V	1Ah
0	0	0	0	0	0	1.612 V	39h	1	0	0	0	0	0	1.100 V	19h
0	0	0	0	0	0	1.596 V	38h	1	0	0	0	0	0	1.084 V	18h
0	0	0	0	0	0	1.580 V	37h	1	0	0	0	0	0	1.068 V	17h
0	0	0	0	0	0	1.564 V	36h	1	0	0	0	0	0	1.052 V	16h
0	0	0	0	0	0	1.548 V	35h	1	0	0	0	0	0	1.036 V	15h
0	0	0	0	0	0	1.532 V	34h	1	0	0	0	0	0	1.020 V	14h
0	0	0	0	0	0	1.516 V	33h	1	0	0	0	0	0	1.004 V	13h
0	0	0	0	0	0	1.500 V	32h	1	0	0	0	0	0	0.988 V	12h
0	0	0	0	0	0	1.484 V	31h	1	0	0	0	0	0	0.972 V	11h
0	0	0	0	0	0	1.468 V	30h	1	0	0	0	0	0	0.956 V	10h
0	0	0	0	0	0	1.452 V	2Fh	1	0	0	0	0	0	0.940 V	0Fh
0	0	0	0	0	0	1.436 V	2Eh	1	0	0	0	0	0	0.924 V	0Eh
0	0	0	0	0	0	1.420 V	2Dh	1	0	0	0	0	0	0.908 V	0Dh
0	0	0	0	0	0	1.404 V	2Ch	1	0	0	0	0	0	0.892 V	0Ch
0	0	0	0	0	0	1.388 V	2Bh	1	0	0	0	0	0	0.876 V	0Bh
0	0	0	0	0	0	1.372 V	2Ah	1	0	0	0	0	0	0.860 V	0Ah
0	0	0	0	0	0	1.356 V	29h	1	0	0	0	0	0	0.844 V	09h
0	0	0	0	0	0	1.340 V	28h	1	0	0	0	0	0	0.828 V	08h
0	0	0	0	0	0	1.324 V	27h	1	0	0	0	0	0	0.812 V	07h
0	0	0	0	0	0	1.308 V	26h	1	0	0	0	0	0	0.796 V	06h
0	0	0	0	0	0	1.292 V	25h	1	0	0	0	0	0	0.780 V	05h
0	0	0	0	0	0	1.276 V	24h	1	0	0	0	0	0	0.764 V	04h
0	0	0	0	0	0	1.260 V	23h	1	0	0	0	0	0	0.748 V	03h
0	0	0	0	0	0	1.244 V	22h	1	0	0	0	0	0	0.732 V	02h
0	0	0	0	0	0	1.228 V	21h	1	0	0	0	0	0	0.716 V	01h
0	0	0	0	0	0	1.212 V	20h	1	0	0	0	0	0	0.700 V	00h

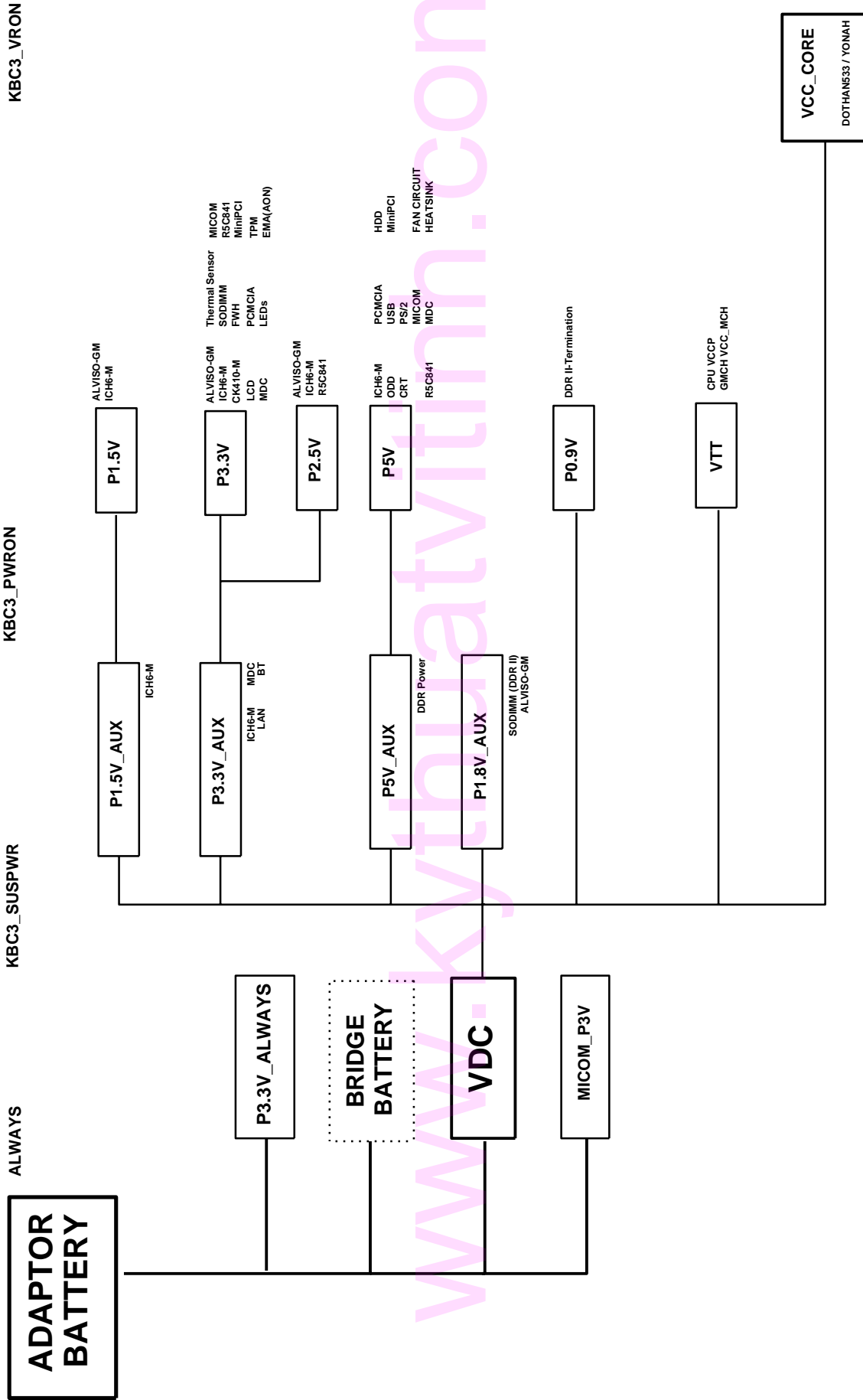
HIGHEST FREQ. LOWEST FREQ. DEEPER SLEEP

System Power States

Signal	SLP_S3#	SLP_S4#	SLP_S5#	+V _{ALW}	+V _S	Clocks
S0 (Full On)	HIGH	HIGH	HIGH	ON	ON	ON
S3-Hot (STR)	LOW	HIGH	HIGH	ON	ON	LOW
S3-Cold (STR)	LOW	HIGH	HIGH	ON	OFF	OFF
S4 (STD)	LOW	LOW	HIGH	ON	OFF	OFF
S5 (Soft Off)	LOW	LOW	LOW	ON	OFF	OFF

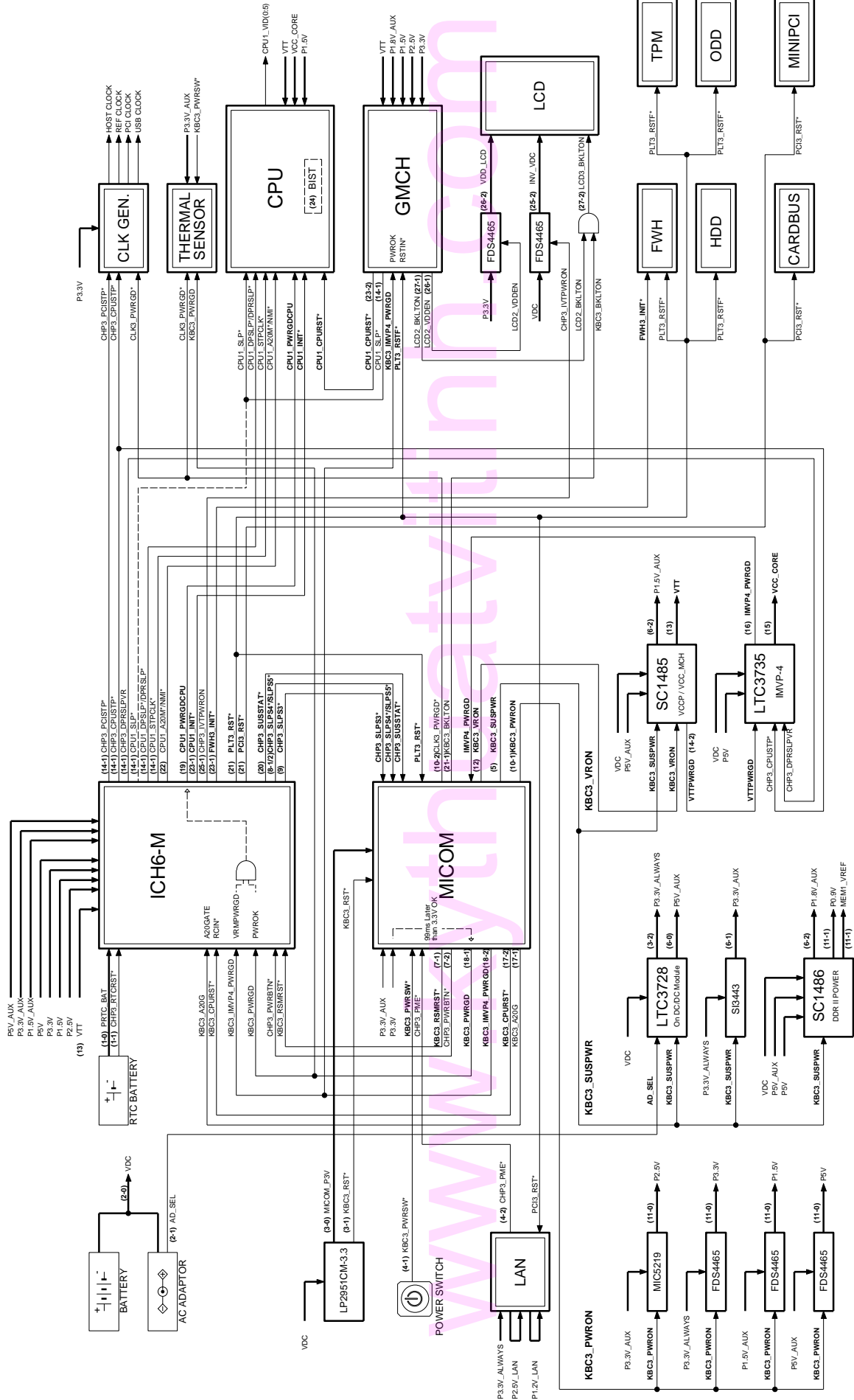
DRAWN	DATE	TITLE	SAMSUNG ELECTRONICS
CHECK	6/25/2005	AQUILA-SONOMA	
APPROVAL	DRW. STEP	MP	BOARD INFORMATION
MODULE CODE	REV	1.0	
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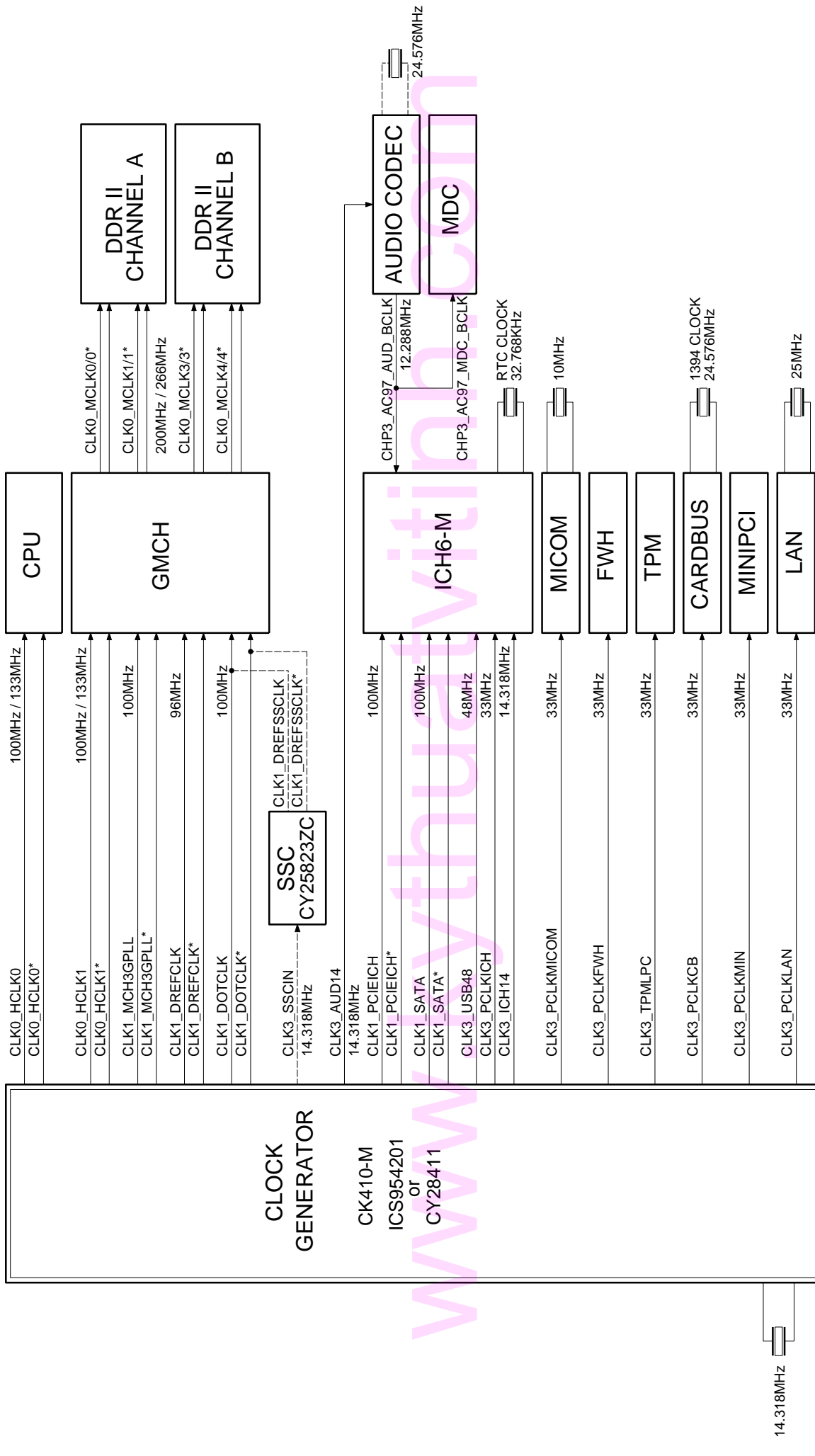
DATE	6/25/2005	TITLE	AQUILA-SONOMA
DRAWN	ZHOU & GUO	REV. STEP	MP
CHECK	ANTONIO	REV.	1.0
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		POWER DIAGRAM	

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DATE	6/25/2005	TITLE	AQUILA-SONOMA
CHK	ANTONIO	MP	MAIN
REV	1.0	POWER ON SEQUENCE	
APPROVAL	KEVIN LEE	PART NO.	BA41-00529A
MODULE CODE		PAGE	5 OF 46
		June 25, 2005 12:21:39 PM	

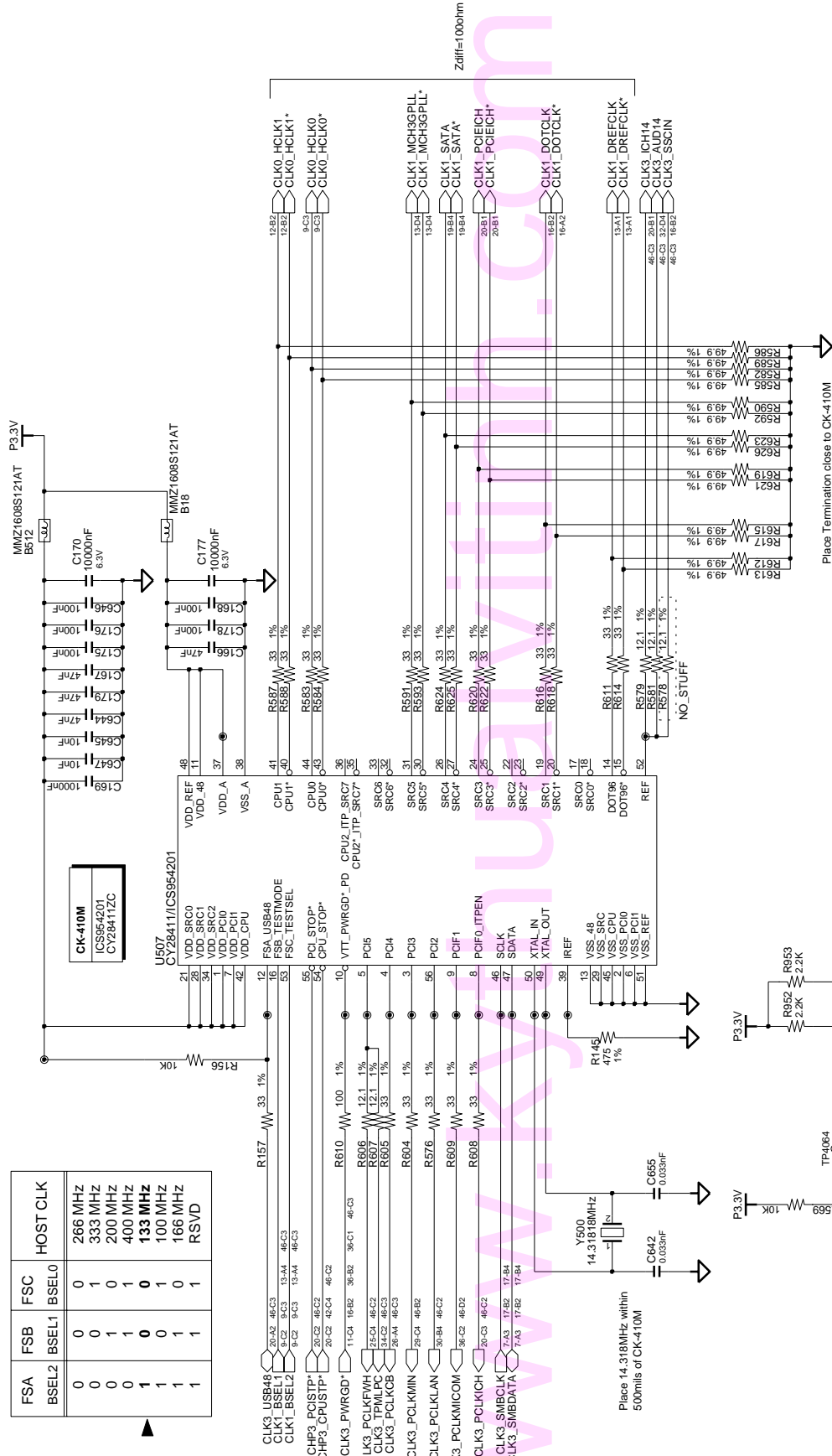
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DATE	6/25/2005	TITLE	AQUILA-SONOMA
DRAWN	ZHOU & GUO	CHKD	ANTONIO
CHECK	ANTONIO	MP	KEVIN LEE
APPROVAL	KEVIN LEE	REV	1.0
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			CLOCK DIAGRAM
			BA41-00529A
			SAMSUNG ELECTRONICS

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FSA	FSB	FSC	HOST CLK
BSEL2	BSEL1	BSEL0	
0	0	0	266 MHz
0	0	1	333 MHz
0	1	0	200 MHz
0	1	1	400 MHz
1	0	0	133 MHz
1	0	1	100 MHz
1	1	0	166 MHz
1	1	1	RSVD



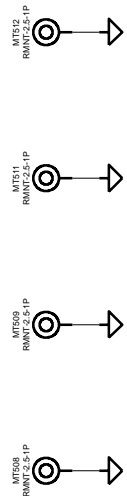
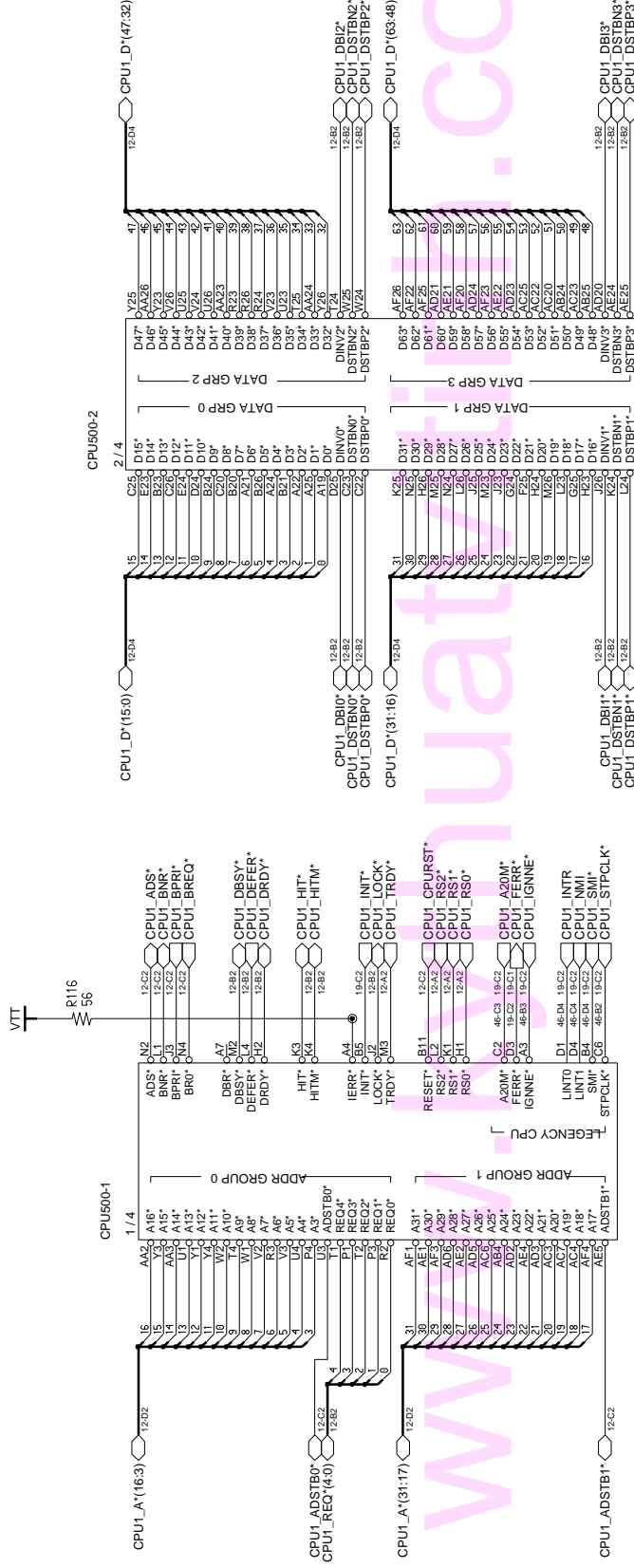
Place Termination close to CK-410M

Place 14.318MHz within
500mils of CK-410M

DATE	6/25/2005	TITLE	AQUILA-SONOMA
DESIGNER	ZHOU & GUO	REV	MP
CHECKER	ANTONIO	REV	MP
APPROVAL	KEVIN LEE	REV	1.0
MODULE CODE		LAST EDIT	June 25, 2005 12:21:39 PM
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DATE	6/25/2005	TITLE	AQUILA-SONOMA
DESIGNER	ZHOU & GUO	REV	MP
CHECKER	ANTONIO	REV	MP
APPROVAL	KEVIN LEE	REV	1.0
MODULE CODE		LAST EDIT	June 25, 2005 12:21:39 PM
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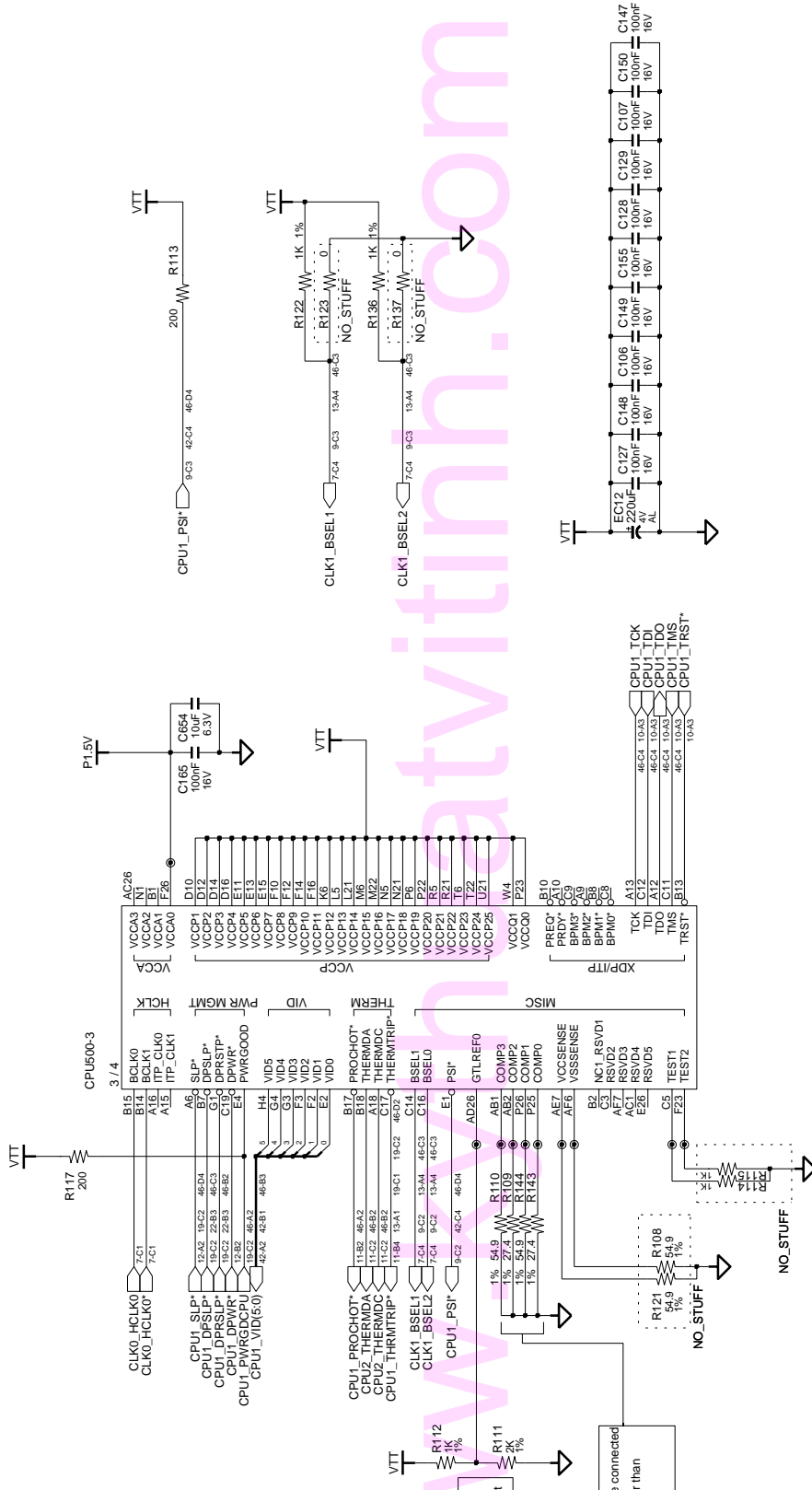


DATE	6/25/2005	TITLE	AQUILA-SONOMA
DRAWN	ZHOU & GUO	CPU1_GRP	MAIN
CHECK	ANTONIO	MP	CPU (1/13)
APPROVAL	KEVIN LEE	REV	1.0
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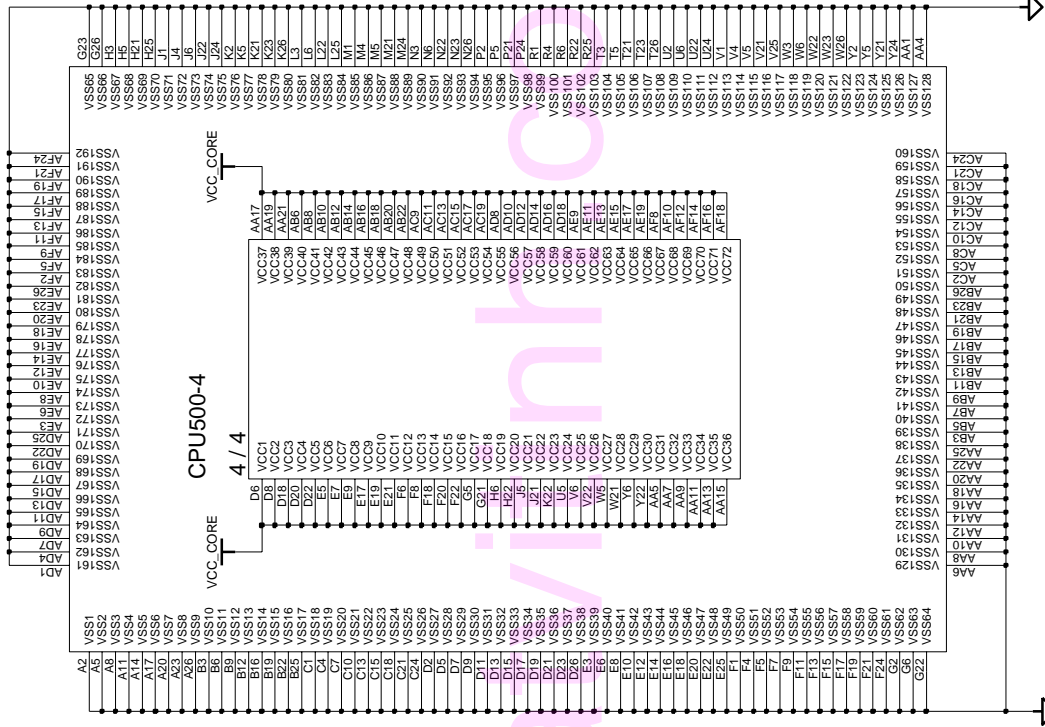
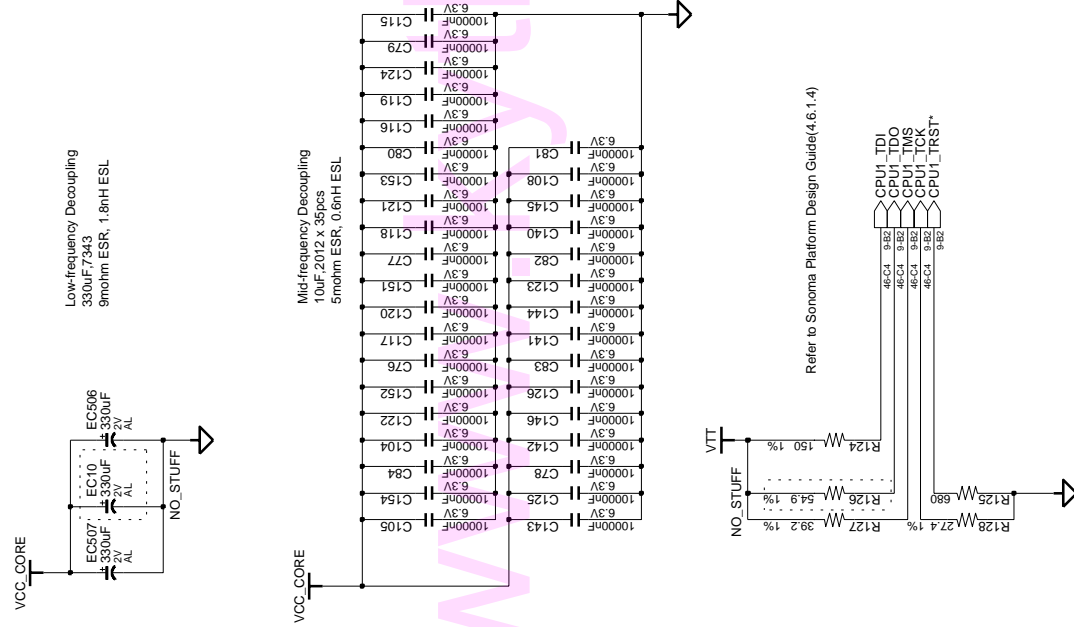
THERTRIP* should connect to ICH6M & Alviso
 OD signal driven by CPU and GMCH

GTLREF : Keep the Voltage divider within 0.5"
 of the First GTLREF0 with Z0= 55 ohm trace
 Minimize coupling of any switching signals to this net

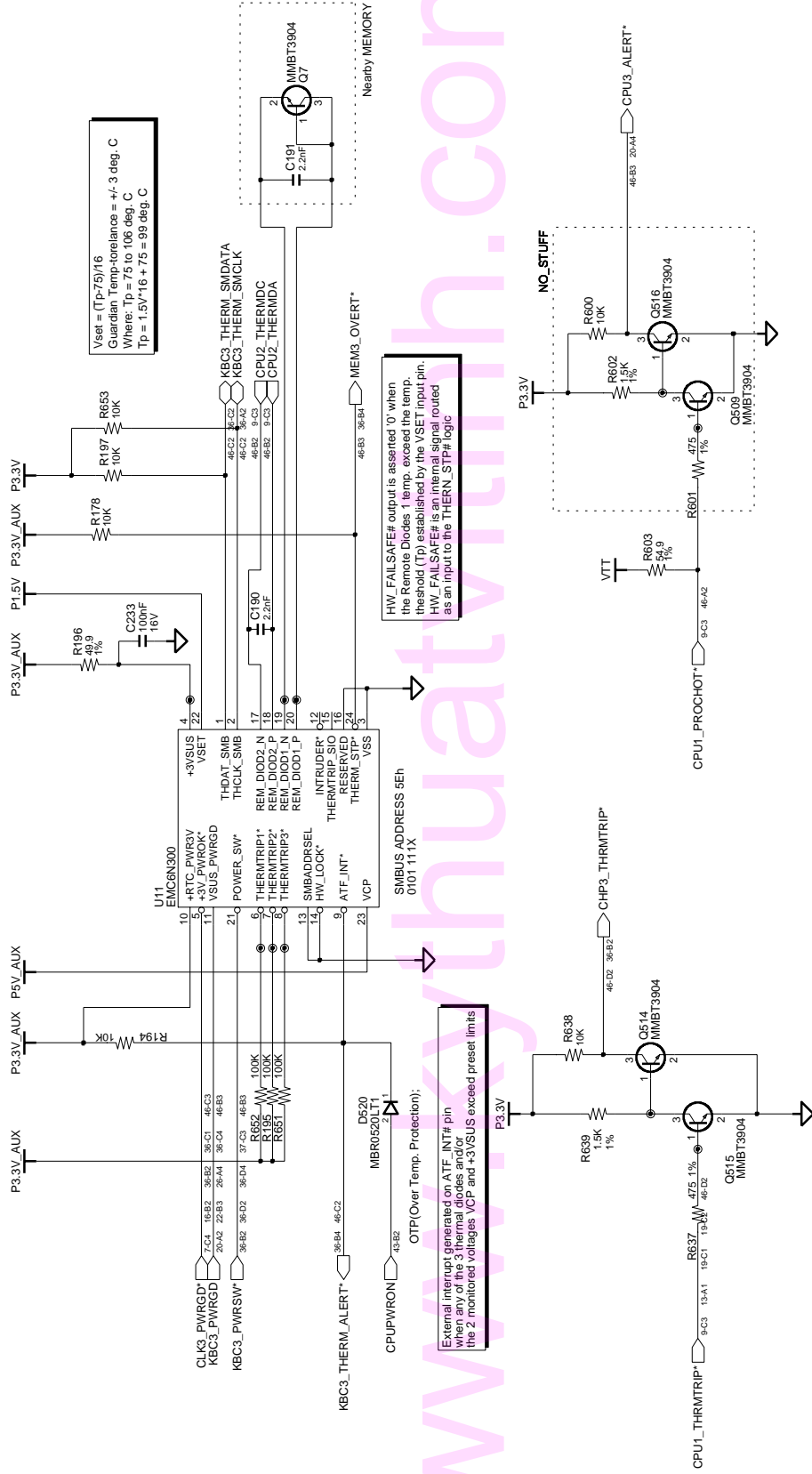
COMP 0 , 2 <(COMP 1,3) should be connected
 Z0=27.4 ohm (55 ohm) trace shorter than
 1/2 " to their respective Banias Pins

DATE	6/25/2005	TITLE	AQUILA-SONOMA
DRAWN	ZHOU & GUO	DEV. STEP	MP
CHECK	ANTONIO	REV	1.0
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Vcc-core Decoupling (Refer to IMVP-IV Option #4)



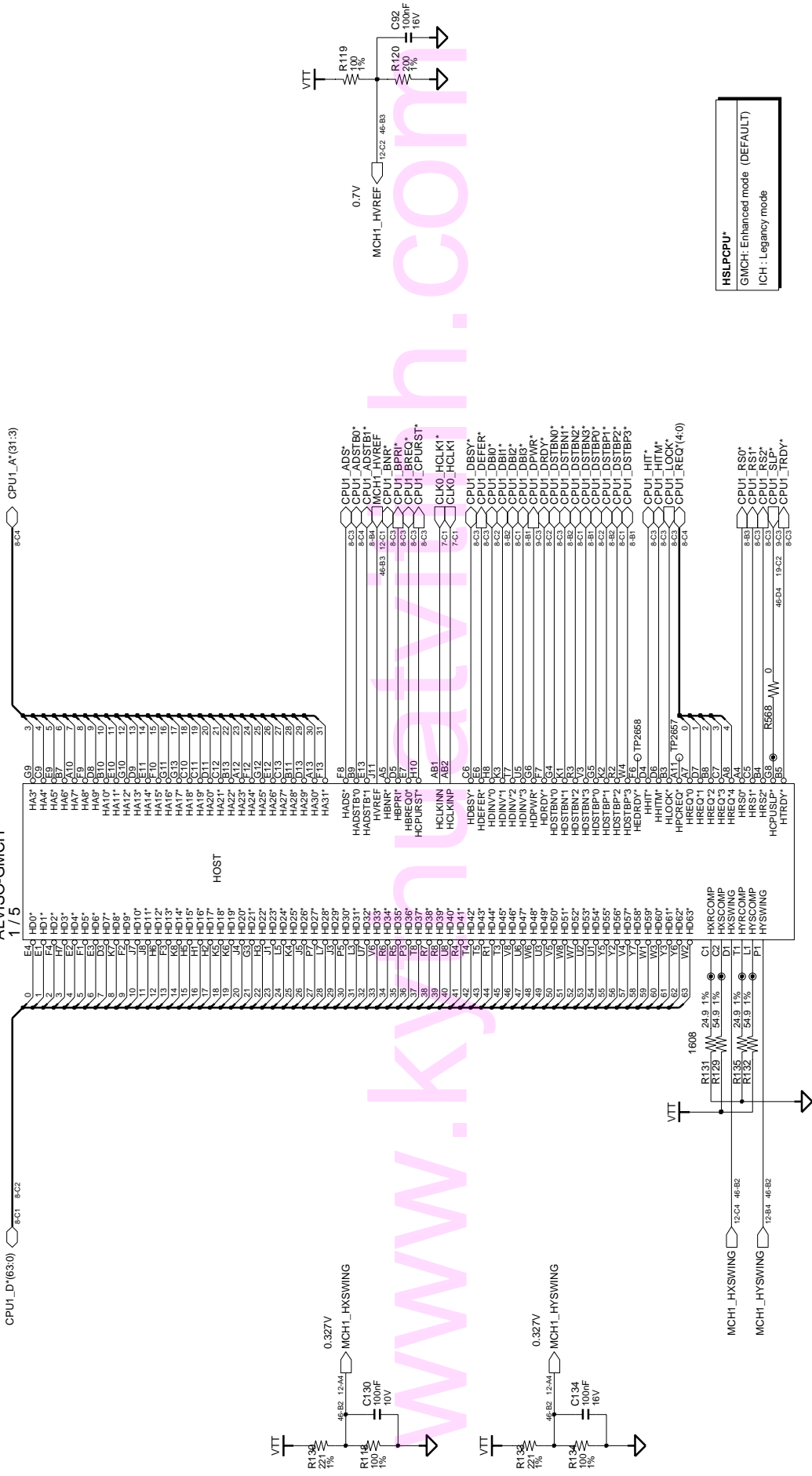
CPU / DDR Thermal Sensor



DATE	6/25/2005	TITLE	AQUILA-SONOMA
CHKD	ANTONIO	MP	MAIN
APPROVAL	KEVIN LEE	REV	1.0
MODULE CODE	undefined	LAST EDIT	June 25, 2005 12:21:39 PM
DRAWN	ZHOU & GUO	DATE	6/25/2005
CHECKED	ANTONIO	MP	MAIN
APPROVAL	KEVIN LEE	REV	1.0
MODULE CODE	undefined	LAST EDIT	June 25, 2005 12:21:39 PM
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AQUILA-SONOMA		CPU/DDR Thermal Sensor	
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ALVISO500-1
 ALVISO-GMCH



HSLPCPU*
 GMCH: Enhanced mode (DEFAULT)
 ICH: Legacy mode

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CHECK	ANTONIO	CHK/STEP	MP	MAIN	
APPROVAL	KEVIN LEE	REV	1.0	ALVISO (1/5)	
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Feature	Alviso	Iris(GrantScale)
VCC_GMCH	VCC 1.05V	VCC 1.5V
VCCP	VTT 1.05V	VTT 1.2V
DMI	x2 / x4	x4 only

PCI-EXPRESS GRAPHICS

SDVOCTRL_DATA
 SDVOCTRL_CLK
 GCLKN
 GCLKP

MISC
 R1P1
 R1P2
 R1P3
 R1P4
 R1P5
 R1P6
 R1P7
 R1P8
 R1P9
 R1P10
 R1P11
 R1P12
 R1P13
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 R1P17
 R1P18
 R1P19
 R1P20
 R1P21
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 R1P23
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 R1P70
 R1P71
 R1P72
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 R1P74
 R1P75
 R1P76
 R1P77
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 R1P81
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 R1P86
 R1P87
 R1P88
 R1P89
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 R1P95
 R1P96
 R1P97
 R1P98
 R1P99
 R1P100

TV
 TVDAC_A
 TVDAC_B
 TVDAC_C
 TV_REFSET
 TV_IRTNA
 TV_IRTNA
 TV_IRTNA
 TV_IRTNA

VGA
 DDCCLK
 DDCDATA
 BLUE
 BLUE
 GREEN
 GREEN
 RED
 RED
 VSYNC
 VSYNC
 HSYNC
 HSYNC
 REFSET

LVDS
 LBK1_CRTL
 LCTLIA_CLK
 LCTLIA_DATA
 LCTLIA_DATA
 LDDC_CLK
 LDDC_DATA
 LVDD_EN
 LVREFH
 LVREFL

LADATANO
 LADATANI
 LADATAP0
 LADATAP1
 LADATAP2

LEDATANO
 LEDATANI
 LEDATAP0
 LEDATAP1
 LEDATAP2

LCDD2_VDDEN
 LCDD2_BKLTORL

LCDD2_VDDEN
 LCDD2_BKLTORL

LCDD2_VDDEN
 LCDD2_BKLTORL

LCDD2_VDDEN
 LCDD2_BKLTORL

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 LCDD2_BKLTORL

LCDD2_VDDEN
 LCDD2_BKLTORL

LCDD2_VDDEN
 LCDD2_BKLTORL

LCDD2_VDDEN
 LCDD2_BKLTORL

LCDD2_VDDEN
 LCDD2_BKLTORL

LCDD2_VDDEN
 LCDD2_BKLTORL

LCDD2_VDDEN
 LCDD2_BKLTORL

LCDD2_VDDEN
 LCDD2_BKLTORL

LCDD2_VDDEN
 LCDD2_BKLTORL

LCDD2_VDDEN
 LCDD2_BKLTORL

LCDD2_VDDEN
 LCDD2_BKLTORL

LCDD2_VDDEN
 LCDD2_BKLTORL

LCDD2_VDDEN
 LCDD2_BKLTORL

LCDD2_VDDEN
 LCDD2_BKLTORL

LCDD2_VDDEN
 LCDD2_BKLTORL

LCDD2_VDDEN
 LCDD2_BKLTORL

LCDD2_VDDEN
 LCDD2_BKLTORL

LCDD2_VDDEN
 LCDD2_BKLTORL

LCDD2_VDDEN
 LCDD2_BKLTORL

LCDD2_VDDEN
 LCDD2_BKLTORL

LCDD2_VDDEN
 LCDD2_BKLTORL

LCDD2_VDDEN
 LCDD2_BKLTORL

LCDD2_VDDEN
 LCDD2_BKLTORL

LCDD2_VDDEN
 LCDD2_BKLTORL

LCDD2_VDDEN
 LCDD2_BKLTORL

LCDD2_VDDEN
 LCDD2_BKLTORL

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 LCDD2_BKLTORL

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LCDD2_VDDEN
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LCDD2_VDDEN
 LCDD2_BKLTORL

LCDD2_VDDEN
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LCDD2_VDDEN
 LCDD2_BKLTORL

LCDD2_VDDEN
 LCDD2_BKLTORL

LCDD2_VDDEN
 LCDD2_BKLTORL

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 LCDD2_BKLTORL

LCDD2_VDDEN
 LCDD2_BKLTORL

LCDD2_VDDEN
 LCDD2_BKLTORL

LCDD2_VDDEN
 LCDD2_BKLTORL

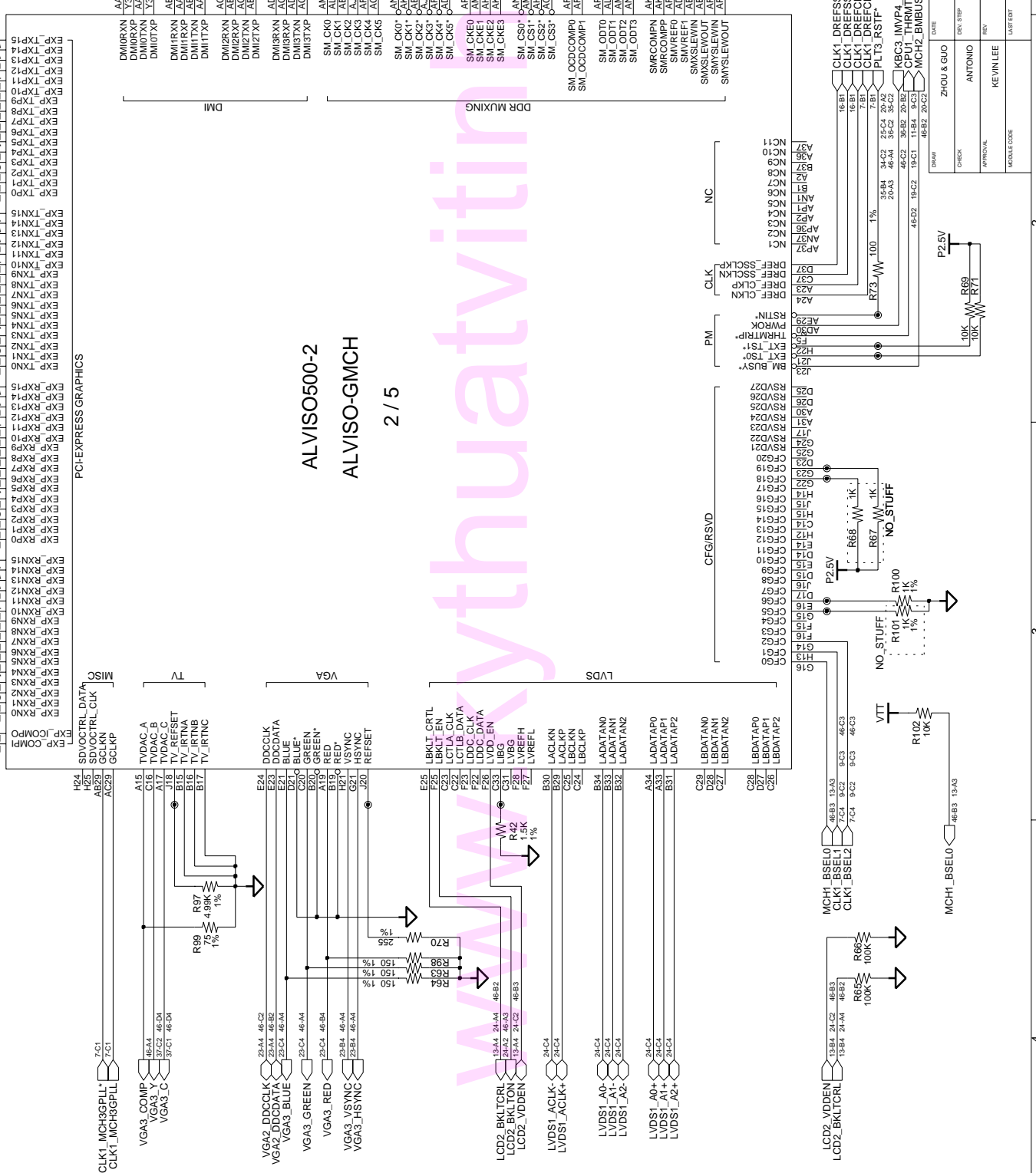
LCDD2_VDDEN
 LCDD2_BKLTORL

LCDD2_VDDEN
 LCDD2_BKLTORL

LCDD2_VDDEN
 LCDD2_BKLTORL

LCDD2_VDDEN
 LCDD2_BKLTORL

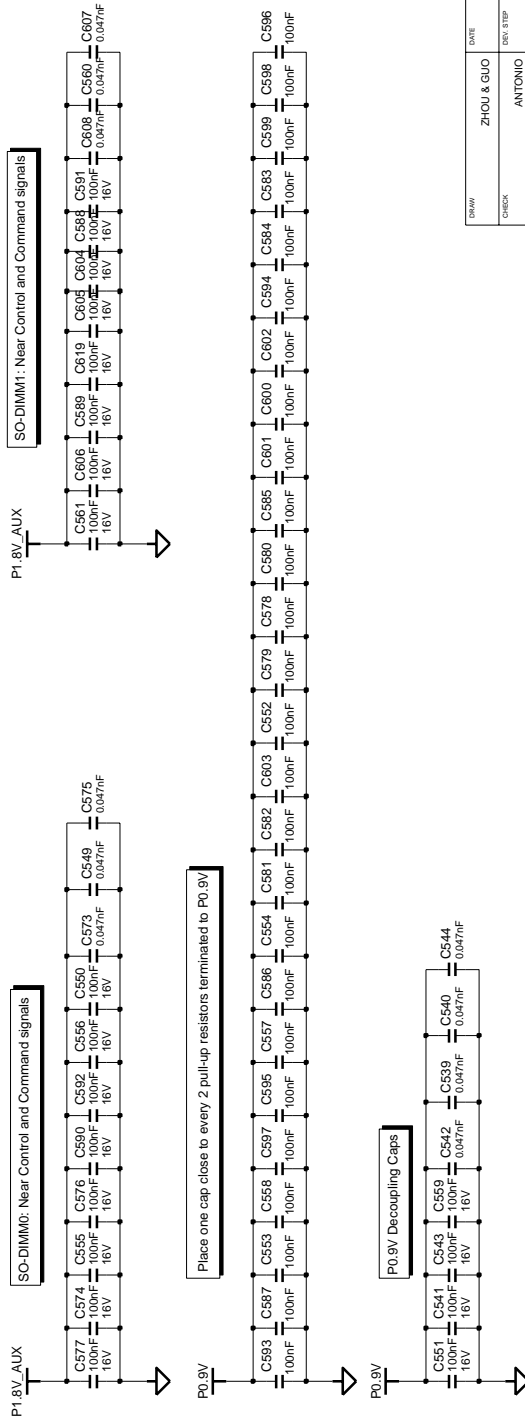
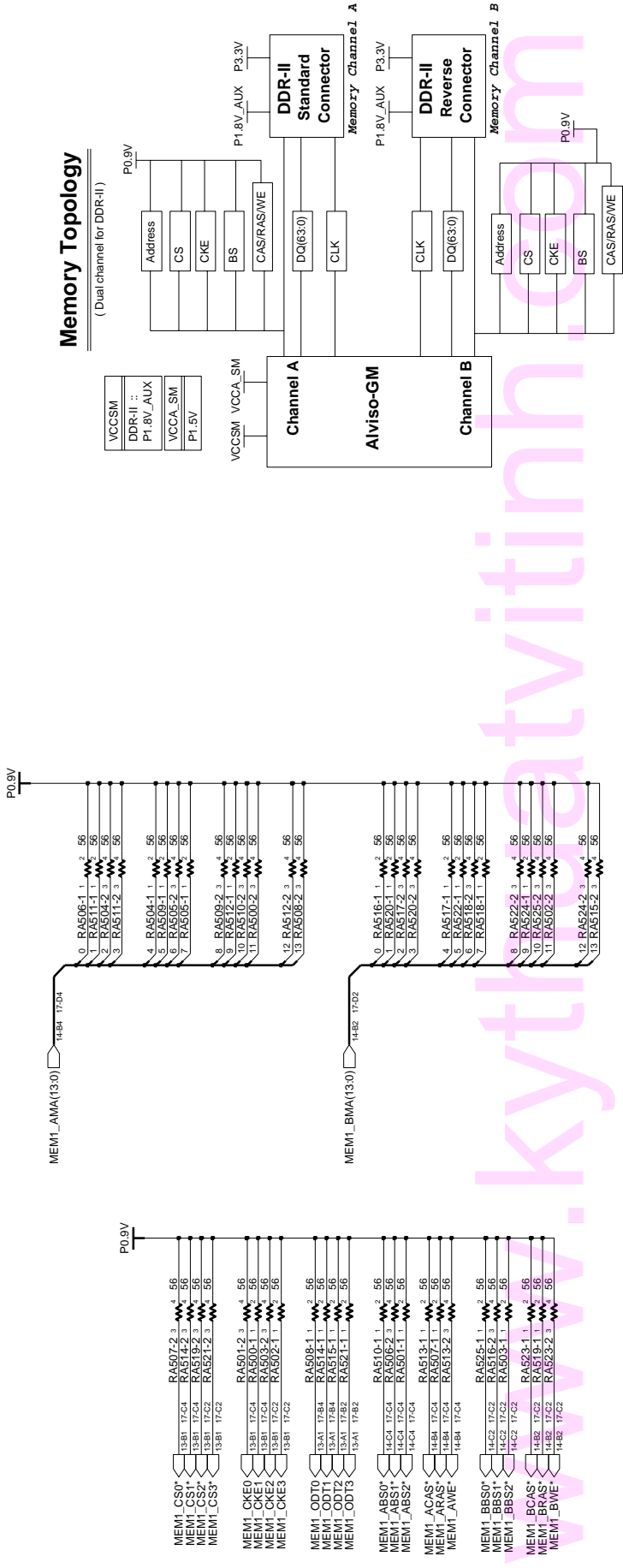
LCDD2_VDDEN
 LCDD2_BKLTORL



Feature	Alviso	Iris(GrantScale)
VCC_GMCH	VCC 1.05V	VCC 1.5V
VCCP	VTT 1.05V	VTT 1.2V
DMI	x2 / x4	x4 only

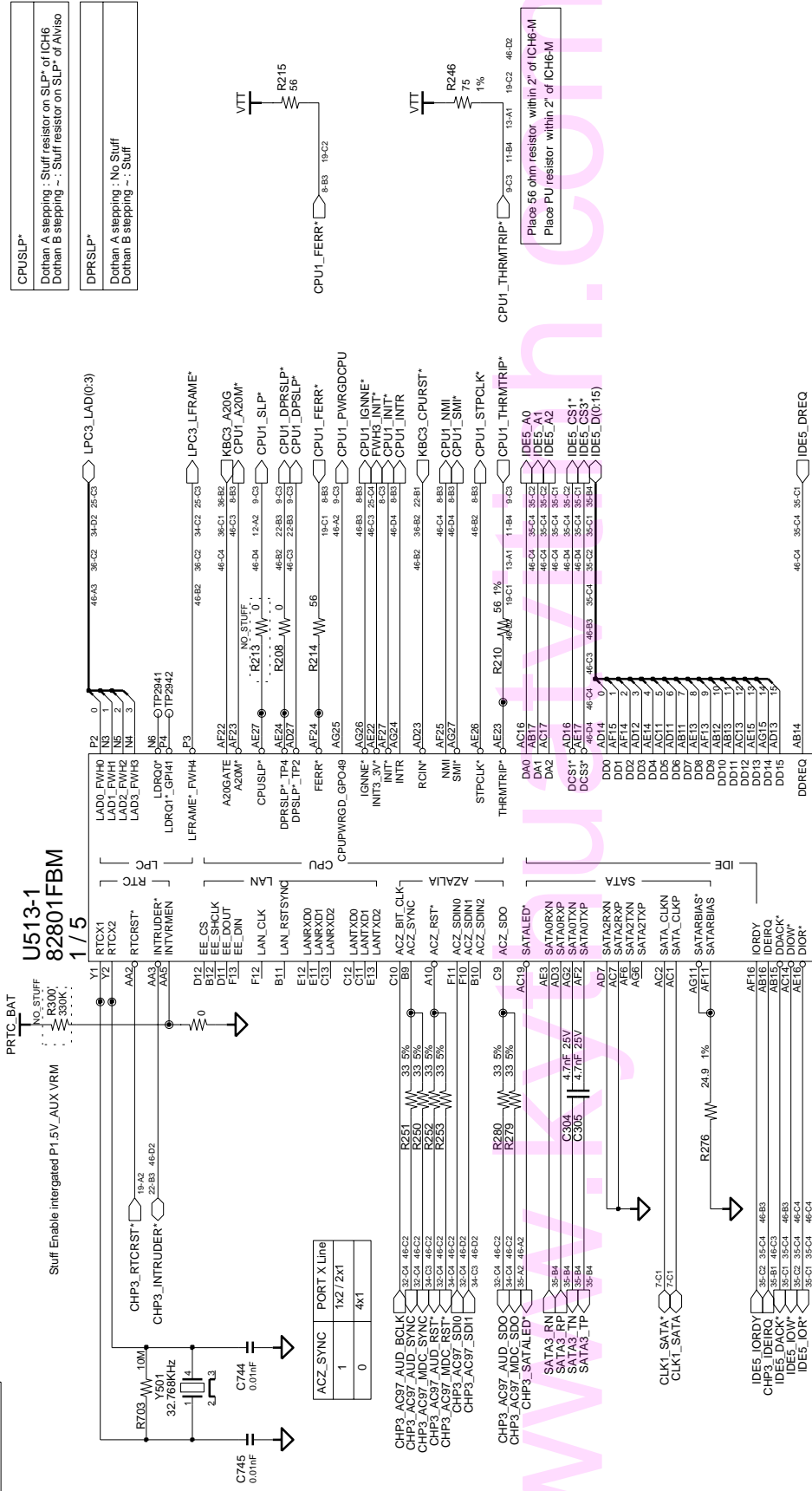
Feature	Alviso	Iris(GrantScale)
DMI0RXN	280C1	DMI1_TXN0
DMI0RXN	280C1	DMI1_TXP0
DMI0TXN	280C1	DMI1_RXN0
DMI0TXP	280C1	DMI1_RXP0
DMI1RXN	280C1	DMI1_TXN1
DMI1RXN	280C1	DMI1_TXN1
DMI1TXN	280C1	DMI1_RXN1
DMI1TXP	280C1	DMI1_RXP1
DMI2RXN	280C1	DMI1_TXN2
DMI2RXN	280C1	DMI1_TXP2
DMI2TXN	280C1	DMI1_RXN2
DMI2TXP	280C1	DMI1_RXP2
DMI3RXN	280C1	DMI1_TXN3
DMI3RXN	280C1	DMI1_TXP3
DMI3TXN	280C1	DMI1_RXN3
DMI3TXP	280C1	DMI1_RXP3
SM_CK0	17-02	CLK1_MCLK0
SM_CK1	17-04	CLK1_MCLK1
SM_CK2	17-04	CLK1_MCLK2
SM_CK3	17-02	CLK1_MCLK3
SM_CK4	17-02	CLK1_MCLK4
SM_CK5	17-02	CLK1_MCLK4*
SM_CK6	17-02	CLK1_MCLK4*
SM_CK7	17-02	CLK1_MCLK4*
SM_CK8	17-02	CLK1_MCLK4*
SM_CK9	17-02	CLK1_MCLK4*
SM_CK10	17-02	CLK1_MCLK4*
SM_CK11	17-02	CLK1_MCLK4*
SM_CK12	17-02	CLK1_MCLK4*
SM_CK13	17-02	CLK1_MCLK4*
SM_CK14	17-02	CLK1_MCLK4*
SM_CK15	17-02	CLK1_MCLK4*
SM_CK16	17-02	CLK1_MCLK4*
SM_CK17	17-02	CLK1_MCLK4*
SM_CK18	17-02	CLK1_MCLK4*
SM_CK19	17-02	CLK1_MCLK4*
SM_CK20	17-02	CLK1_MCLK4*
SM_CK21	17-02	CLK1_MCLK4*
SM_CK22	17-02	CLK1_MCLK4*
SM_CK23	17-02	CLK1_MCLK4*
SM_CK24	17-02	CLK1_MCLK4*
SM_CK25	17-02	CLK1_MCLK4*
SM_CK26	17-02	CLK1_MCLK4*
SM_CK27	17-02	CLK1_MCLK4*
SM_CK28	17-02	CLK1_MCLK4*
SM_CK29	17-02	CLK1_MCLK4*
SM_CK30	17-02	CLK1_MCLK4*
SM_CK31	17-02	CLK1_MCLK4*
SM_CK32	17-02	CLK1_MCLK4*
SM_CK33	17-02	CLK1_MCLK4*
SM_CK34	17-02	CLK1_MCLK4*
SM_CK35	17-02	CLK1_MCLK4*
SM_CK36	17-02	CLK1_MCLK4*
SM_CK37	17-02	CLK1_MCLK4*
SM_CK38	17-02	CLK1_MCLK4*
SM_CK39	17-02	CLK1_MCLK4*
SM_CK40	17-02	CLK1_MCLK4*
SM_CK41	17-02	CLK1_MCLK4*
SM_CK42	17-02	CLK1_MCLK4*
SM_CK43	17-02	CLK1_MCLK4*
SM_CK44	17-02	CLK1_MCLK4*
SM_CK45	17-02	CLK1_MCLK4*
SM_CK46	17-02	CLK1_MCLK4*
SM_CK47	17-02	CLK1_MCLK4*
SM_CK48	17-02	CLK1_MCLK4*
SM_CK49	17-02	CLK1_M

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DATE	6/25/2005	TITLE	AQUILA-SONOMA
DRWN	ZHOU & GUO	CHKD	ANTONIO
REV	1.0	APPROVAL	KEVIN LEE
MODULE CODE		LAST EDIT	June 25, 2005 12:21:39 PM
MP		MAIN	
MP		DDR II TERMINATIONS	
PART NO.	BA41-00529A	PAGE	18 OF 46
SAMSUNG ELECTRONICS		SAMSUNG X06 916	

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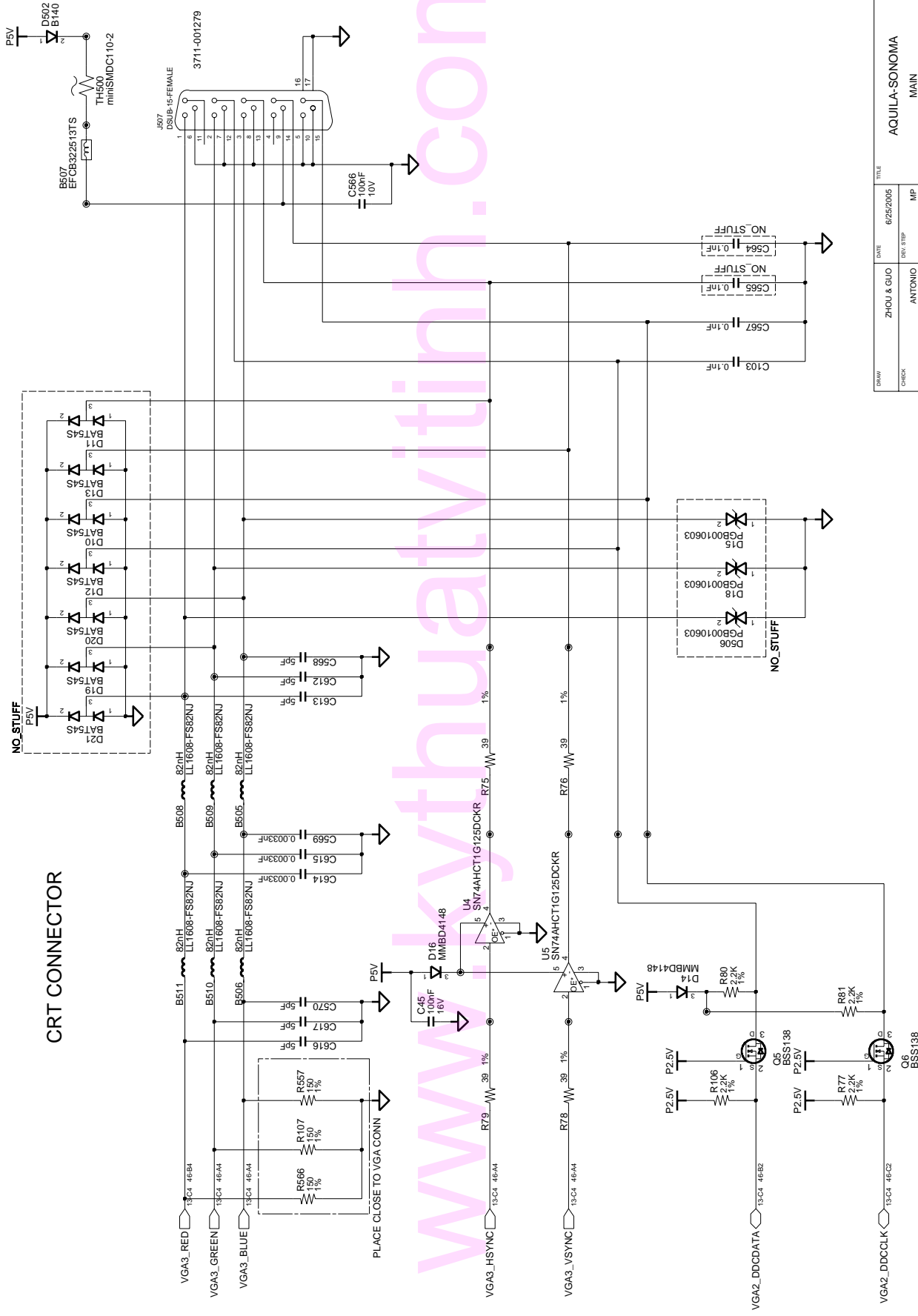
CPUSLP*
 Dothan A stepping: Stuff resistor on SLP* of ICH6
 Dothan B stepping -: Stuff resistor on SLP* of Alviso

DPRSLLP*
 Dothan A stepping: No Stuff
 Dothan B stepping -: Stuff

Place 56 ohm resistor within 2' of ICH6-M
 Place PU resistor within 2' of ICH6-M

DATE	6/25/2005	TITLE	AQUILA-SONOMA
CHKD	ANTONIO	REV	MP
APPROVAL	KEVIN LEE	REV	1.0
MODULE CODE		LAST EDIT	June 25, 2005 12:21:39 PM
DRAWN	ZHOU & GUO	PART NO.	BA41-00529A
CHECK	ANTONIO	MP	
APPROVAL	KEVIN LEE	REV	1.0
MODULE CODE		LAST EDIT	June 25, 2005 12:21:39 PM
SAMSUNG ELECTRONICS		PAGE	19 OF 46

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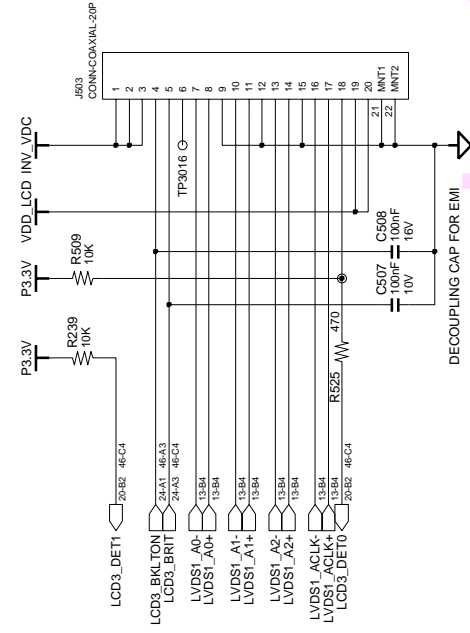


CRT CONNECTOR

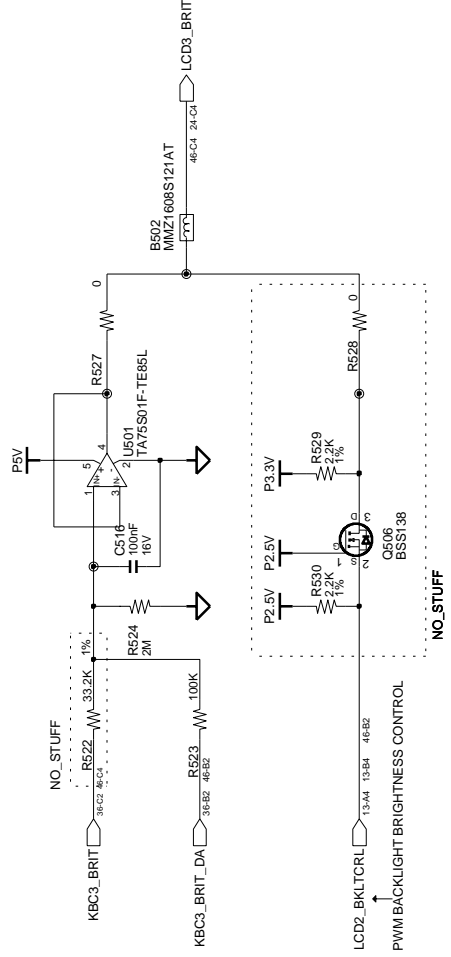
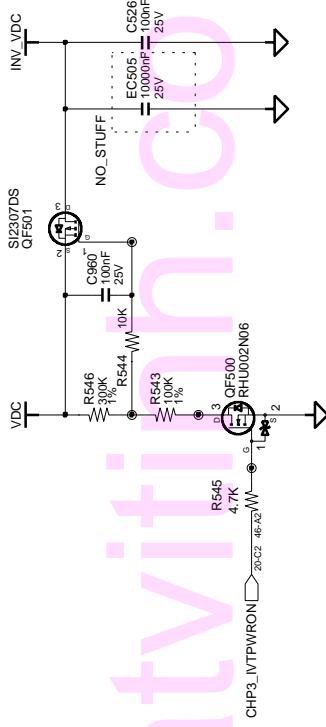
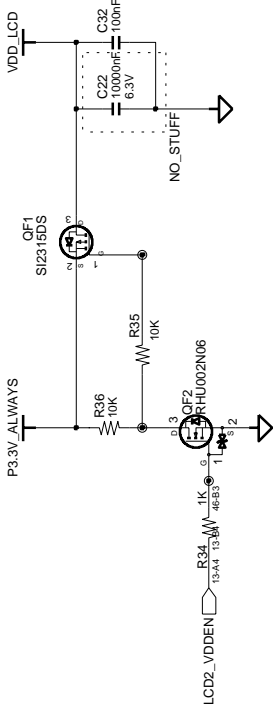
DRAWN		DATE		TITLE	
ZHOUI & GUO		6/25/2005		AQUILA-SONOMA	
CHECK		DRW. STEP		MAIN	
ANTONIO		MP		CRT	
APPROVAL		REV		PART NO.	
KEVIN LEE		1.0		BA41-00529A	
MODULE CODE		LAST EDIT		PAGE	
		June 25, 2005 12:21:39 PM		23 OF 46	

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XGA LCD CONNECTOR



DECOUPLING CAP FOR EMI

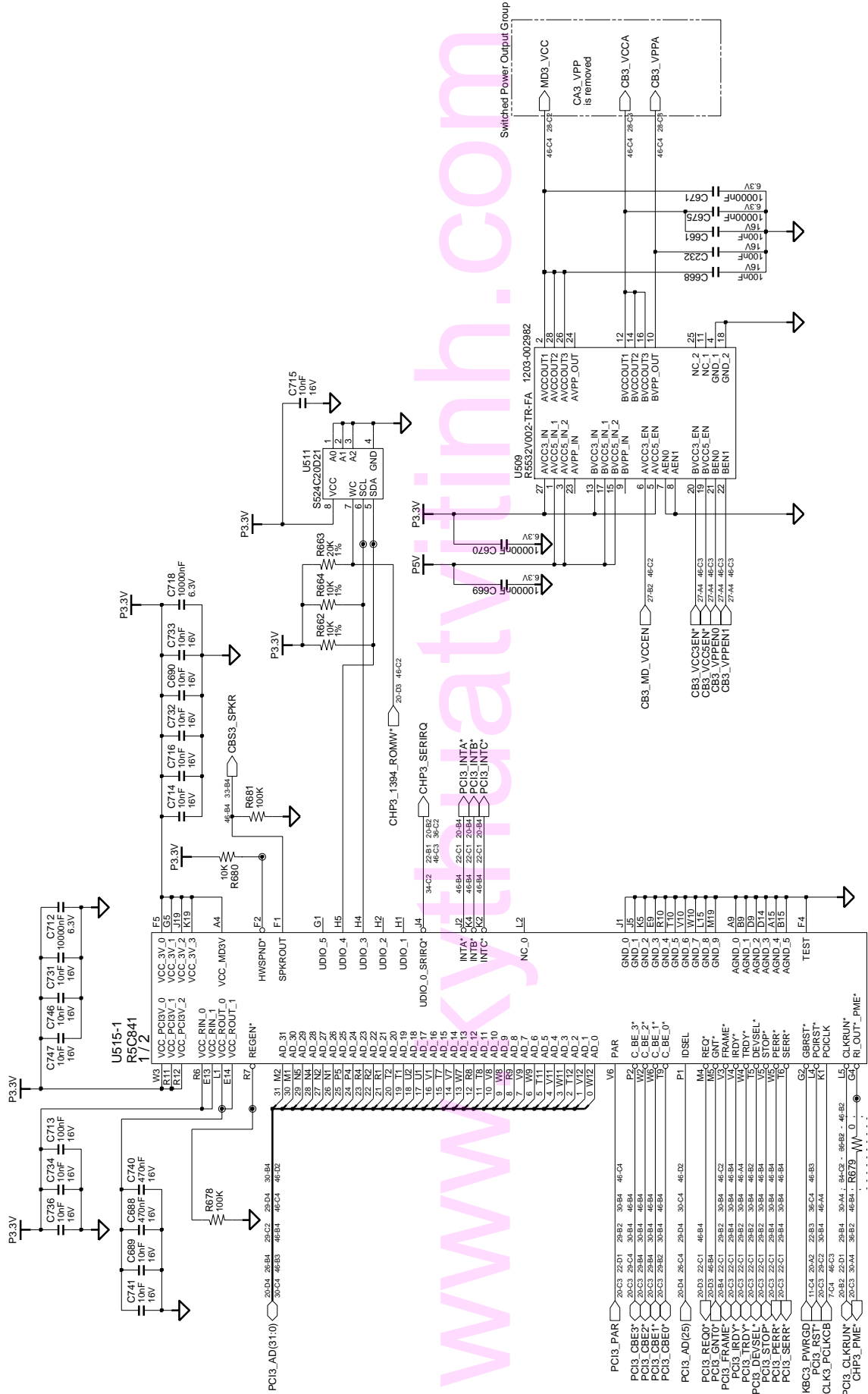


NO_STUFF

DATE	6/25/2005	TITLE	AQUILA-SONOMA
DRWN	ZHOU & GUO	MP	MAIN
CHECK	ANTONIO	REV	1.0
APPROVAL	KEVIN LEE	MODULE CODE	LCD CONNECTOR
LAST EDIT		PAGE	24 OF 46
			June 25, 2005 12:21:39 PM

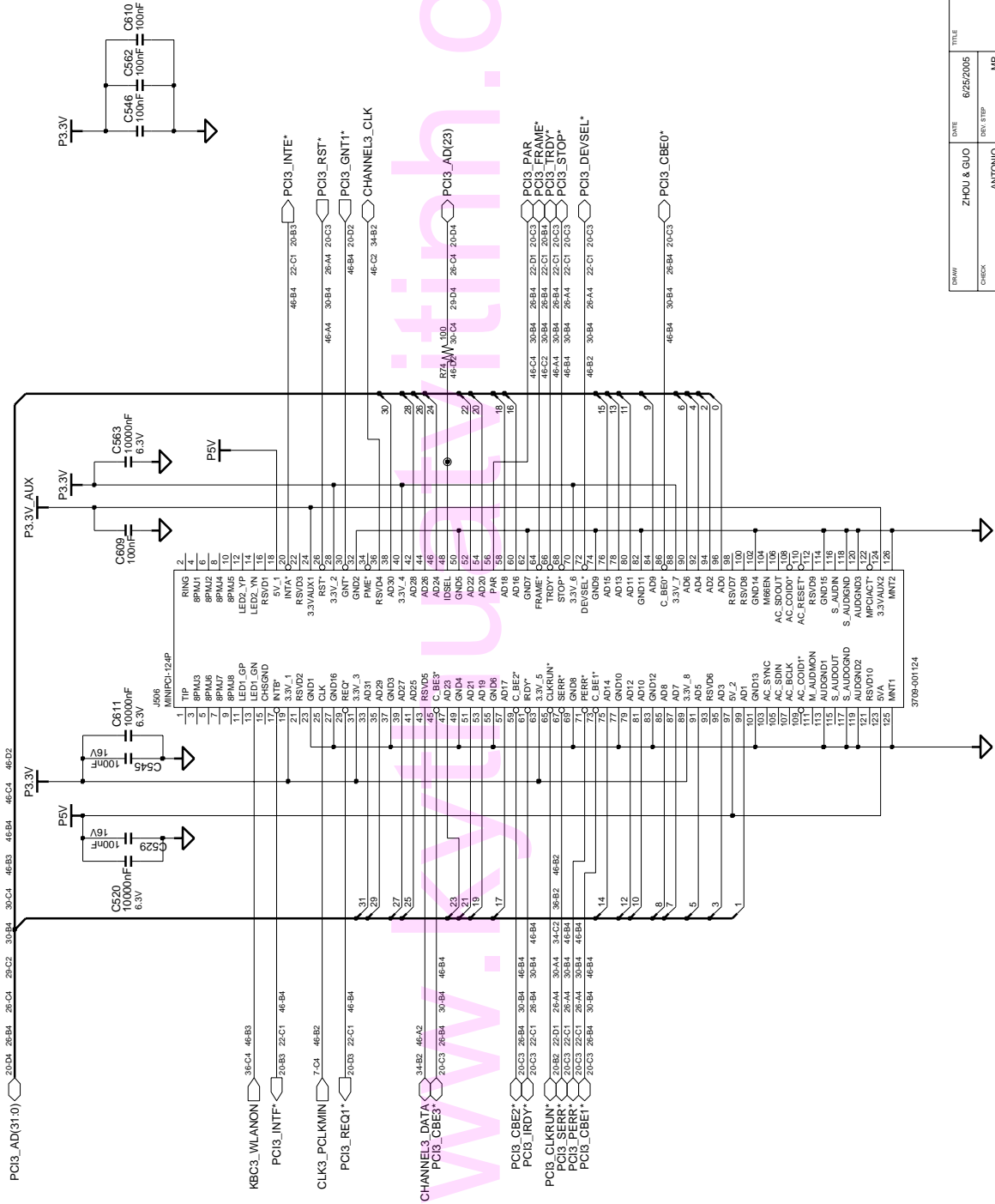
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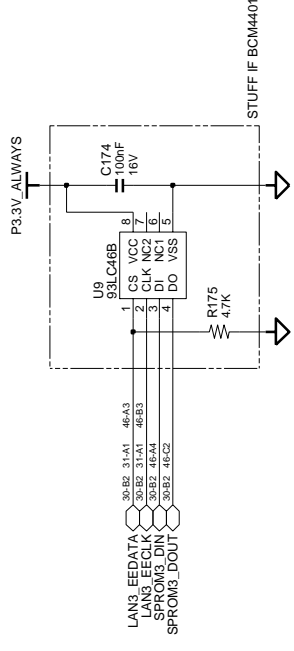
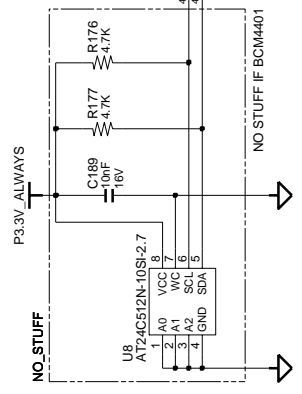
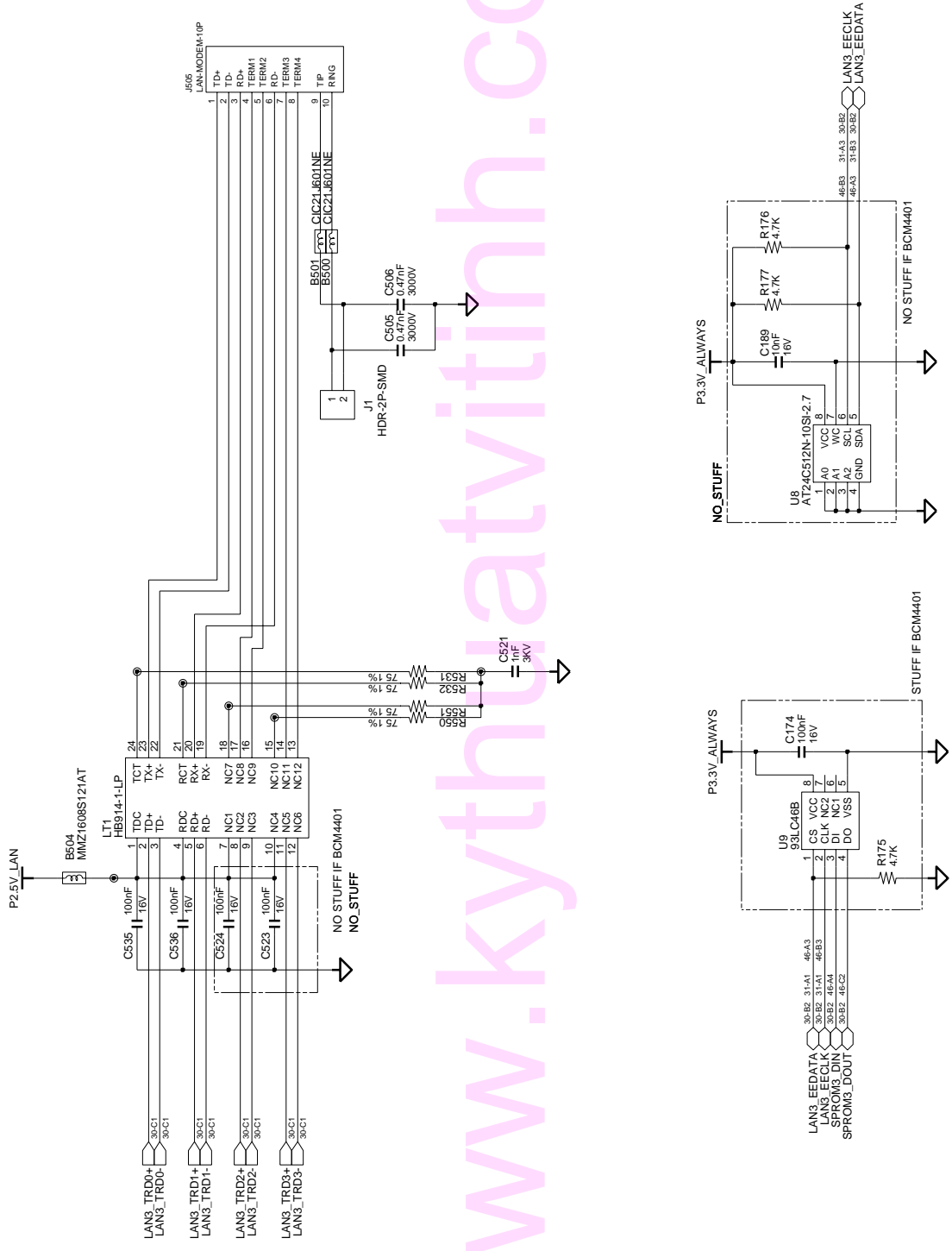
MINI PCI



DATE	6/25/2005	TITLE	AQUILA-SONOMA
DRW	ZHU & GUO	REV	1.0
CHECK	ANTONIO	MP	
APPROVAL	KEVIN LEE	REV	1.0
MODULE CODE		LAST EDIT	June 25, 2005 12:21:39 PM
		PAGE	29 OF 46
			SAMSUNG X06 9-25

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 PART NO. BA41-00529A

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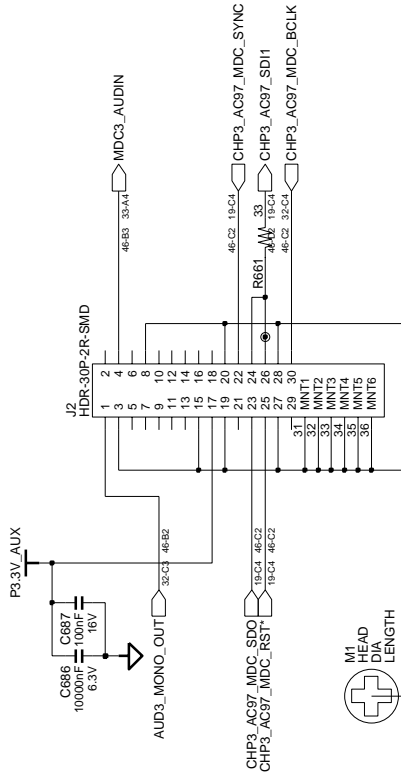
DATE	6/25/2005	TITLE	AQUILA-SONOMA
DRAWN	ZHOU & GUO	CHKD	ANTONIO
CHECK	ANTONIO	MP	MAIN
APPROVAL	KEVIN LEE	REV	1.0
MODULE CODE		LOM (2/2)	
LAST EDIT	June 25, 2005 12:21:39 PM	PAGE	31 OF 46
		PART NO.	BA41-00529A

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SAMSUNG X06 931	

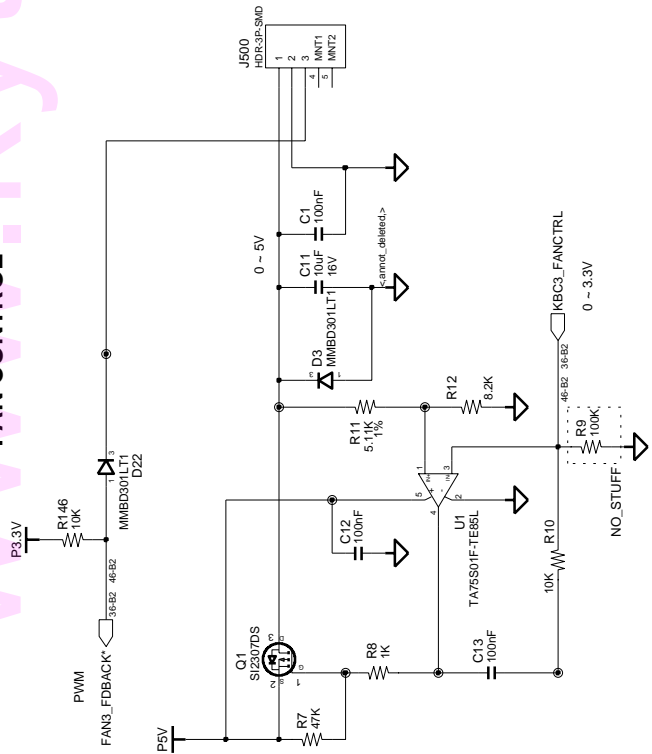
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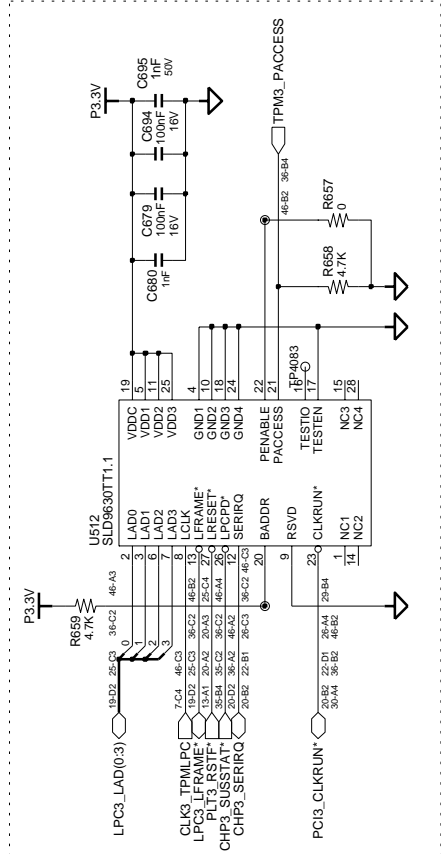
MDC Connector



FAN CONTROL



TPM

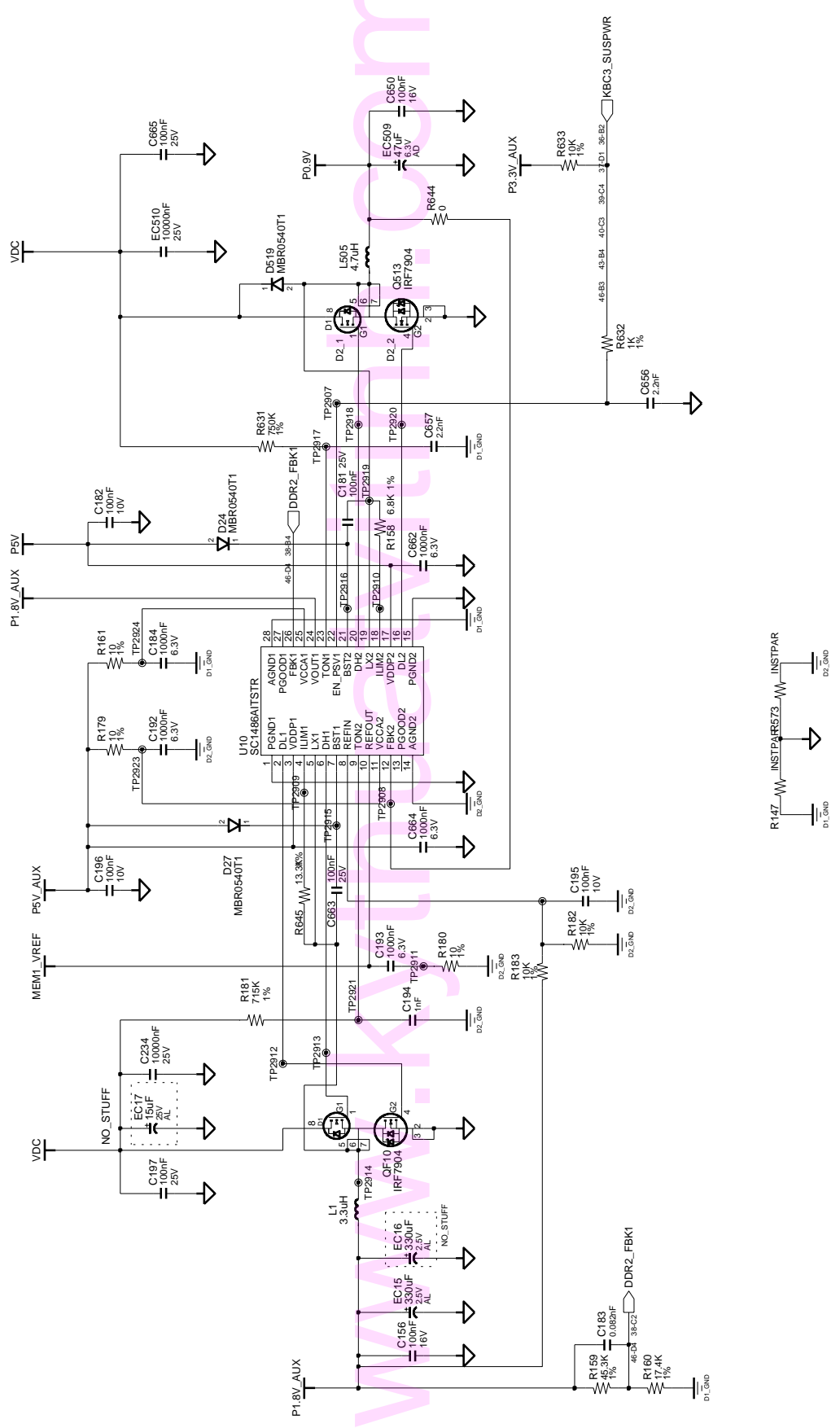


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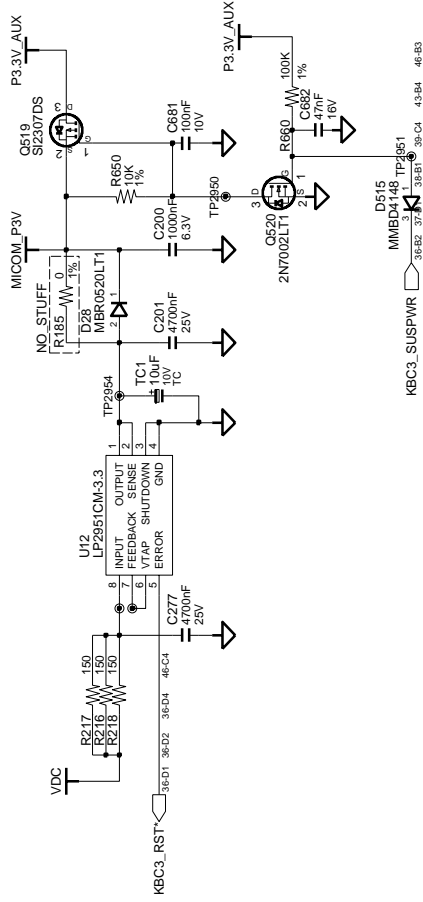
DDR II Power



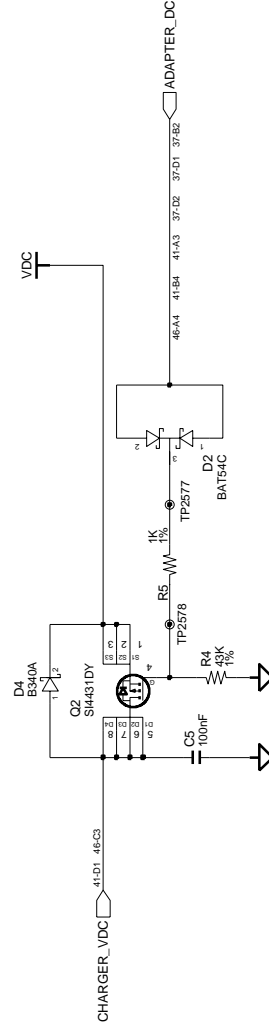
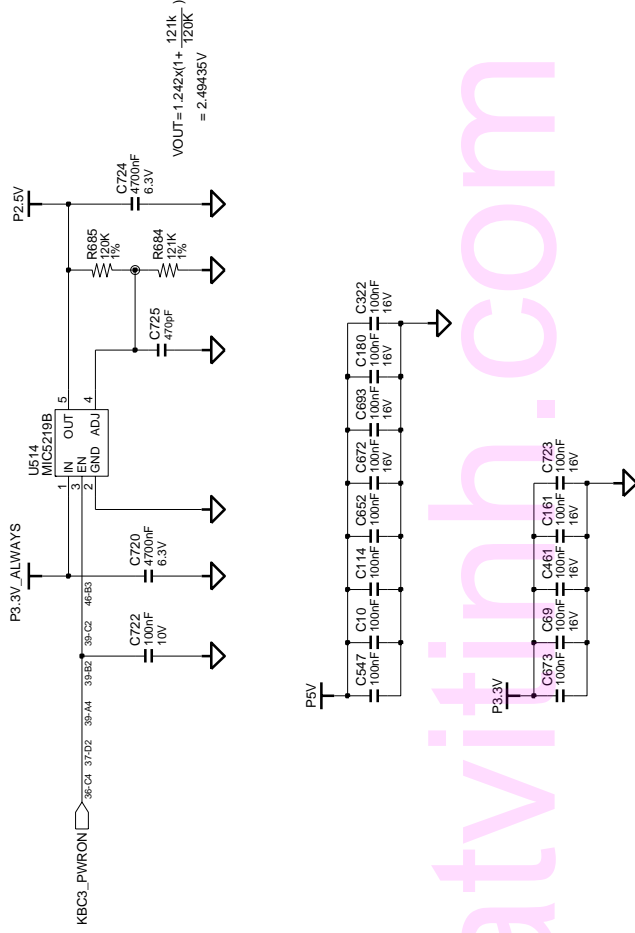
DATE	6/25/2005	TITLE	AQUILA-SONOMA
DRW	ZHOU & GUO	REV	MAIN
CHECK	ANTONIO	MP	DDR II POWER
APPROVAL	KEVIN LEE	REV	1.0
MODULE CODE		LAST EDIT	June 25, 2005 12:21:39 PM
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MICOM_P3V



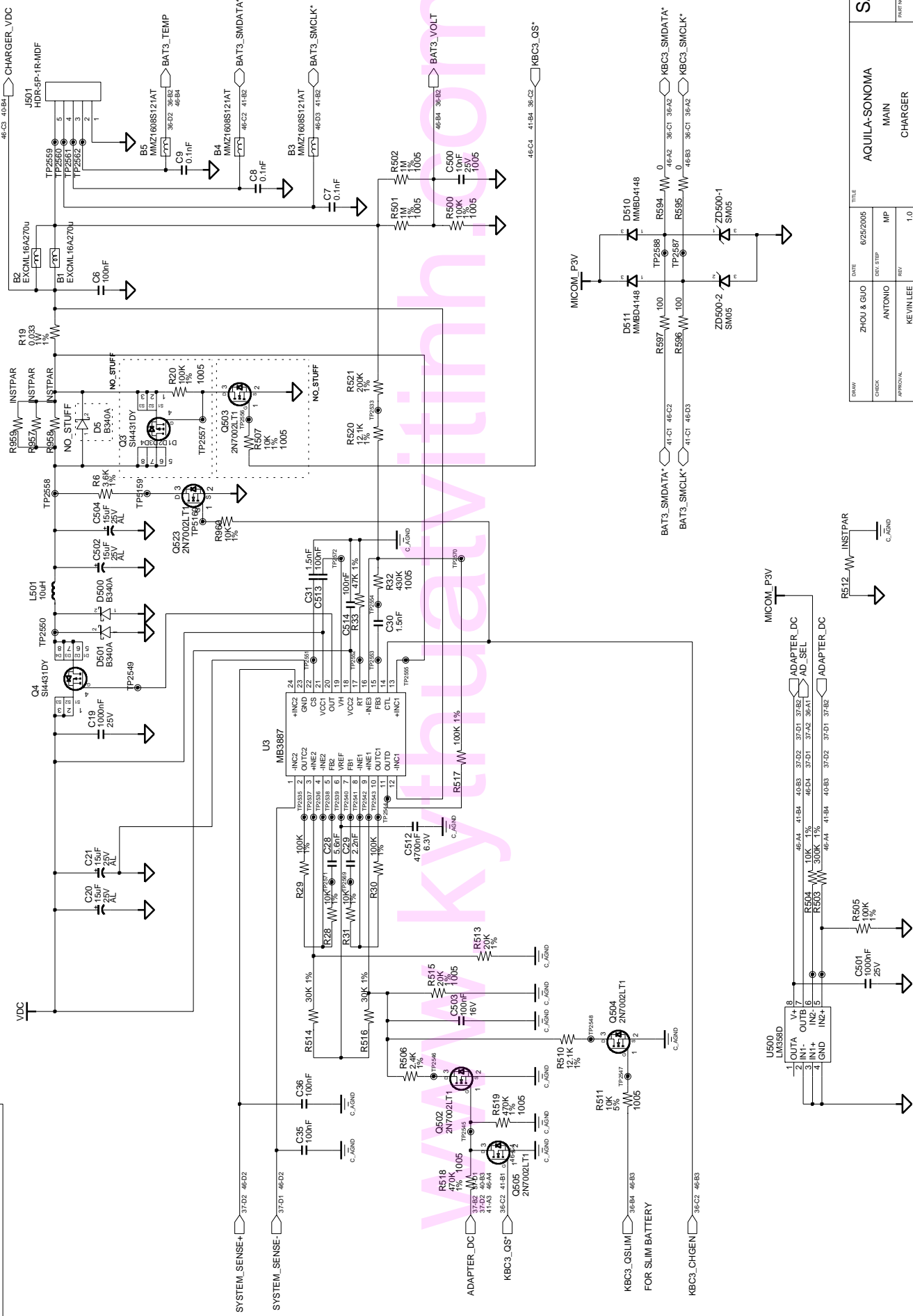
P2.5V



DATE	6/25/2005	TITLE	AQUILA-SONOMA
DESIGNER	ZHOU & GUO	DESIGN STEP	MAIN
CHECKER	ANTONIO	REV	MICOM_P3V / P2.5V
APPROVAL	KEVIN LEE	REV	1.0
MODULE CODE		LAST EDIT	June 25, 2005 12:21:39 PM
		PAGE	40 OF 46
		PART NO.	BA41-00529A
		SAMSUNG ELECTRONICS	
		SAMSUNG X06 9-00	

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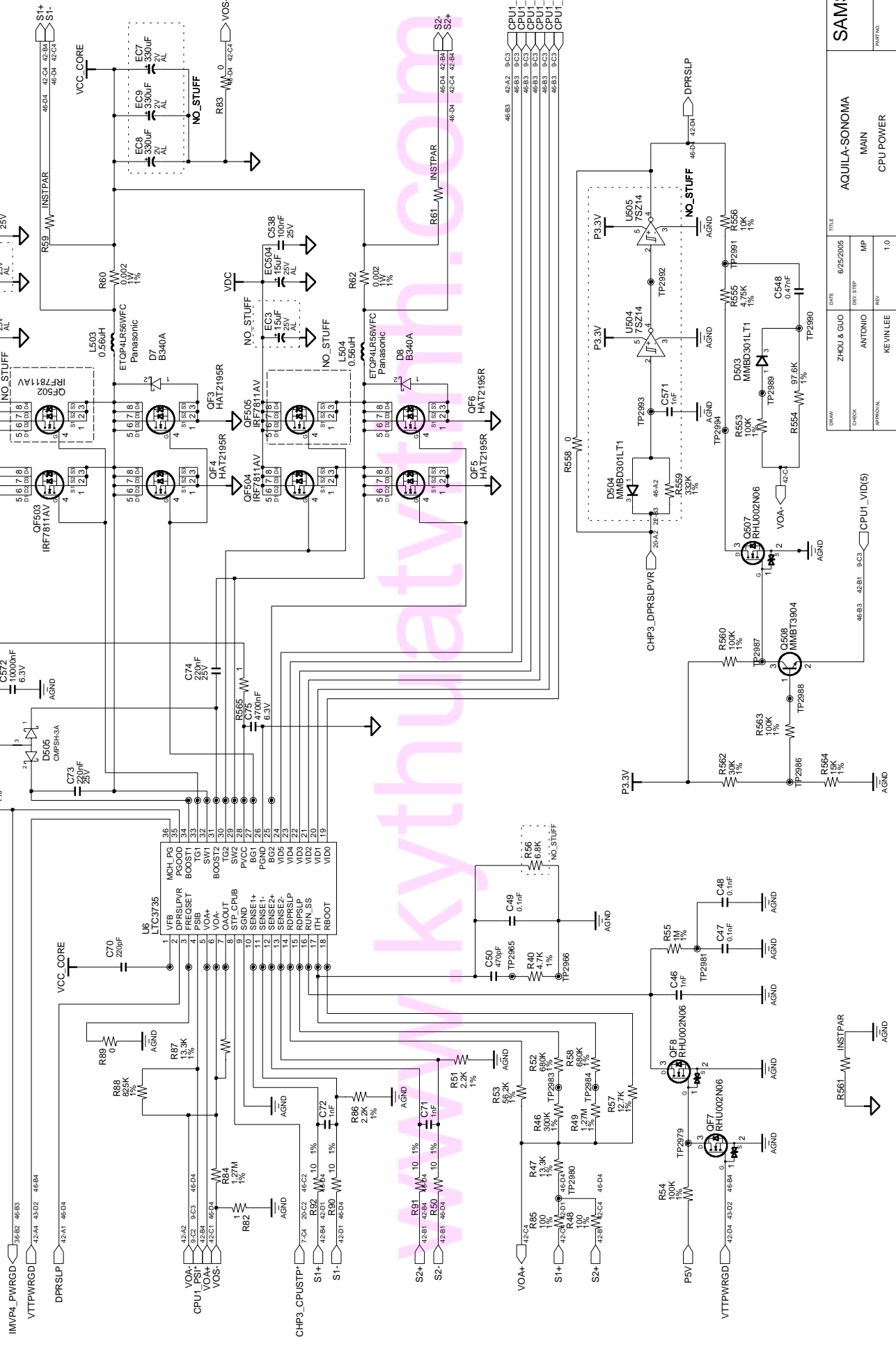
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R512 - INSTPAR
 C_GND

MICOM_P3V
 D511 MMBD4148
 TP2588
 R597 100
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 TP2587
 R595 100
 R596 100
 TP2586
 R594 100
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 R593 100
 TP2584
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 TP2582
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 TP2581
 R589 100
 TP2580
 R588 100
 TP2579
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 R586 100
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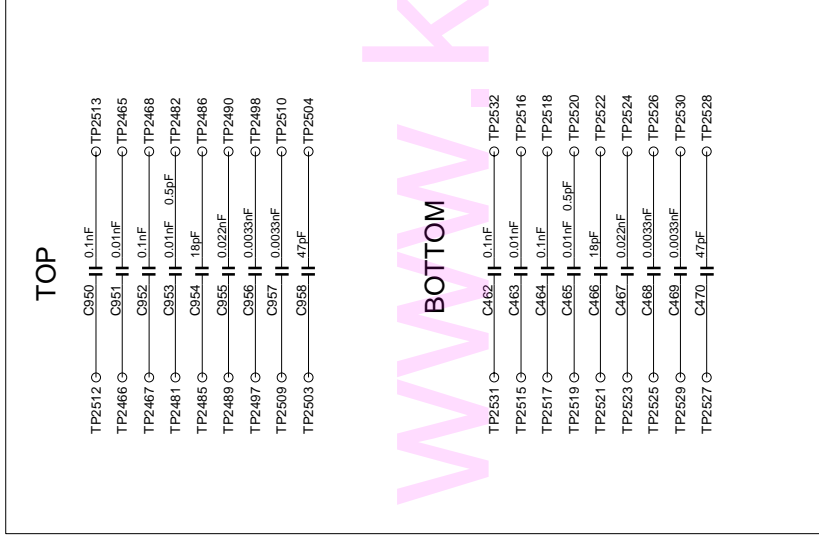


DATE	6/25/2005	TITLE	AQUILA-SONOMA MAIN CPU POWER
DRW	ZHOU & GUO	CHK	ANTONIO
APP	ANTONIO	REV	1.0
DES	ANTONIO	APPROVAL	KEVIN LEE
MOD	ANTONIO	DATE	June 25, 2005 12:21:39 PM
REV	1.0	PAGE	42 OF 46
REV	1.0	PART NO.	BA41-00529A
REV	1.0	MODULE CODE	
REV	1.0	LAST EDIT	

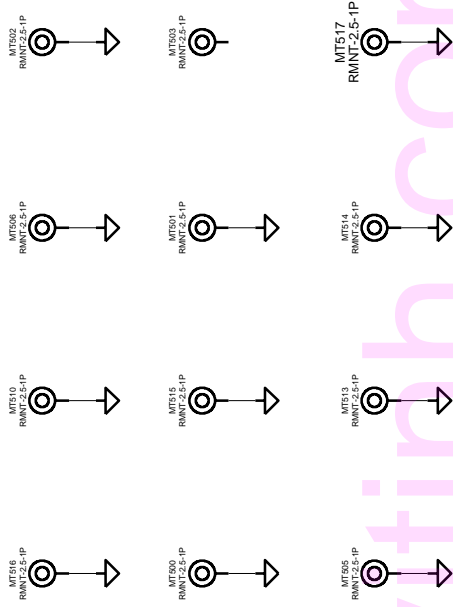
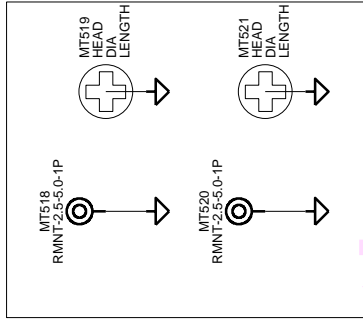
DATE	6/25/2005	TITLE	AQUILA-SONOMA MAIN CPU POWER
DRW	ZHOU & GUO	CHK	ANTONIO
APP	ANTONIO	REV	1.0
DES	ANTONIO	APPROVAL	KEVIN LEE
MOD	ANTONIO	DATE	June 25, 2005 12:21:39 PM
REV	1.0	PAGE	42 OF 46
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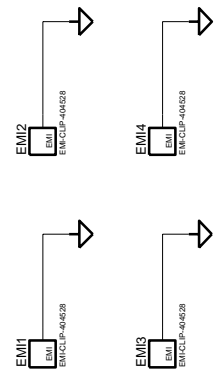


FOR THERMAL



PCB REVISION CONTROL (ICT)		
NO	CONNECTION DATE(Y/M/D)	REVISION STEP
1	N.C.	1.0 MP
2	1-2	
3	2-3	
4	3-1	
5	1-2-3	
6	N.C.	
7	1-2	
8	2-3	
9	3-1	
10	1-2-3	

REV:000
 1 O
 2 O O3



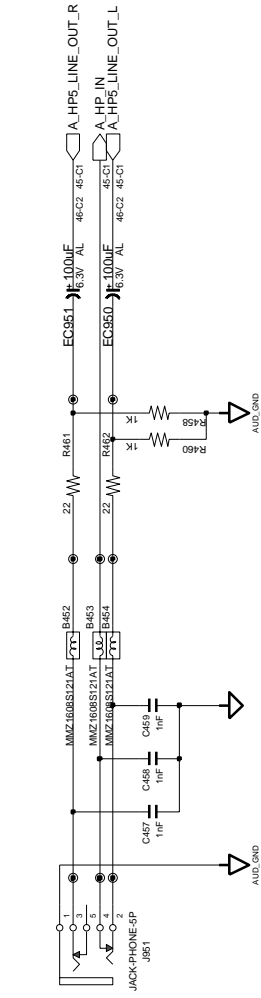
DATE	6/25/2005	TITLE	AQUILA-SONOMA
DRAWN	ZHOU & GUO	DEV. STEP	MP
CHECK	ANTONIO	REV	1.0
APPROVAL	KEVIN LEE	LAST EDIT	June 25, 2005 12:21:39 PM
MODULE CODE		PAGE	44 OF 46
			PCB STUFFS
			MAIN
			PCB STUFFS
			BA41-00529A
			PART NO.
			44 OF 46
			PAGE
			SAMSUNG X06 9-44

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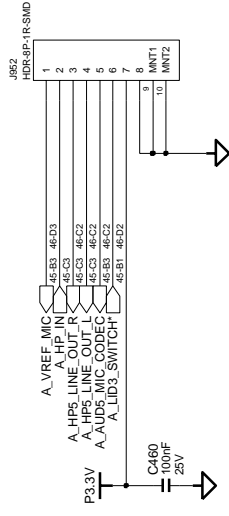
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AUDIO BOARD

HEADPHONE

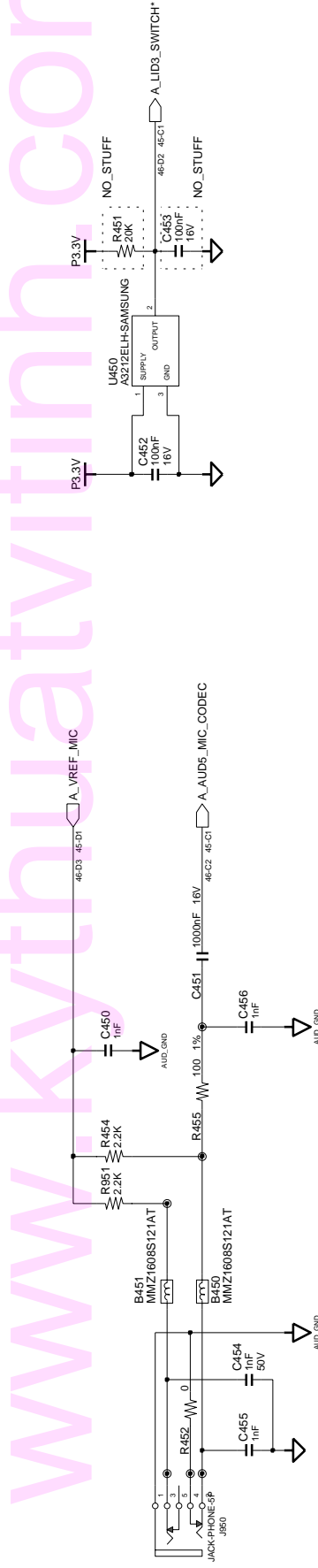


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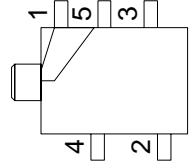


EXTERNAL MIC

LID SWITCH



TOP VIEW



DATE	6/25/2005	TITLE	AQUILA-SONOMA SUB BOARD AUDIO BOARD
DRAWN	ZHOU & GUO	REV	1.0
CHECK	ANTONIO	MP	
APPROVAL	KEVIN LEE	REV	1.0
MODULE CODE		LAST EDIT	June 25, 2005 12:21:39 PM
PART NO.	BA41-00529A	PAGE	45 OF 46

9 Schematic Diagram

9-1-2 Signal Location

Net Name	Sheet Number-Location	Net Name	Sheet Number-Location
A_AUD5_MIC_CODEC	45-B3, 45-C1, 46-C2	CB3_A_A_18	27-B4, 28-C3, 46-D3
A_HP_IN	45-C1, 45-C3	CB3_A_A_19	27-B4, 28-C3, 46-D3
A_HP5_LINE_OUT_L	45-C1, 45-C3, 46-C2	CB3_A_D_14	27-B4, 28-D3, 46-D3
A_HP5_LINE_OUT_R	45-C1, 45-C3, 46-C2	CB3_A_D_2	27-B4, 28-B3, 46-B4
A_LID3_SWITCH*	45-B1, 45-C1, 46-D2	CB3_CAD(0)	27-D4, 28-D3, 46-D3
A_VREF_MIC	45-B3, 45-D1, 46-D3	CB3_CAD(1)	27-D4, 28-D3, 46-D3
AD_SEL	36-A1, 37-A2, 37-D1, 41-A3, 46-D4	CB3_CAD(10)	27-D4, 28-D3, 46-D3
ADAPTER_DC	37-B2, 37-D1, 37-D2, 40-B3, 41-A3, 41-A3, 41-B4, 46-A4	CB3_CAD(11)	27-D4, 28-D3, 46-D3
ADAPTER_IN*	36-A1, 36-A2, 36-D1, 37-C1, 46-A4	CB3_CAD(12)	27-D4, 28-D3, 46-D3
AMP5_PWRDOWN	32-A1, 32-C4, 46-C2	CB3_CAD(13)	27-D4, 28-D3, 46-D3
AUD3_MONO_OUT	32-C3, 34-C4, 46-B2	CB3_CAD(14)	27-D4, 28-D3, 46-D3
AUD3_PCBEEP	32-B3, 33-B3, 46-D3	CB3_CAD(15)	27-D4, 28-D3, 46-D3
AUD3_PHONE	32-C3, 33-A3, 46-A4	CB3_CAD(16)	27-D4, 28-D3, 46-D3
AUD5_CDGND	32-C3, 33-B3, 46-A4	CB3_CAD(17)	27-D4, 28-D3, 46-D3
AUD5_CDL	32-C3, 33-B3, 46-C4	CB3_CAD(18)	27-D4, 28-D3, 46-C3
AUD5_CDR	32-C3, 33-B3, 46-C4	CB3_CAD(19)	27-D4, 28-D3, 46-C3
AUD5_LINE_OUT_L	32-A4, 32-C3, 46-C2	CB3_CAD(2)	27-D4, 28-D3, 46-D3
AUD5_LINE_OUT_R	32-A4, 32-C3, 46-C2	CB3_CAD(20)	27-D4, 28-D3, 46-C3
AUD5_MIC_CODEC	32-C3, 33-B2, 46-D2	CB3_CAD(21)	27-D4, 28-D3, 46-C3
AUD5_SPDIF	33-B1, 37-C2, 46-D3	CB3_CAD(22)	27-D4, 28-D3, 46-C3
AUD5_SPK_L-	33-C2, 37-A4, 46-D3	CB3_CAD(23)	27-D4, 28-D3, 46-C3
AUD5_SPK_L+	33-C2, 37-A4, 46-D3	CB3_CAD(24)	27-D4, 28-D3, 46-C3
AUD5_SPK_R-	33-C2, 37-A4, 46-D3	CB3_CAD(25)	27-D4, 28-D3, 46-C3
AUD5_SPK_R+	33-C2, 37-A4, 46-D3	CB3_CAD(26)	27-D4, 28-D3, 46-C3
AUD5_SPKMUTE*	33-C1, 33-C2, 46-B2	CB3_CAD(27)	27-D4, 28-D3, 46-C3
AUD5_STANDBY*	32-A4, 32-B2, 46-B2	CB3_CAD(28)	27-D4, 28-D3, 46-C3
BAT3_SMCLK*	41-B2, 41-C1, 46-D3	CB3_CAD(29)	27-D4, 28-D3, 46-C3
BAT3_SMDATA*	41-B2, 41-C1, 46-C2	CB3_CAD(3)	27-D4, 28-D3, 46-D3
BAT3_TEMP	36-B2, 36-D2, 41-D1, 46-B4	CB3_CAD(30)	27-D4, 28-D3, 46-C3
BAT3_VOLT	36-B2, 41-C1, 46-B4	CB3_CAD(31)	27-D4, 28-D3, 46-C3
BLT3_BTON_LED*	36-A3, 37-C4, 46-D2	CB3_CAD(31:0)	27-D4, 28-D3
BLT3_PWRBTN*	36-C4, 37-C3, 46-C2	CB3_CAD(4)	27-D4, 28-D3, 46-D3
BLT3_RFON	34-B2, 36-A3, 46-B4	CB3_CAD(5)	27-D4, 28-D3, 46-D3
BLT3_WAKEUP	34-B2, 36-A2, 36-C1, 36-D1, 46-D3	CB3_CAD(6)	27-D4, 28-D3, 46-D3
		CB3_CAD(7)	27-D4, 28-D3, 46-D3
		CB3_CAD(8)	27-D4, 28-D3, 46-D3
		CB3_CAD(9)	27-D4, 28-D3, 46-C3
		CB3_CAUDIO	27-B4, 28-B3, 46-C3

9 Schematic Diagram

Net Name	Sheet Number-Location	Net Name	Sheet Number-Location
CB3_CCBE0*	27-B4, 28-D3, 46-C3	CD5_COM	33-B4, 35-D1, 35-D2, 46-D4
CB3_CCBE1*	27-C4, 28-C3, 46-C3	CD5_L	33-B4, 35-D2, 46-D4
CB3_CCBE2*	27-C4, 28-C3, 46-C3	CD5_R	33-B4, 35-D1, 46-D4
CB3_CCBE3*	27-C4, 28-B3, 46-C3	CHANNEL3_CLK	29-C2, 34-B2, 46-C2
CB3_CCD1*	27-B4, 28-D3, 46-B4	CHANNEL3_DATA	29-C4, 34-B2, 46-A2
CB3_CCD2*	27-B4, 28-B3, 46-B4	CHARGER_VDC	40-B4, 41-D1, 46-C3
CB3_CCLK	27-B4, 28-C3, 46-C4	CHP3_1394_ROMW*	20-D3, 26-C2, 46-C2
CB3_CCLKRUN*	27-B4, 28-B3, 46-C2	CHP3_AC97_AUD_BCLK	19-C4, 32-C4, 46-C2
CB3_CDEVSEL*	27-B4, 28-C3, 46-C2	CHP3_AC97_AUD_RST*	19-C4, 32-C4, 46-C2
CB3_CFRAME*	27-B4, 28-C3, 46-C3	CHP3_AC97_AUD_SDO	19-C4, 32-C4, 46-C2
CB3_CGNT*	27-B4, 28-C3, 46-B4	CHP3_AC97_AUD_SYNC	19-C4, 32-C4, 46-C2
CB3_CINT*	27-B4, 28-C3, 46-B4	CHP3_AC97_MDC_BCLK	32-C4, 34-C3, 46-C2
CB3_CIRDY*	27-B4, 28-C3, 46-C3	CHP3_AC97_MDC_RST*	19-C4, 34-C4, 46-C2
CB3_CPAR	27-B4, 28-C3, 46-C4	CHP3_AC97_MDC_SDO	19-C4, 34-C4, 46-C2
CB3_CPERR*	27-B4, 28-C3, 46-C3	CHP3_AC97_MDC_SYNC	19-C4, 34-C3, 46-C2
CB3_CREQ*	27-B4, 28-B3, 46-B4	CHP3_AC97_SDI0	19-C4, 32-C4, 46-D2
CB3_CRST*	27-B4, 28-B3, 46-B4	CHP3_AC97_SDI1	19-C4, 34-C3, 46-D2
CB3_CSERR*	27-B4, 28-B3, 46-C3	CHP3_BATLOW*	20-A2, 22-C3, 46-C2
CB3_CSTOP*	27-B4, 28-C3, 46-C3	CHP3_BIOSTBL*	20-D3, 25-C4, 46-A2
CB3_CSTSCHG	27-B4, 28-B3, 46-C3	CHP3_BIOSWP*	20-C3, 25-C4, 46-C2
CB3_CTRDY*	27-B4, 28-C3, 46-C3	CHP3_CPUSTP*	7-C4, 20-C2, 42-C4, 46-C2
CB3_CVS1	27-B4, 28-C3, 46-C4	CHP3_CRISIS*	20-A4, 20-B2
CB3_CVS2	27-B4, 28-B3, 46-C4	CHP3 DPRSLPVR	20-A2, 22-B3, 42-B2, 46-A2
CB3_MD_CLK	27-B2, 28-B2, 46-C3	CHP3_IDEIRQ	19-B4, 35-B1, 46-C3
CB3_MD_DATA0_MS_SDIO	27-B2, 28-C2, 46-C2	CHP3_INTRUDER*	19-D3, 22-B3, 46-D2
CB3_MD_DATA1	27-B2, 28-C2, 46-C2	CHP3_IVTPWRON	20-C2, 24-B2, 46-A2
CB3_MD_DATA2	27-B2, 28-C2, 46-C2	CHP3_PCISTP*	7-C4, 20-C2, 46-C2
CB3_MD_DATA3	27-B2, 28-B2, 46-C2	CHP3_PME*	20-C3, 26-A4, 30-A4, 36-B2, 46-B4
CB3_MD_VCCEN	26-B2, 27-B2, 46-C2	CHP3_PWRBTN*	20-A2, 22-C3, 36-B2, 46-C2
CB3_MS_BS_SD_CMD	27-B2, 28-B2, 46-C2	CHP3_RTCRST*	19-A2, 19-D3
CB3_MS_INS*_XD_CD*	27-B2, 28-B2, 46-C2	CHP3_SATALED*	19-C4, 35-A2, 46-A2
CB3_SD_CD*_XD_CD*	27-B2, 28-B2, 46-C2	CHP3_SERIRQ	20-B2, 22-B1, 26-C3, 34-C2, 36-C2, 46-C3
CB3_SD_WP*_XD_R_B*	27-B2, 28-B2, 46-C2	CHP3_SLPS3*	20-A2, 36-A2, 36-C1, 36-D1, 46-C3
CB3_VCC3EN*	26-B2, 27-A4, 46-C3	CHP3_SLPS4*	20-A2, 36-A2, 36-C1, 46-C3
CB3_VCC5EN*	26-B2, 27-A4, 46-C3	CHP3_SLPS5*	20-A2, 36-A2, 46-C3
CB3_VCCA	26-B1, 28-C3, 28-C3, 46-C4	CHP3_SMLINK0	20-D2, 22-D3, 46-C2
CB3_VPPA	26-B1, 28-C3, 28-C3, 46-C4	CHP3_SMLINK1	20-D2, 22-C3, 46-C2
CB3_VPPEN0	26-B2, 27-A4, 46-C3		
CB3_VPPEN1	26-B2, 27-A4, 46-C3		
CBS3_SPKR	26-C3, 33-B4, 46-B4		

9 Schematic Diagram

Net Name	Sheet Number-Location	Net Name	Sheet Number-Location
CHP3_SPKR	20-D2, 33-B4, 46-B4	CLK3_PWRGD*	7-C4, 11-C4, 16-B2, 36-B2, 36-C1, 46-C3
CHP3_SUSSTAT*	20-D2, 34-C2, 36-A2, 46-A2	CLK3_SMBCLK	7-A3, 7-C4, 17-B2, 17-B4
CHP3_THRM*	20-B2, 20-B3, 46-C3	CLK3_SMBDATA	7-A3, 7-B4, 17-B2, 17-B4
CHP3_THRMTRIP*	11-B3, 36-B2, 46-D2	CLK3_SSCIN	7-B1, 16-B2, 46-C3
CLK0_HCLK0	7-C1, 9-C3	CLK3_TPMLPC	7-C4, 34-C2, 46-C3
CLK0_HCLK0*	7-C1, 9-C3	CLK3_USB48	7-C4, 20-A2, 46-C3
CLK0_HCLK1	7-C1, 12-B2	CPU1_A*(10)	8-C4, 12-D2
CLK0_HCLK1*	7-C1, 12-B2	CPU1_A*(11)	8-C4, 12-D2
CLK1_BSEL1	7-C4, 9-C2, 9-C3, 13-A4, 46-C3	CPU1_A*(12)	8-C4, 12-D2
CLK1_BSEL2	7-C4, 9-C2, 9-C3, 13-A4, 46-C3	CPU1_A*(13)	8-C4, 12-D2
CLK1_DOTCLK	7-B1, 16-B2	CPU1_A*(14)	8-C4, 12-D2
CLK1_DOTCLK*	7-B1, 16-A2	CPU1_A*(15)	8-C4, 12-D2
CLK1_DREFCLK	7-B1, 13-A1	CPU1_A*(16)	8-C4, 12-D2
CLK1_DREFCLK*	7-B1, 13-A1	CPU1_A*(16:3)	8-C4
CLK1_DREFSSCLK	13-A1, 16-B1	CPU1_A*(17)	8-C4, 12-D2
CLK1_DREFSSCLK*	13-A1, 16-B1	CPU1_A*(18)	8-C4, 12-D2
CLK1_MCH3GPLL	7-C1, 13-D4	CPU1_A*(19)	8-C4, 12-D2
CLK1_MCH3GPLL*	7-C1, 13-D4	CPU1_A*(20)	8-C4, 12-D2
CLK1_MCLK0	13-C1, 17-C4	CPU1_A*(21)	8-C4, 12-D2
CLK1_MCLK0*	13-B1, 17-C4	CPU1_A*(22)	8-C4, 12-D2
CLK1_MCLK1	13-C1, 17-C4	CPU1_A*(23)	8-C4, 12-D2
CLK1_MCLK1*	13-B1, 17-C4	CPU1_A*(24)	8-C4, 12-D2
CLK1_MCLK3	13-C1, 17-C2	CPU1_A*(25)	8-C4, 12-D2
CLK1_MCLK3*	13-B1, 17-C2	CPU1_A*(26)	8-C4, 12-D2
CLK1_MCLK4	13-C1, 17-C2	CPU1_A*(27)	8-C4, 12-D2
CLK1_MCLK4*	13-B1, 17-C2	CPU1_A*(28)	8-C4, 12-D2
CLK1_PCIEICH	7-C1, 20-B1	CPU1_A*(29)	8-C4, 12-D2
CLK1_PCIEICH*	7-C1, 20-B1	CPU1_A*(3)	8-C4, 12-D2
CLK1_SATA	7-C1, 19-B4	CPU1_A*(30)	8-C4, 12-D2
CLK1_SATA*	7-C1, 19-B4	CPU1_A*(31)	8-C4, 12-D2
CLK3_AUD14	7-B1, 32-D4, 46-C3	CPU1_A*(31:17)	8-C4
CLK3_ICH14	7-B1, 20-B1, 46-C3	CPU1_A*(31:3)	12-D2
CLK3_PCLKCB	7-C4, 26-A4, 46-C3	CPU1_A*(4)	8-C4, 12-D2
CLK3_PCLKFWH	7-C4, 25-C4, 46-C2	CPU1_A*(5)	8-C4, 12-D2
CLK3_PCLKICH	7-C4, 20-C3, 46-C2	CPU1_A*(6)	8-C4, 12-D2
CLK3_PCLKLAN	7-C4, 30-B4, 46-C2	CPU1_A*(7)	8-C4, 12-D2
CLK3_PCLKMICOM	7-C4, 36-C2, 46-D2	CPU1_A*(8)	8-C4, 12-D2
CLK3_PCLKMIN	7-C4, 29-C4, 46-B2	CPU1_A*(9)	8-C4, 12-D2
		CPU1_A20M*	8-B3, 19-C2, 46-C3

9 Schematic Diagram

Net Name	Sheet Number-Location	Net Name	Sheet Number-Location
CPU1_ADS*	8-C3, 12-C2	CPU1_D*(37)	8-C1, 12-D4
CPU1_ADSTB0*	8-C4, 12-C2	CPU1_D*(38)	8-C1, 12-D4
CPU1_ADSTB1*	8-B4, 12-C2	CPU1_D*(39)	8-C1, 12-D4
CPU1_BNR*	8-C3, 12-C2	CPU1_D*(4)	8-C2, 12-D4
CPU1_BPRI*	8-C3, 12-C2	CPU1_D*(40)	8-C1, 12-D4
CPU1_BREQ*	8-C3, 12-C2	CPU1_D*(41)	8-C1, 12-D4
CPU1_CPURST*	8-C3, 12-C2	CPU1_D*(42)	8-C1, 12-D4
CPU1_D*(0)	8-C2, 12-D4	CPU1_D*(43)	8-C1, 12-D4
CPU1_D*(1)	8-C2, 12-D4	CPU1_D*(44)	8-C1, 12-D4
CPU1_D*(10)	8-C2, 12-D4	CPU1_D*(45)	8-C1, 12-D4
CPU1_D*(11)	8-C2, 12-D4	CPU1_D*(46)	8-C1, 12-D4
CPU1_D*(12)	8-C2, 12-D4	CPU1_D*(47)	8-C1, 12-D4
CPU1_D*(13)	8-C2, 12-D4	CPU1_D*(47:32)	8-C1
CPU1_D*(14)	8-C2, 12-D4	CPU1_D*(48)	8-C1, 12-D4
CPU1_D*(15)	8-C2, 12-D4	CPU1_D*(49)	8-C1, 12-D4
CPU1_D*(15:0)	8-C2	CPU1_D*(5)	8-C2, 12-D4
CPU1_D*(16)	8-C2, 12-D4	CPU1_D*(50)	8-C1, 12-D4
CPU1_D*(17)	8-C2, 12-D4	CPU1_D*(51)	8-C1, 12-D4
CPU1_D*(18)	8-C2, 12-D4	CPU1_D*(52)	8-C1, 12-D4
CPU1_D*(19)	8-C2, 12-D4	CPU1_D*(53)	8-C1, 12-D4
CPU1_D*(2)	8-C2, 12-D4	CPU1_D*(54)	8-C1, 12-D4
CPU1_D*(20)	8-C2, 12-D4	CPU1_D*(55)	8-C1, 12-D4
CPU1_D*(21)	8-C2, 12-D4	CPU1_D*(56)	8-C1, 12-D4
CPU1_D*(22)	8-C2, 12-D4	CPU1_D*(57)	8-C1, 12-D4
CPU1_D*(23)	8-C2, 12-D4	CPU1_D*(58)	8-C1, 12-D4
CPU1_D*(24)	8-C2, 12-D4	CPU1_D*(59)	8-C1, 12-D4
CPU1_D*(25)	8-C2, 12-D4	CPU1_D*(6)	8-C2, 12-D4
CPU1_D*(26)	8-C2, 12-D4	CPU1_D*(60)	8-C1, 12-D4
CPU1_D*(27)	8-C2, 12-D4	CPU1_D*(61)	8-C1, 12-D4
CPU1_D*(28)	8-C2, 12-D4	CPU1_D*(62)	8-C1, 12-D4
CPU1_D*(29)	8-C2, 12-D4	CPU1_D*(63)	8-C1, 12-D4
CPU1_D*(3)	8-C2, 12-D4	CPU1_D*(63:0)	12-D4
CPU1_D*(30)	8-C2, 12-D4	CPU1_D*(63:48)	8-C1
CPU1_D*(31)	8-C2, 12-D4	CPU1_D*(7)	8-C2, 12-D4
CPU1_D*(31:16)	8-C2	CPU1_D*(8)	8-C2, 12-D4
CPU1_D*(32)	8-C1, 12-D4	CPU1_D*(9)	8-C2, 12-D4
CPU1_D*(33)	8-C1, 12-D4	CPU1_DBI0*	8-C2, 12-B2
CPU1_D*(34)	8-C1, 12-D4	CPU1_DBI1*	8-B2, 12-B2
CPU1_D*(35)	8-C1, 12-D4	CPU1_DBI2*	8-C1, 12-B2
CPU1_D*(36)	8-C1, 12-D4	CPU1_DBI3*	8-B1, 12-B2

9 Schematic Diagram

Net Name	Sheet Number-Location	Net Name	Sheet Number-Location
CPU1_DBSY*	8-C3, 12-B2	CPU1_THRMTRIP*	9-C3, 11-B4, 13-A1, 19-C1, 19-C2, 46-D2
CPU1_DEFER*	8-C3, 12-B2	CPU1_TMS	9-B2, 10-A3, 46-C4
CPU1 DPRSLP*	9-C3, 19-C2, 22-B3, 46-B2	CPU1_TRDY*	8-C3, 12-A2
CPU1_DPSLP*	9-C3, 19-C2, 22-B3, 46-C3	CPU1_TRST*	9-B2, 10-A3
CPU1_DPWR*	9-C3, 12-B2	CPU1_VID(0)	9-C3, 42-B1, 46-B3
CPU1_DRDY*	8-C3, 12-B2	CPU1_VID(1)	9-C3, 42-B1, 46-B3
CPU1_DSTBN0*	8-C2, 12-B2	CPU1_VID(2)	9-C3, 42-B1, 46-B3
CPU1_DSTBN1*	8-B2, 12-B2	CPU1_VID(3)	9-C3, 42-B1, 46-B3
CPU1_DSTBN2*	8-C1, 12-B2	CPU1_VID(4)	9-C3, 42-B1, 46-B3
CPU1_DSTBN3*	8-B1, 12-B2	CPU1_VID(5)	9-C3, 42-A2, 42-B1, 46-B3
CPU1_DSTBP0*	8-C2, 12-B2	CPU1_VID(5:0)	9-C3
CPU1_DSTBP1*	8-B2, 12-B2	CPU2_THERMDA	9-C3, 11-C2, 46-B2
CPU1_DSTBP2*	8-C1, 12-B2	CPU2_THERMDC	9-C3, 11-C2, 46-B2
CPU1_DSTBP3*	8-B1, 12-B2	CPU3_ALERT*	11-B1, 20-A4, 46-B3
CPU1_FERR*	8-B3, 19-C1, 19-C2	CPUPWRON	11-C4, 43-B2
CPU1_HIT*	8-C3, 12-B2	DDR2_FBK1	38-B4, 38-C2, 46-D4
CPU1_HITM*	8-C3, 12-B2	DMI1_RXN0	13-D1, 20-C1
CPU1_IGNNE*	8-B3, 19-C2, 46-B3	DMI1_RXN1	13-C1, 20-C1
CPU1_INIT*	8-C3, 19-C2	DMI1_RXN2	13-C1, 20-C1
CPU1_INTR	8-B3, 19-C2, 46-D4	DMI1_RXN3	13-C1, 20-C1
CPU1_LOCK*	8-C3, 12-B2	DMI1_RXP0	13-D1, 20-C1
CPU1_NMI	8-B3, 19-C2, 46-C4	DMI1_RXP1	13-C1, 20-C1
CPU1_PROCHOT*	9-C3, 11-B2, 46-A2	DMI1_RXP2	13-C1, 20-C1
CPU1_PSI*	9-C2, 9-C3, 42-C4, 46-D4	DMI1_RXP3	13-C1, 20-C1
CPU1_PWRGDCPU	9-C3, 19-C2, 46-A2	DMI1_TXN0	13-D1, 20-C1
CPU1_REQ*(0)	8-C4, 12-B2	DMI1_TXN1	13-D1, 20-C1
CPU1_REQ*(1)	8-C4, 12-B2	DMI1_TXN2	13-C1, 20-C1
CPU1_REQ*(2)	8-C4, 12-B2	DMI1_TXN3	13-C1, 20-B1
CPU1_REQ*(3)	8-C4, 12-B2	DMI1_TXP0	13-D1, 20-C1
CPU1_REQ*(4)	8-C4, 12-B2	DMI1_TXP1	13-D1, 20-C1
CPU1_REQ*(4:0)	8-C4, 12-B2	DMI1_TXP2	13-C1, 20-C1
CPU1_RS0*	8-B3, 12-A2	DMI1_TXP3	13-C1, 20-B1
CPU1_RS1*	8-C3, 12-A2	DPRSLP	42-A1, 42-D4, 46-D4
CPU1_RS2*	8-C3, 12-A2	FAN3_FDBACK*	34-B4, 36-B2, 46-B2
CPU1_SLP*	9-C3, 12-A2, 19-C2, 46-D4	FWH3_INIT*	19-C2, 25-C4, 46-C3
CPU1_SMI*	8-B3, 19-C2, 46-D4	HDD3_LED*	35-A1, 37-C3, 46-D4
CPU1_STPCLK*	8-B3, 19-C2, 46-B2	HDD3_SEL	35-B3, 35-C2, 46-C4
CPU1_TCK	9-B2, 10-A3, 46-C4	HDD5_PDIAG*	35-C1, 35-C4, 46-B3
CPU1_TDI	9-B2, 10-A3, 46-C4	HP5_DETECT	32-C3, 33-B2, 33-C2, 46-C3
CPU1_TDO	9-B2, 10-A3, 46-C4		

9 Schematic Diagram

Net Name	Sheet Number-Location	Net Name	Sheet Number-Location
HP5_L	32-C4, 33-B2, 33-C4, 46-D4	KBC3_CPURST*	19-C2, 22-B1, 36-B2, 46-B2
HP5_R	32-C4, 33-B2, 33-C4, 46-D4	KBC3_EXTSMI*	20-C2, 36-C4, 46-B2
IDE5_A0	19-C2, 35-C2, 35-C4, 46-C4	KBC3_EZ_BTN0*	36-B1, 36-C4, 37-C4, 46-A2
IDE5_A1	19-C2, 35-C2, 35-C4, 46-C4	KBC3_EZ_BTN1*	36-B1, 36-C4, 37-C3, 46-C2
IDE5_A2	19-C2, 35-C1, 35-C4, 46-C4	KBC3_EZ_BTN2*	36-B1, 36-C4, 37-C4, 46-C2
IDE5_CS1*	19-B2, 35-C2, 35-C4, 46-D4	KBC3_FANCTRL	34-A4, 36-B2, 46-B2
IDE5_CS3*	19-B2, 35-C1, 35-C4, 46-D4	KBC3_IMVP4_PWRGD	13-A1, 20-B2, 36-B2, 46-C2
IDE5_D(0)	19-B2, 35-C2, 35-C4, 46-D4	KBC3_LBLED*	36-C4, 37-B4, 46-B3
IDE5_D(0:15)	19-B2	KBC3_MD0	36-D1, 36-D4, 46-C4
IDE5_D(0:7)	35-C2, 35-C4	KBC3_MD1	36-D1, 36-D4, 46-C4
IDE5_D(1)	19-B2, 35-C2, 35-C4, 46-C4	KBC3_NUMLED*	36-C4, 37-C4, 46-B2
IDE5_D(10)	19-B2, 35-B4, 35-C1, 46-C3	KBC3_PWRGD	11-C4, 20-A2, 22-B3, 26-A4, 36-C4, 46-B3
IDE5_D(11)	19-B2, 35-B4, 35-C1, 46-C3	KBC3_PWRON	36-C4, 37-D2, 39-A4, 39-B2, 39-C2, 40-D2, 46-B3
IDE5_D(12)	19-B2, 35-B4, 35-C1, 46-C3	KBC3_PWRSW*	11-C4, 36-B2, 36-D2, 36-D4, 37-C3, 46-B3
IDE5_D(13)	19-B2, 35-B4, 35-C1, 46-C3	KBC3_QS*	36-C2, 41-B1, 41-B4, 46-C4
IDE5_D(14)	19-B2, 35-B4, 35-C1, 46-C3	KBC3_QSLIM	36-B4, 41-B4, 46-B3
IDE5_D(15)	19-B2, 35-B4, 35-C1, 46-B3	KBC3_RSMRST*	20-A2, 22-B3, 36-C2, 46-B2
IDE5_D(2)	19-B2, 35-C2, 35-C4, 46-C4	KBC3_RST*	36-D1, 36-D2, 36-D4, 40-C4, 46-C4
IDE5_D(3)	19-B2, 35-C2, 35-C4, 46-C4	KBC3_RUNSCI*	20-C2, 22-C1, 36-C4, 46-A2
IDE5_D(4)	19-B2, 35-C2, 35-C4, 46-C4	KBC3_RXD	36-A2, 36-D1, 46-C4
IDE5_D(5)	19-B2, 35-C2, 35-C4, 46-C4	KBC3_SCLED*	36-C4, 37-C4, 46-B3
IDE5_D(6)	19-B2, 35-C2, 35-C4, 46-C4	KBC3_SMCLK*	36-A2, 36-C1, 41-B1, 46-B3
IDE5_D(7)	19-B2, 35-C2, 35-C4, 46-C4	KBC3_SMDATA*	36-A2, 36-C1, 41-B1, 46-A2
IDE5_D(8)	19-B2, 35-B4, 35-C1, 46-C4	KBC3_SPKMUTE	33-C2, 36-B2, 46-A2
IDE5_D(8:15)	35-B4, 35-C1	KBC3_SUSPWR	36-B2, 37-D1, 38-B1, 39-C4, 40-C3, 43-B4, 46-B3
IDE5_D(9)	19-B2, 35-B4, 35-C1, 46-C4	KBC3_THERM_ALERT*	11-C4, 36-B4, 46-C2
IDE5_DACK*	19-B4, 35-C1, 35-C4, 46-B3	KBC3_THERM_SMCLK	11-C2, 36-A2, 46-C2
IDE5_DASP*	35-A2, 35-C2, 35-C4, 46-B3	KBC3_THERM_SMDATA	11-C2, 36-C2, 46-C2
IDE5_DREQ	19-B2, 35-C1, 35-C4, 46-C4	KBC3_THRM*	20-A4, 36-A2, 36-D1, 46-B3
IDE5_IOR*	19-B4, 35-C1, 35-C4, 46-C4	KBC3_VRON	36-B2, 43-B2, 46-C4
IDE5_IORDY	19-B4, 35-C2, 35-C4, 46-B3	KBC3_WAKESCI*	20-C2, 36-B2, 46-C2
IDE5_IOW*	19-B4, 35-C2, 35-C4, 46-C4	KBC3_WLANON	29-C4, 36-C4, 46-B3
IDE5_IRQ	35-B2, 35-C2, 35-C4, 46-C4	KBC5_3DON	32-A4, 36-C4, 46-C4
IMVP4_PWRGD	36-B2, 42-D4, 46-B3	KBC5_AVDD_ON	32-A1, 36-C4, 46-A2
KBC3_A20G	19-C2, 36-B2, 36-C1, 46-C4	KBC5_KSI(0)	36-B4, 37-D3, 46-B3
KBC3_BKLTON	24-A2, 36-C2, 46-B3		
KBC3_BRIT	24-B4, 36-C2, 46-C4		
KBC3_BRIT_DA	24-A4, 36-B2, 46-B2		
KBC3_CAPSLED*	36-C4, 37-C3, 46-A2		
KBC3_CHGEN	36-C2, 41-B4, 46-B3		

9 Schematic Diagram

Net Name	Sheet Number-Location	Net Name	Sheet Number-Location
KBC5_KSI(0:7)	36-B4, 37-D3	LCD2_VDDEN	13-A4, 13-B4, 24-C2, 46-B3
KBC5_KSI(1)	36-B4, 37-D3, 46-B3	LCD3_BKLTON	24-A1, 24-C4, 46-A3
KBC5_KSI(2)	36-B4, 37-D3, 46-B3	LCD3_BRIT	24-A3, 24-C4, 46-C4
KBC5_KSI(3)	36-B4, 37-D3, 46-B3	LCD3_DET0	20-B2, 24-C4, 46-C4
KBC5_KSI(4)	36-B4, 37-D3, 46-B3	LCD3_DET1	20-B2, 24-D4, 46-C4
KBC5_KSI(5)	36-B4, 37-D3, 46-B3	LED3_CHARGE	36-C2, 37-A2, 46-A3
KBC5_KSI(6)	36-B4, 37-D3, 46-B3	LED5_AMBER	37-A1, 37-A3, 46-B3
KBC5_KSI(7)	36-B4, 37-D3, 46-B3	LED5_GREEN	37-A3, 37-B1, 46-B3
KBC5_KSO(0)	36-B4, 37-C3, 46-B3	LID3_SWITCH*	33-B2, 36-B4, 46-B2
KBC5_KSO(0:15)	36-B4, 37-C3	LINE5_OUT_L	32-A3, 33-C4, 46-A3
KBC5_KSO(1)	36-B4, 37-C3, 46-B3	LINE5_OUT_R	32-A3, 33-D4, 46-A3
KBC5_KSO(10)	36-B4, 37-C3, 46-A2	LPC3_LAD(0)	19-D2, 25-C3, 34-D2, 36-C2, 46-A3
KBC5_KSO(11)	36-B4, 37-C3, 46-A2	LPC3_LAD(0:3)	19-D2, 25-C3, 34-D2, 36-C2
KBC5_KSO(12)	36-B4, 37-C3, 46-A2	LPC3_LAD(1)	19-D2, 25-C3, 34-D2, 36-C2, 46-A3
KBC5_KSO(13)	36-B4, 37-C3, 46-A2	LPC3_LAD(2)	19-D2, 25-C3, 34-D2, 36-C2, 46-A3
KBC5_KSO(14)	36-B4, 37-C3, 46-A2	LPC3_LAD(3)	19-D2, 25-C3, 34-D2, 36-C2, 46-A3
KBC5_KSO(15)	36-B4, 37-C3, 46-B2	LPC3_LFRAME*	19-D2, 25-C3, 34-C2, 36-C2, 46-B2
KBC5_KSO(2)	36-B4, 37-C3, 46-B3	LVDS1_A0-	13-B4, 24-C4
KBC5_KSO(3)	36-B4, 37-C3, 46-A3	LVDS1_A0+	13-B4, 24-C4
KBC5_KSO(4)	36-B4, 37-C3, 46-A3	LVDS1_A1-	13-B4, 24-C4
KBC5_KSO(5)	36-B4, 37-C3, 46-A3	LVDS1_A1+	13-B4, 24-C4
KBC5_KSO(6)	36-B4, 37-C3, 46-A3	LVDS1_A2-	13-B4, 24-C4
KBC5_KSO(7)	36-B4, 37-C3, 46-A3	LVDS1_A2+	13-B4, 24-C4
KBC5_KSO(8)	36-B4, 37-C3, 46-B3	LVDS1_ACLK-	13-B4, 24-C4
KBC5_KSO(9)	36-B4, 37-C3, 46-B3	LVDS1_ACLK+	13-B4, 24-C4
KBC5_TCLK	36-C4, 37-A3, 46-C4	MCH1_BSEL0	13-A3, 13-A3, 46-B3
KBC5_TDATA	36-C4, 37-A3, 46-B3	MCH1_HVREF	12-C1, 12-C2, 46-B3
LAN3_EECLK	30-B2, 31-A1, 31-A3, 46-B3	MCH1_HXSWING	12-A4, 12-C4, 46-B2
LAN3_EEDATA	30-B2, 31-A1, 31-B3, 46-A3	MCH1_HYSWING	12-A4, 12-B4, 46-B2
LAN3_LOWPWR	30-A4, 36-C4, 46-A3	MCH2_BMBUSY*	13-A1, 20-C2, 46-B2
LAN3_TRD0-	30-C1, 31-D4	MD3_VCC	26-B1, 28-C2, 46-C4
LAN3_TRD0+	30-C1, 31-D4	MDC3_AUDIN	33-A4, 34-C3, 46-B3
LAN3_TRD1-	30-C1, 31-C4	MEM1_ABS0*	14-C4, 17-C4, 18-C4
LAN3_TRD1+	30-C1, 31-C4	MEM1_ABS1*	14-C4, 17-C4, 18-C4
LAN3_TRD2-	30-C1, 31-C4	MEM1_ABS2*	14-C4, 17-C4, 18-C4
LAN3_TRD2+	30-C1, 31-C4		
LAN3_TRD3-	30-C1, 31-C4		
LAN3_TRD3+	30-C1, 31-C4		
LCD2_BKLTCTRL	13-A4, 13-B4, 24-A4, 46-B2		
LCD2_BKLTON	13-B4, 24-A2, 46-A3		

9 Schematic Diagram

Net Name	Sheet Number-Location	Net Name	Sheet Number-Location
MEM1_ACAS*	14-B4, 17-C4, 18-C4	MEM1_ADQ(36)	14-D4, 17-D4
MEM1_ADM(0)	14-C4, 17-B4	MEM1_ADQ(37)	14-D4, 17-D4
MEM1_ADM(1)	14-C4, 17-B4	MEM1_ADQ(38)	14-D4, 17-D4
MEM1_ADM(2)	14-C4, 17-B4	MEM1_ADQ(39)	14-D4, 17-D4
MEM1_ADM(3)	14-C4, 17-B4	MEM1_ADQ(4)	14-D4, 17-D4
MEM1_ADM(4)	14-C4, 17-B4	MEM1_ADQ(40)	14-D4, 17-D4
MEM1_ADM(5)	14-C4, 17-B4	MEM1_ADQ(41)	14-D4, 17-D4
MEM1_ADM(6)	14-C4, 17-B4	MEM1_ADQ(42)	14-D4, 17-D4
MEM1_ADM(7)	14-C4, 17-B4	MEM1_ADQ(43)	14-D4, 17-D4
MEM1_ADM(7:0)	14-C4, 17-B4	MEM1_ADQ(44)	14-D4, 17-D4
MEM1_ADQ(0)	14-D4, 17-D4	MEM1_ADQ(45)	14-D4, 17-D4
MEM1_ADQ(1)	14-D4, 17-D4	MEM1_ADQ(46)	14-D4, 17-D4
MEM1_ADQ(10)	14-D4, 17-D4	MEM1_ADQ(47)	14-D4, 17-D4
MEM1_ADQ(11)	14-D4, 17-D4	MEM1_ADQ(48)	14-D4, 17-D4
MEM1_ADQ(12)	14-D4, 17-D4	MEM1_ADQ(49)	14-D4, 17-D4
MEM1_ADQ(13)	14-D4, 17-D4	MEM1_ADQ(5)	14-D4, 17-D4
MEM1_ADQ(14)	14-D4, 17-D4	MEM1_ADQ(50)	14-D4, 17-D4
MEM1_ADQ(15)	14-D4, 17-D4	MEM1_ADQ(51)	14-D4, 17-D4
MEM1_ADQ(16)	14-D4, 17-D4	MEM1_ADQ(52)	14-D4, 17-D4
MEM1_ADQ(17)	14-D4, 17-D4	MEM1_ADQ(53)	14-D4, 17-D4
MEM1_ADQ(18)	14-D4, 17-D4	MEM1_ADQ(54)	14-D4, 17-D4
MEM1_ADQ(19)	14-D4, 17-D4	MEM1_ADQ(55)	14-D4, 17-D4
MEM1_ADQ(2)	14-D4, 17-D4	MEM1_ADQ(56)	14-D4, 17-D4
MEM1_ADQ(20)	14-D4, 17-D4	MEM1_ADQ(57)	14-D4, 17-D4
MEM1_ADQ(21)	14-D4, 17-D4	MEM1_ADQ(58)	14-D4, 17-D4
MEM1_ADQ(22)	14-D4, 17-D4	MEM1_ADQ(59)	14-D4, 17-D4
MEM1_ADQ(23)	14-D4, 17-D4	MEM1_ADQ(6)	14-D4, 17-D4
MEM1_ADQ(24)	14-D4, 17-D4	MEM1_ADQ(60)	14-D4, 17-D4
MEM1_ADQ(25)	14-D4, 17-D4	MEM1_ADQ(61)	14-D4, 17-D4
MEM1_ADQ(26)	14-D4, 17-D4	MEM1_ADQ(62)	14-D4, 17-D4
MEM1_ADQ(27)	14-D4, 17-D4	MEM1_ADQ(63)	14-D4, 17-D4
MEM1_ADQ(28)	14-D4, 17-D4	MEM1_ADQ(63:0)	14-D4, 17-D4
MEM1_ADQ(29)	14-D4, 17-D4	MEM1_ADQ(7)	14-D4, 17-D4
MEM1_ADQ(3)	14-D4, 17-D4	MEM1_ADQ(8)	14-D4, 17-D4
MEM1_ADQ(30)	14-D4, 17-D4	MEM1_ADQ(9)	14-D4, 17-D4
MEM1_ADQ(31)	14-D4, 17-D4	MEM1_ADQS(0)	14-C4, 17-B4
MEM1_ADQ(32)	14-D4, 17-D4	MEM1_ADQS(1)	14-C4, 17-B4
MEM1_ADQ(33)	14-D4, 17-D4	MEM1_ADQS(2)	14-C4, 17-B4
MEM1_ADQ(34)	14-D4, 17-D4	MEM1_ADQS(3)	14-C4, 17-B4
MEM1_ADQ(35)	14-D4, 17-D4	MEM1_ADQS(4)	14-C4, 17-B4

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Net Name	Sheet Number-Location	Net Name	Sheet Number-Location
MEM1_ADQS(5)	14-C4, 17-B4	MEM1_BDM(6)	14-C2, 17-B2
MEM1_ADQS(6)	14-C4, 17-B4	MEM1_BDM(7)	14-C2, 17-B2
MEM1_ADQS(7)	14-C4, 17-B4	MEM1_BDM(7:0)	14-C2, 17-B2
MEM1_ADQS(7:0)	14-C4, 17-B4	MEM1_BDQ(0)	14-A2, 17-D2
MEM1_ADQS*(0)	14-C4, 17-B4	MEM1_BDQ(1)	14-A2, 17-D2
MEM1_ADQS*(1)	14-C4, 17-B4	MEM1_BDQ(10)	14-A2, 17-D2
MEM1_ADQS*(2)	14-C4, 17-B4	MEM1_BDQ(11)	14-A2, 17-D2
MEM1_ADQS*(3)	14-C4, 17-B4	MEM1_BDQ(12)	14-A2, 17-D2
MEM1_ADQS*(4)	14-C4, 17-B4	MEM1_BDQ(13)	14-A2, 17-D2
MEM1_ADQS*(5)	14-C4, 17-B4	MEM1_BDQ(14)	14-A2, 17-D2
MEM1_ADQS*(6)	14-C4, 17-B4	MEM1_BDQ(15)	14-A2, 17-D2
MEM1_ADQS*(7)	14-C4, 17-B4	MEM1_BDQ(16)	14-A2, 17-D2
MEM1_ADQS*(7:0)	14-C4, 17-B4	MEM1_BDQ(17)	14-A2, 17-D2
MEM1_AMA(0)	14-B4, 17-D4, 18-D3	MEM1_BDQ(18)	14-A2, 17-D2
MEM1_AMA(1)	14-B4, 17-D4, 18-D3	MEM1_BDQ(19)	14-A2, 17-D2
MEM1_AMA(10)	14-B4, 17-D4, 18-D3	MEM1_BDQ(2)	14-A2, 17-D2
MEM1_AMA(11)	14-B4, 17-D4, 18-D3	MEM1_BDQ(20)	14-A2, 17-D2
MEM1_AMA(12)	14-B4, 17-D4, 18-D3	MEM1_BDQ(21)	14-A2, 17-D2
MEM1_AMA(13)	14-B4, 17-D4, 18-D3	MEM1_BDQ(22)	14-A2, 17-D2
MEM1_AMA(13:0)	14-B4, 17-D4, 18-D3	MEM1_BDQ(23)	14-A2, 17-D2
MEM1_AMA(2)	14-B4, 17-D4, 18-D3	MEM1_BDQ(24)	14-A2, 17-D2
MEM1_AMA(3)	14-B4, 17-D4, 18-D3	MEM1_BDQ(25)	14-A2, 17-D2
MEM1_AMA(4)	14-B4, 17-D4, 18-D3	MEM1_BDQ(26)	14-A2, 17-D2
MEM1_AMA(5)	14-B4, 17-D4, 18-D3	MEM1_BDQ(27)	14-A2, 17-D2
MEM1_AMA(6)	14-B4, 17-D4, 18-D3	MEM1_BDQ(28)	14-A2, 17-D2
MEM1_AMA(7)	14-B4, 17-D4, 18-D3	MEM1_BDQ(29)	14-A2, 17-D2
MEM1_AMA(8)	14-B4, 17-D4, 18-D3	MEM1_BDQ(3)	14-A2, 17-D2
MEM1_AMA(9)	14-B4, 17-D4, 18-D3	MEM1_BDQ(30)	14-A2, 17-D2
MEM1_ARAS*	14-B4, 17-C4, 18-C4	MEM1_BDQ(31)	14-A2, 17-D2
MEM1_AWE*	14-B4, 17-C4, 18-C4	MEM1_BDQ(32)	14-A2, 17-D2
MEM1_BBS0*	14-C2, 17-C2, 18-C4	MEM1_BDQ(33)	14-A2, 17-D2
MEM1_BBS1*	14-C2, 17-C2, 18-C4	MEM1_BDQ(34)	14-A2, 17-D2
MEM1_BBS2*	14-C2, 17-C2, 18-C4	MEM1_BDQ(35)	14-A2, 17-D2
MEM1_BCAS*	14-B2, 17-C2, 18-C4	MEM1_BDQ(36)	14-A2, 17-D2
MEM1_BDM(0)	14-C2, 17-B2	MEM1_BDQ(37)	14-A2, 17-D2
MEM1_BDM(1)	14-C2, 17-B2	MEM1_BDQ(38)	14-A2, 17-D2
MEM1_BDM(2)	14-C2, 17-B2	MEM1_BDQ(39)	14-A2, 17-D2
MEM1_BDM(3)	14-C2, 17-B2	MEM1_BDQ(4)	14-A2, 17-D2
MEM1_BDM(4)	14-C2, 17-B2	MEM1_BDQ(40)	14-A2, 17-D2
MEM1_BDM(5)	14-C2, 17-B2	MEM1_BDQ(41)	14-A2, 17-D2

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Net Name	Sheet Number-Location	Net Name	Sheet Number-Location
MEM1_BDQ(42)	14-A2, 17-D2	MEM1_BDQS*(3)	14-C2, 17-B2
MEM1_BDQ(43)	14-A2, 17-D2	MEM1_BDQS*(4)	14-C2, 17-B2
MEM1_BDQ(44)	14-A2, 17-D2	MEM1_BDQS*(5)	14-C2, 17-B2
MEM1_BDQ(45)	14-A2, 17-D2	MEM1_BDQS*(6)	14-C2, 17-B2
MEM1_BDQ(46)	14-A2, 17-D2	MEM1_BDQS*(7)	14-C2, 17-B2
MEM1_BDQ(47)	14-A2, 17-D2	MEM1_BDQS*(7:0)	14-C2, 17-B2
MEM1_BDQ(48)	14-A2, 17-D2	MEM1_BMA(0)	14-B2, 17-D2, 18-C3
MEM1_BDQ(49)	14-A2, 17-D2	MEM1_BMA(1)	14-B2, 17-D2, 18-C3
MEM1_BDQ(5)	14-A2, 17-D2	MEM1_BMA(10)	14-B2, 17-D2, 18-C3
MEM1_BDQ(50)	14-A2, 17-D2	MEM1_BMA(11)	14-B2, 17-D2, 18-C3
MEM1_BDQ(51)	14-A2, 17-D2	MEM1_BMA(12)	14-B2, 17-D2, 18-C3
MEM1_BDQ(52)	14-A2, 17-D2	MEM1_BMA(13)	14-B2, 17-D2, 18-C3
MEM1_BDQ(53)	14-A2, 17-D2	MEM1_BMA(13:0)	14-B2, 17-D2, 18-C3
MEM1_BDQ(54)	14-A2, 17-D2	MEM1_BMA(2)	14-B2, 17-D2, 18-C3
MEM1_BDQ(55)	14-A2, 17-D2	MEM1_BMA(3)	14-B2, 17-D2, 18-C3
MEM1_BDQ(56)	14-A2, 17-D2	MEM1_BMA(4)	14-B2, 17-D2, 18-C3
MEM1_BDQ(57)	14-A2, 17-D2	MEM1_BMA(5)	14-B2, 17-D2, 18-C3
MEM1_BDQ(58)	14-A2, 17-D2	MEM1_BMA(6)	14-B2, 17-D2, 18-C3
MEM1_BDQ(59)	14-A2, 17-D2	MEM1_BMA(7)	14-B2, 17-D2, 18-C3
MEM1_BDQ(6)	14-A2, 17-D2	MEM1_BMA(8)	14-B2, 17-D2, 18-C3
MEM1_BDQ(60)	14-A2, 17-D2	MEM1_BMA(9)	14-B2, 17-D2, 18-C3
MEM1_BDQ(61)	14-A2, 17-D2	MEM1_BRAS*	14-B2, 17-C2, 18-B4
MEM1_BDQ(62)	14-A2, 17-D2	MEM1_BWE*	14-B2, 17-C2, 18-B4
MEM1_BDQ(63)	14-A2, 17-D2	MEM1_CKE0	13-B1, 17-C4, 18-C4
MEM1_BDQ(63:0)	14-A2, 17-D2	MEM1_CKE1	13-B1, 17-C4, 18-C4
MEM1_BDQ(7)	14-A2, 17-D2	MEM1_CKE2	13-B1, 17-C2, 18-C4
MEM1_BDQ(8)	14-A2, 17-D2	MEM1_CKE3	13-B1, 17-C2, 18-C4
MEM1_BDQ(9)	14-A2, 17-D2	MEM1_CS0*	13-B1, 17-C4, 18-D4
MEM1_BDQS(0)	14-C2, 17-B2	MEM1_CS1*	13-B1, 17-C4, 18-D4
MEM1_BDQS(1)	14-C2, 17-B2	MEM1_CS2*	13-B1, 17-C2, 18-D4
MEM1_BDQS(2)	14-C2, 17-B2	MEM1_CS3*	13-B1, 17-C2, 18-C4
MEM1_BDQS(3)	14-C2, 17-B2	MEM1_ODT0	13-A1, 17-B4, 18-C4
MEM1_BDQS(4)	14-C2, 17-B2	MEM1_ODT1	13-A1, 17-B4, 18-C4
MEM1_BDQS(5)	14-C2, 17-B2	MEM1_ODT2	13-A1, 17-B2, 18-C4
MEM1_BDQS(6)	14-C2, 17-B2	MEM1_ODT3	13-A1, 17-B2, 18-C4
MEM1_BDQS(7)	14-C2, 17-B2	MEM3_OVERT*	11-C2, 36-B4, 46-B3
MEM1_BDQS(7:0)	14-C2, 17-B2	PCI3_AD(0)	20-D4, 26-C4, 29-D4, 30-C4, 46-C4
MEM1_BDQS*(0)	14-C2, 17-B2	PCI3_AD(1)	20-D4, 26-C4, 29-D4, 30-C4, 46-C4
MEM1_BDQS*(1)	14-C2, 17-B2		
MEM1_BDQS*(2)	14-C2, 17-B2		

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Net Name	Sheet Number-Location	Net Name	Sheet Number-Location
PCI3_AD(10)	20-D4, 26-C4, 29-D4, 30-C4, 46-B3	PCI3_AD(29)	20-D4, 26-C4, 29-D4, 30-C4, 46-D2
PCI3_AD(11)	20-D4, 26-C4, 29-D4, 30-C4, 46-B3	PCI3_AD(3)	20-D4, 26-C4, 29-D4, 30-C4, 46-C4
PCI3_AD(12)	20-D4, 26-C4, 29-D4, 30-C4, 46-B3	PCI3_AD(30)	20-D4, 26-C4, 29-D4, 30-C4, 46-D2
PCI3_AD(13)	20-D4, 26-C4, 29-D4, 30-C4, 46-B3	PCI3_AD(31)	20-D4, 26-C4, 29-D4, 30-C4, 46-D2
PCI3_AD(14)	20-D4, 26-C4, 29-D4, 30-C4, 46-B3	PCI3_AD(31:0)	20-D4, 26-C4, 29-D4, 30-C4
PCI3_AD(15)	20-D4, 26-C4, 29-D4, 30-C4, 46-B3	PCI3_AD(4)	20-D4, 26-C4, 29-D4, 30-C4, 46-C4
PCI3_AD(16)	20-D4, 26-C4, 29-D4, 30-C4, 46-B3	PCI3_AD(5)	20-D4, 26-C4, 29-D4, 30-C4, 46-C4
PCI3_AD(17)	20-D4, 26-C4, 29-D4, 30-C4, 46-B3	PCI3_AD(6)	20-D4, 26-C4, 29-D4, 30-C4, 46-B4
PCI3_AD(18)	20-D4, 26-C4, 29-D4, 30-C4, 46-D2	PCI3_AD(7)	20-D4, 26-C4, 29-D4, 30-C4, 46-B4
PCI3_AD(19)	20-D4, 26-C4, 29-D4, 30-C4, 46-D2	PCI3_AD(8)	20-D4, 26-C4, 29-D4, 30-C4, 46-B4
PCI3_AD(2)	20-D4, 26-C4, 29-D4, 30-C4, 46-C4	PCI3_AD(9)	20-D4, 26-C4, 29-D4, 30-C4, 46-B4
PCI3_AD(20)	20-D4, 26-C4, 29-D4, 30-C4, 46-D2	PCI3_CBE0*	20-C3, 26-B4, 29-B2, 30-B4, 46-B4
PCI3_AD(21)	20-D4, 26-C4, 29-D4, 30-B4, 30-C4, 46-D2	PCI3_CBE1*	20-C3, 26-B4, 29-B4, 30-B4, 46-B4
PCI3_AD(22)	20-D4, 26-C4, 29-D4, 30-C4, 46-D2	PCI3_CBE2*	20-C3, 26-B4, 29-B4, 30-B4, 46-B4
PCI3_AD(23)	20-D4, 26-C4, 29-C2, 29-D4, 30-C4, 46-D2	PCI3_CBE3*	20-C3, 26-B4, 29-C4, 30-B4, 46-B4
PCI3_AD(24)	20-D4, 26-C4, 29-D4, 30-C4, 46-D2	PCI3_CLKRUN*	20-B2, 22-D1, 26-A4, 29-B4, 30-A4, 34-C2, 36-B2, 46-B2
PCI3_AD(25)	20-D4, 26-B4, 26-C4, 29-D4, 30-C4, 46-D2	PCI3_DEVSEL*	20-C3, 22-C1, 26-A4, 29-B2, 30-B4, 46-B2
PCI3_AD(26)	20-D4, 26-C4, 29-D4, 30-C4, 46-D2	PCI3_FRAME*	20-B4, 22-C1, 26-B4, 29-B2, 30-B4, 46-C2
PCI3_AD(27)	20-D4, 26-C4, 29-D4, 30-C4, 46-D2	PCI3_GNT0*	20-D3, 26-B4, 46-B4
PCI3_AD(28)	20-D4, 26-C4, 29-D4, 30-C4, 46-D2	PCI3_GNT1*	20-D2, 29-C2, 46-B4
		PCI3_GNT2*	20-D3, 22-B1, 30-B4, 46-B4
		PCI3_GNT3*	20-D3, 22-B1, 46-B4
		PCI3_INTA*	20-B4, 22-C1, 26-C3, 46-B4

9 Schematic Diagram

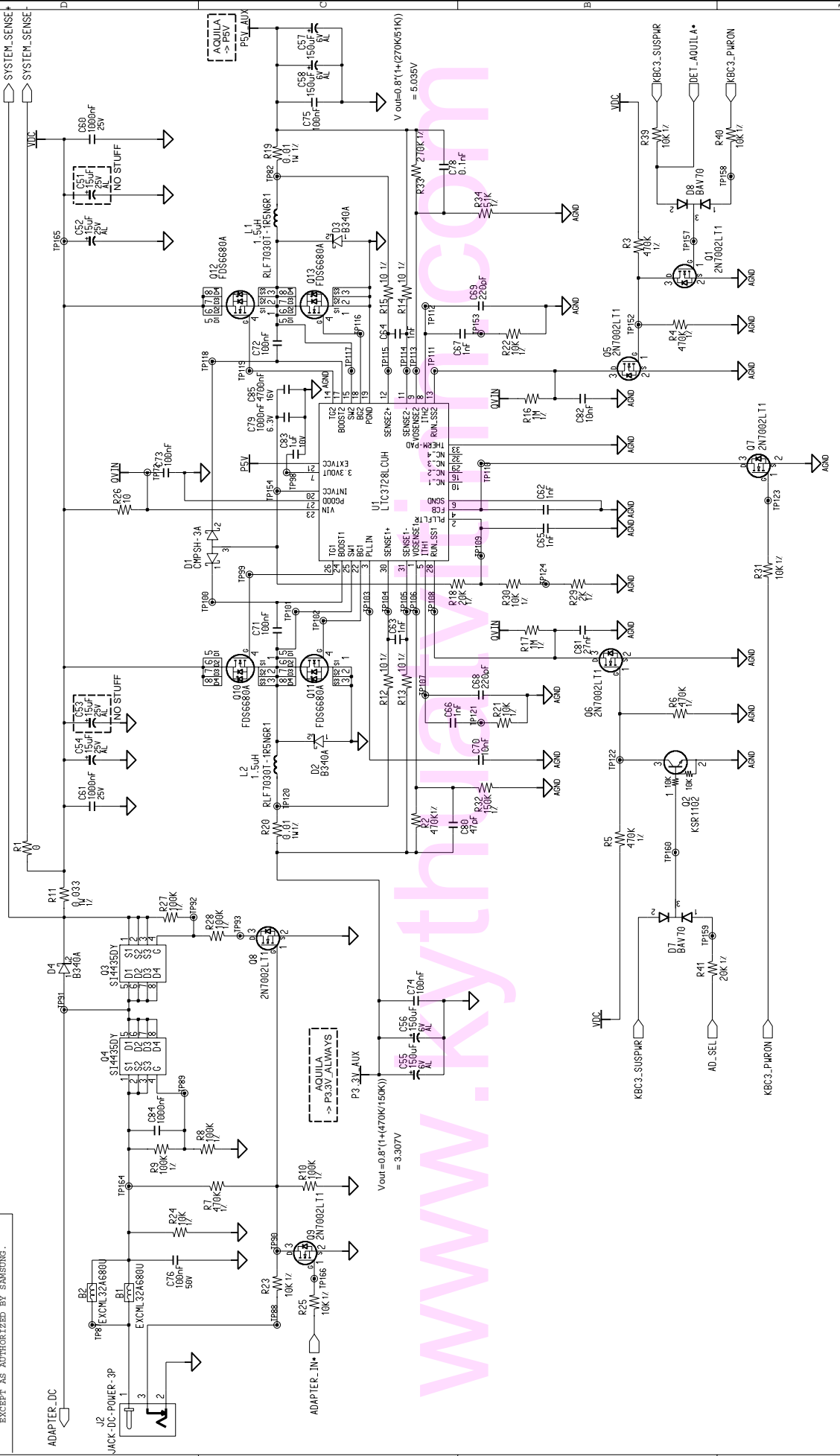
Net Name	Sheet Number-Location	Net Name	Sheet Number-Location
PCI3_INTB*	20-B4, 22-C1, 26-B3, 46-B4	SATA3_TP	19-B4, 35-B4
PCI3_INTC*	20-B4, 22-C1, 26-B3, 46-B4	SMB3_ALERT*	20-C2, 22-C3, 46-C2
PCI3_INTD*	20-B4, 22-C1, 46-B4	SMB3_CLK	7-A4, 16-B2, 20-D2, 22-D3, 46-C4
PCI3_INTE*	20-B3, 22-C1, 29-C2, 46-B4	SMB3_DATA	7-A4, 16-B2, 20-D2, 22-D3, 46-A4
PCI3_INTF*	20-B3, 22-C1, 29-C4, 46-B4	SMB3_LINKALERT*	20-D2, 22-C3, 46-C2
PCI3_INTG*	20-B3, 22-C1, 30-B4, 46-B4	SPDIF5_ON*	33-A1, 36-C2, 46-A4
PCI3_INTH*	20-B3, 22-C1, 46-B4	SPDIF5_OUT	32-C4, 33-B2, 46-A4
PCI3_IRDY*	20-C3, 22-C1, 26-B4, 29-B4, 30-B4, 46-B4	SPROM3_DIN	30-B2, 31-A3, 46-A4
PCI3_PAR	20-C3, 22-D1, 26-B4, 29-B2, 30-B4, 46-C4	SPROM3_DOUT	30-B2, 31-A3, 46-C2
PCI3_PERR*	20-C3, 22-C1, 26-A4, 29-B4, 30-B4, 46-B4	SYSTEM_SENSE-	37-D1, 41-C4, 46-D2
PCI3_PLOCK*	20-C3, 22-C1, 46-C2	SYSTEM_SENSE+	37-D2, 41-C4, 46-D2
PCI3_REQ0*	20-D3, 22-C1, 26-B4, 46-B4	TPM3_PACCESS	34-C1, 36-B4, 46-B2
PCI3_REQ1*	20-D3, 22-C1, 29-C4, 46-B4	USB3_P0-	20-A1, 34-B2
PCI3_REQ2*	20-D3, 22-C1, 30-B4, 46-B4	USB3_P0+	20-A1, 34-B2
PCI3_REQ3*	20-D3, 22-C1, 46-B4	USB3_P1-	20-A1, 37-C2
PCI3_RST*	20-C3, 26-A4, 29-C2, 30-B4, 46-A4	USB3_P1+	20-A1, 37-C2
PCI3_SERR*	20-C3, 22-C1, 26-A4, 29-B4, 30-B4, 46-B4	USB3_P2-	20-A1, 34-B2
PCI3_STOP*	20-C3, 22-C1, 26-A4, 29-B2, 30-B4, 46-B4	USB3_P2+	20-A1, 34-B2
PCI3_TRDY*	20-C3, 22-C1, 26-B4, 29-B2, 30-B4, 46-A4	USB3_P3-	20-A1, 37-A4
PCIE3_WAKE*	20-B2, 22-C3, 46-C2	USB3_P3+	20-A1, 37-A4
PLT3_RST*	20-A3, 20-C3, 46-A4	VGA2_DDCCLK	13-C4, 23-A4, 46-C2
PLT3_RSTF*	13-A1, 20-A2, 20-A3, 25-C4, 34-C2, 35-B4, 35-C2, 36-C2, 46-A4	VGA2_DDCDATA	13-C4, 23-A4, 46-B2
QUICK3_BOOT*	36-B1, 36-B2, 46-B2	VGA3_BLUE	13-C4, 23-C4, 46-A4
S1-	42-C4, 42-D1, 46-D4	VGA3_C	13-D4, 37-C1, 46-D4
S1+	42-B4, 42-C4, 42-D1, 46-D4	VGA3_COMP	13-D4, 46-A4
S2-	42-B1, 42-B4, 46-D4	VGA3_GREEN	13-C4, 23-C4, 46-A4
S2+	42-B1, 42-B4, 42-C4, 46-D4	VGA3_HSYNC	13-C4, 23-B4, 46-A4
SATA3_DET*	20-C2, 35-B4, 46-A4	VGA3_RED	13-C4, 23-C4, 46-B4
SATA3_RN	19-C4, 35-B4	VGA3_VSYNC	13-C4, 23-B4, 46-A4
SATA3_RP	19-B4, 35-B4	VGA3_Y	13-D4, 37-C2, 46-D4
SATA3_TN	19-B4, 35-B4	VOA-	42-A2, 42-C4
		VOA+	42-B4, 42-C4
		VOS-	42-C1, 42-C4, 46-D4
		VREF3_OUT_MIC	32-C2, 33-B2, 46-D2
		VTTTPWRGD	42-A4, 42-D4, 43-D2, 46-B4

9 Schematic Diagram

9-2 DCDC BOARD

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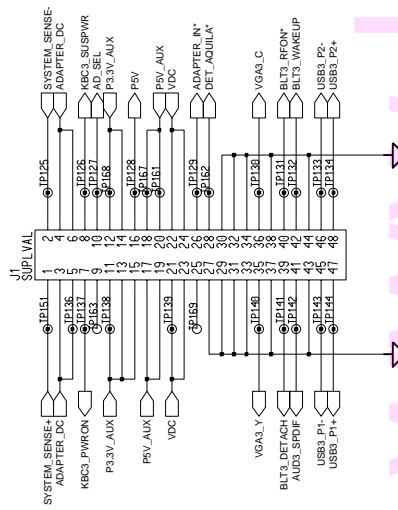


DRWN	DATE	TITLE
SEO, HJ	02/08/2003	AQUILA-C
HA, JS	REV: 310*	DC-DC BOARD
LEE, CH	REV: 10	3.3V & 5V GENERATION
MODULE CODE	LAST EDIT	

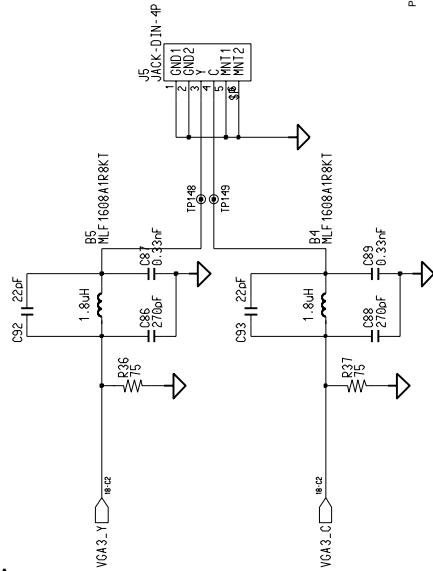
COMP-22C-015(1996.6.5)-REV. 3	2	3	4
<p>ADAPTER_DC</p> <p>J2 JACK-DC-POWER-3P</p> <p>ADAPTER-IN</p> <p>P3_3V_AUX</p> <p>Vout=0.8*(1+470K/150K) = 3.307V</p> <p>Vout=0.8*(1+270K/61K) = 5.039V</p>	<p>Q1 CHPSH-3A</p> <p>Q2 B340A</p> <p>Q3 B340A</p> <p>Q4 S14435DY</p> <p>Q5 S14435DY</p> <p>Q6 S14435DY</p> <p>Q7 S14435DY</p> <p>Q8 2N7002L11</p> <p>Q9 2N7002L11</p> <p>Q10 2N7002L11</p> <p>Q11 2N7002L11</p> <p>Q12 2N7002L11</p> <p>Q13 2N7002L11</p> <p>Q14 2N7002L11</p> <p>Q15 2N7002L11</p> <p>Q16 2N7002L11</p> <p>Q17 2N7002L11</p> <p>Q18 2N7002L11</p> <p>Q19 2N7002L11</p> <p>Q20 2N7002L11</p> <p>Q21 2N7002L11</p> <p>Q22 2N7002L11</p> <p>Q23 2N7002L11</p> <p>Q24 2N7002L11</p> <p>Q25 2N7002L11</p> <p>Q26 2N7002L11</p> <p>Q27 2N7002L11</p> <p>Q28 2N7002L11</p> <p>Q29 2N7002L11</p> <p>Q30 2N7002L11</p> <p>Q31 2N7002L11</p> <p>Q32 2N7002L11</p> <p>Q33 2N7002L11</p> <p>Q34 2N7002L11</p> <p>Q35 2N7002L11</p> <p>Q36 2N7002L11</p> <p>Q37 2N7002L11</p> <p>Q38 2N7002L11</p> <p>Q39 2N7002L11</p> <p>Q40 2N7002L11</p>	<p>R1 0.000</p> <p>R2 470K</p> <p>R3 470K</p> <p>R4 470K</p> <p>R5 470K</p> <p>R6 470K</p> <p>R7 470K</p> <p>R8 470K</p> <p>R9 100K</p> <p>R10 100K</p> <p>R11 0.000</p> <p>R12 0.000</p> <p>R13 10.0V</p> <p>R14 10.0V</p> <p>R15 10.0V</p> <p>R16 10.0V</p> <p>R17 10.0V</p> <p>R18 10.0V</p> <p>R19 10.0V</p> <p>R20 10.0V</p> <p>R21 10.0V</p> <p>R22 10.0V</p> <p>R23 10.0V</p> <p>R24 10.0V</p> <p>R25 10.0V</p> <p>R26 10.0V</p> <p>R27 10.0V</p> <p>R28 10.0V</p> <p>R29 10.0V</p> <p>R30 10.0V</p> <p>R31 10.0V</p> <p>R32 10.0V</p> <p>R33 10.0V</p> <p>R34 10.0V</p> <p>R35 10.0V</p> <p>R36 10.0V</p> <p>R37 10.0V</p> <p>R38 10.0V</p> <p>R39 10.0V</p> <p>R40 10.0V</p>	<p>C1 1000nF</p> <p>C2 1000nF</p> <p>C3 1000nF</p> <p>C4 1000nF</p> <p>C5 1000nF</p> <p>C6 1000nF</p> <p>C7 1000nF</p> <p>C8 1000nF</p> <p>C9 1000nF</p> <p>C10 1000nF</p> <p>C11 1000nF</p> <p>C12 1000nF</p> <p>C13 1000nF</p> <p>C14 1000nF</p> <p>C15 1000nF</p> <p>C16 1000nF</p> <p>C17 1000nF</p> <p>C18 1000nF</p> <p>C19 1000nF</p> <p>C20 1000nF</p> <p>C21 1000nF</p> <p>C22 1000nF</p> <p>C23 1000nF</p> <p>C24 1000nF</p> <p>C25 1000nF</p> <p>C26 1000nF</p> <p>C27 1000nF</p> <p>C28 1000nF</p> <p>C29 1000nF</p> <p>C30 1000nF</p> <p>C31 1000nF</p> <p>C32 1000nF</p> <p>C33 1000nF</p> <p>C34 1000nF</p> <p>C35 1000nF</p> <p>C36 1000nF</p> <p>C37 1000nF</p> <p>C38 1000nF</p> <p>C39 1000nF</p> <p>C40 1000nF</p>

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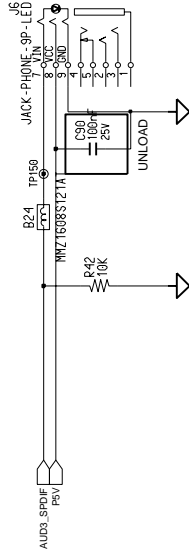
**CHANGE THIS CONNECTOR TO HEADER < 12809A-48G5T>
 FROM DCDC TO MAIN BOARD**



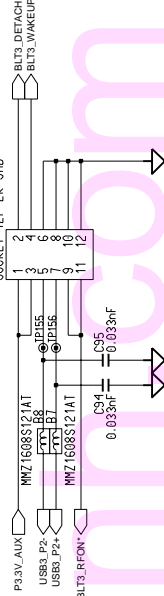
SUPER - VHS TV OUT



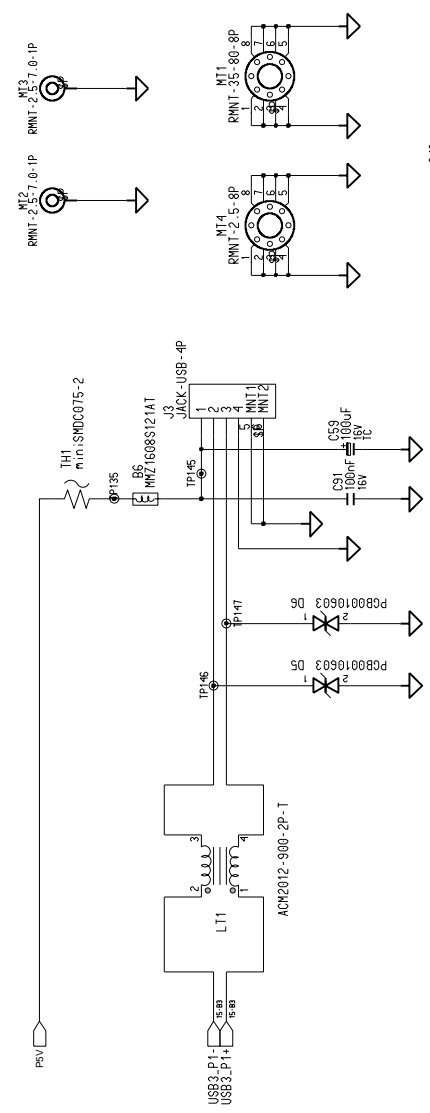
SPDIF OUT



bluetooth connector



USB
 90ohm +/- 10%

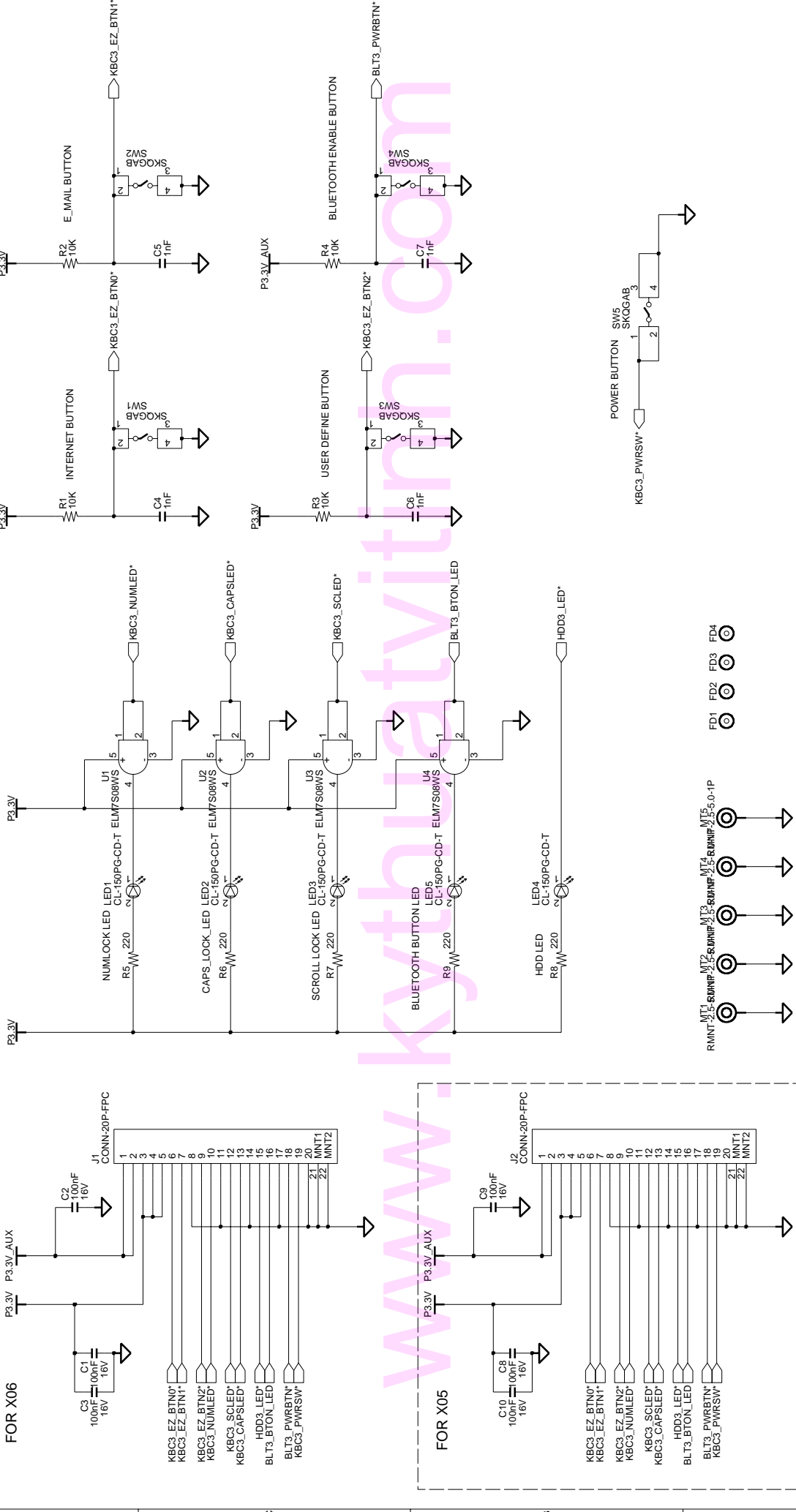


9 Schematic Diagram

9-3 ONTOP BOARD

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DRAW	GUO, MICKY	DATE	5/25/2005	TITLE	AGUILA-SO
CHECK	ZHOU, PHIL	DEV. STEP	MP	ONTOP PCB	ONTOP PCB
APPROVAL	CHEN, ANTONIO	REV	1.0	ONTOP MAIN	ONTOP MAIN
MODULE CODE		LAST EDIT			
			May, 25, 2005, 10:09:07 AM	PAGE	1 OF 1

DRAW	GUO, MICKY	DATE	5/25/2005	TITLE	AGUILA-SO
CHECK	ZHOU, PHIL	DEV. STEP	MP	ONTOP PCB	ONTOP PCB
APPROVAL	CHEN, ANTONIO	REV	1.0	ONTOP MAIN	ONTOP MAIN
MODULE CODE		LAST EDIT			
			May, 25, 2005, 10:09:07 AM	PAGE	1 OF 1