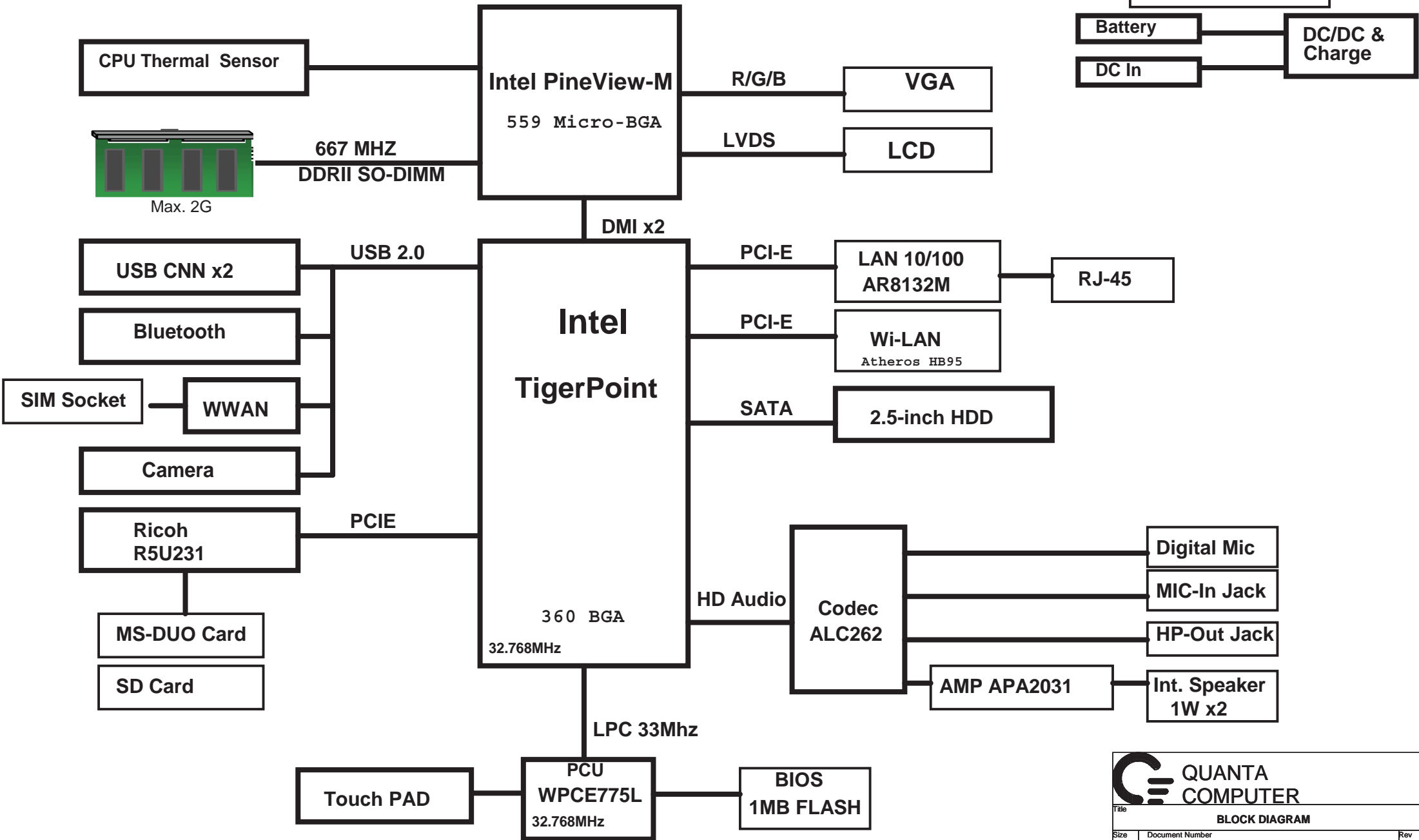


SY3 BLOCK DIAGRAM




QUANTA COMPUTER

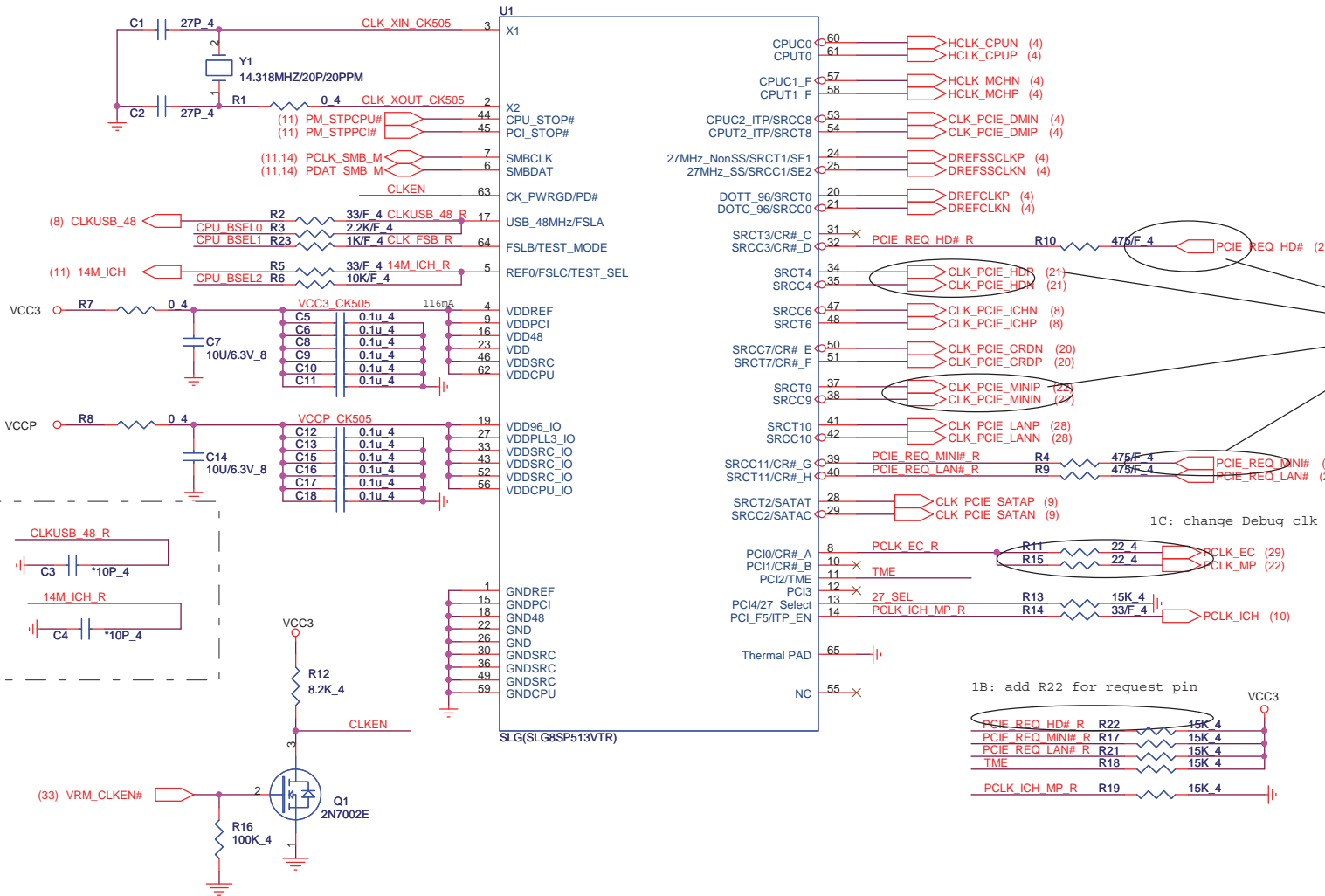
Title: **BLOCK DIAGRAM**

Size: Custom | Document Number: **SY3 MB** | Rev: 1G

Date: **Tuesday, March 31, 2009** | Sheet: 1 of 44

1.Level 1 Environment-related Substances Should NEVER be Used.
 2.Purchase ink, paint, wire rods, and Molding resins only from the business Partners that Sony approves as Green Partners.

 QUANTA COMPUTER		
Title <p style="text-align: center;">Front Page</p>		
Size A	Document Number <p style="text-align: center;">SY3 MB</p>	Rev 1G
Date: Monday, March 30, 2009 Sheet 2 of 44		



1B: re-arrange clock for HD decoder

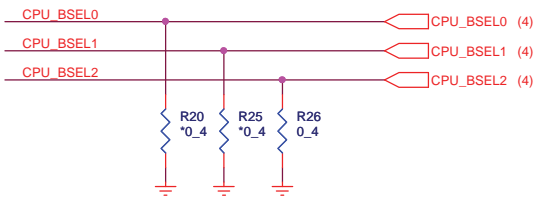
1C: change Debug clk to same with EC(for layout)

1B: add R22 for request pin

27 Select PIN13	PIN 20/21	PIN 24/25
* 0	DOT_96 / DOT_96#	LCDCCLK / LCDCCLK#
1	SRC_0 / SRC_0#	27M / 27M_SS

ITP_EN (PIN14)	PIN53/54
* 0	SRC8#/SRC8
1	ITP/ITP#

FSC BSEL2	FSB BSEL1	FSA BSEL0	CPU	SRC	PCI	REF	USB	DOT	Spread %
0	0	0	266.66	100	33.33	14.318	48	96	0.5 Down
0	0	1	133.33	100	33.33	14.318	48	96	0.5 Down
0	1	0	200.00	100	33.33	14.318	48	96	0.5 Down
0	1	1	166.66	100	33.33	14.318	48	96	0.5 Down
1	0	0	333.33	100	33.33	14.318	48	96	0.5 Down
1	0	1	100.00	100	33.33	14.318	48	96	0.5 Down
1	1	0	400.00	100	33.33	14.318	48	96	0.5 Down
1	1	1							RESERVED



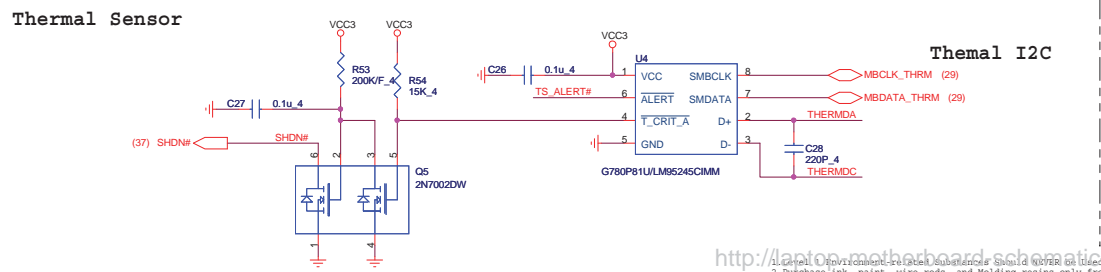
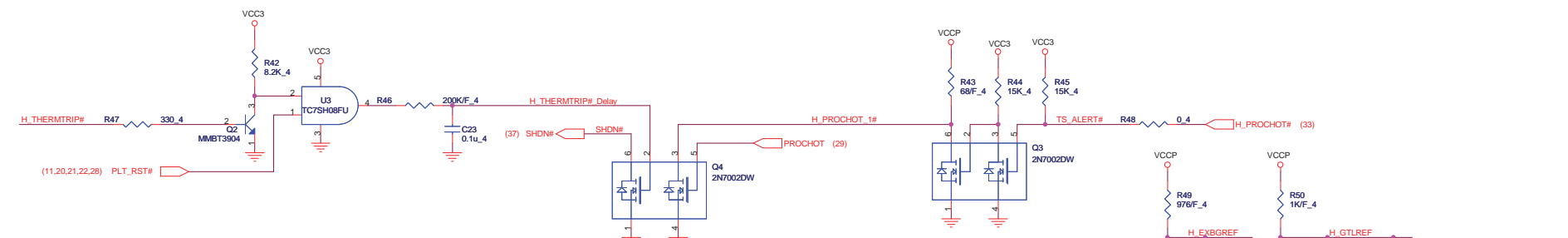
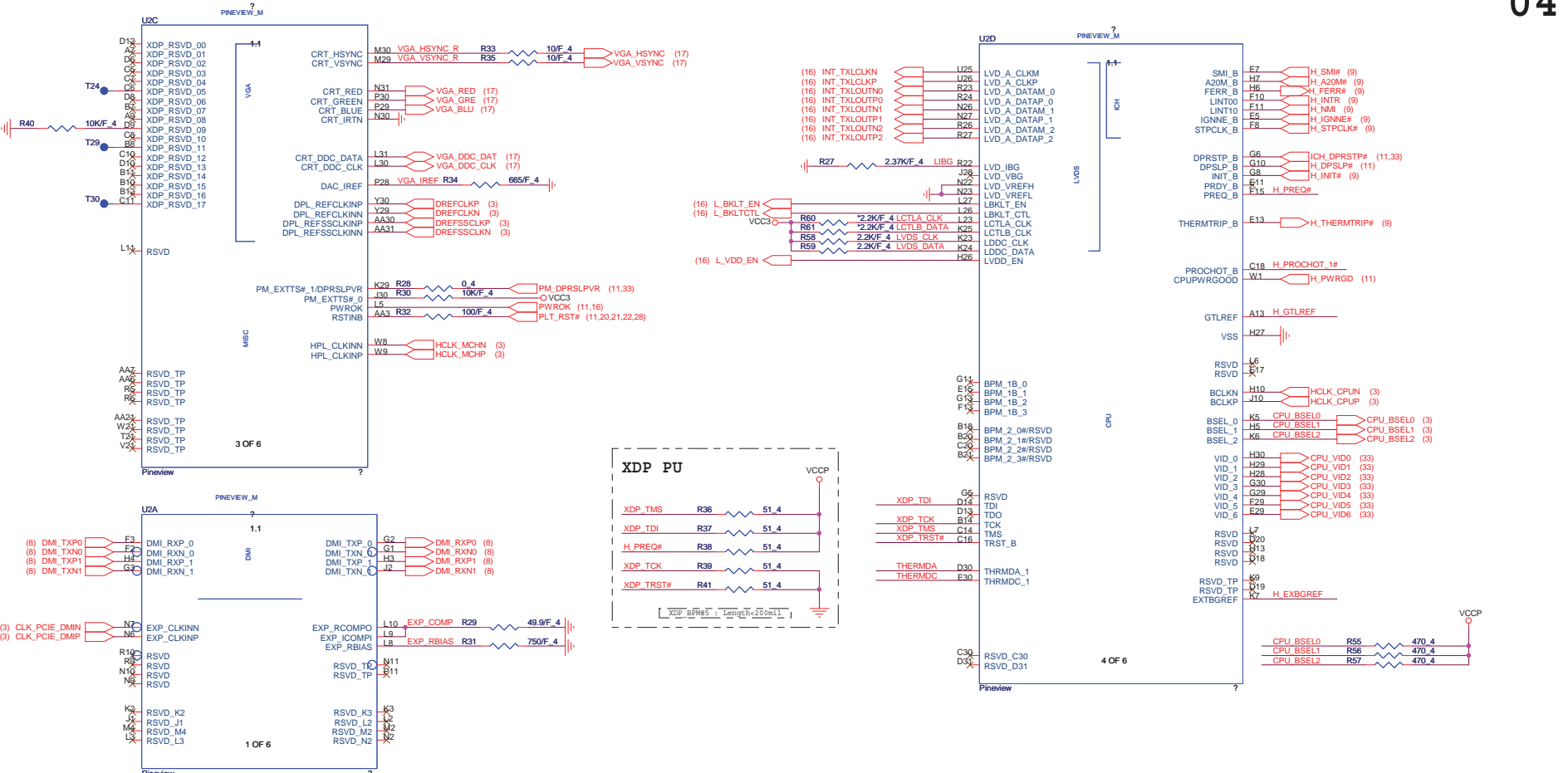
1. Level 1 Environment-related Substances Should NEVER be Used.
 2. Purchase ink, paint, wire rods, and Molding resins only from the business Partners that Sony approves as Green Partners.

QUANTA COMPUTER

Title: **Clock Gen SLG8SP513VTR**

Size B Document Number: **SY3 MB** Rev 1G

Date: **Tuesday, March 31, 2009** Sheet 3 of 44

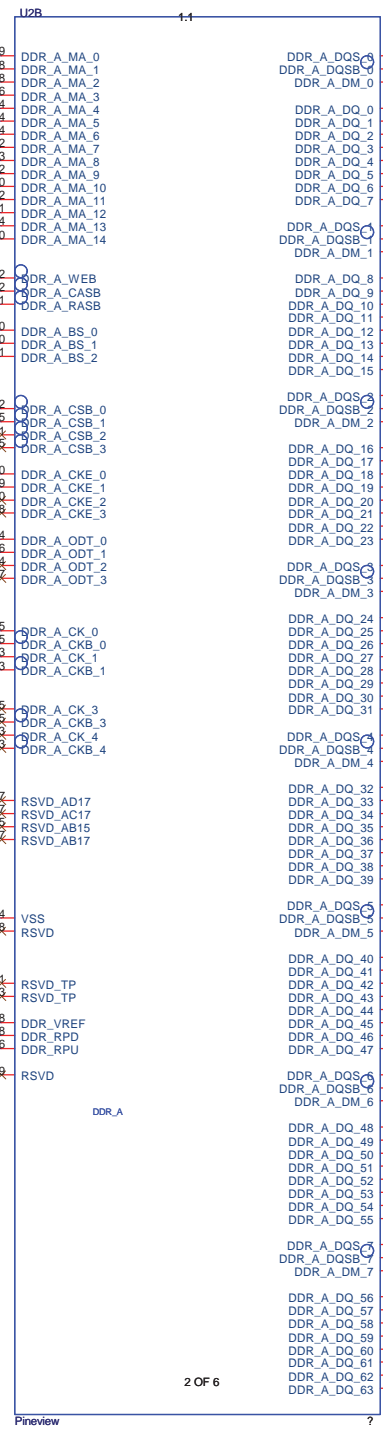


QUANTA COMPUTER

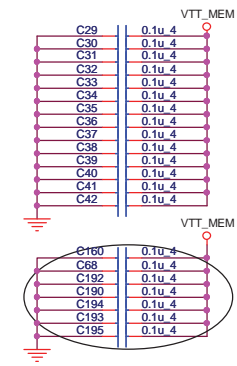
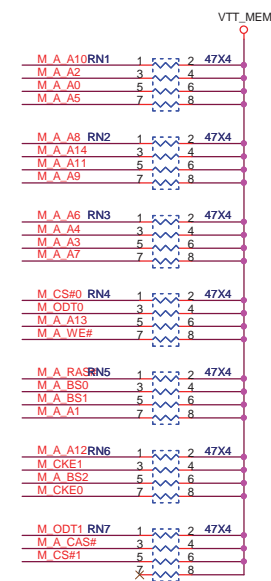
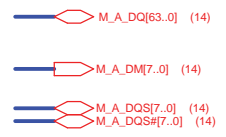
Title: **Pineview DMI/Display**

Size: Custom Document Number: **SY3 MB** Rev: 1G

Date: Tuesday, March 31, 2009 Sheet: 4 of 44

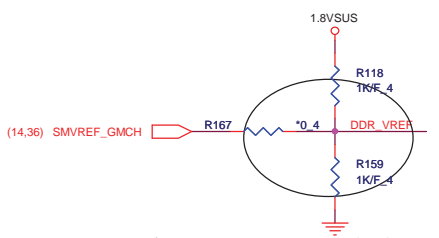
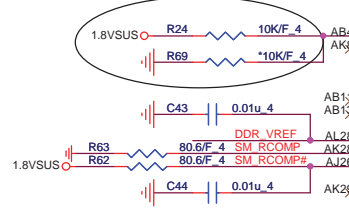


- AD3 M_A DQS0
- AD2 M_A DQS#0
- AD4 M_A DM0
- AC4 M_A DQ0
- AC1 M_A DQ1
- AF4 M_A DQ2
- AG2 M_A DQ3
- AB2 M_A DQ4
- AB3 M_A DQ5
- AE2 M_A DQ6
- AE3 M_A DQ7
- AB8 M_A DQS1
- AD7 M_A DQS#1
- AA9 M_A DM1
- AB6 M_A DQ8
- AB7 M_A DQ9
- AE5 M_A DQ10
- AG5 M_A DQ11
- AA5 M_A DQ12
- AB5 M_A DQ13
- AB9 M_A DQ14
- AD6 M_A DQ15
- AD8 M_A DQS2
- AD10 M_A DQS#2
- AE8 M_A DM2
- AG8 M_A DQ16
- AG7 M_A DQ17
- AF10 M_A DQ18
- AG11 M_A DQ19
- AE7 M_A DQ20
- AF8 M_A DQ21
- AD11 M_A DQ22
- AE10 M_A DQ23
- AK5 M_A DQS3
- AK3 M_A DQS#3
- AJ3 M_A DM3
- AH1 M_A DQ24
- AJ2 M_A DQ25
- AK6 M_A DQ26
- AJ7 M_A DQ27
- AF3 M_A DQ28
- AH2 M_A DQ29
- AL5 M_A DQ30
- AJ6 M_A DQ31
- AG22 M_A DQS4
- AG21 M_A DQS#4
- AD19 M_A DM4
- AE19 M_A DQ32
- AC19 M_A DQ33
- AF22 M_A DQ34
- AD22 M_A DQ35
- AG17 M_A DQ36
- AF19 M_A DQ37
- AE21 M_A DQ38
- AD21 M_A DQ39
- AE26 M_A DQS5
- AG27 M_A DQS#5
- AJ27 M_A DM5
- AE24 M_A DQ40
- AG25 M_A DQ41
- AD25 M_A DQ42
- AD24 M_A DQ43
- AC22 M_A DQ44
- AC24 M_A DQ45
- AD27 M_A DQ46
- AE27 M_A DQ47
- AE30 M_A DQS6
- AE29 M_A DQS#6
- AE30 M_A DM6
- AG31 M_A DQ48
- AG30 M_A DQ49
- AD30 M_A DQ50
- AD29 M_A DQ51
- AJ30 M_A DQ52
- AJ29 M_A DQ53
- AE29 M_A DQ54
- AD28 M_A DQ55
- AB27 M_A DQS7
- AA27 M_A DQS#7
- AB26 M_A DM7
- AA24 M_A DQ56
- AB25 M_A DQ57
- W24 M_A DQ58
- W22 M_A DQ59
- AB24 M_A DQ60
- AB23 M_A DQ61
- AA23 M_A DQ62
- W27 M_A DQ63



1D: add 7 0.1u for VTT_MEM(Intel)

1C: reserve for ES1(WW10 MOW)



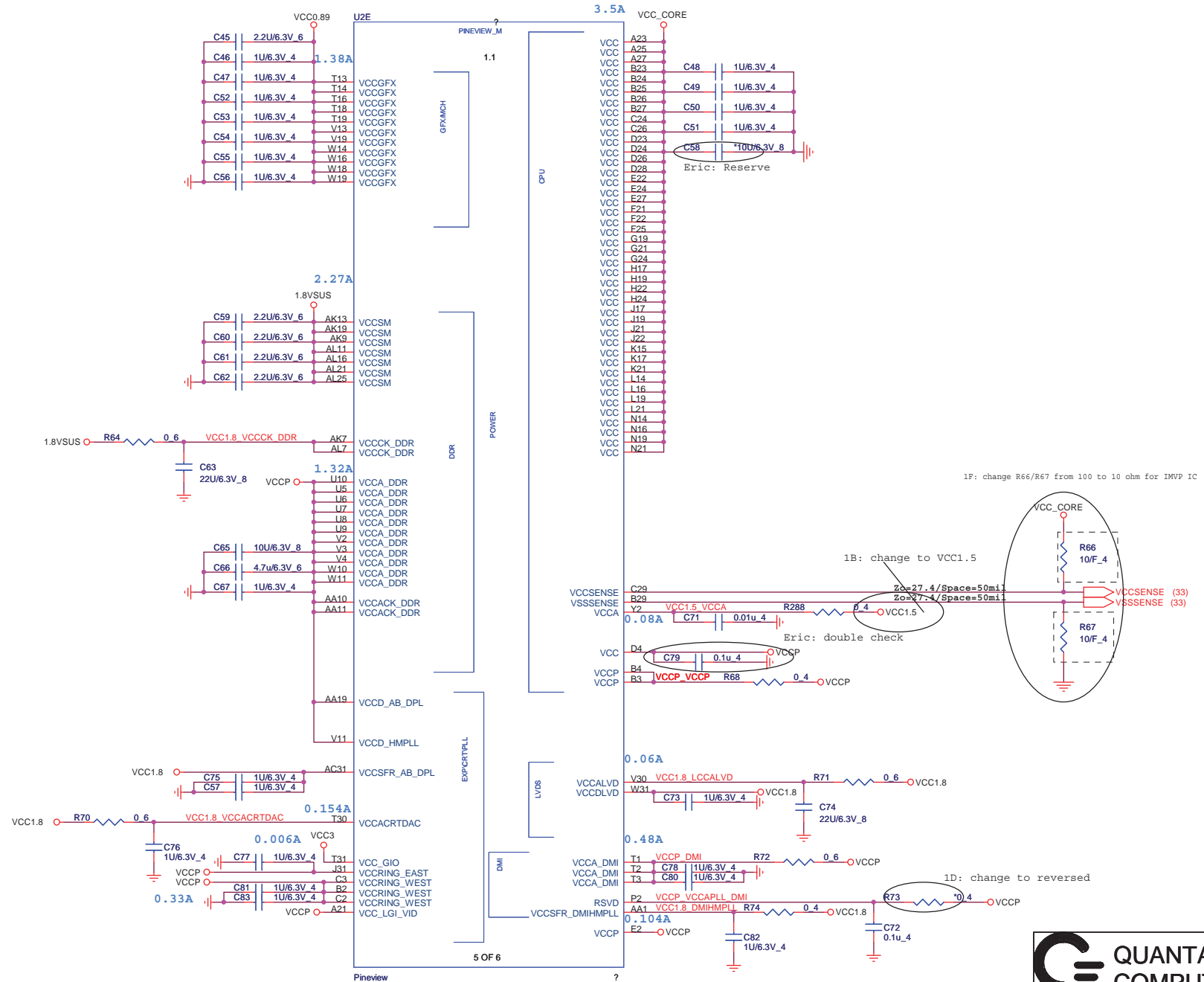
1D: add optional power for DDR_VREF(CRB)

QUANTA COMPUTER

Title: Pineview DDR

Size: Custom Document Number: SY3 MB Rev: 1G

Date: Tuesday, March 31, 2009 Sheet: 5 of 44

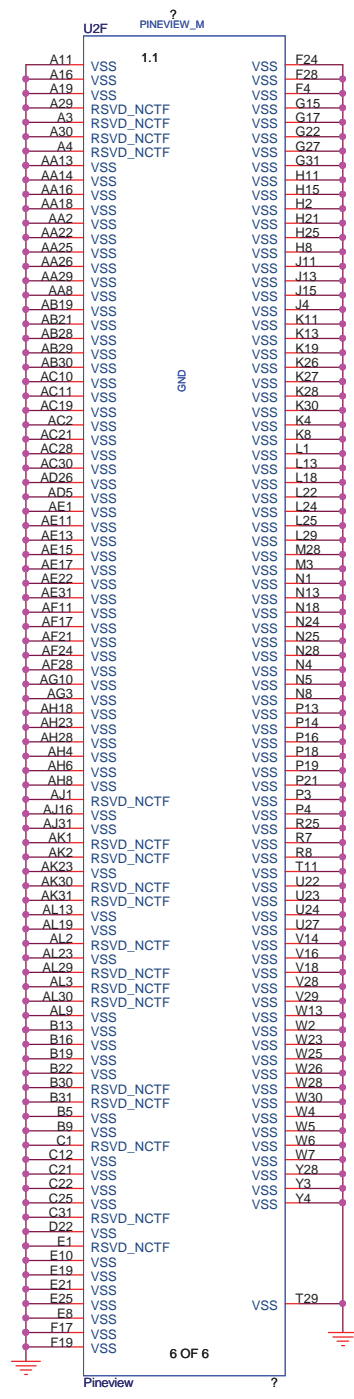


QUANTA COMPUTER

Title: **Pineview Power**

Size: Custom	Document Number: SY3 MB	Rev: 1G
Date: Tuesday, March 31, 2009		Sheet: 6 of 44

1. Level 1 Environment-related Substances Should NEVER be Used.
 2. Purchase ink, paint, wire rods, and Molding resins only from the business Partners that Sony approves as Green Partners.



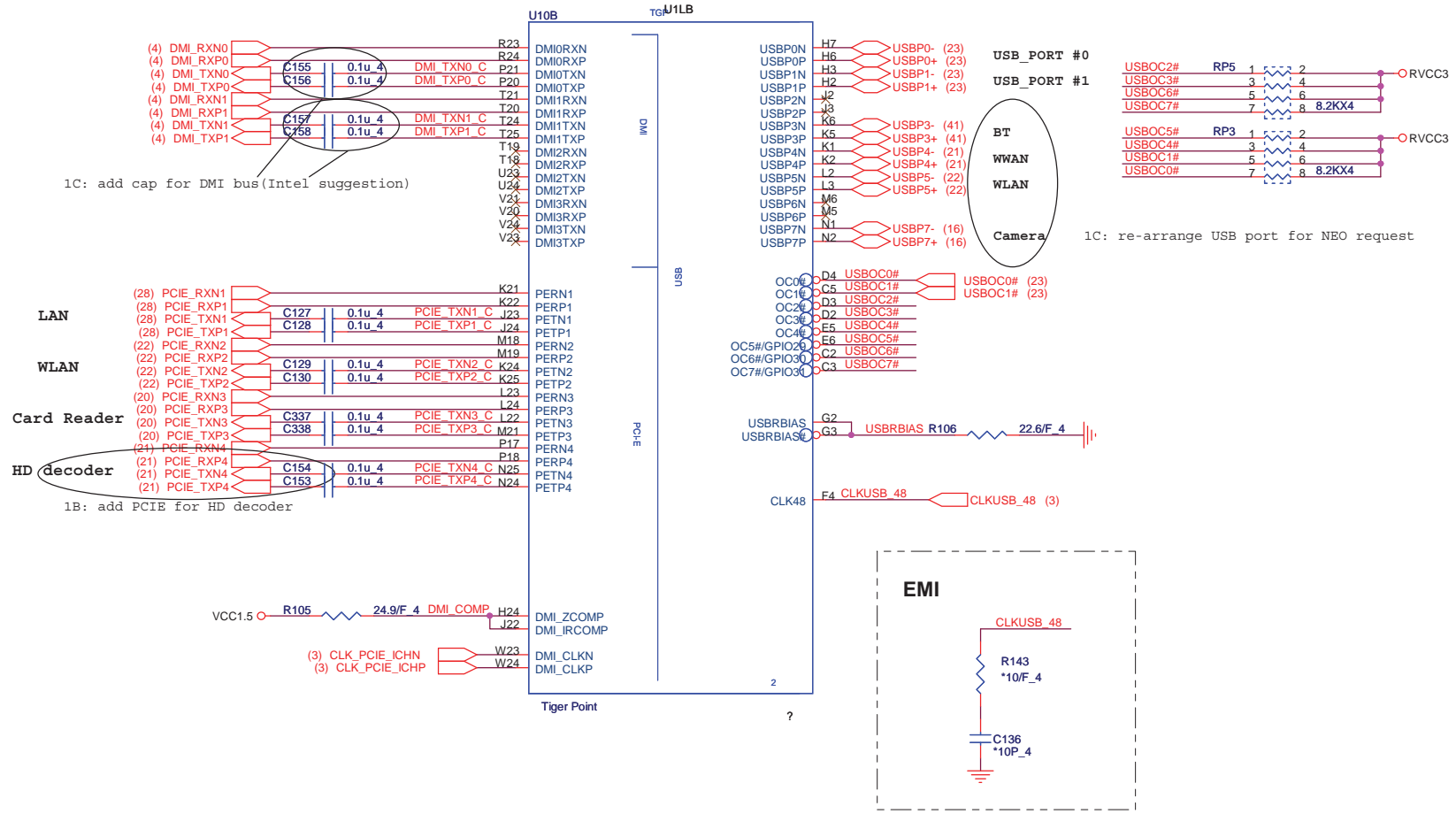
QUANTA COMPUTER

Title: **Pineview GND**

Size	Document Number	Rev
Custom	SY3 MB	1G

Date: Tuesday, March 31, 2009 Sheet 7 of 44

1. Level 1 Environment-related Substances should NEVER be Used.
 2. Purchase Link (Paint) will be used, and welding resin only from the business Partners that Sony approves as Green Partners.



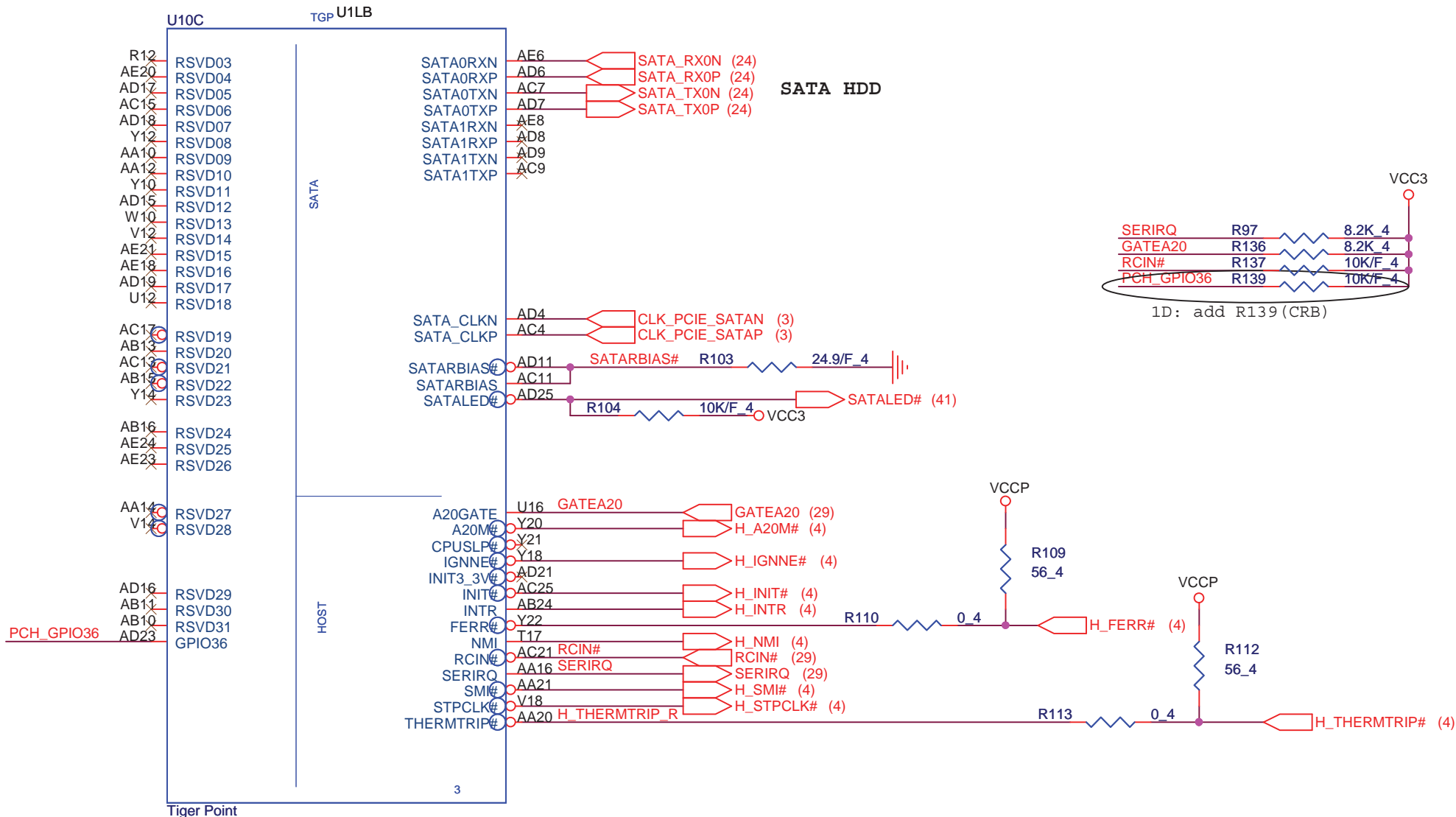
1.Level 1 Environment-related Substances should NEVER be Used.
 2.Purchase ink, paint, wire rods, and Molding resins only from the business Partners that Sony approves as Green Partners.

QUANTA COMPUTER


Title: **Tiger Point DMI/PCIE/USB**

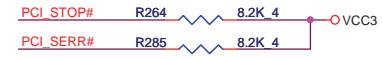
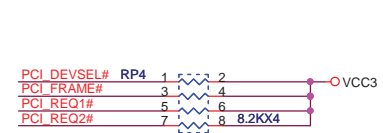
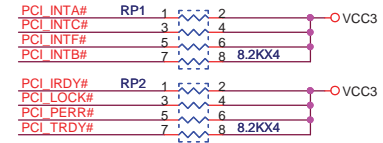
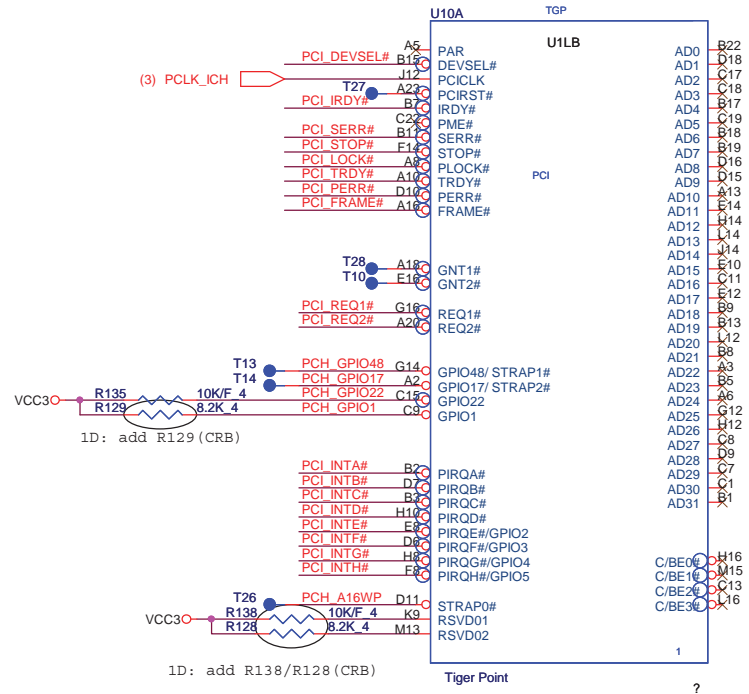
Size B	Document Number	Rev 1G
	SY3 MB	

Date: Tuesday, March 31, 2009 Sheet 8 of 44



1.Level 1 Environment-related Substances Should NEVER be Used.
 2.Purchase ink, paint, wire rods, and Molding resins only from the business Partners that Sony approves as Green Partners.

 QUANTA COMPUTER		
Title		
Tiger Point Sata/Host		
Size	Document Number	Rev
A	SY3 MB	1G
Date: Tuesday, March 31, 2009		Sheet 9 of 44



ICH Boot BIOS select

PCH_GPIO17 (INT PU)	PCH_GPIO48 (INT PU)	Boot BIOS Location
0	1	SPI
1	0	PCI
1	1	LPC (Default)

A16 SWAP Override strap

PCH_A16WP (INT PU)	Low = A16 swap override enabled High = Default

IRQ	Description
PIRQA	USB UHCI Controller #1, #4
PIRQB	AC'97 Codec; option for SMBUS
PIRQC	USB UH Controller #3; SATA/IDE Native Mode
PIRQD	USB UHCI Controller #2
PIRQE	Internal LAN; Option for SCI, TCO, HPET#0,1,2
PIRQF	Option for SCI, TCO, HPET#0,1,2
PIRQG	Option for SCI, TCO, HPET#0,1,2
PIRQH	USB EHCI Controller; Option for SCI, TCO, HPET#0,1,2

PCI_GNT#2	Internal PU Should not be PD

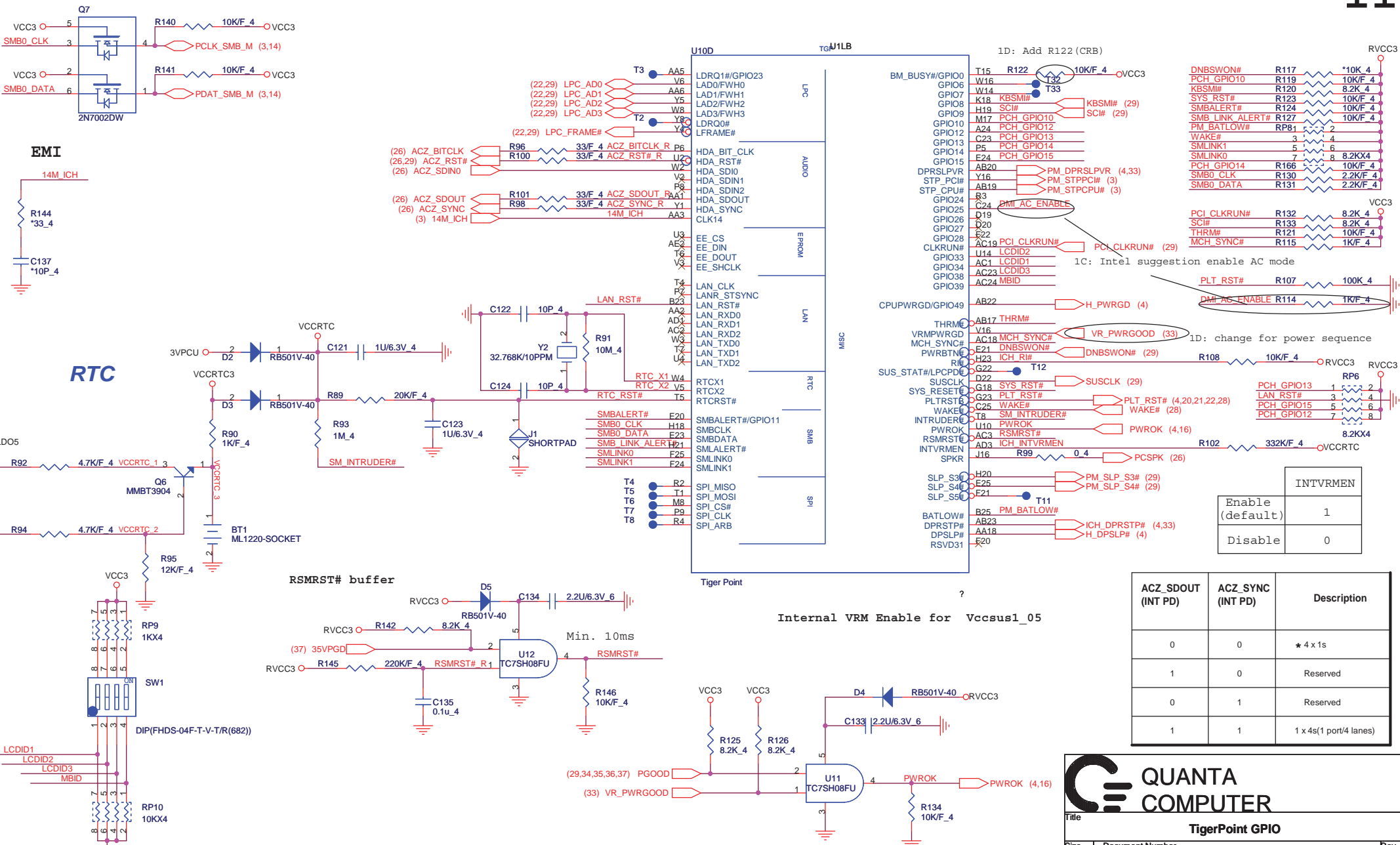
**QUANTA
COMPUTER**

Title: **TigerPoint PCI(3/6)**

Size B	Document Number SY3 MB	Rev 1G
--------	----------------------------------	-----------

Date: Tuesday, March 31, 2009 Sheet 10 of 44

1. Level 1 Environment-related Substances should NEVER be Used.
 2. Purchase ink, paint, wire rods, and Molding resins only from the business Partners that Sony approves as Green Partners.



DNBSWON#	R117	10K/4
PCH GPIO10	R119	10K/F 4
KBSM#	R120	8.2K 4
SYS_RST#	R123	10K/F 4
SMBALERT#	R124	10K/F 4
SMB LINK ALERT#	R127	10K/F 4
PM BATLOW#	RP81	2
WAKE#	3	4
SMLINK1	5	6
SMLINK0	7	8
PCH GPIO14	R166	8.2KX4
SMB0_CLK	R130	2.2K/F 4
SMB0_DATA	R131	2.2K/F 4
PCI CLKRUN#	R132	8.2K 4
SCI#	R133	8.2K 4
THRM#	R121	10K/F 4
MCH_SYNC#	R115	1K/F 4
PLT_RST#	R107	100K 4
DMI AC_ENABLE	R114	1K/F 4

VR_PWRGOOD (33)	R108	10K/F 4
DNBSWON# (29)	R108	10K/F 4
PLT_RST# (4,20,21,22,28)	R102	332K/F 4
WAKE# (28)	R102	332K/F 4
PCH GPIO13	RP6	2
LAN_RST#	3	4
PCH GPIO15	5	6
PCH GPIO12	7	8
PCH GPIO17	8	8
INTVRMEN		8.2KX4

Enable (default)	1
Disable	0

ACZ_SDOUT (INT PD)	ACZ_SYNC (INT PD)	Description
0	0	* 4 x 1s
1	0	Reserved
0	1	Reserved
1	1	1 x 4s(1 port/4 lanes)

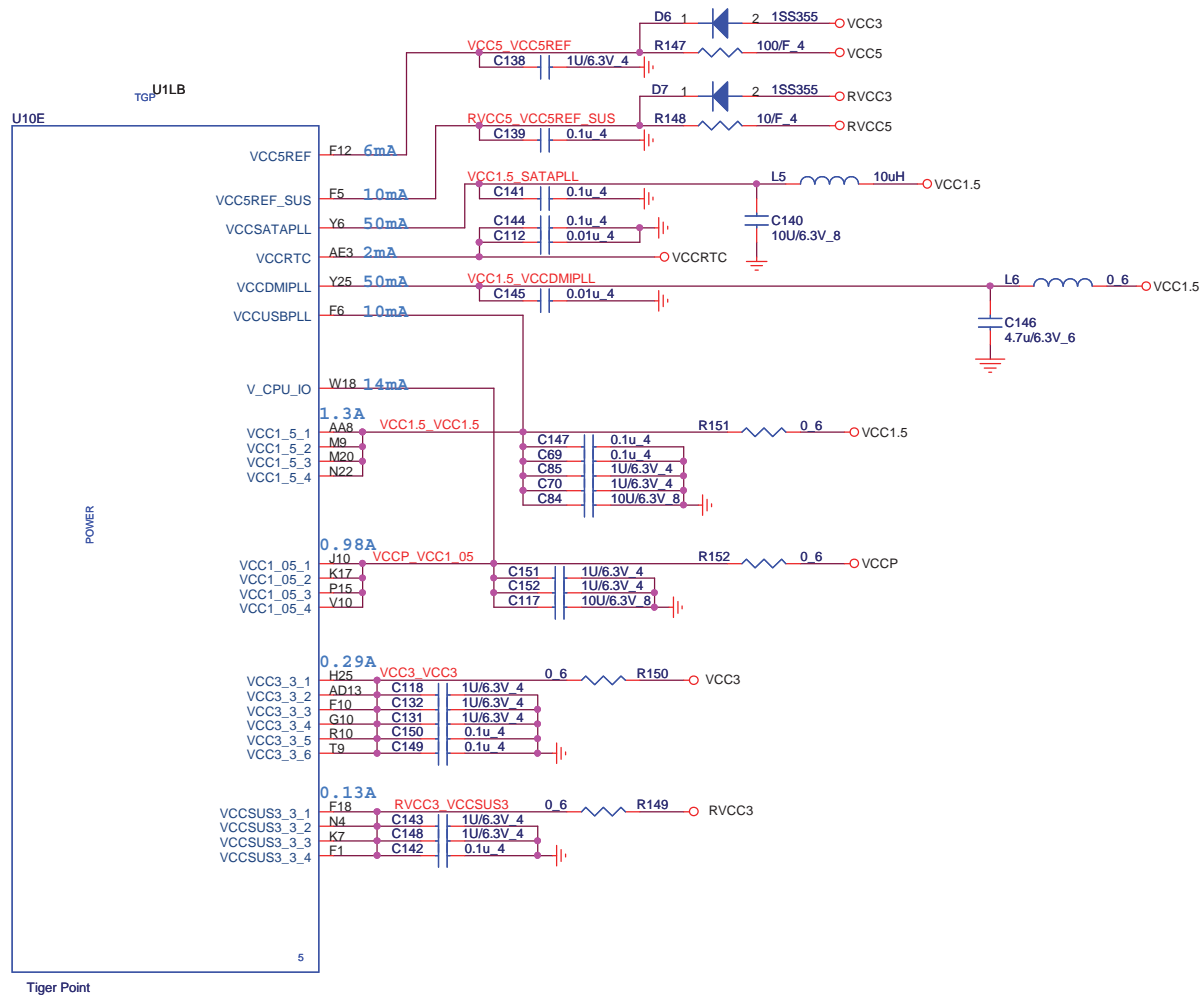
QUANTA COMPUTER

Title: **TigerPoint GPIO**

Size B	Document Number	Rev 1G
	SY3 MB	


Date: Tuesday, March 31, 2009 Sheet 11 of 44

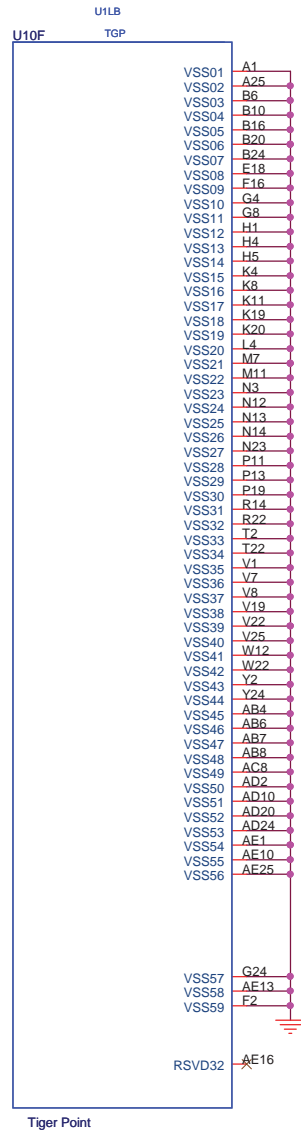
1.Level 1 Environment-related Substances should NEVER be Used.
 2.Purchase ink, paint, wire rods, and Molding resins only from the business Partners that Sony approves as Green Partners.




Tiger Point
?

1.Level 1 Environment-related Substances Should NEVER be Used.
2.Purchase ink, paint, wire rods, and Molding resins only from the business Partners that Sony approves as Green Partners.

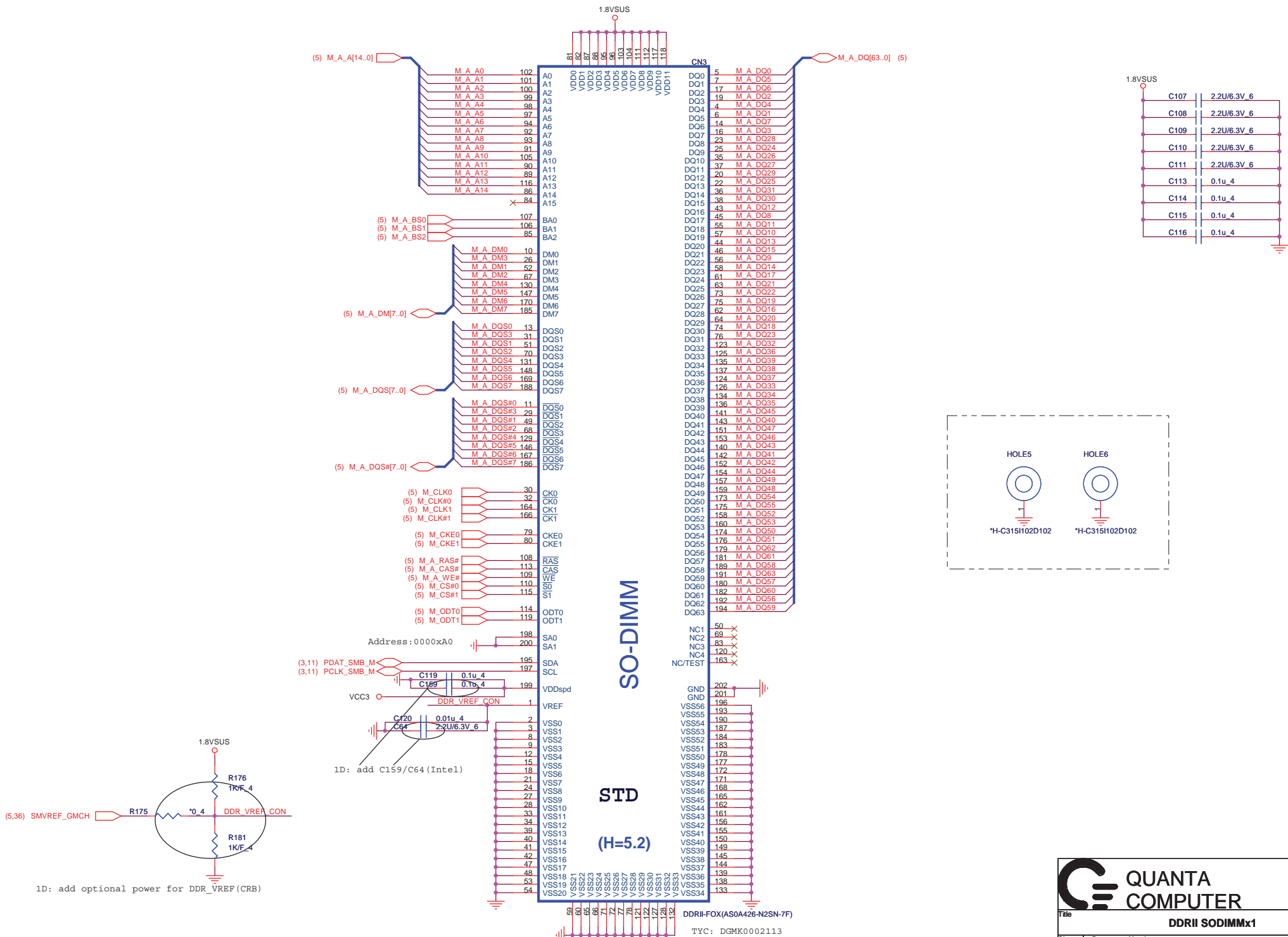
 QUANTA COMPUTER		
Title		
TigerPoint Power		
Size	Document Number	Rev
B	SY3 MB	1G
Date: Tuesday, March 31, 2009		Sheet 12 of 44



Tiger Point

		
Title		
TigerPoint GND		
Size	Document Number	Rev
B	SY3 MB	1G
Date: Tuesday, March 31, 2009		Sheet 13 of 44

1.Level 1 Environment-related Substances Should NEVER be Used.
 2.Purchase ink, paint, wire rods, and Molding resins only from the business Partners that Sony approves as Green Partners.



SO-DIMM

STD (H=5.2)

DDR2-FOX(AS0A426-N2SN-7F)

TYC: DGMK0002113

1. Level 1 Environment-related Substances Should NEVER be Used.
 2. Purchase ink, paint, wire, and soldering resins only from the business Partners that Sony approves as Green Partners.

http://laptop-motherboard-and-schematic.blogspot.com/

QUANTA COMPUTER

Title: **DDR2 SO-DIMMx1**

Size	Document Number	Rev
Custom	SY3 MB	1G


Date: Tuesday, March 31, 2009 Sheet 14 of 44

A

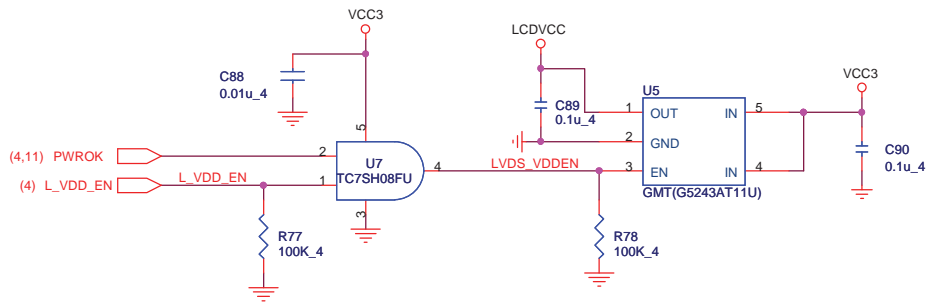
A

- 1.Level 1 Environment-related Substances Should NEVER be Used.
- 2.Purchase ink, paint, wire rods, and Molding resins only from the business Partners that Sony approves as Green Partners.

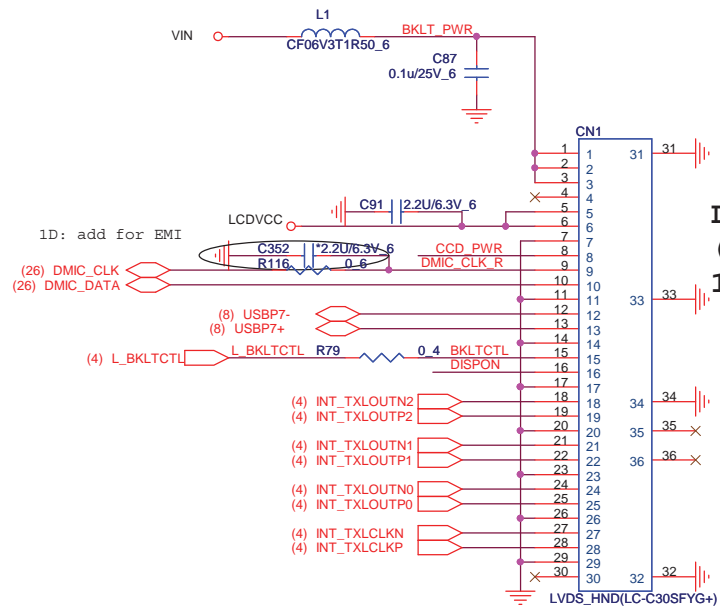
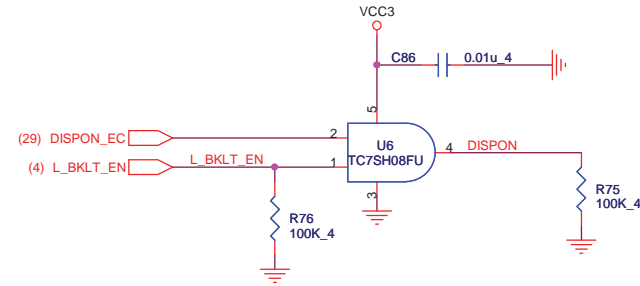
<http://laptop-motherboard-schematic.blogspot.com/>

 QUANTA COMPUTER		
Title DDRII SODIMMx1		
Size A	Document Number SY3 MB	Rev 1G
Date: Tuesday, March 31, 2009	Sheet 15	of 44

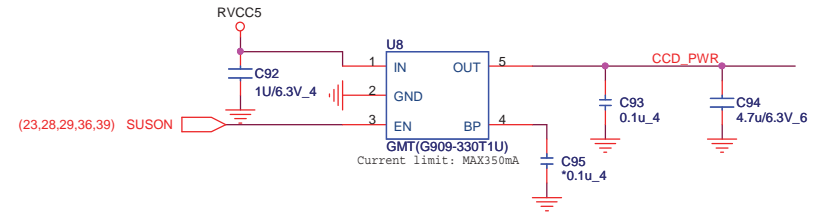
LVDS Enable



Display On

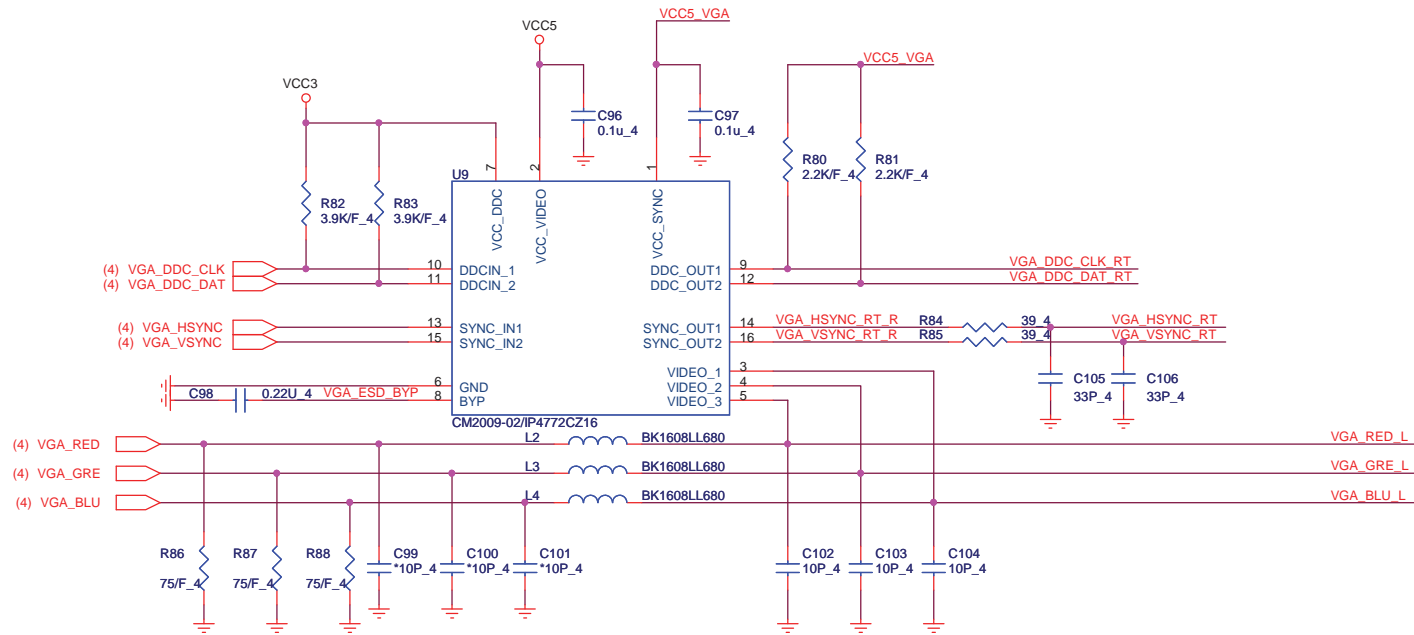


LVDS (10.1")
(1024x600,
1366x768)

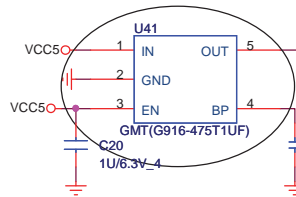


Title			LVDS		
Size	Document Number				Rev
B	SY3 MB				1G
Date:	Tuesday, March 31, 2009	Sheet	16	of	44

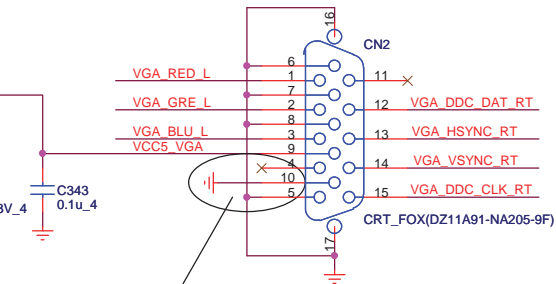
1. Level 1 Environment-related Substances Should NEVER be Used.
2. Purchase ink, paint, wire rods, and Molding resins only from the business Partners that Sony approves as Green Partners.



VCC5_VGA (5V+-10%)
Current limit <1A
VCC5<55mA
Current limit ~ 350mA



1E: change to same with audio(add C196)



1C: don't support PnP delete Q23 and R290



Title			CRT
Size	Document Number	Rev	
B	SY3 MB	1G	
Date:	Tuesday, March 31, 2009	Sheet	17 of 44


- 1.Level 1 Environment-related Substances Should NEVER be Used.
- 2.Purchase ink, paint, wire rods, and Molding resins only from the business Partners that Sony approves as Green Partners.

A

A

1.Level 1 Environment-related Substances Should NEVER be Used.
 2.Purchase ink, paint, wire rods, and Molding resins only from the business Partners that Sony approves as Green Partners.

<http://laptop-motherboard-schematic.blogspot.com/>


 QUANTA COMPUTER		
Title Display I/O		
Size A	Document Number SY3 MB	Rev 1G
Date: Tuesday, March 31, 2009		
Sheet 18 of 44		

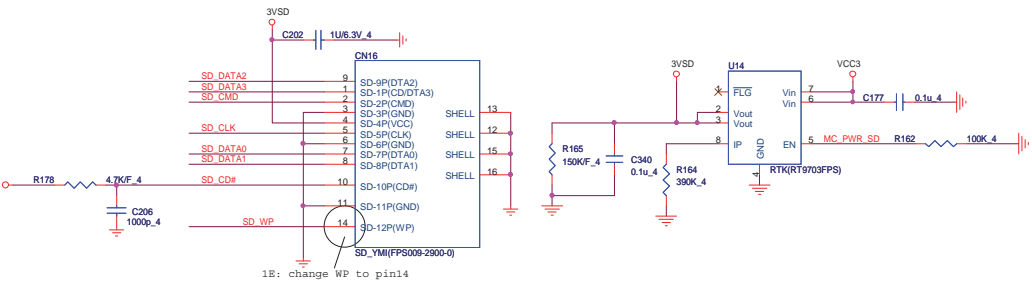
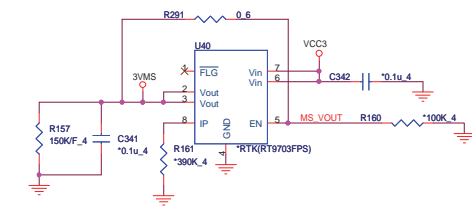
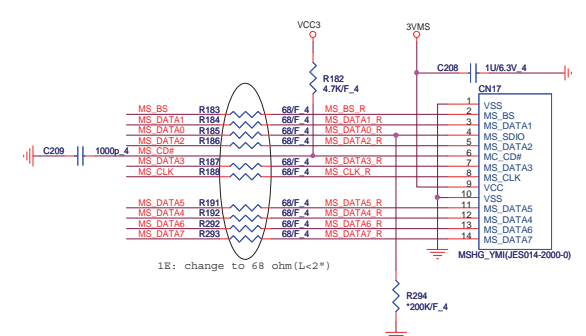
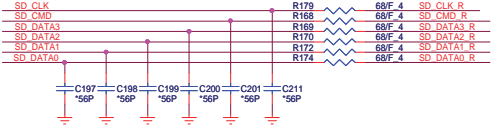
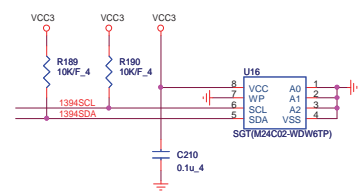
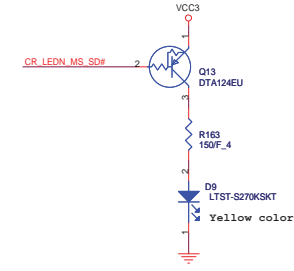
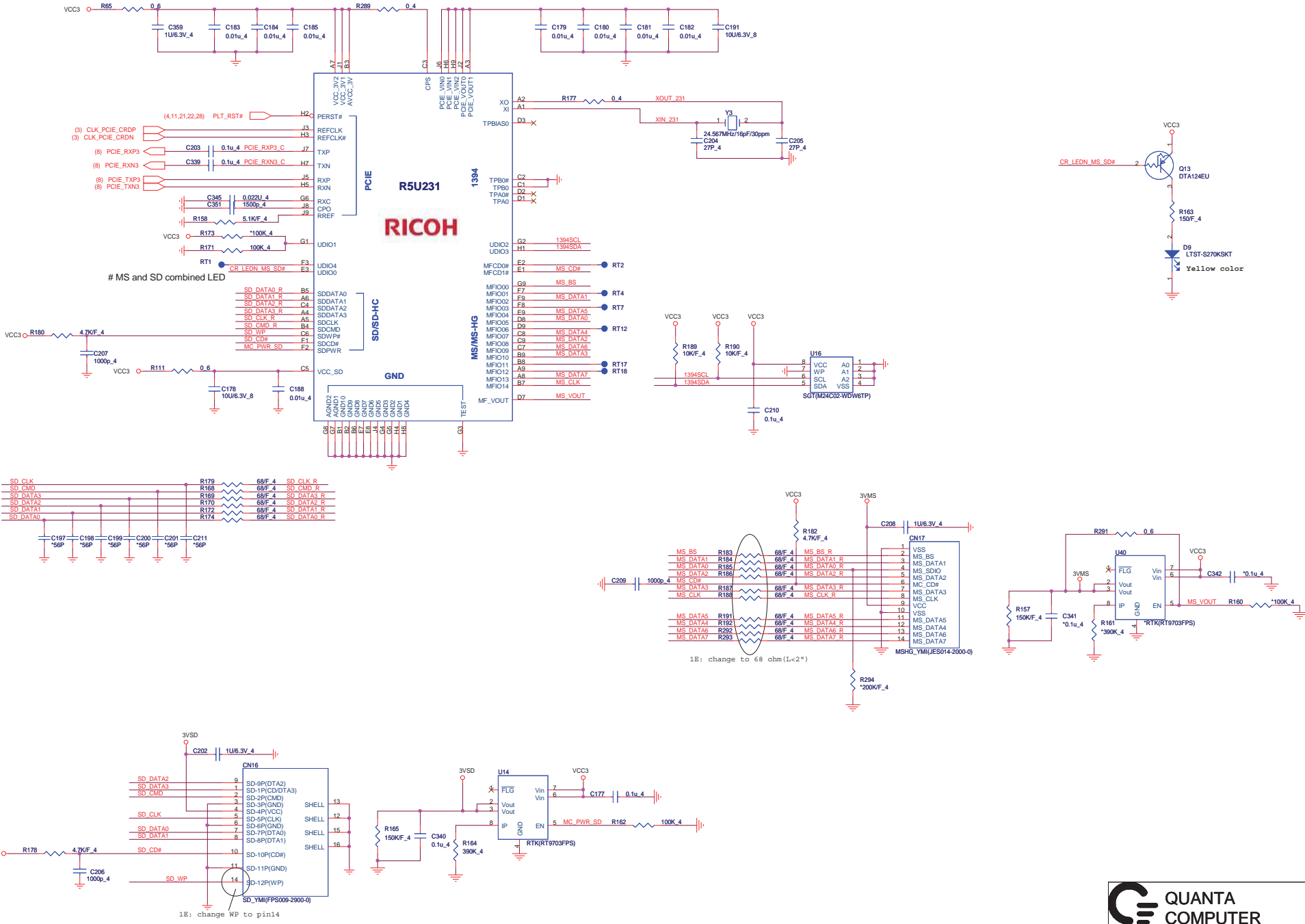
A

A

1.Level 1 Environment-related Substances Should NEVER be Used.
 2.Purchase ink, paint, wire rods, and Molding resins only from the business Partners that Sony approves as Green Partners.

<http://laptop-motherboard-schematic.blogspot.com/>

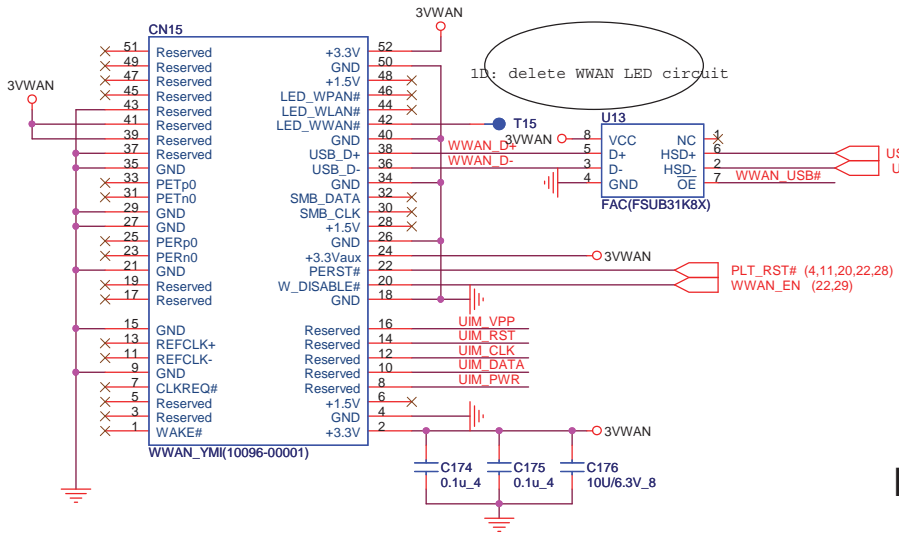
 QUANTA COMPUTER		
Title Display I/O		
Size A	Document Number SY3 MB	Rev 1G
Date: Tuesday, March 31, 2009		
Sheet 19 of 44		



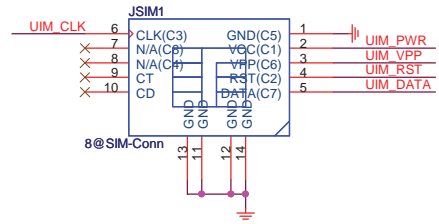
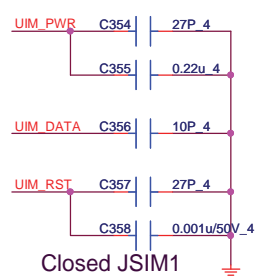
**QUANTA
COMPUTER**

File: CardReader R5U231

Size C	Document Number SY3 MB	Rev 1G
Date: Tuesday, March 31, 2009	Sheet 20 of 44	

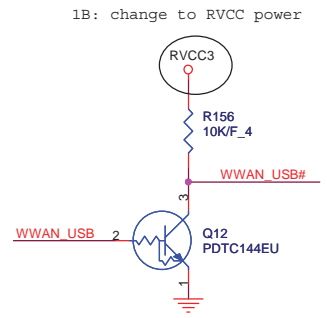
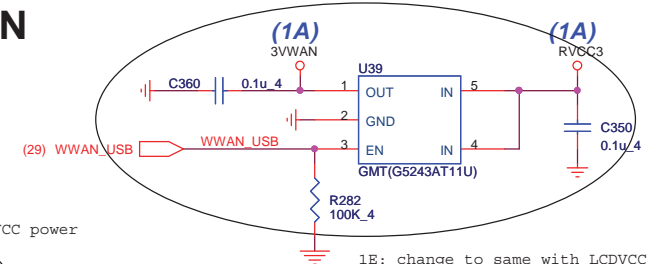
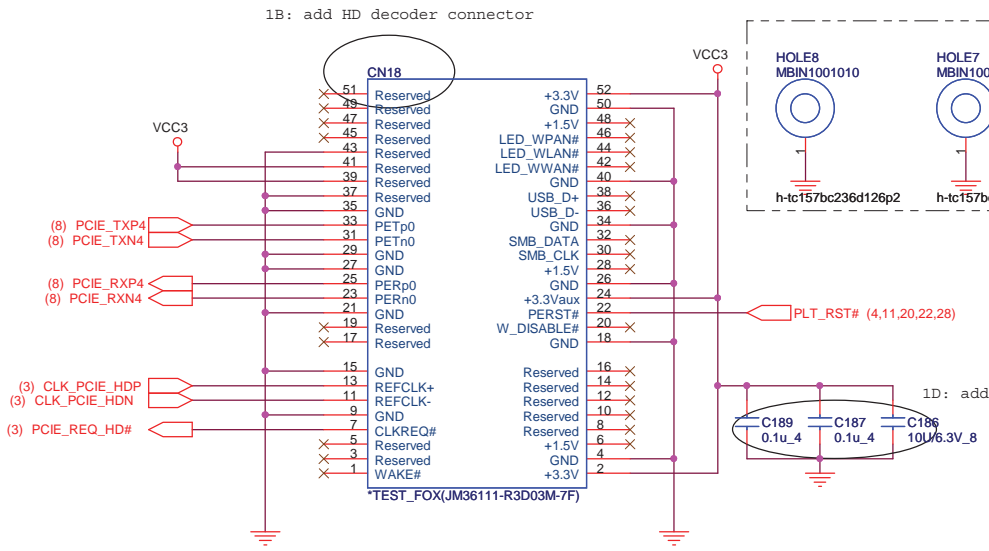


SIM CARD SIGNALS ROUTE PARALLEL



The value of the capacitor is suggest by Siemens HQ expert.
 For against 900MHz RF interference. The value of capacitor is 27pF.
 For against 1800MHz RF interference. The value of capacitor is 10pF.
 1nF/10nF value capacitor use for against ESD purpose.

Mini PCI-E Card FOR WWAN

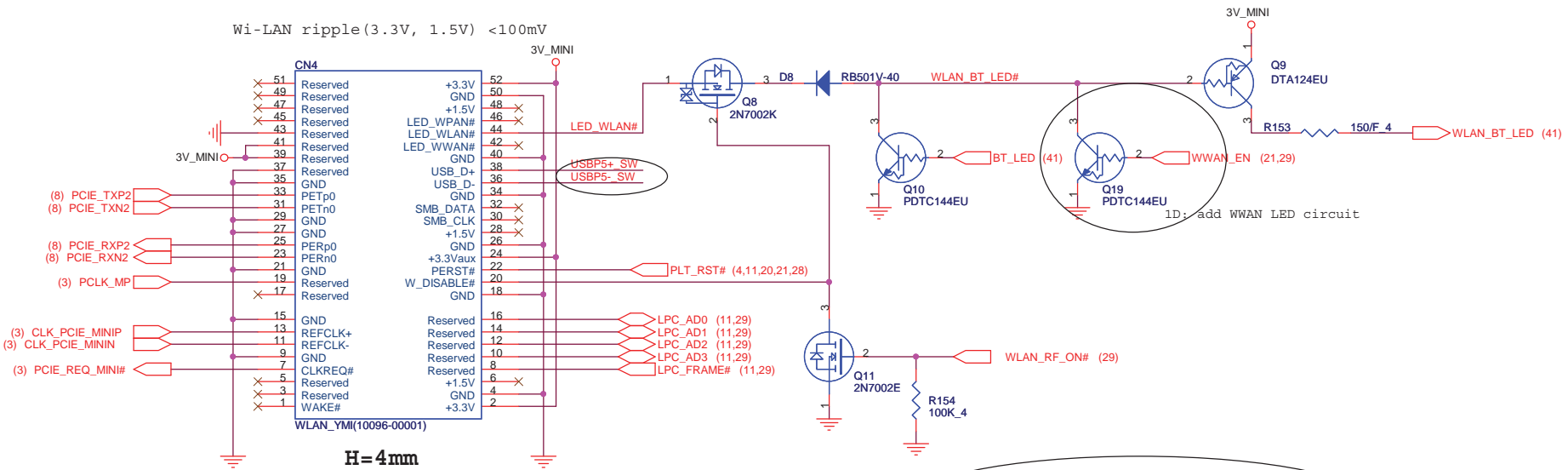


Title		
WWAN		
Size B	Document Number	Rev 1G
	SY3 MB	
Date: Tuesday, March 31, 2009	Sheet	21 of 44

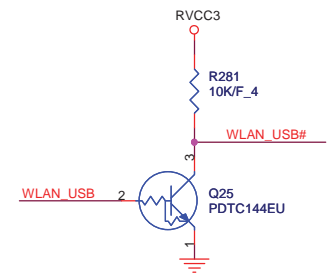
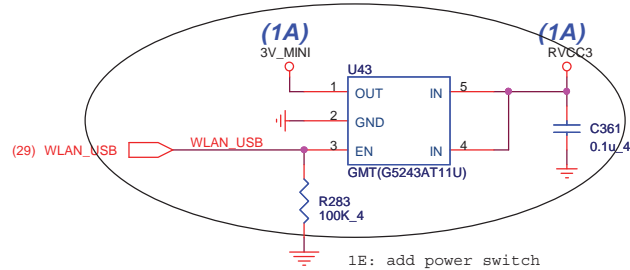
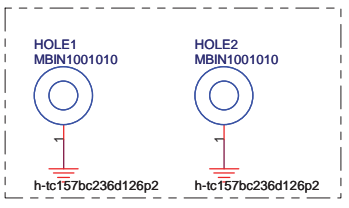
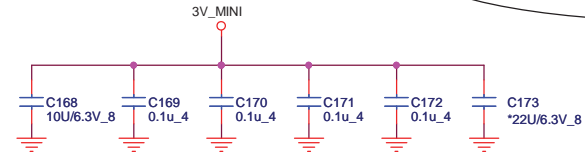
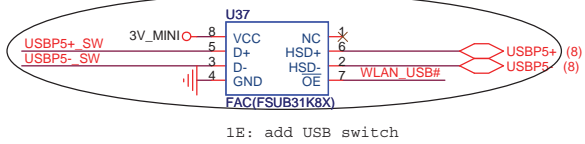
1.Level 1 Environment-related Substances Should NEVER be Used.
 2.Purchase ink, paint, wire rods, and Molding resins only from the business Partners that Sony approves as Green Partners.

Mini PCI-E Card--W-LAN

Wi-LAN ripple(3.3V, 1.5V) <100mV



H = 4mm



1.Level 1 Environment-related Substances Should NEVER be Used.
 2.Purchase ink, paint, wire rods, and Molding resins only from the business Partners that Sony approves as Green Partners.

QUANTA COMPUTER

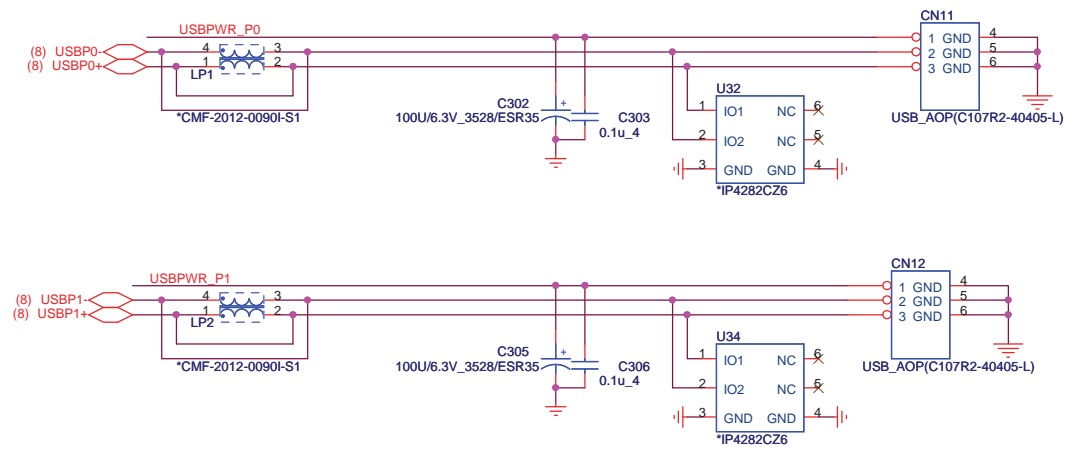
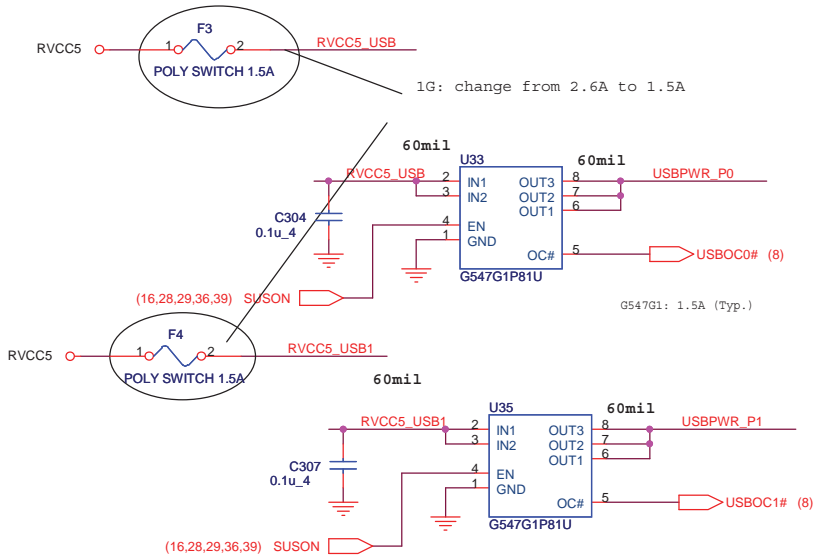
Title: **WLAN**

Size B | Document Number: **SY3 MB** | Rev 1G


Date: Tuesday, March 31, 2009 | Sheet 22 of 44

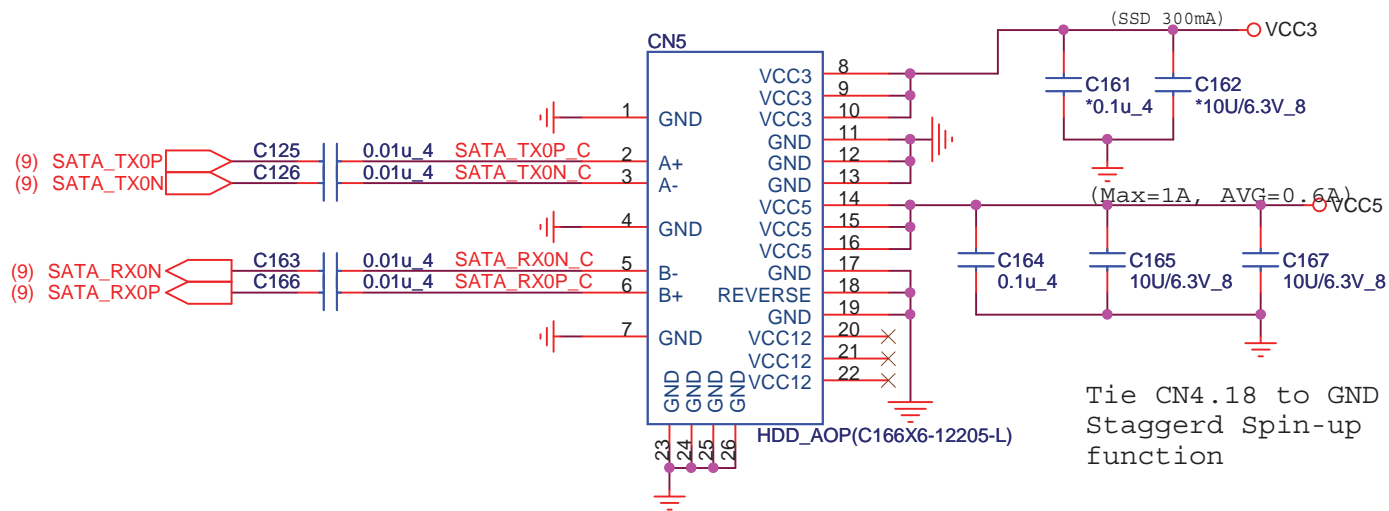
USB Power and Over current

USB Connector




1.Level 1 Environment-related Substances Should NEVER be Used.
 2.Purchase ink, paint, wire rods, and Molding resins only from the business Partners that Sony approves as Green Partners.

 QUANTA COMPUTER		
Title		
USB		
Size	Document Number	Rev
B	SY3 MB	1G
Date: Tuesday, March 31, 2009		Sheet 23 of 44



1.Level 1 Environment-related Substances Should NEVER be Used.
 2.Purchase ink, paint, wire rods, and Molding resins only from the business Partners that Sony approves as Green Partners.


		
Title		
SATA		
Size	Document Number	Rev
A	SY3 MB	1G
Date:	Tuesday, March 31, 2009	Sheet 24 of 44

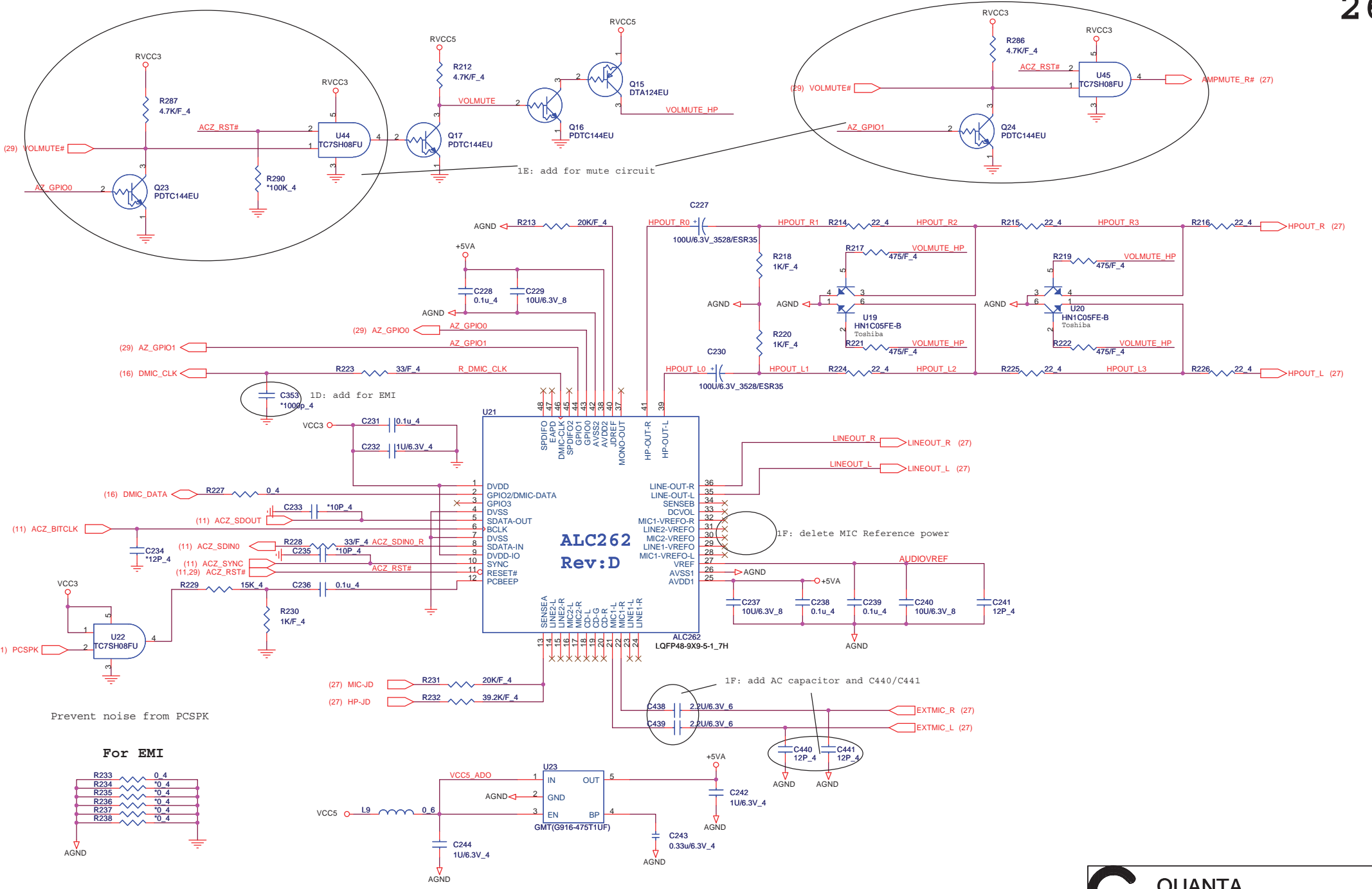
A

A

1.Level 1 Environment-related Substances Should NEVER be Used.
 2.Purchase ink, paint, wire rods, and Molding resins only from the business Partners that Sony approves as Green Partners.

<http://laptop-motherboard-schematic.blogspot.com/>

 QUANTA COMPUTER		
Title USB3		
Size A	Document Number SY3 MB	Rev 1G
Date: Tuesday, March 31, 2009 Sheet 25 of 44		



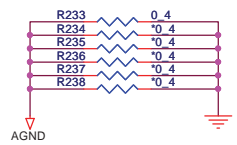
1E: add for mute circuit

1F: delete MIC Reference power

1F: add AC capacitor and C440/C441

Prevent noise from PCSPK

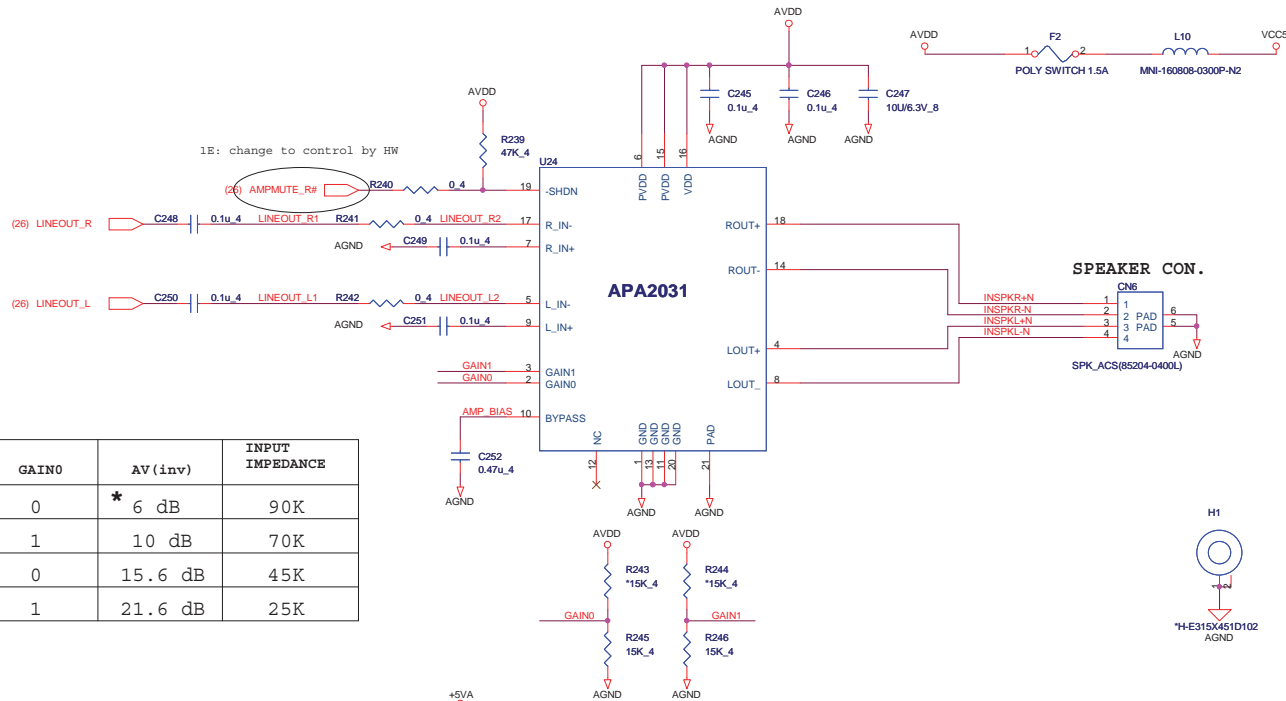
For EMI



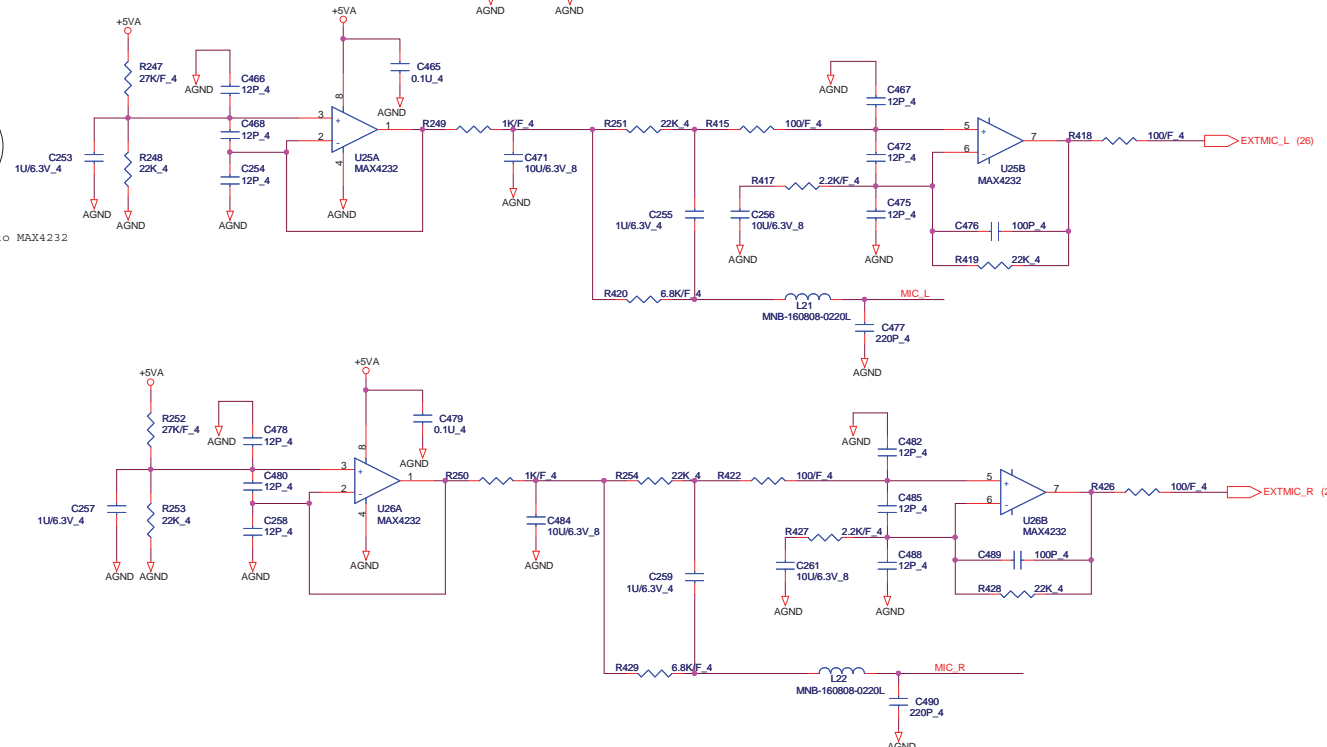
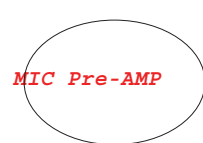
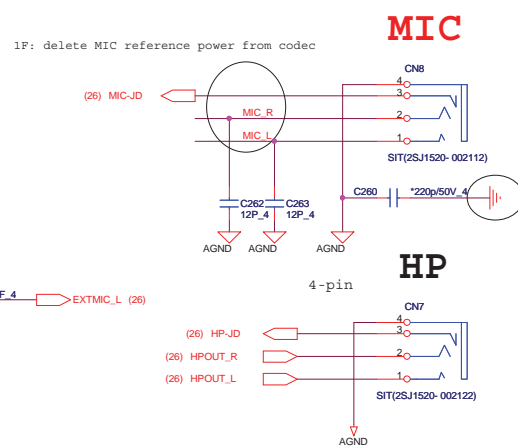
QUANTA COMPUTER

Title: **Codec ALC262**

Size: Custom	Document Number: SY3 MB	Rev: 1G
Date: Tuesday, March 31, 2009		Sheet: 26 of 44



GAIN1	GAIN0	AV (inv)	INPUT IMPEDANCE
0	0	* 6 dB	90K
0	1	10 dB	70K
1	0	15.6 dB	45K
1	1	21.6 dB	25K

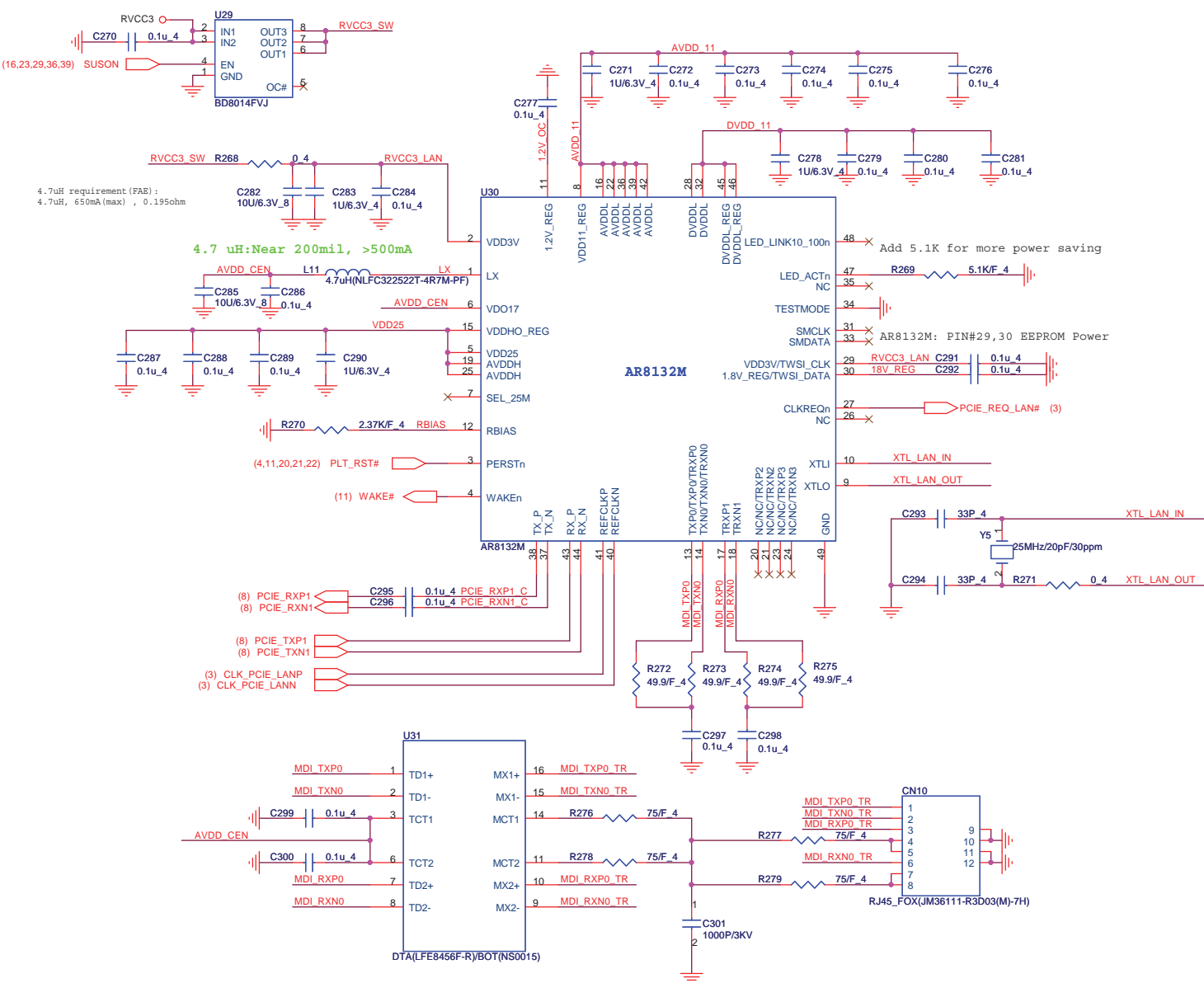


QUANTA COMPUTER

File: **Audio CNN TPA6017A2**

Size: Custom Document Number: **SY3 MB** Rev: 1G

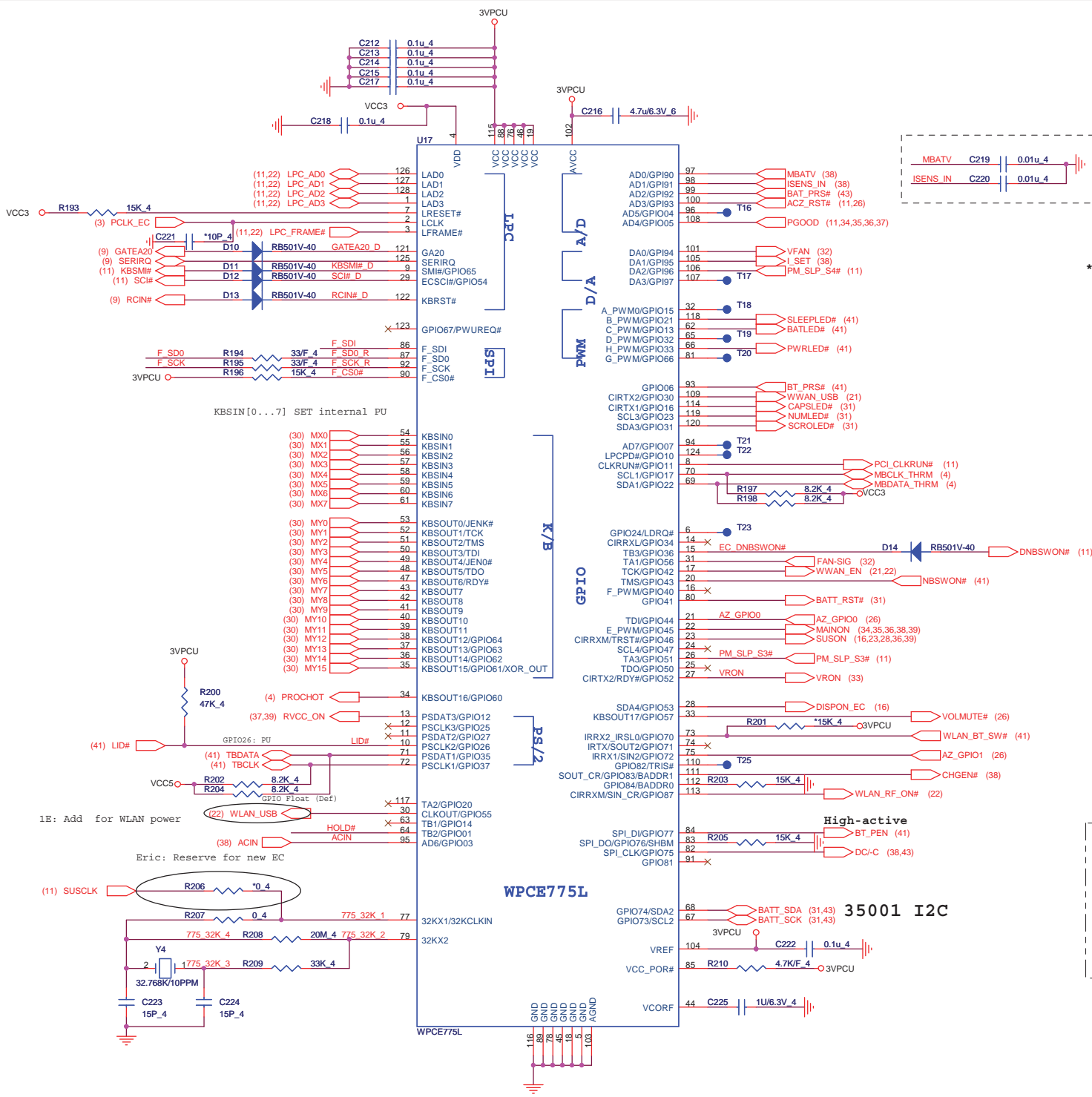
Date: Monday, March 30, 2009 Sheet: 27 of 44



QUANTA COMPUTER

Title: LAN/Transformer AR8132M

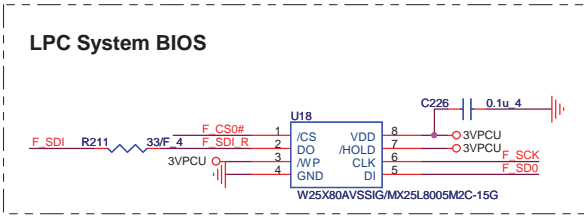
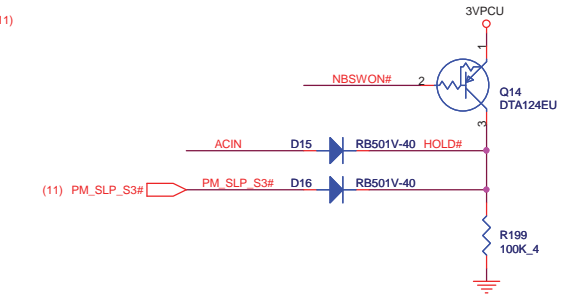
Size: Custom	Document Number: SY3 MB	Rev: 1G
Date: Tuesday, March 31, 2009		Sheet: 28 of 44



I/O Address

BADDR1-0	Index	Data
1 0	2E	2F
1 1	4E	4F
0 0	[HCFGBAH, HCFGBAL] / [HCFGBAH, HCFGBAL]+1	
0 1	XOR-TREE TEST	

SHMB: SHBM (If = 0 Enable share host BIOS memory)
 DOCK_RST# : BADDR0
 T?: BADDR1

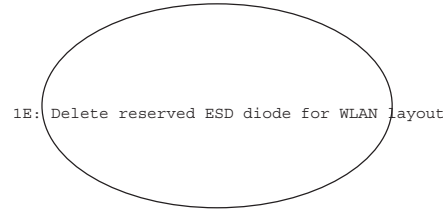
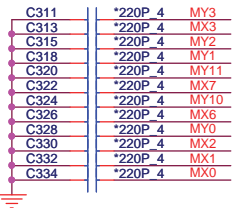
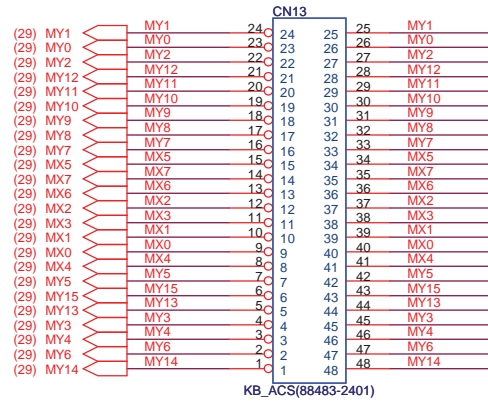
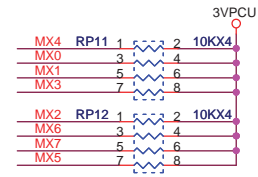


QUANTA COMPUTER

Title: **EC/ROM/SPI WPCE775L**

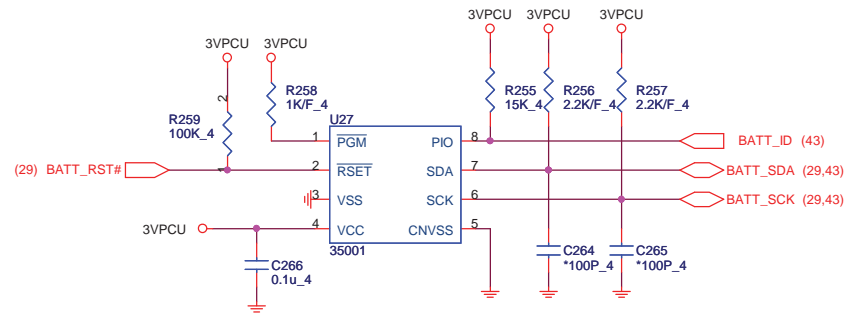
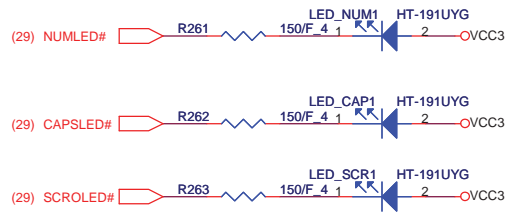
Size	Document Number	Rev
	SY3 MB	1G

Date: Monday, March 30, 2009 Sheet 29 of 44



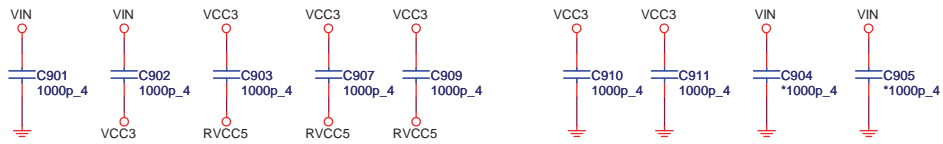
QUANTA COMPUTER		
Title		
KB/BT/TP		
Size	Document Number	Rev
B	SY3 MB	1G
Date:	Tuesday, March 31, 2009	Sheet 30 of 44

1.Level 1 Environment-related Substances Should NEVER be Used.
 2.Purchase ink, paint, wire rods, and Molding resins only from the business Partners that Sony approves as Green Partners.



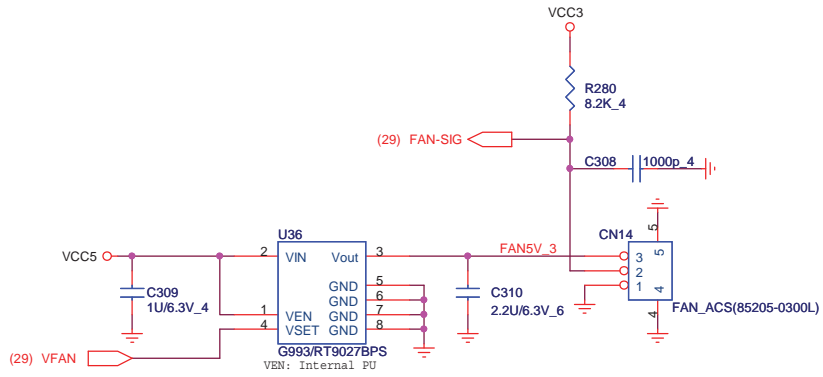
Decoupling Cap

1E: add decoupling cap




1.Level 1 Environment-related Substances Should NEVER be Used.
 2.Purchase ink, paint, wire rods, and Molding resins only from the business Partners that Sony approves as Green Partners.

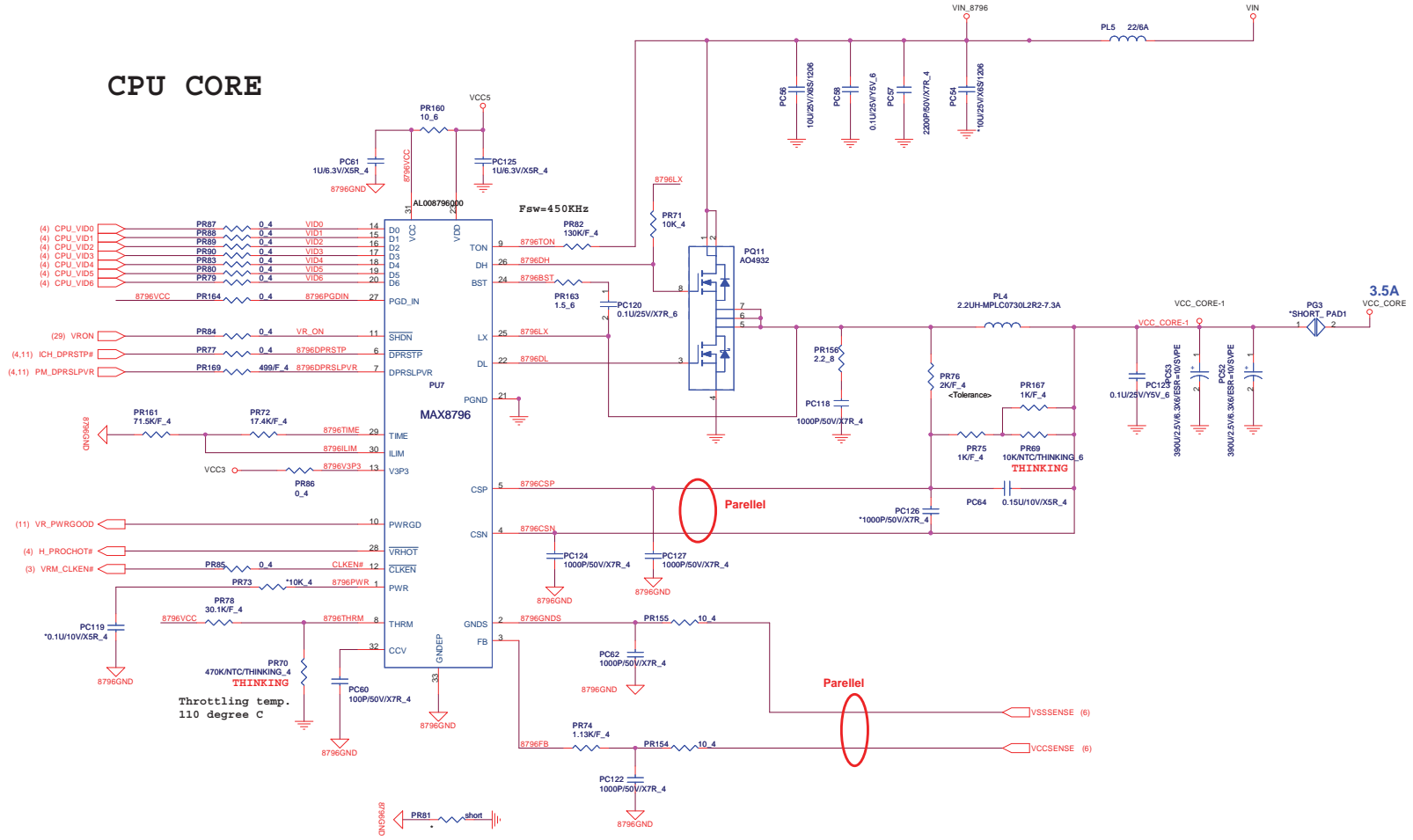
Title		
SW/LED/Other		
Size	Document Number	Rev
B	SY3 MB	1G
Date:	Tuesday, March 31, 2009	Sheet 31 of 28



- 1.Level 1 Environment-related Substances Should NEVER be Used.
- 2.Purchase ink, paint, wire rods, and Molding resins only from the business Partners that Sony approves as Green Partners.

 QUANTA COMPUTER			
			Title FAN/Thermal/Docking
Size B	Document Number SY3 MB	Rev 1G	
Date: Tuesday, March 31, 2009		Sheet 32 of 44	

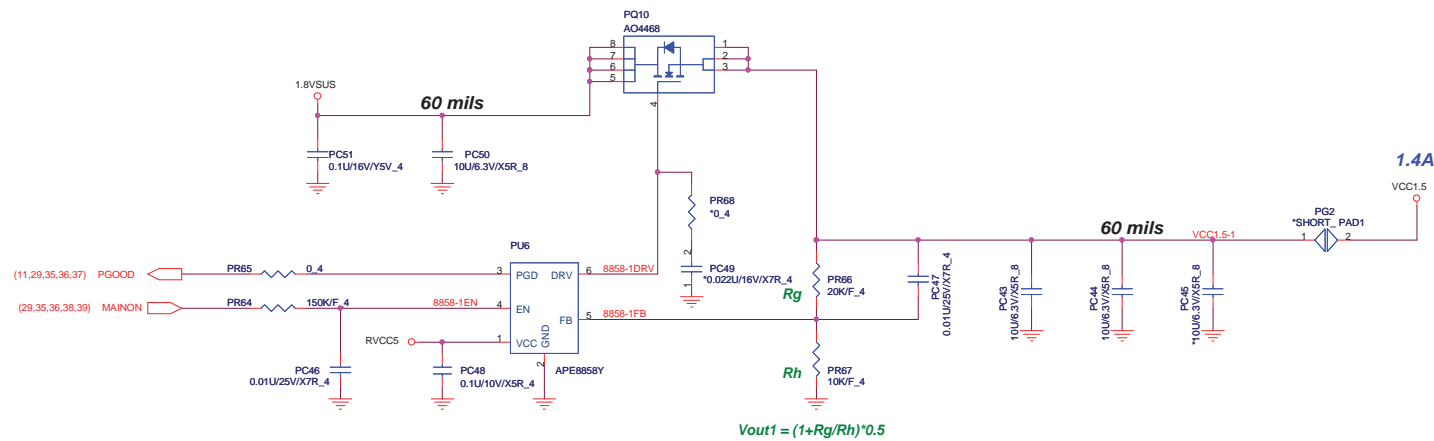
CPU CORE



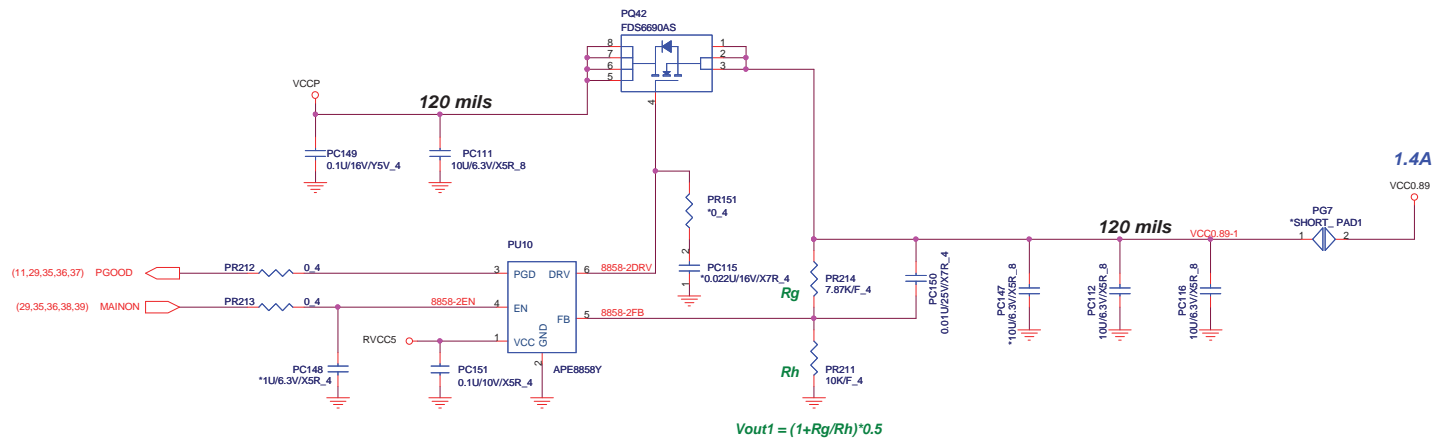
QUANTA COMPUTER		
CPU CORE		
File	Document Number	Rev
Size	Custom	1G
Date: Tuesday, March 31, 2009		Sheet 33 of 44


1. Level 1 Environment-related Substances should NEVER be Used.
 2. Purchase ink, paint, wire rods, and Molding resins only from the business Partners that Sony approves as Green Partners.

VCC1.5

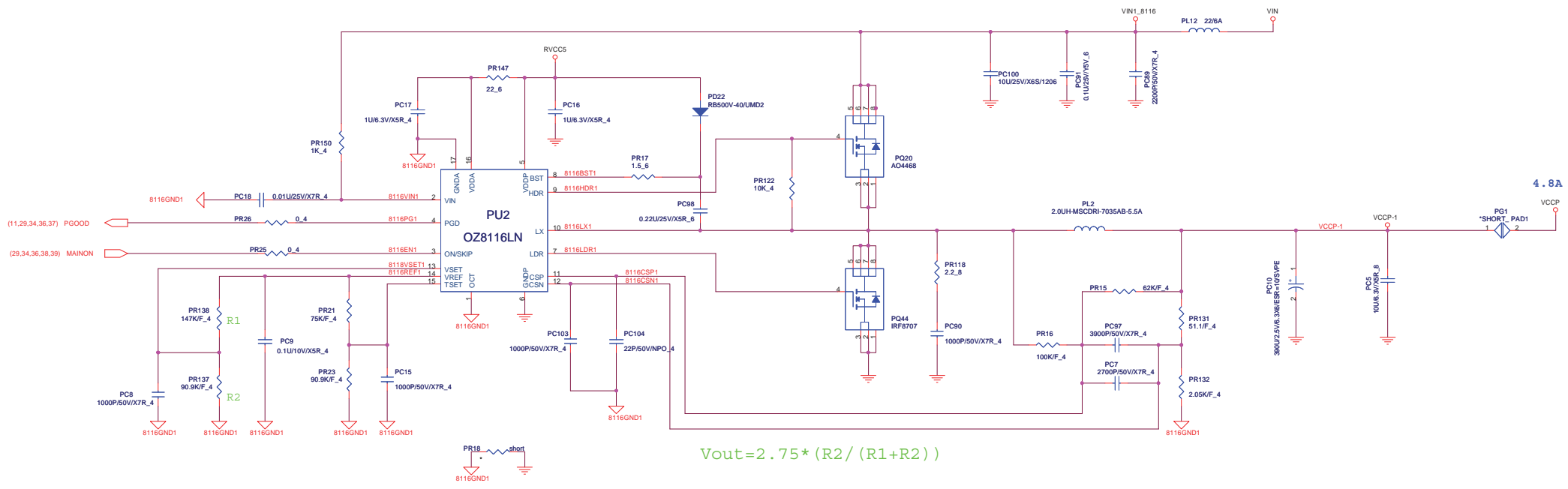


VCC0.89



 QUANTA COMPUTER		Size	Document Number	Rev
			SY3 MB	1G
Date:	Monday, March 30, 2009	Sheet	34	of 44

VCCP



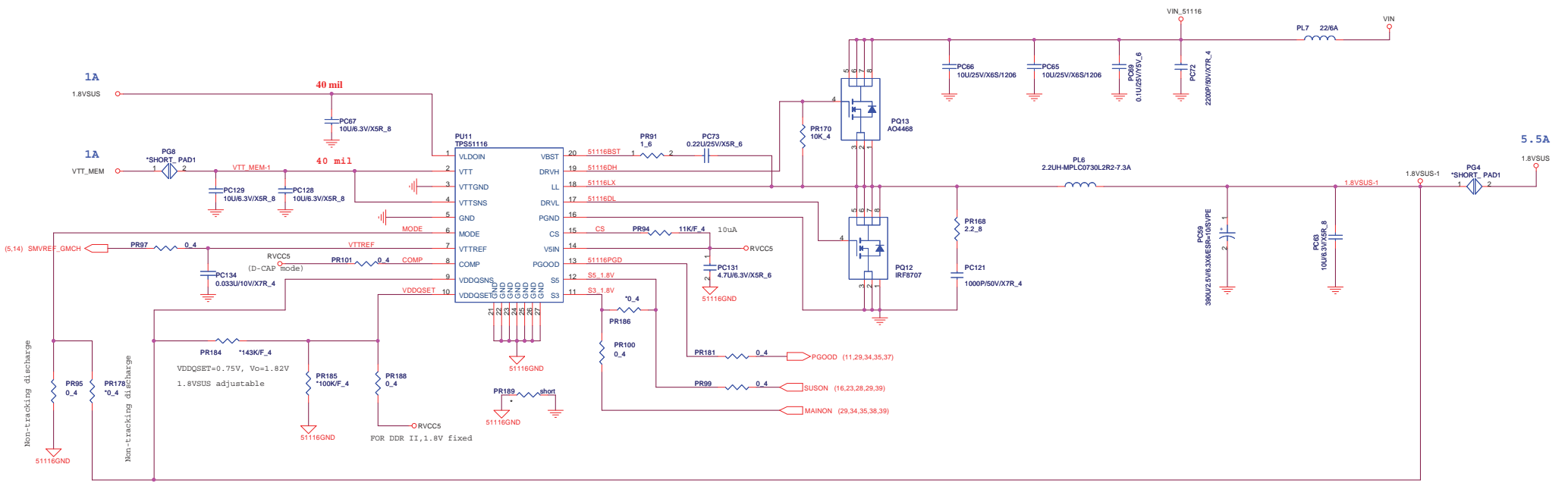
$V_{out} = 2.75 * (R2 / (R1 + R2))$

**QUANTA
COMPUTER**

Size	Document Number	Rev
	SY3 MB	1G
Date:	Monday, March 30, 2009	Sheet 35 of 44

1. Level 1 Environment-related Substances should NEVER be used.
 2. Purchase ink, paint, wire rods, and Molding resins only from the business Partners that Sony approves as Green Partners.

1.8VSUS & VTT_MEM

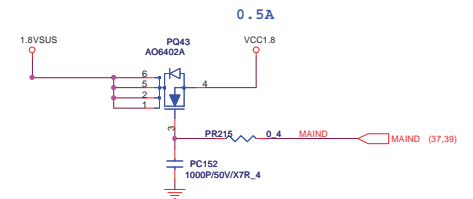


MODE	DISCHARGE MODE
+5V	No discharge
+1.8V	Tracking discharge
GND	Non-tracking discharge

VDDQSET	VDDQ(V)	VTTREF & VTT	NOTE
GND	2.5 fixed	VDDQSNS/2	DDR
5V	1.8 fixed	VDDQSNS/2	DDR2
FB-Resistor	Adjustable	VDDQSNS/2	1.5V<VDDQ<3V

VTT = VTTREF = VDDQSNS/2 = 0.9V

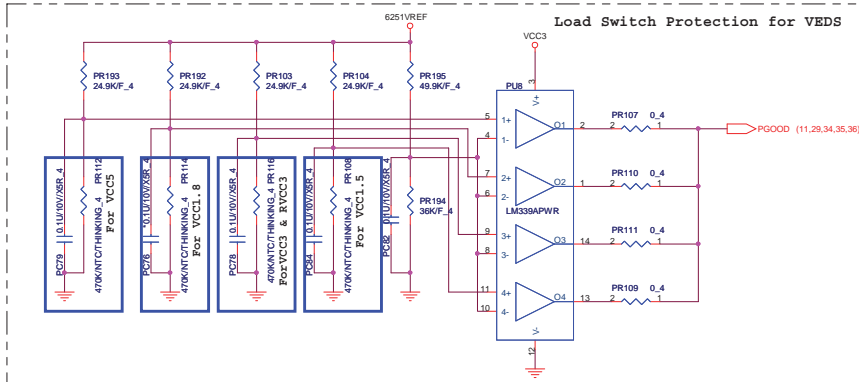
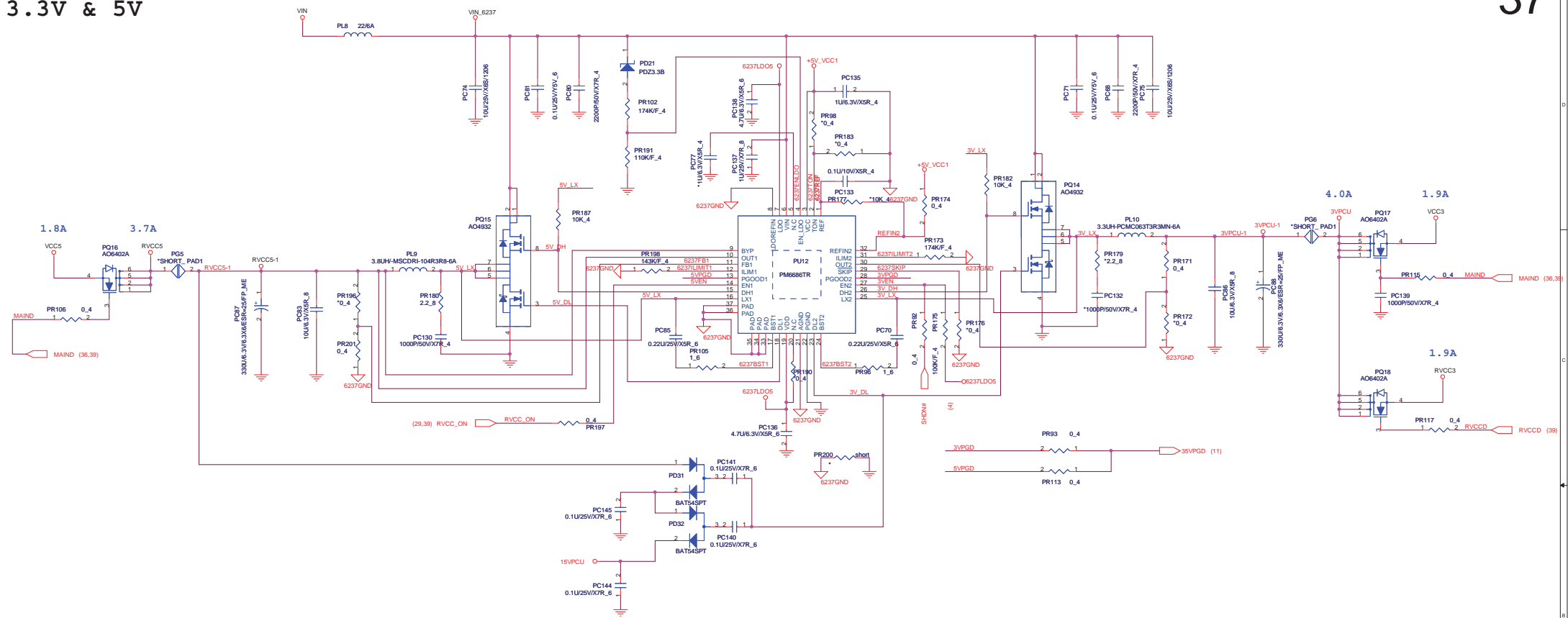
STATE	S3	S5	1.8VSUS	VTTREF	VTT
S0	1	1	on	on	on
S3	0	1	on	on	off
S4/S5	0	0	off	off	off



1.Level 1 Environment-related Substances should NEVER be Used.
 2.Purchase Ink, paint, wire rods, and Soldering resins only from the business Partners that Sony approves as Green Partners.

**QUANTA
COMPUTER**

Size	Document Number	Rev
	SY3 MB	1G
Date:	Monday, March 30, 2009	Sheet 36 of 44

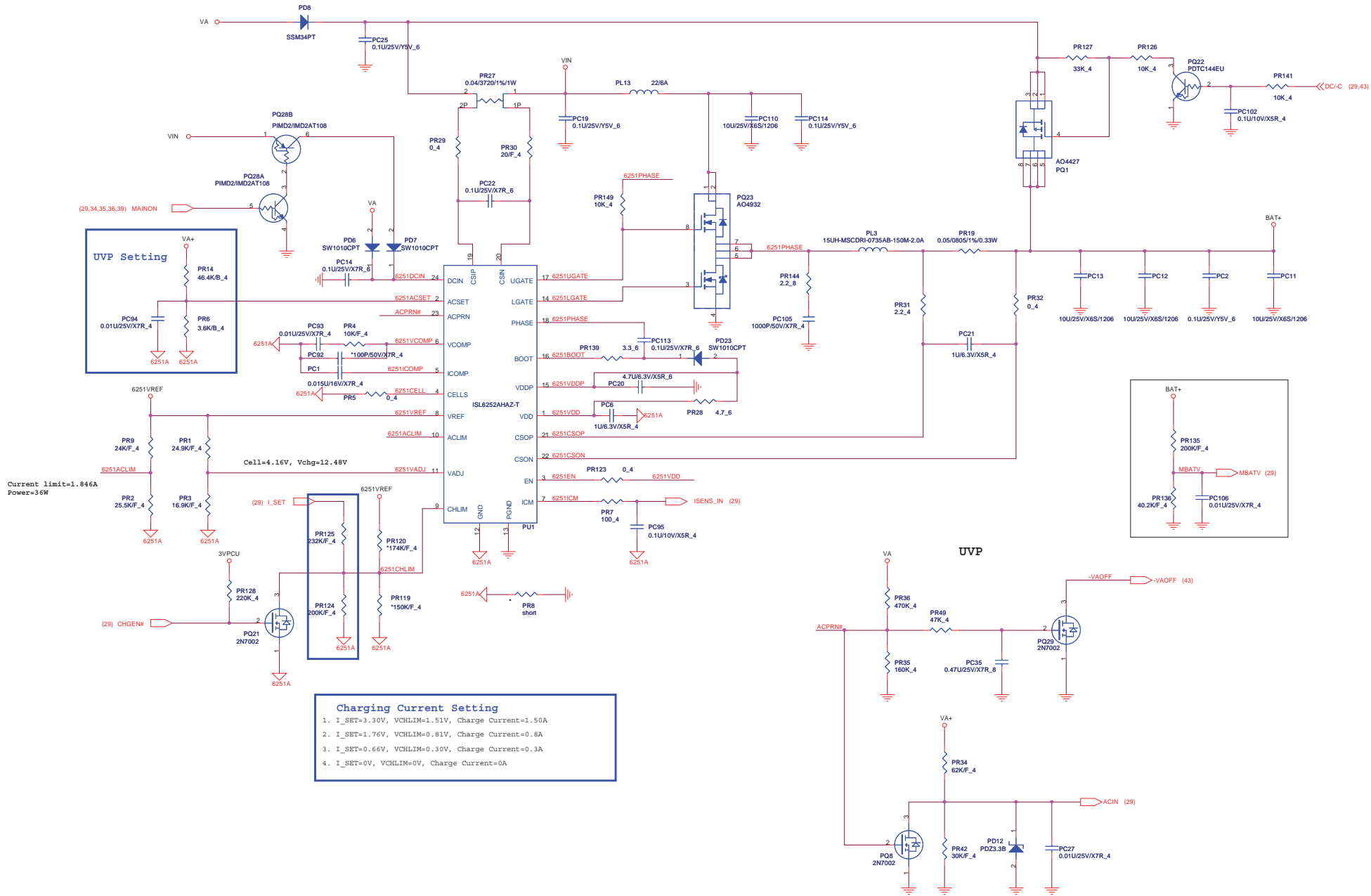


QUANTA COMPUTER

File: **Power 3V & 5V**

Site: **Custom** Document Number: **SY3 MB** Rev: **1G**

Date: **Tuesday, March 31, 2009** Sheet: **37 of 44**



UVP Setting

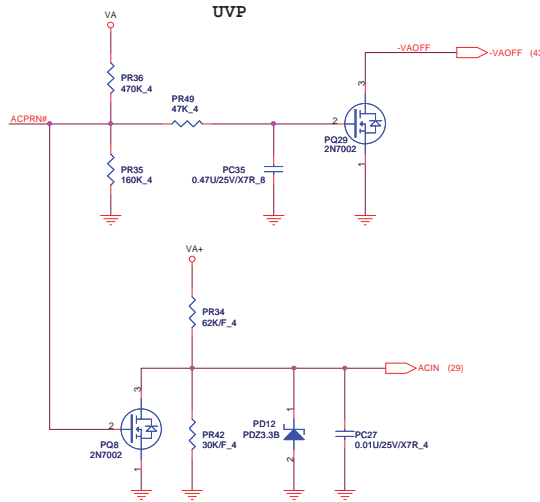
PC84 0.01U/25V/X7R_4
PR14 46.4K/B_4
PR6 3.6K/B_4
6251A

Charging Current Setting

1. I_SET=3.30V, VCHLIM=1.51V, Charge Current=1.50A
2. I_SET=1.76V, VCHLIM=0.81V, Charge Current=0.8A
3. I_SET=0.66V, VCHLIM=0.30V, Charge Current=0.3A
4. I_SET=0V, VCHLIM=0V, Charge Current=0A

Current limit=1.846A
Power=36W

Cell=4.16V, Vchg=12.48V

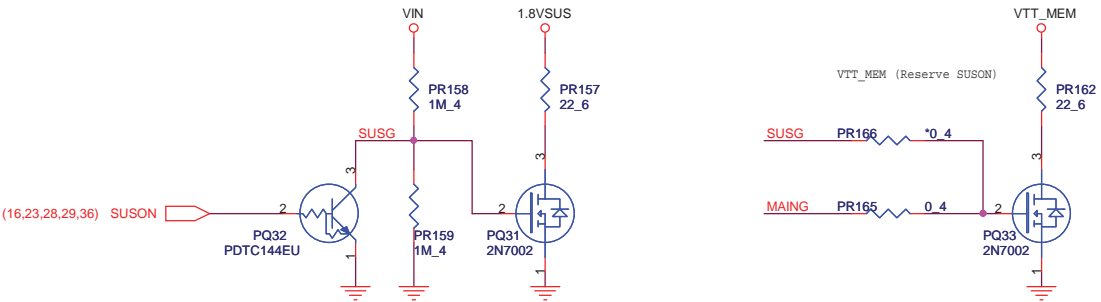
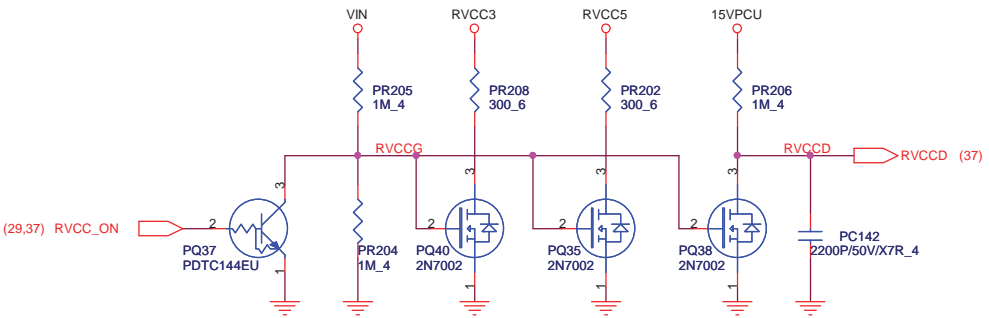
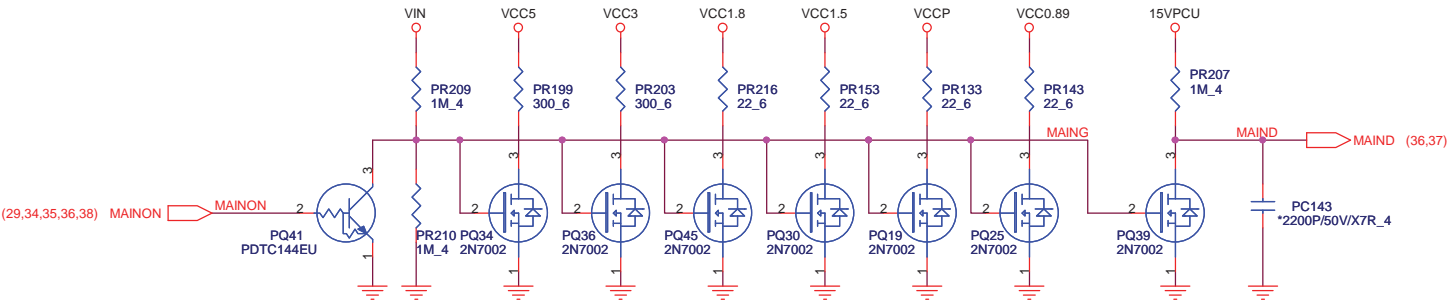



QUANTA COMPUTER

Charger

File: Custom
Document Number: SY3 MB
Date: Tuesday, March 31, 2009
Sheet: 38 of 44
Rev: 1G


1. Level 1 Environment-related Substances should NEVER be Used.
2. Purchase ink, paint, wire rods, and Molding resins only from the business Partners that Sony approves as Green Partners.



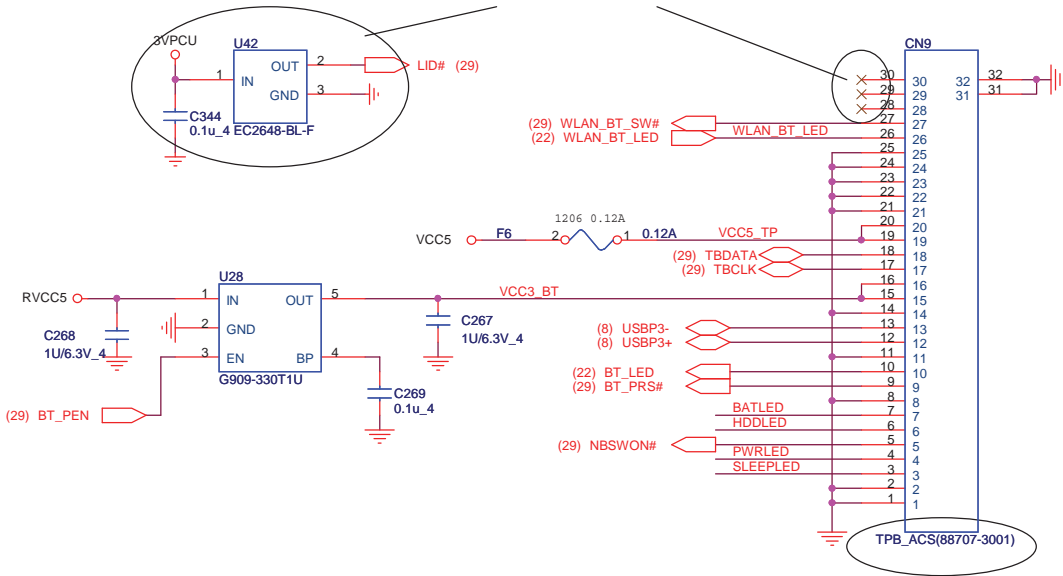
 QUANTA COMPUTER		
Title: Discharge		
Size: B	Document Number: SY3 MB	Rev: 1G
Date: Tuesday, March 31, 2009		
Sheet: 39 of 44		

- 1.Level 1 Environment-related Substances Should NEVER be Used.
- 2.Purchase ink, paint, wire rods, and Molding resins only from the business Partners that Sony approves as Green Partners.

<http://laptop-motherboard-schematic.blogspot.com/>

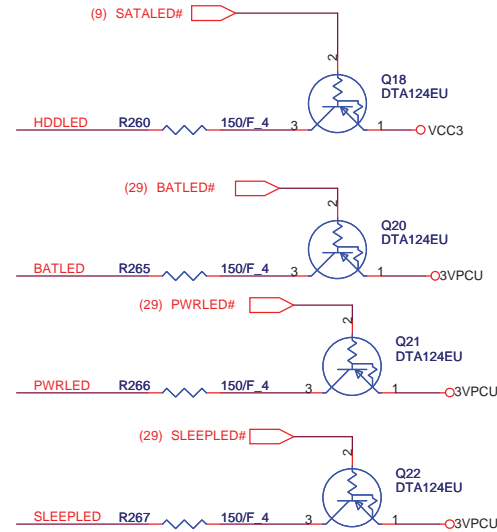
 QUANTA COMPUTER		
Title XDP		
Size A	Document Number SY3 MB	Rev 1G
Date: Tuesday, March 31, 2009 Sheet 40 of 44		

1C: move hall sensor from TP board to MB



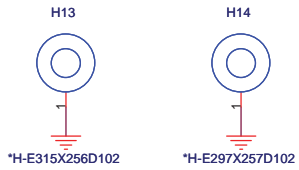
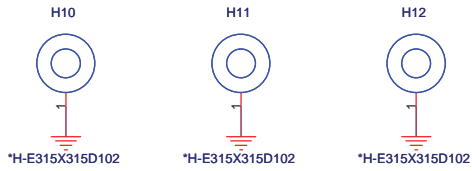
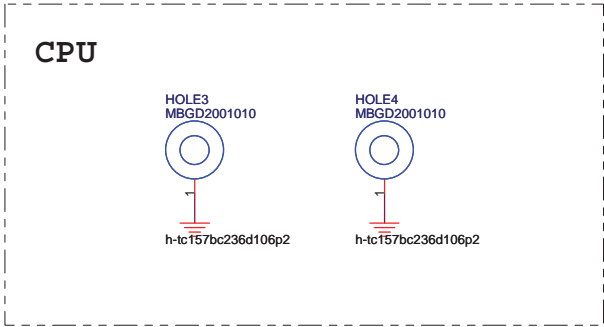
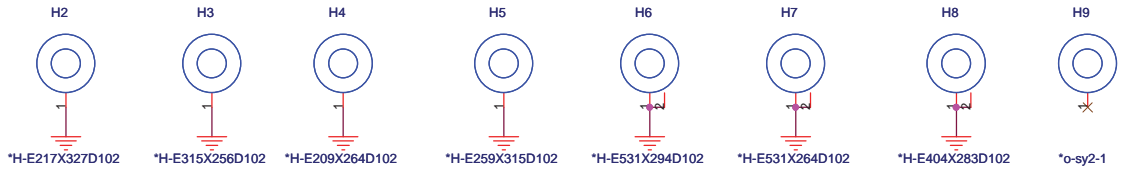
1E: change to the same one with TP board

- "Power, S0 (green), S3 (Umber flash), S4 (off), S5 (off)
- "Charger status in Umber color, Charge (on), Error (flash quickly)
- "HDD status, LED in yellow color
- "Caps Lock/Num Lock/Scroll Lock in green color
- "Wireless status in green color
- "Memory Stick Duo/SD Card status in yellow color
- "Camera status in green color (on Camera module)



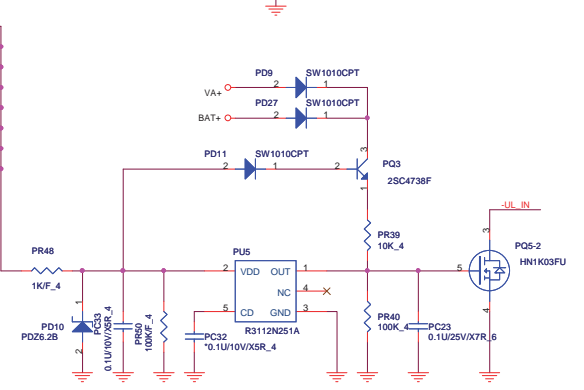
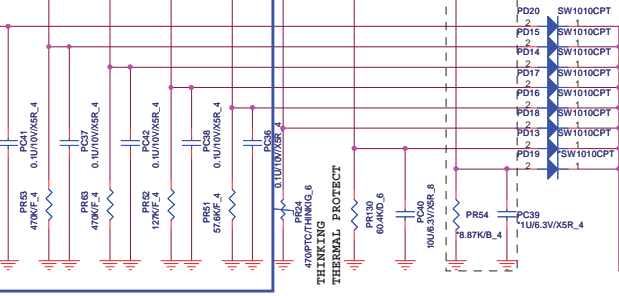
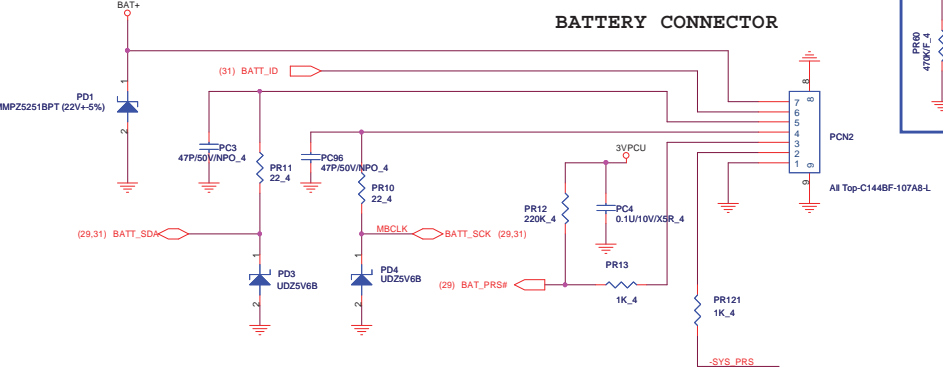
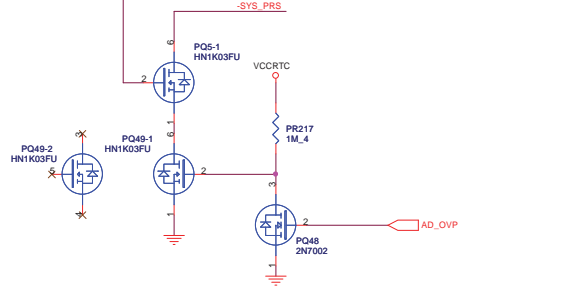
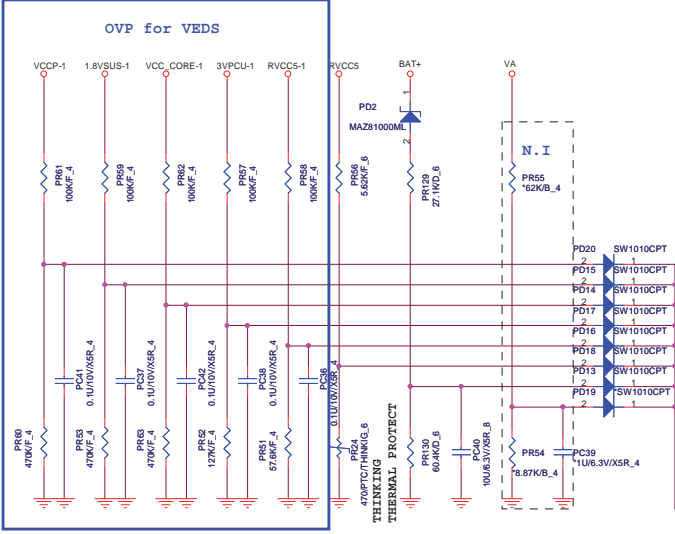
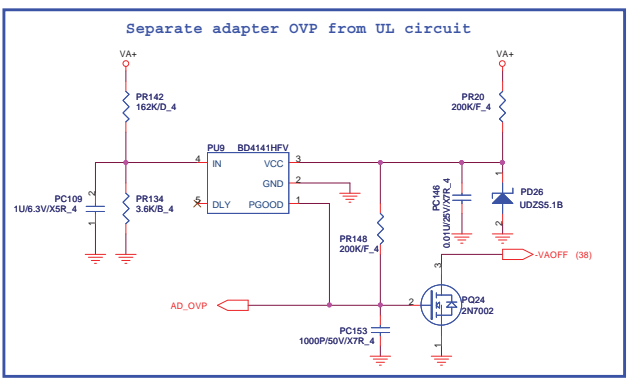
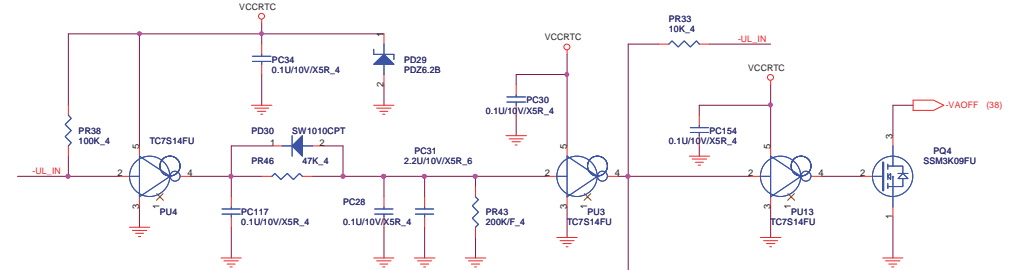
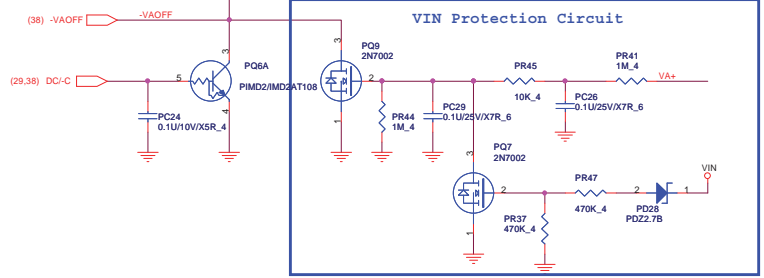
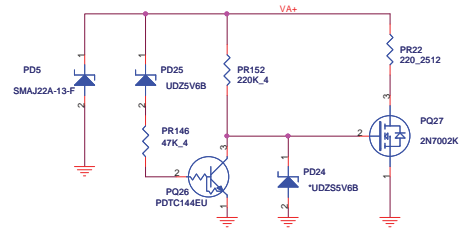
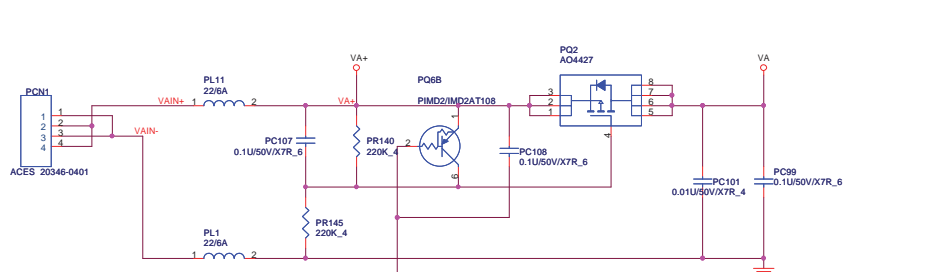
QUANTA COMPUTER		
Title: Small Board		
Size B	Document Number: SY3 MB	Rev 1G
Date: Tuesday, March 31, 2009		
Sheet 41 of 44		E

1.Level 1 Environment-related Substances Should NEVER be Used.
 2.Purchase ink, paint, wire rods, and Molding resins only from the business Partners that Sony approves as Green Partners.



QUANTA COMPUTER		
Title Screw Hole		
Size B	Document Number SY3 MB	Rev 1G
Date: Tuesday, March 31, 2009 Sheet 42 of 44		

1.Level 1 Environment-related Substances Should NEVER be Used.
 2.Purchase ink, paint, wire rods, and Molding resins only from the business Partners that Sony approves as Green Partners.



QUANTA COMPUTER

Adapter/BAT IN

File:
 Size:
 Date: Tuesday, March 31, 2009

Document Number: **SY3 MB**

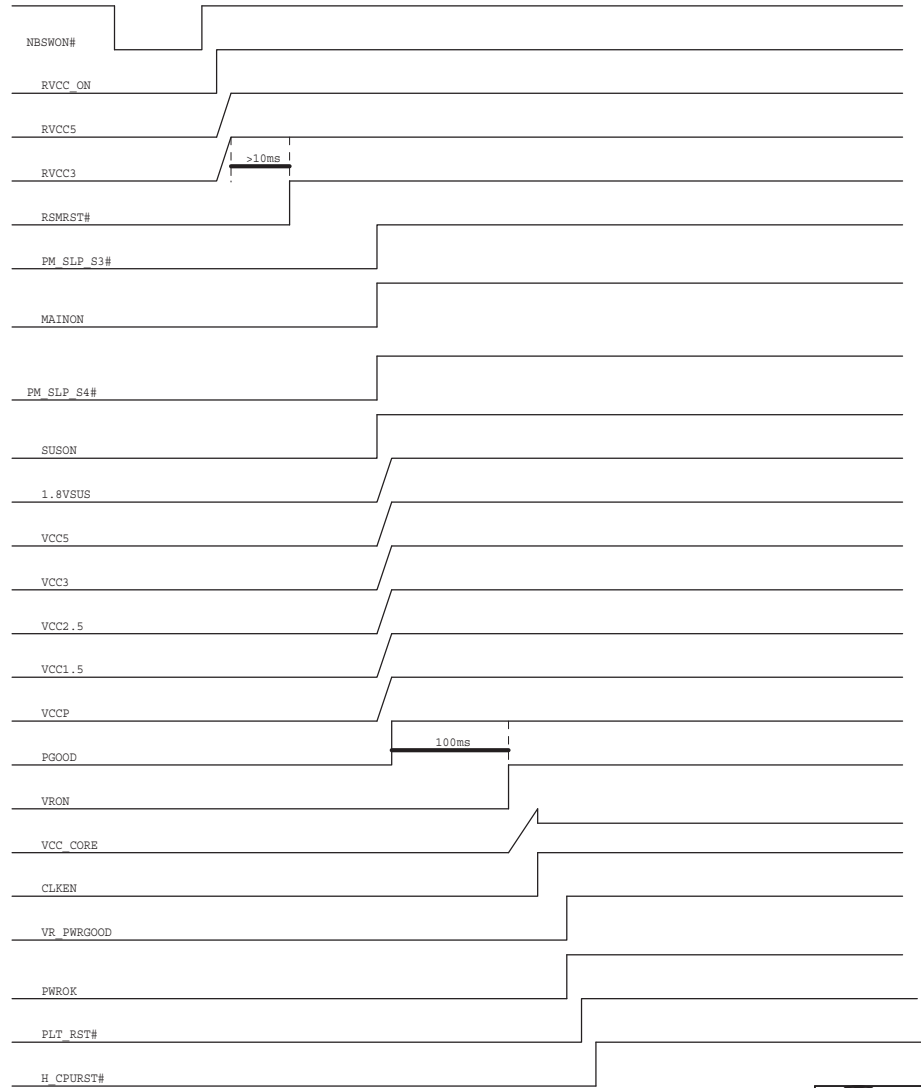
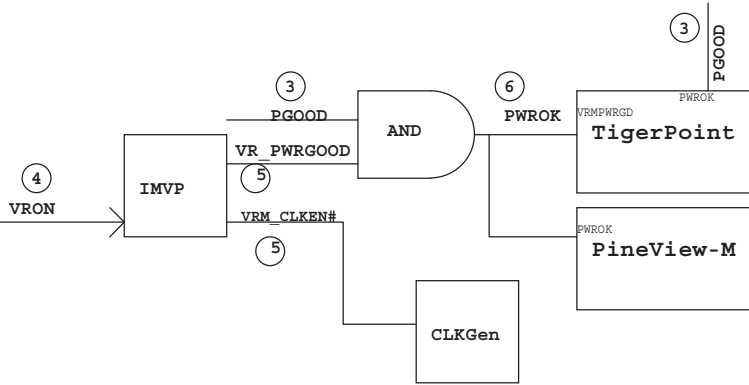
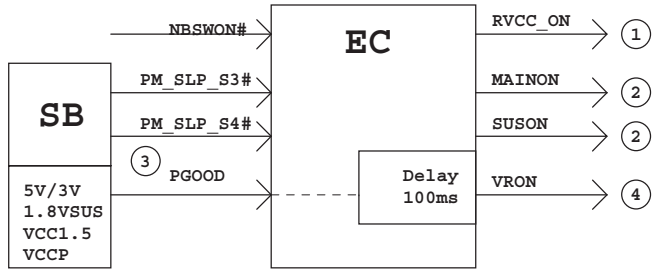
Rev: **1G**

Date: Tuesday, March 31, 2009 Sheet: 43 of 44

1. Level 1 Environment-related Substances should NEVER be used.
 2. Purchase ink, paint, wire rods, and Molding resins only from the business Partners that Sony approves as Green Partners.

SY3 Power-up Block

SY3 Power-up Sequencing



AC Mode	S0	S3	S4	S5	BATT	S0	S3	S4	S5
6237LDO5	ON	ON	ON	ON	6237LDO5	ON	ON	ON	ON
3VPCU	ON	ON	ON	ON	3VPCU	ON	ON	ON	ON
RVCC5	ON	ON	ON	OFF	RVCC5	ON	ON	OFF	OFF
RVCC3	ON	ON	ON	OFF	RVCC3	ON	ON	OFF	OFF
1.8VSUS	ON	ON	OFF	OFF	1.8VSUS	ON	ON	OFF	OFF
VCC5	ON	OFF	OFF	OFF	VCC5	ON	OFF	OFF	OFF
VCC3	ON	OFF	OFF	OFF	VCC3	ON	OFF	OFF	OFF
VCC1.8	ON	OFF	OFF	OFF	VCC1.8	ON	OFF	OFF	OFF
VCCP	ON	OFF	OFF	OFF	VCCP	ON	OFF	OFF	OFF
VCC_CORE	ON	OFF	OFF	OFF	VCC_CORE	ON	OFF	OFF	OFF

**QUANTA
COMPUTER**

Title: **Power-up Sequence**

Size B	Document Number SY3 MB	Rev 1G
--------	----------------------------------	--------

Date: Tuesday, March 31, 2009 Sheet 44 of 44