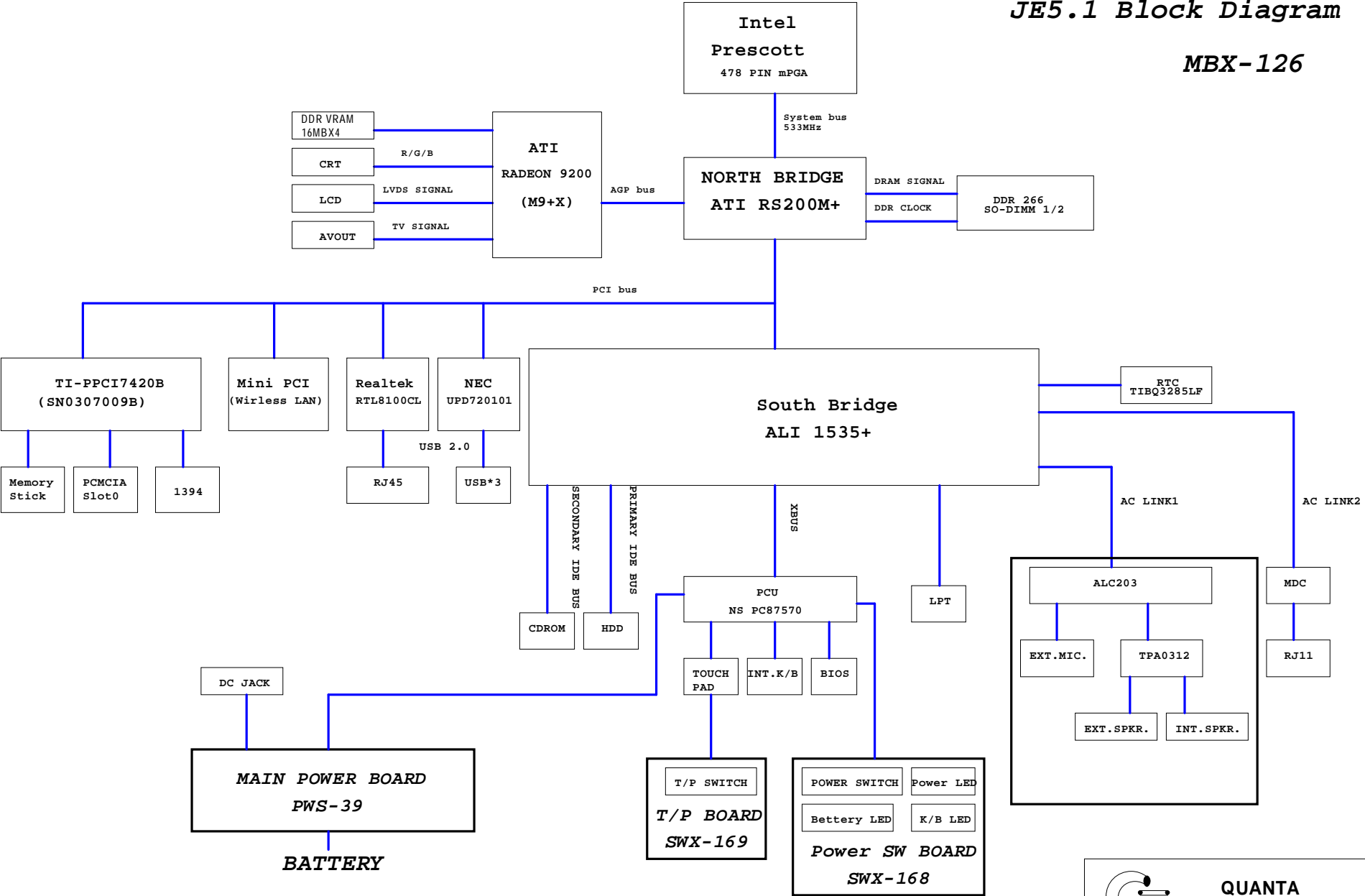



# JE5.1 Block Diagram

## MBX-126



Level 1 environment-related substances should NEVER be used.  
 Molding resin, ink, paint and covered wiring material should be procured from the supplier approved as a Sony's green partner.

 <b>QUANTA COMPUTER</b>		
	JE5.1 Main Board	1A
Date:	Monday, August 30, 2004	Sheet 1 of 36

# Page List

01 Block Diagram  
 02 Page List  
 03 Prescott CPU-1  
 04 Prescott CPU-2  
 05 RS200M-AGTL+

06 RS200M-DDR I/F  
 07 RS200M-PCI & AGP I/F  
 08 RS200M-VIDEO I/F & CLKGEN  
 09 SYSTEM CONFIGURATION  
 10 DDR Terminator

11 DDR SODIMMx2  
 12 CRT PORT/TV  
 13 ALI M1535+ (PCI,ISA)-1/2  
 14 ALI M1535+ (IDE)-2/2  
 15 USB2.0 Controller (NEC-uPD720101)

16 ATI M9+ 1/3  
 17 ATI M9+ 2/3  
 18 ATI M9+ 3/3  
 19 VGA DDR VRAMX2 1/2  
 20 VGA DDR VRAMX2 2/2

21 AGP SSC  
 22 TI7420-A (CARDBUS+1394+MS)  
 23 TI7420-B (CARDBUS+1394+MS)  
 24 Cardbus slot  
 25 LAN interface RTL8100CL

26 MiniPCI & MDC  
 27 RTC/POWER GOOD LOGIC  
 28 IDE/HDD/LPT Connector  
 29 PCU NS 87570  
 30 Int. Keyboard/Bios/FAN

31 VCORE POWER 1  
 32 MAX1546(VCORE POWER 2)  
 33 MAX1632(5V/3V/12V)  
 34 MAX1844(2.5V/1.8V/1.25V )  
 35 MAX1844(VCC1.5)  
 36 Load Switch

5 HA[3..31] HA[3..31]

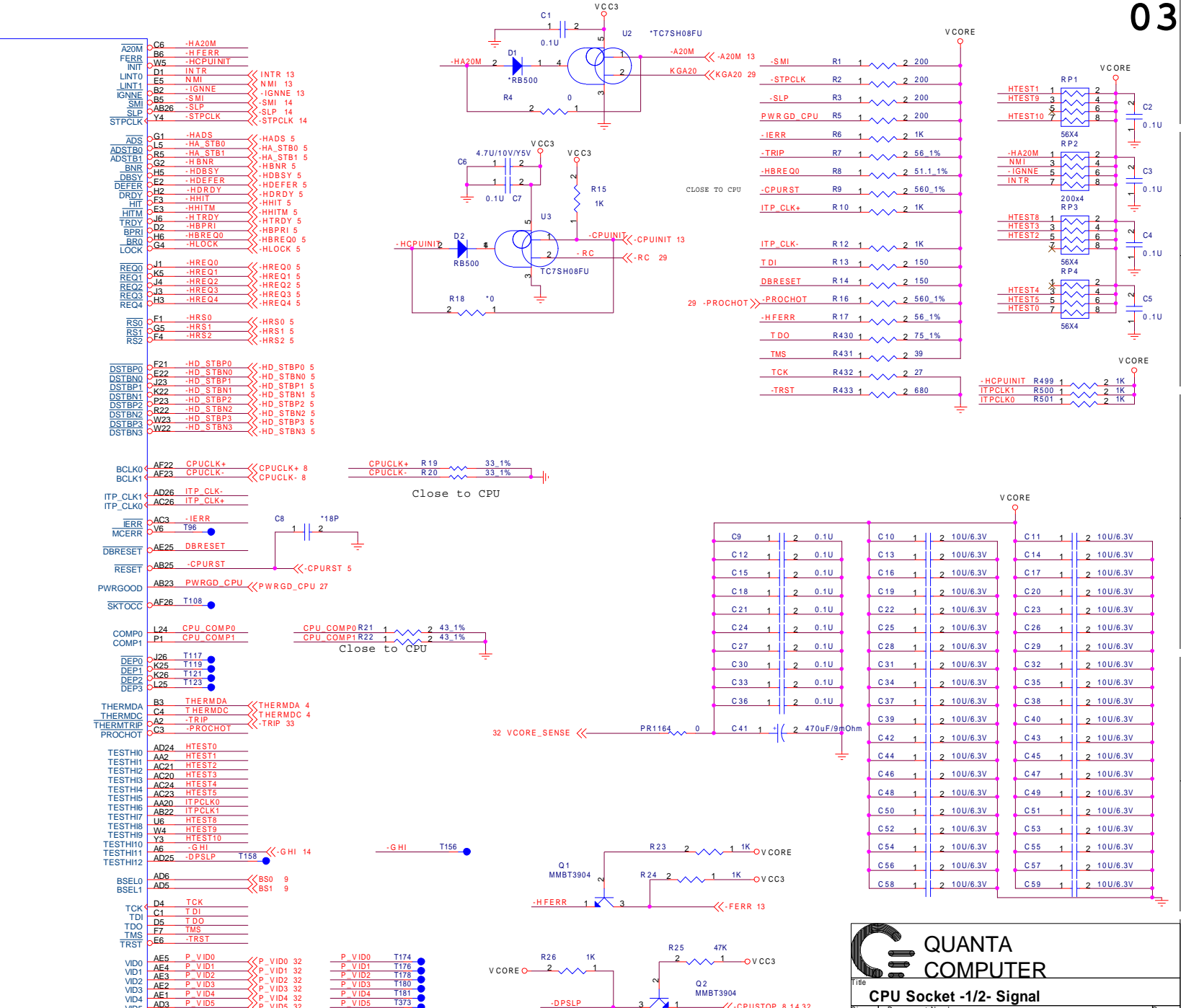
HA3	K2	AF#3
HA4	K4	AF#4
HA5	L4	AF#5
HA6	K3	AF#6
HA7	L3	AF#7
HA8	M3	AF#8
HA9	M2	AF#9
HA10	M3	AF#10
HA11	M4	AF#11
HA12	M1	AF#12
HA13	M1	AF#13
HA14	N2	AF#14
HA15	N4	AF#15
HA16	N5	AF#16
HA17	T1	AF#17
HA18	R2	AF#18
HA19	P2	AF#19
HA20	P3	AF#20
HA21	R3	AF#21
HA22	R2	AF#22
HA23	U1	AF#23
HA24	P4	AF#24
HA25	U3	AF#25
HA26	T4	AF#26
HA27	V2	AF#27
HA28	R6	AF#28
HA29	W1	AF#29
HA30	T3	AF#30
HA31	U4	AF#31
T57	V3	AF#32
T58	W2	AF#33
T60	Y2	AF#34
T62	AB1	AF#35

5 HD[0..63] HD[0..63]

H D 0	B21	DA#0
H D 1	A25	DA#1
H D 2	A26	DA#2
H D 3	A28	DA#3
H D 4	C21	DA#4
H D 5	D22	DA#5
H D 6	B24	DA#6
H D 7	C23	DA#7
H D 8	C24	DA#8
H D 9	B25	DA#9
HD 10	G22	DA#10
HD 11	H21	DA#11
HD 12	C26	DA#12
HD 13	D23	DA#13
HD 14	J21	DA#14
HD 15	D25	DA#15
HD 16	H22	DA#16
HD 17	E24	DA#17
HD 18	G23	DA#18
HD 19	F23	DA#19
HD 20	F24	DA#20
HD 21	F25	DA#21
HD 22	F26	DA#22
HD 23	D26	DA#23
HD 24	L21	DA#24
HD 25	G26	DA#25
HD 26	H24	DA#26
HD 27	M21	DA#27
HD 28	L22	DA#28
HD 29	J24	DA#29
HD 30	K23	DA#30
HD 31	H23	DA#31
HD 32	M23	DA#32
HD 33	N22	DA#33
HD 34	P21	DA#34
HD 35	M24	DA#35
HD 36	N23	DA#36
HD 37	M26	DA#37
HD 38	N26	DA#38
HD 39	N25	DA#39
HD 40	R21	DA#40
HD 41	R22	DA#41
HD 42	R23	DA#42
HD 43	R24	DA#43
HD 44	T26	DA#44
HD 45	T25	DA#45
HD 46	T23	DA#46
HD 47	T22	DA#47
HD 48	U26	DA#48
HD 49	U24	DA#49
HD 50	U23	DA#50
HD 51	U22	DA#51
HD 52	U21	DA#52
HD 53	V22	DA#53
HD 54	V24	DA#54
HD 55	W26	DA#55
HD 56	Y22	DA#56
HD 57	W25	DA#57
HD 58	Y23	DA#58
HD 59	Y24	DA#59
HD 60	Y21	DA#60
HD 61	A25	DA#61
HD 62	A22	DA#62
HD 63	A24	DA#63
-HDB10	E21	DB#10
-HDB11	P21	DB#11
-HDB12	P22	DB#12
-HDB13	V21	DB#13

5 -HDB[0..3] -HDB[0..3]

U1A ZIF SOCKET479	A20M	C6	-HA20M	
	FERR	B6	-HFERR	
	LINT0	L4	-HCHPUINIT	
	LINT1	D1	INTR	INTR 13
	LINT2	E5	NMI	NMI 13
	IGNNE	B2	-IGNNE	-IGNNE 13
	SMI	G5	-SMI	SMI 14
	SLP	AB26	-SLP	SLP 14
	STPCLK	Y4	-STPCLK	STPCLK 14
	HADS	G1	-HADS	HADS 5
	HA_STB0	L5	-HA_STB0	HA_STB0 5
	HA_STB1	R5	-HA_STB1	HA_STB1 5
	HBNR	G2	-HBNR	HBNR 5
	HDBSY	H5	-HDBSY	HDBSY 5
	HDEFER	E2	-HDEFER	HDEFER 5
	HDRDY	H2	-HDRDY	HDRDY 5
	HHITM	E3	-HHITM	HHITM 5
	HTRDY	J6	-HTRDY	HTRDY 5
	HBPR1	D2	-HBPR1	HBPR1 5
	HBREQ0	H6	-HBREQ0	HBREQ0 5
	HLOCK	G4	-HLOCK	HLOCK 5
	HREQ0	J1	-HREQ0	HREQ0 5
	HREQ1	K5	-HREQ1	HREQ1 5
	HREQ2	J4	-HREQ2	HREQ2 5
	HREQ3	U3	-HREQ3	HREQ3 5
	HREQ4	H3	-HREQ4	HREQ4 5
	HRS0	F1	-HRS0	HRS0 5
	HRS1	G5	-HRS1	HRS1 5
	HRS2	F4	-HRS2	HRS2 5
	HD_STBP0	F21	-HD_STBP0	HD_STBP0 5
	HD_STBN0	E22	-HD_STBN0	HD_STBN0 5
	HD_STBP1	J23	-HD_STBP1	HD_STBP1 5
	HD_STBN1	K22	-HD_STBN1	HD_STBN1 5
	HD_STBP2	P23	-HD_STBP2	HD_STBP2 5
	HD_STBN2	R22	-HD_STBN2	HD_STBN2 5
	HD_STBP3	W23	-HD_STBP3	HD_STBP3 5
	HD_STBN3	W22	-HD_STBN3	HD_STBN3 5
	CPUCLK+	AF22	CPUCLK+	CPUCLK+ 8
	CPUCLK-	AF23	CPUCLK-	CPUCLK- 8
	ITP_CLK+	AD26	ITP_CLK+	ITP_CLK+ 8
	ITP_CLK-	AC26	ITP_CLK-	ITP_CLK- 8
	-IERR	AC3	-IERR	-IERR 5
	DBRESET	AE25	DBRESET	DBRESET 5
	-CPURST	AB25	-CPURST	-CPURST 5
	PWRGD_CPU	AB23	PWRGD_CPU	PWRGD_CPU 27
	CPU_COMP0	L24	CPU_COMP0	CPU_COMP0 21
	CPU_COMP1	P1	CPU_COMP1	CPU_COMP1 22
	THERMDA	B3	THERMDA	THERMDA 4
	THERMDC	C4	THERMDC	THERMDC 4
	-TRIP	A2	-TRIP	-TRIP 33
	-PROCHOT	C3	-PROCHOT	-PROCHOT 33
	HTEST0	AD24	HTEST0	HTEST0 1
	HTEST1	AA2	HTEST1	HTEST1 2
	HTEST2	AC21	HTEST2	HTEST2 3
	HTEST3	AC20	HTEST3	HTEST3 4
	HTEST4	AC24	HTEST4	HTEST4 5
	HTEST5	AC23	HTEST5	HTEST5 6
	ITPCLK0	AA20	ITPCLK0	ITPCLK0 7
	ITPCLK1	AB22	ITPCLK1	ITPCLK1 8
	HTEST8	U6	HTEST8	HTEST8 9
	HTEST9	W4	HTEST9	HTEST9 10
	HTEST10	Y3	HTEST10	HTEST10 11
	-GHI	A6	-GHI	-GHI 14
	-DPSP	AD25	-DPSP	-DPSP 15
	B50	AD6	B50	B50 9
	B51	AD5	B51	B51 9
	TCK	D4	TCK	TCK 1
	TDI	C1	TDI	TDI 1
	TDO	D5	TDO	TDO 1
	TMS	E7	TMS	TMS 1
	-TRST	E6	-TRST	-TRST 1
	P_VID0	AE5	P_VID0	P_VID0 32
	P_VID1	AE4	P_VID1	P_VID1 32
	P_VID2	AE3	P_VID2	P_VID2 32
	P_VID3	AE2	P_VID3	P_VID3 32
	P_VID4	AE1	P_VID4	P_VID4 32
	P_VID5	AD3	P_VID5	P_VID5 32
	T174	P_VID0	T174	T174 32
	T176	P_VID1	T176	T176 32
	T178	P_VID2	T178	T178 32
	T180	P_VID3	T180	T180 32
	T181	P_VID4	T181	T181 32
	T373	P_VID5	T373	T373 32



**QUANTA COMPUTER**

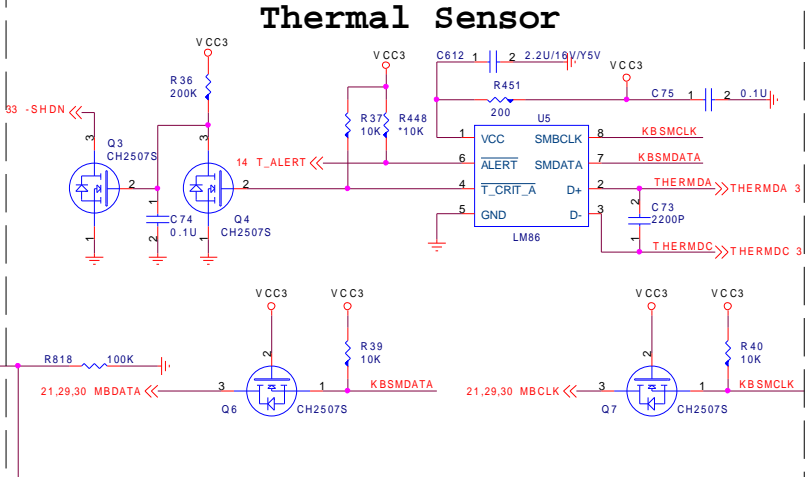
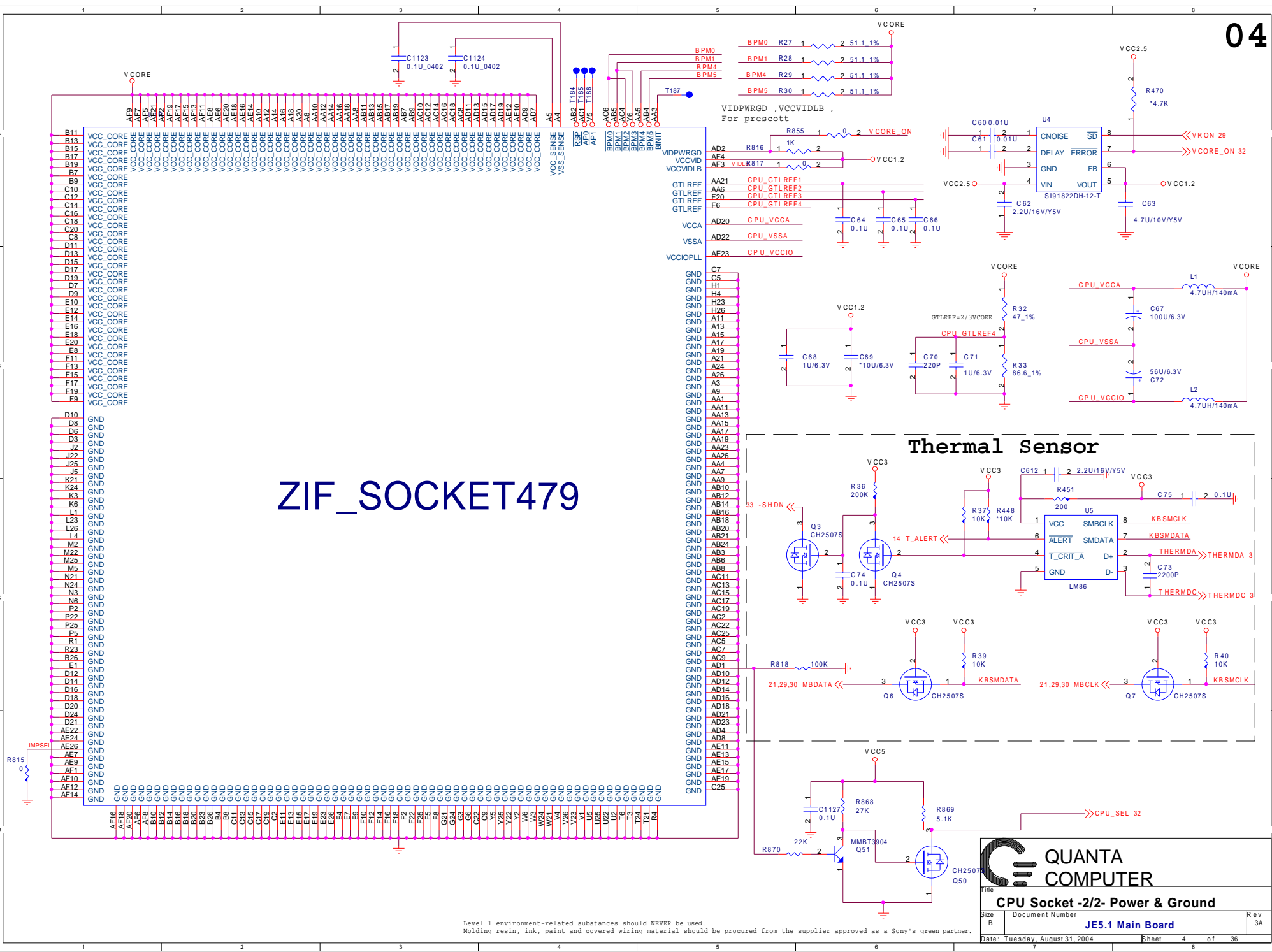
File: **CPU Socket -1/2- Signal**

Size B Document Number **JE5.1 Main Board** Rev 3A

Date: Tuesday, August 31, 2004 Sheet 3 of 36

Level 1 environment-related substances should NEVER be used. Molding resin, ink, paint and covered wiring material should be procured from the supplier approved as a Sony's green partner.

# ZIF\_SOCKET479



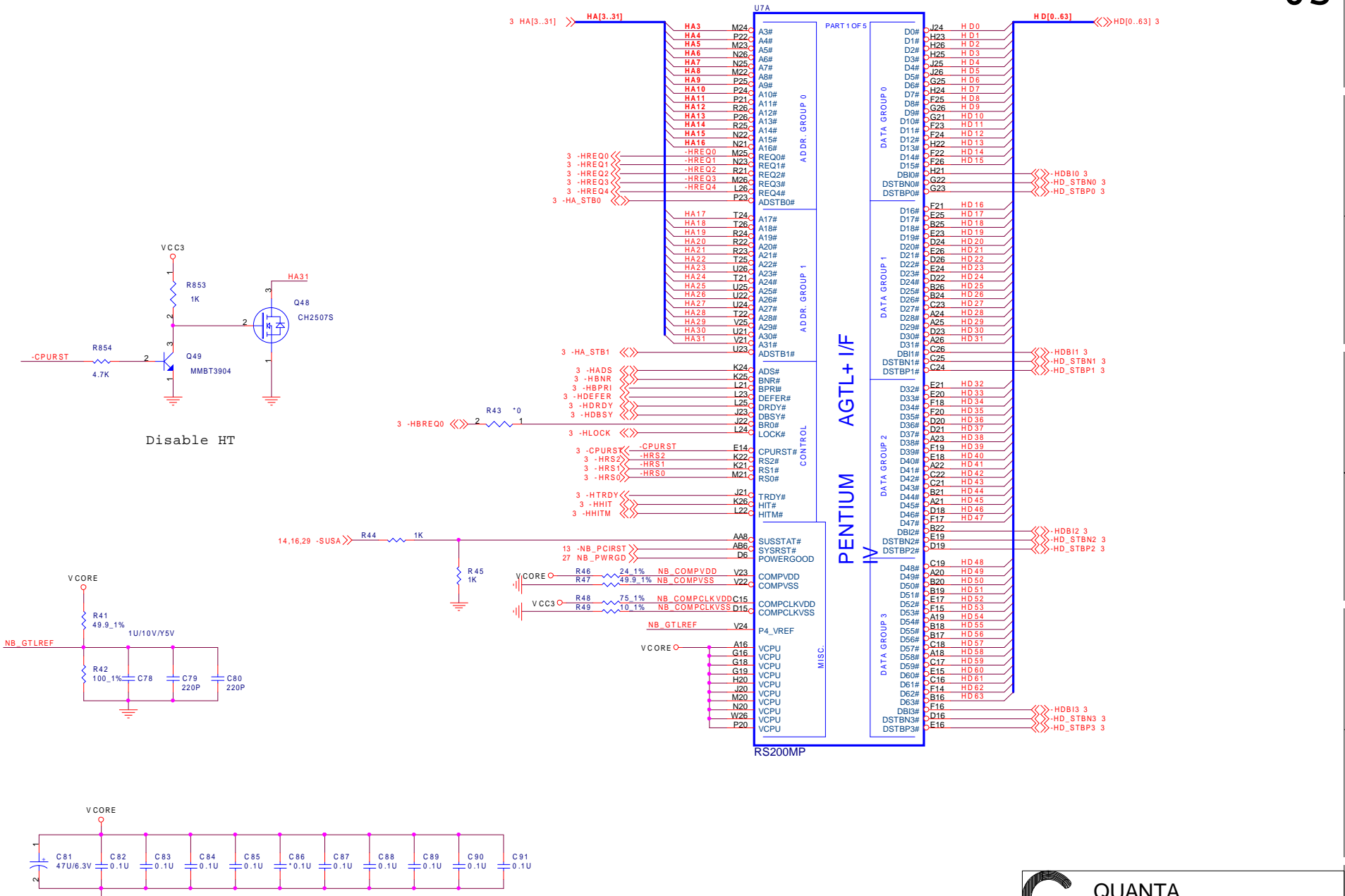
**QUANTA COMPUTER**

**CPU Socket -2/2- Power & Ground**

Size B	Document Number	<b>JE5.1 Main Board</b>	Rev 3A
--------	-----------------	-------------------------	--------

Date: Tuesday, August 31, 2004 Sheet 4 of 36

Level 1 environment-related substances should NEVER be used.  
Molding resin, ink, paint and covered wiring material should be procured from the supplier approved as a Sony's green partner.



Disable HT

**QUANTA COMPUTER**

File: **RS200M 1/4**

Size B	Document Number	JE5.1 Main Board	Rev 2A
--------	-----------------	------------------	--------

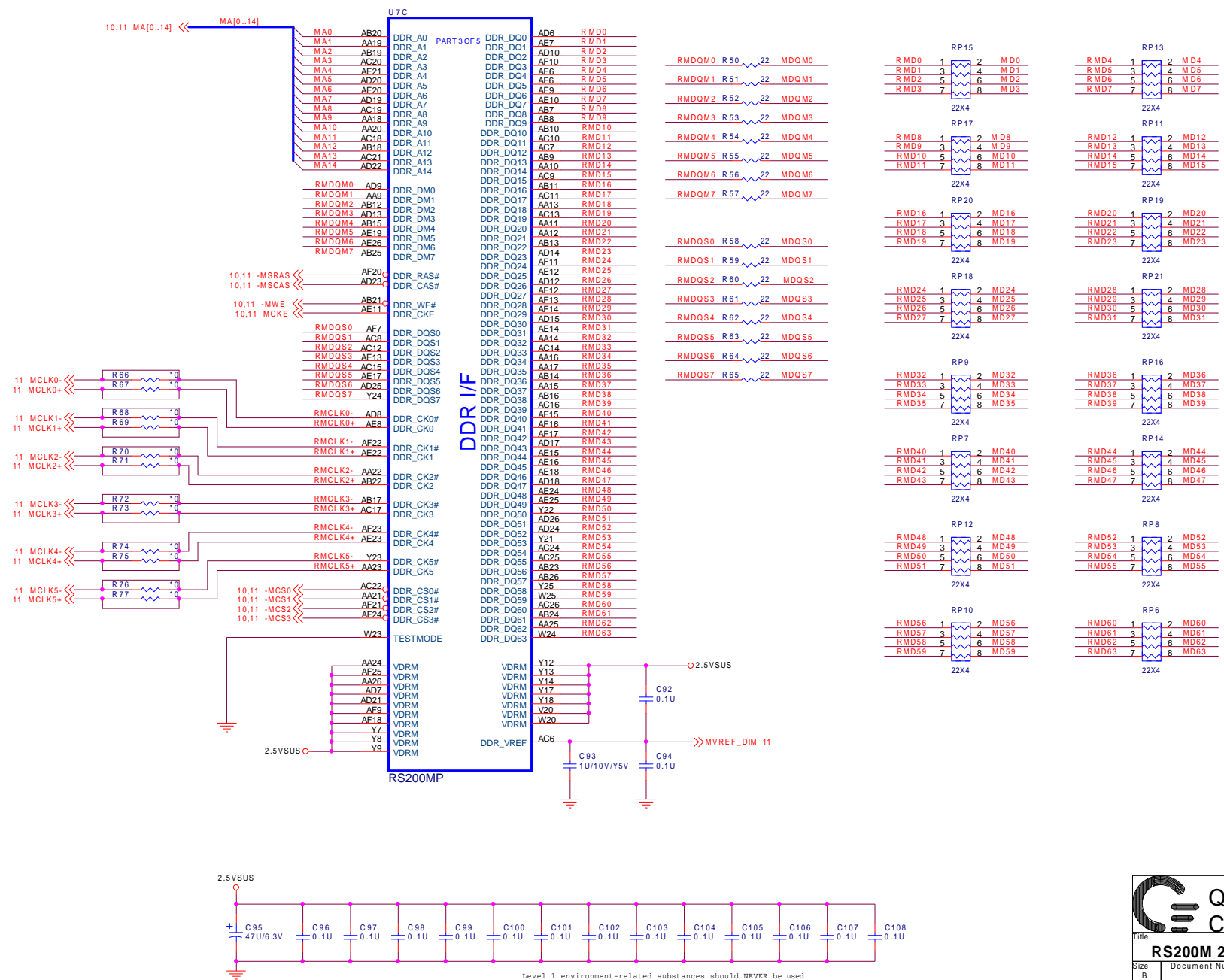
Date: Tuesday, August 31, 2004 Sheet 5 of 36

Level 1 environment-related substances should NEVER be used.  
Molding resin, ink, paint and covered wiring material should be procured from the supplier approved as a Sony's green partner.

# ATI\_RS200M+ - 2 ( DDR Interface )

06

MD[0..63] >> MD[0..63] 10,11  
 MDQM[0..7] >> MDQM[0..7] 10,11  
 MDQS[0..7] >> MDQS[0..7] 10,11



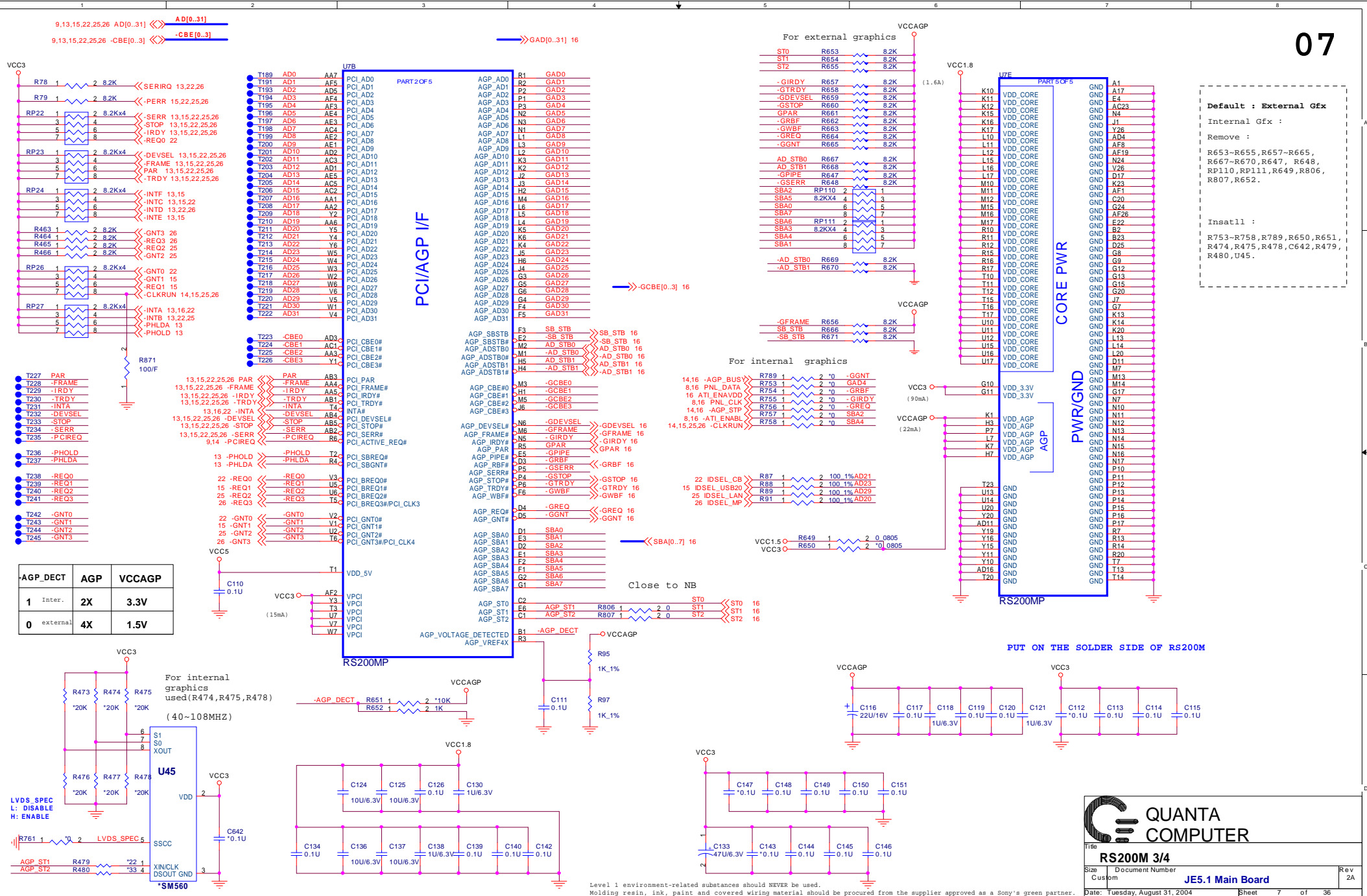
**QUANTA COMPUTER**

File: **RS200M 2/4**

Size: B Document Number: **JE5.1 Main Board** Rev: 1A

Date: Tuesday, August 31, 2004 Sheet: 6 of 36

Level 1 environment-related substances should NEVER be used.  
 Molding resin, ink, paint and covered wiring material should be procured from the supplier approved as a Sony's green partner.



**Default : External Gfx**

Internal Gfx :

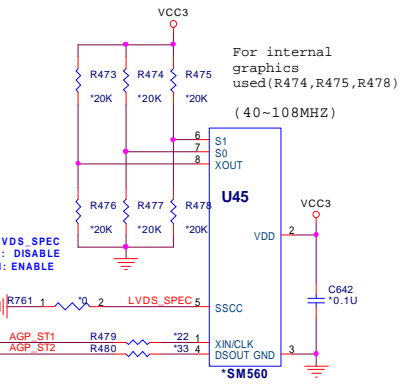
Remove :

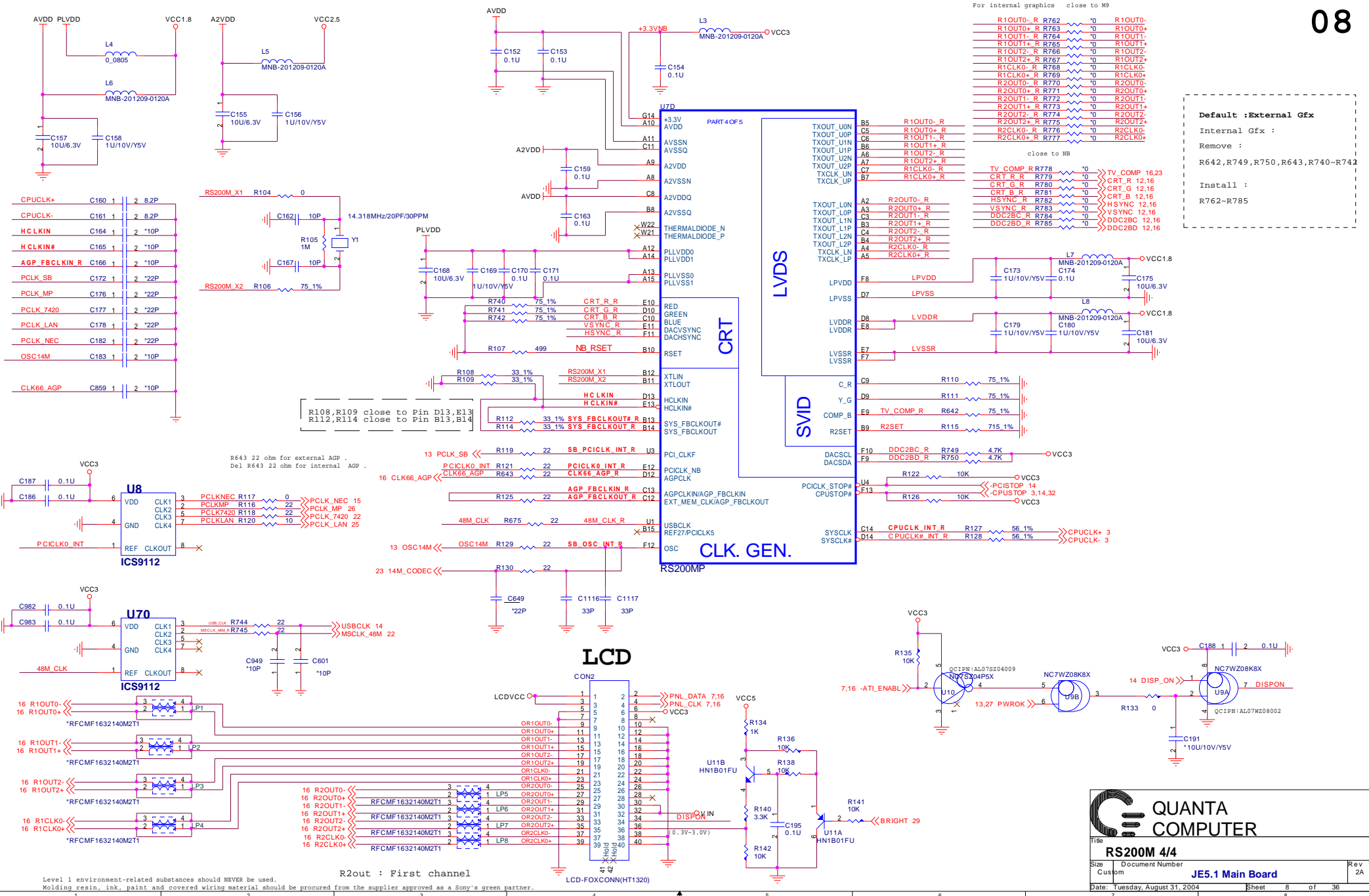
R653-R655, R657-R665, R667-R670, R647, R648, RP110, RP111, R649, R806, R807, R652.

Install1 :

R753-R758, R789, R650, R651, R474, R475, R478, C642, R479, R480, U45.

AGP_DECT	AGP	VCCAGP
1	Inter.	2X 3.3V
0	external	4X 1.5V





For internal graphics close to M9

R10U0-	R762	0	R10U0-
R10U0+	R763	0	R10U0+
R10U1-	R764	0	R10U1-
R10U1+	R765	0	R10U1+
R10U2-	R766	0	R10U2-
R10U2+	R767	0	R10U2+
R1CLK0-	R768	0	R1CLK0-
R1CLK0+	R769	0	R1CLK0+
R20U0-	R770	0	R20U0-
R20U0+	R771	0	R20U0+
R20U1-	R772	0	R20U1-
R20U1+	R773	0	R20U1+
R20U2-	R774	0	R20U2-
R20U2+	R775	0	R20U2+
R2CLK0-	R776	0	R2CLK0-
R2CLK0+	R777	0	R2CLK0+

close to NB

TV_COMP	R778	0	TV_COMP 1623
CRT_R	R779	0	CRT_R 12,16
CRT_G	R780	0	CRT_G 12,16
CRT_B	R781	0	CRT_B 12,16
HSYNC	R782	0	HSYNC 12,16
VSYNC	R783	0	VSYNC 12,16
DDC2B	R784	0	DDC2B 12,16
DDC2D	R785	0	DDC2D 12,16

**Default : External Gfx**

Internal Gfx :

Remove :

R642, R749, R750, R643, R740-R742

Install :

R762-R785

R108, R109 close to Pin D13, E13  
R112, R114 close to Pin B13, B14

R643 22 ohm for external AGP.  
Del R643 22 ohm for internal AGP.

R2out : First channel

Level 1 environment-related substances should NEVER be used.  
Molding resin, ink, paint and covered wiring material should be procured from the supplier approved as a Sony's green partner.

**QUANTA COMPUTER**

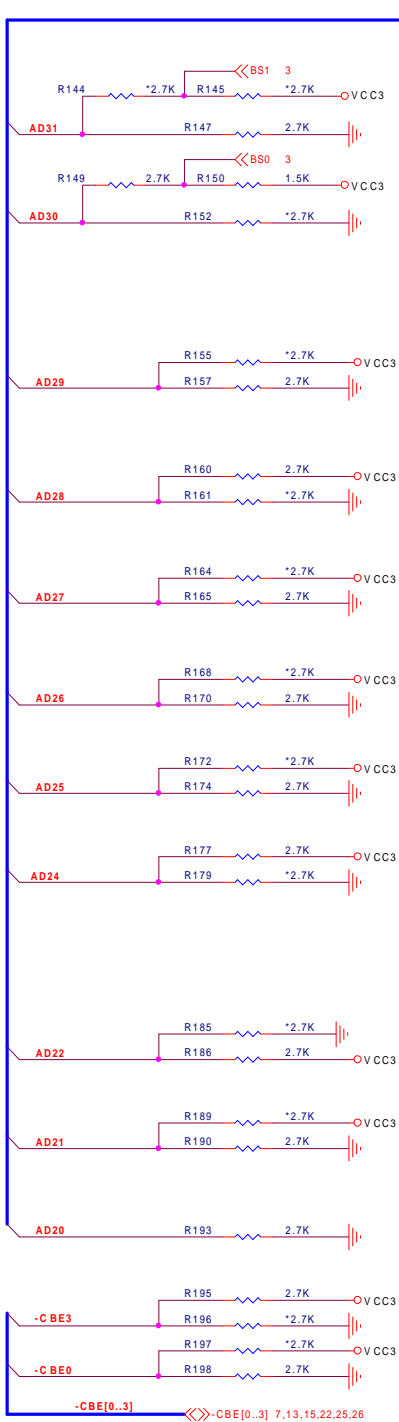
Title: **RS200M 4/4**

Size: Document Number

Com: **JE5.1 Main Board**

Date: Tuesday, August 31, 2004 Sheet 8 of 36 Rev 2A





**AD[31..30] : CLK SPEED**  
**DEFAULT: 01**

00: 100 MHZ  
 01: 133 MHZ  
 10: 166MHZ  
 11: 66 MHZ

**AD29: RS200M FULL CONFIGURATION**  
**DEFAULT: 1**

0: FULL CONFIGURATION  
 1: USES DEFAULT VALUES

**AD28: SPREAD SPECTRUM ENABLE**  
**DEFAULT: 1**

0: DISABLE  
 1: ENABLE

**AD27: MCERR OBSERVATION**  
**DEFAULT: 0**

0: ENABLE  
 1: DISABLE

**AD26: BUS PARKING**  
**DEFAULT: 0**

0: ENABLE  
 1: DISABLE

**AD25: BINIT# OBSERVATION**  
**DEFAULT: 0**

0: ENABLE  
 1: DISABLE

**AD24: IOQ ENABLE**  
**DEFAULT: 1**

0: SET IOQ TO 1  
 1: SET IOQ TO 12

**AD22: REF27 ENABLE**  
**DEFAULT: 1**

0: PCICLK5 OUTPUT  
 1: 27 MHz REFERENCE CLOCK INPUT

**AD21 : CLK\_BYPASS (TEST ONLY)**  
**DEFAULT : 0**

0: NORMAL  
 1: TEST MODE

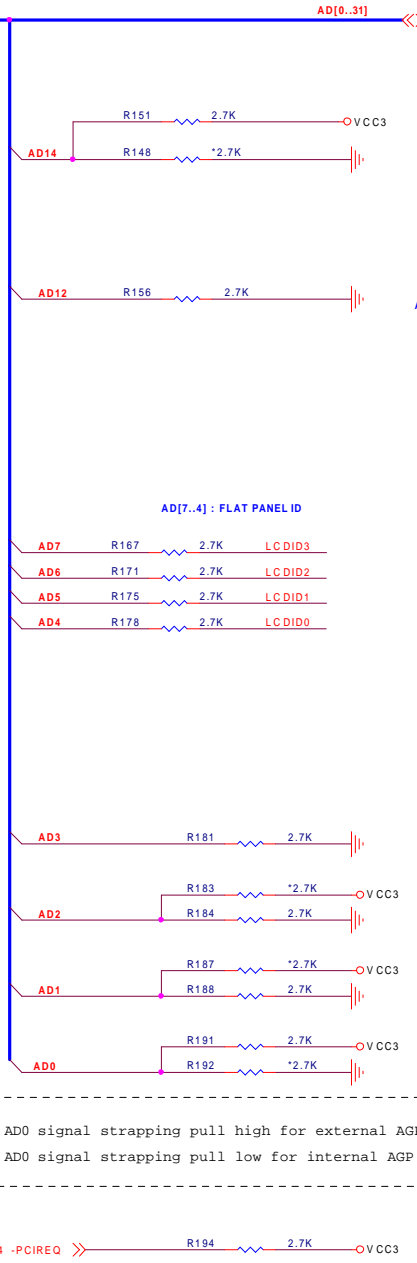
**AD20 - PCICLK EXPANSION**  
**DEFAULT: 0**

1: PCI\_REQ#3, PCI\_GNT#3 USED AS PCICLK  
 0: PCI\_REQ#3, PCI\_GNT#3 USED AS REQ/GNT

**CBE#3: PROD TEST**  
**DEFAULT: 1**

0: SHORT TIMERS  
 1: NORMAL OPERATION

**CBE#0: RESERVE**



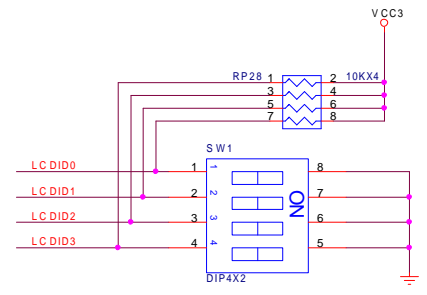
AD[0..31] <<<>> AD[0..31] 7,13,15,22,25,26

**AD14 : AGTL+ VDDQ**

0: 1.2V MOBILE PART  
 1: 1.7V DESKTOP PART

**AD[12] : FLAT PANEL ID MSB**

**AD[7..4] : FLAT PANEL ID**



**AD3: A\_LINK ENABLE**  
**DEFAULT: 0**

0: PCI- 33MHz, MULTI POINT CONNECTION  
 1: A\_LINK, POINT TO POINT INTERCONNECT

**AD2: AUTO CALIBRATION ENABLE**  
**DEFAULT: 0**

0: AUTO CALIBRATION ENABLE  
 1: AUTO CALIBRATION DISABLE

**AD1: FrcShortReset**  
**DEFAULT: 0**

0: NORMAL OPERATION  
 1: TEST MODE

**AD0: PCI/AGP CONFIG.**  
**DEFAULT: 1**

1: EXTERNAL AGP 66 MHz  
 0: INTERNAL AGP 33 MHz

**PCI\_PREQACT#: INTERNAL CLOCK GEN.**  
**DEFAULT: 1**

0: DISABLE  
 1: ENABLE

The AD0 signal strapping pull high for external AGP .  
 The AD0 signal strapping pull low for internal AGP .



Level 1 environment-related substances should NEVER be used.  
 Molding resin, ink, paint and covered wiring material should be procured from the supplier approved as a Sony's green partner.

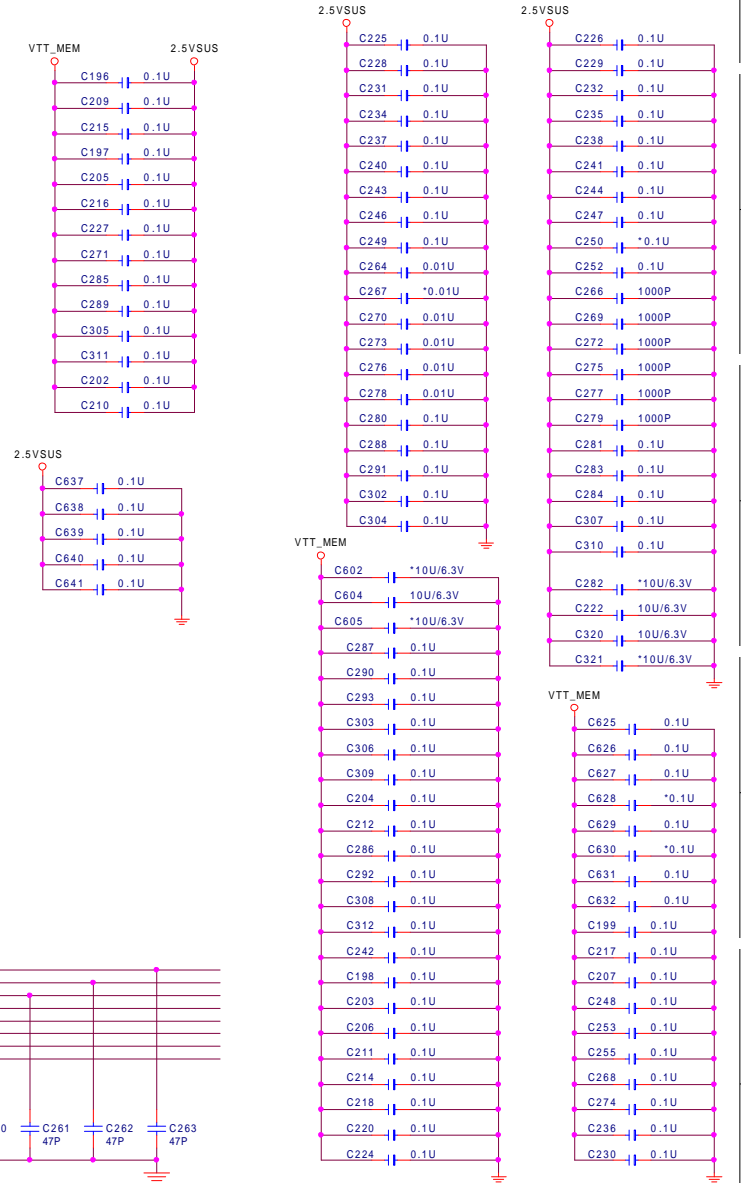
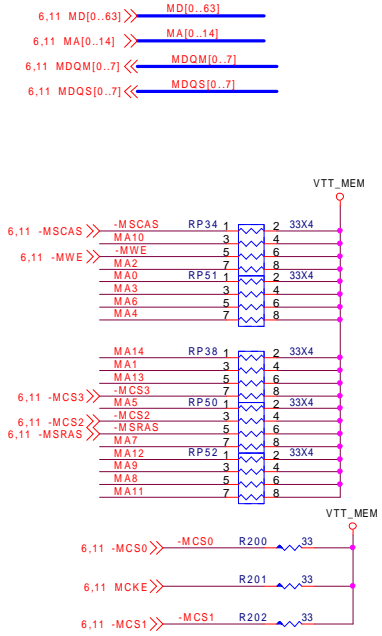
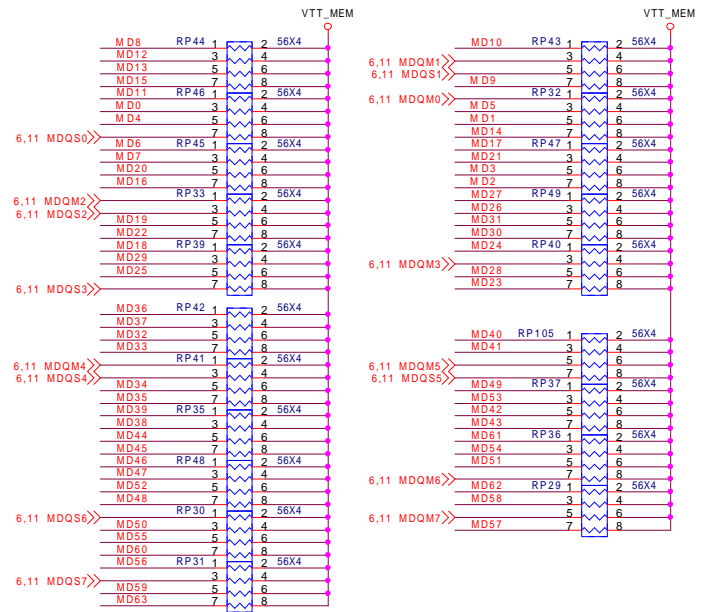
**QUANTA COMPUTER**

**SYSTEM CONFIG**

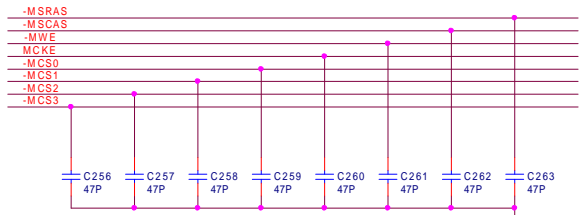
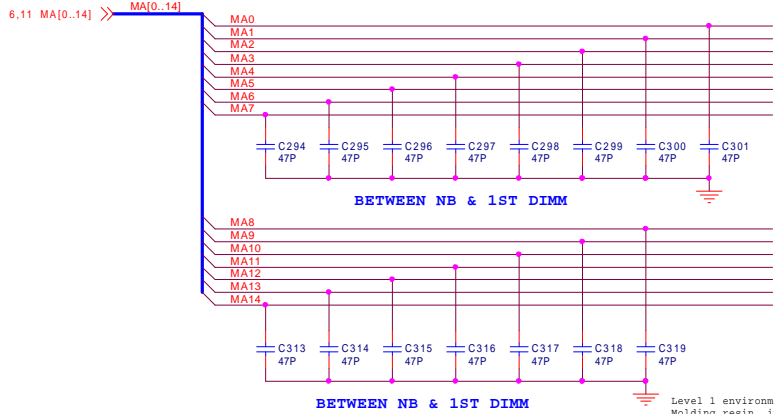
Size B	Document Number	JE5.1 Main Board	Rev 1A
Date: Tuesday, August 31, 2004	Sheet 9	of 36	

# DDR TERMINATOR

10



# MA Terminator



**QUANTA COMPUTER**

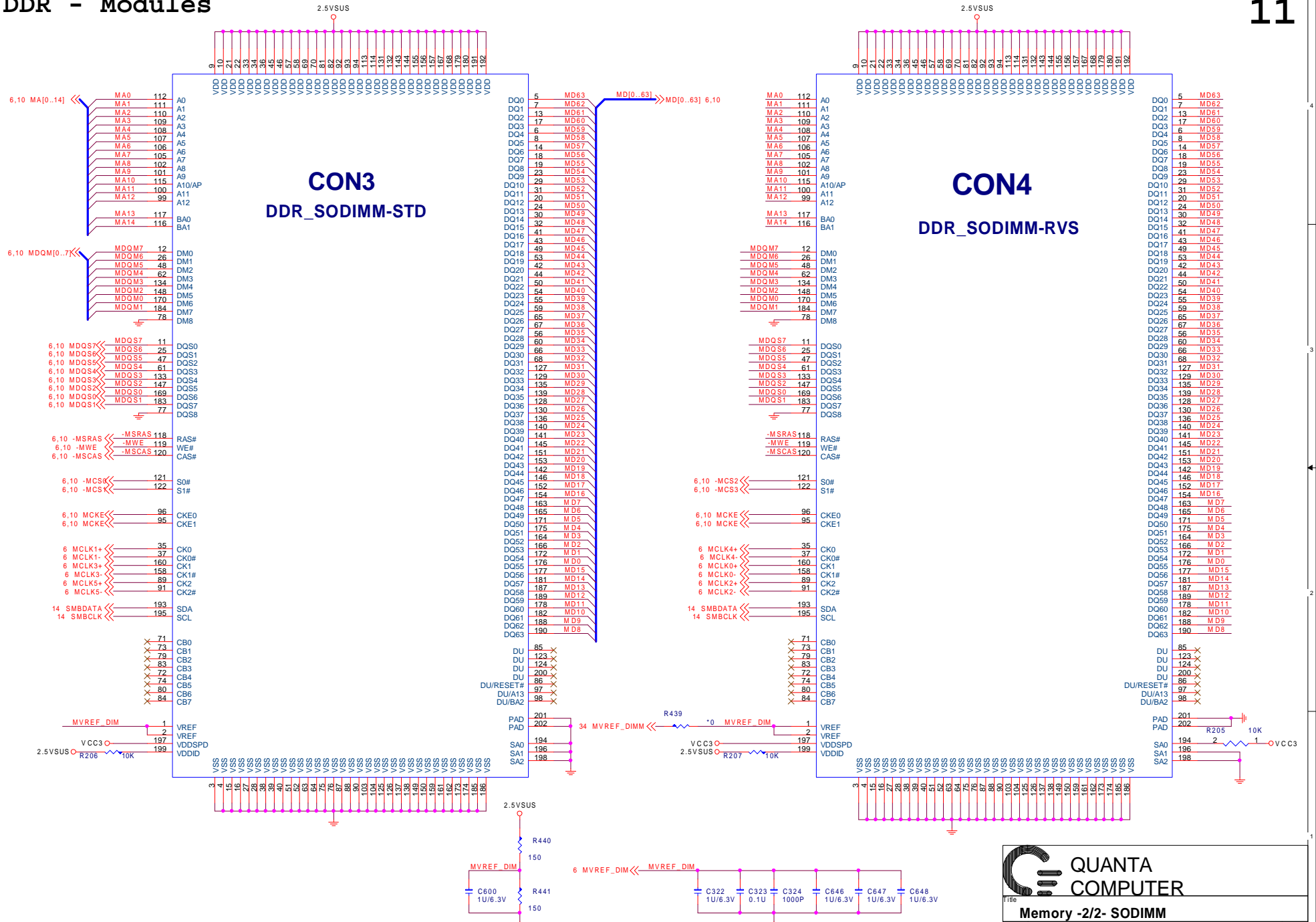
File: **Memory -1/2- Terminator**

Size B Document Number **JE5.1 Main Board** Rev 1A

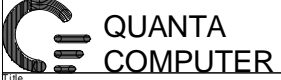
Date: Tuesday, August 31, 2004 Sheet 10 of 36

Level 1 environment-related substances should NEVER be used.  
 Molding resin, ink, paint and covered wiring material should be procured from the supplier approved as a Sony's green partner.

# DDR - Modules



Level 1 environment-related substances should NEVER be used. Molding resin, ink, paint and covered wiring material should be procured from the supplier approved as a Sony's green partner.

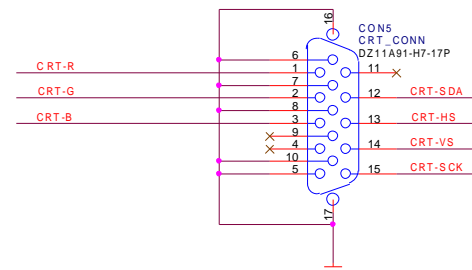
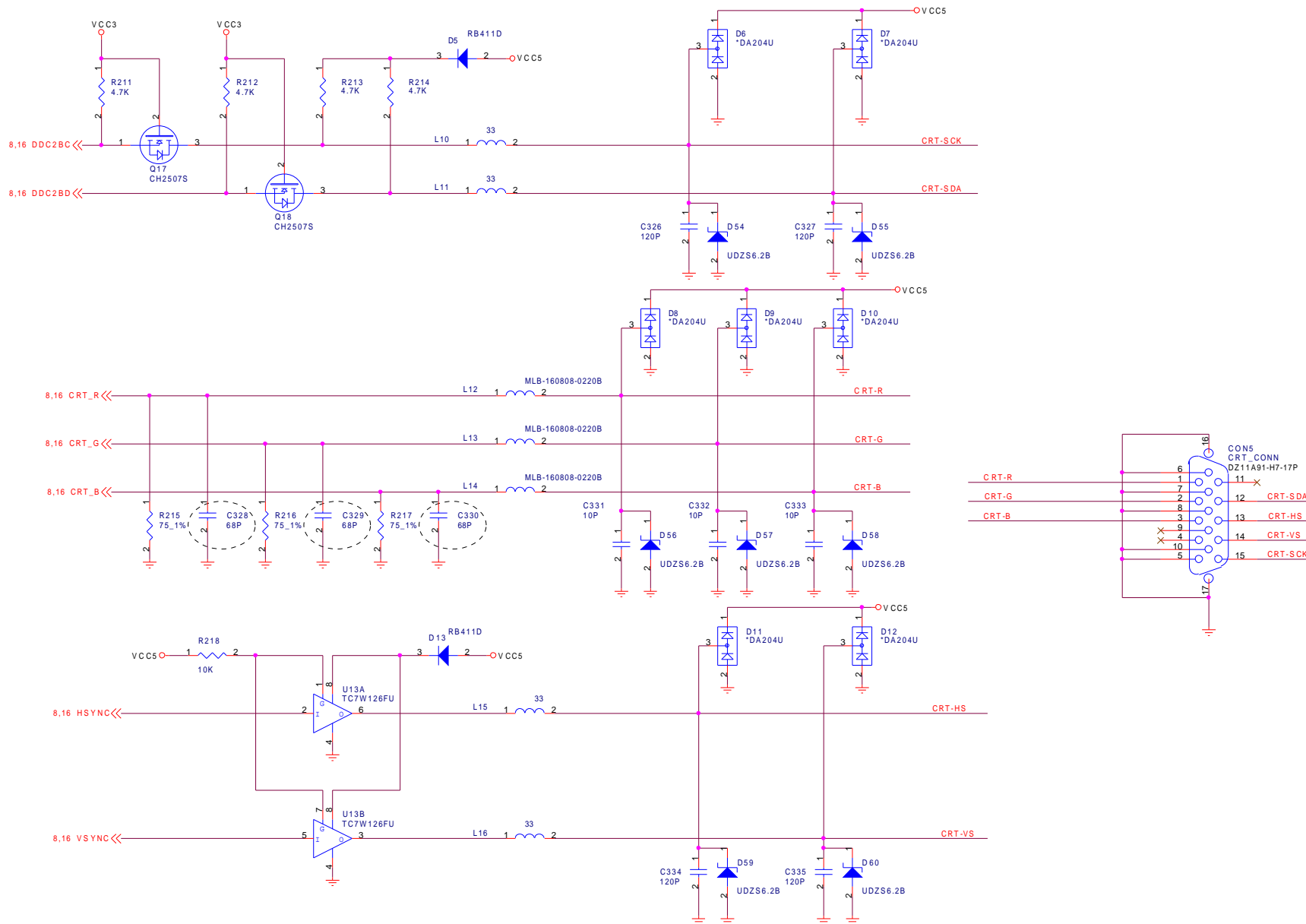


**QUANTA  
COMPUTER**

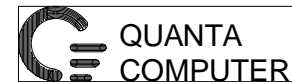
**Memory -2/2- SODIMM**

Title: \_\_\_\_\_ Document Number: **JE5.1 Main Board** Rev: 1A

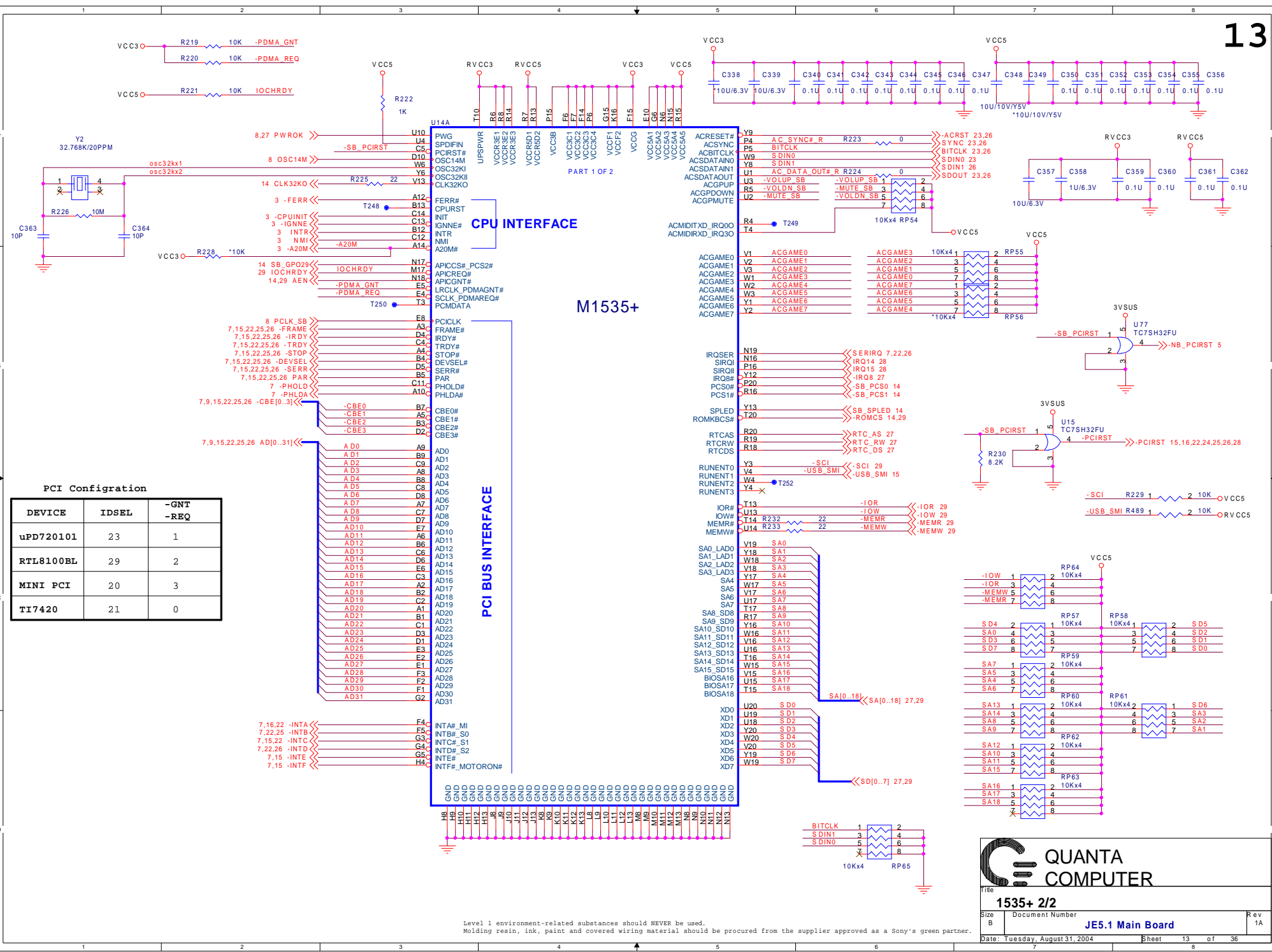
Date: Tuesday, August 31, 2004 Sheet 11 of 36



Level 1 environment-related substances should NEVER be used.  
Molding resin, ink, paint and covered wiring material should be procured from the supplier approved as a Sony's green partner.



Title			CRT PORT/TV		
Document Number			JE5.1 Main Board		
Size	Date: Tuesday, August 31, 2004				Rev
B	Sheet 12 of 36				1A



**PCI Configuration**

DEVICE	IDSEL	-GNT -REQ
uPD720101	23	1
RTL8100BL	29	2
MINI PCI	20	3
TI7420	21	0

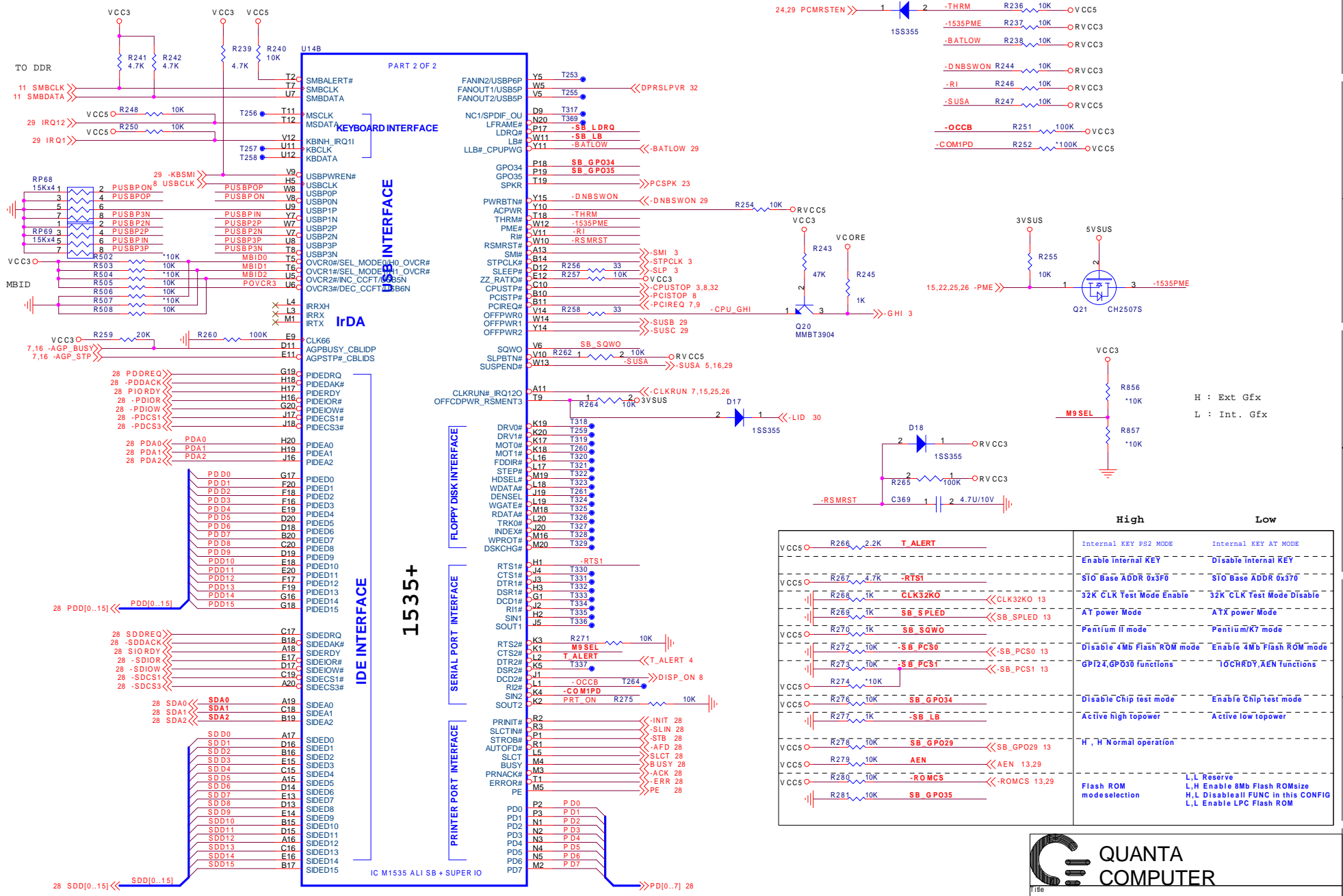
Level 1 environment-related substances should NEVER be used.  
Molding resin, ink, paint and covered wiring material should be procured from the supplier approved as a Sony's green partner.

**QUANTA COMPUTER**

File: **1535+ 2/2**

Size B Document Number **JE5.1 Main Board** Rev 1A

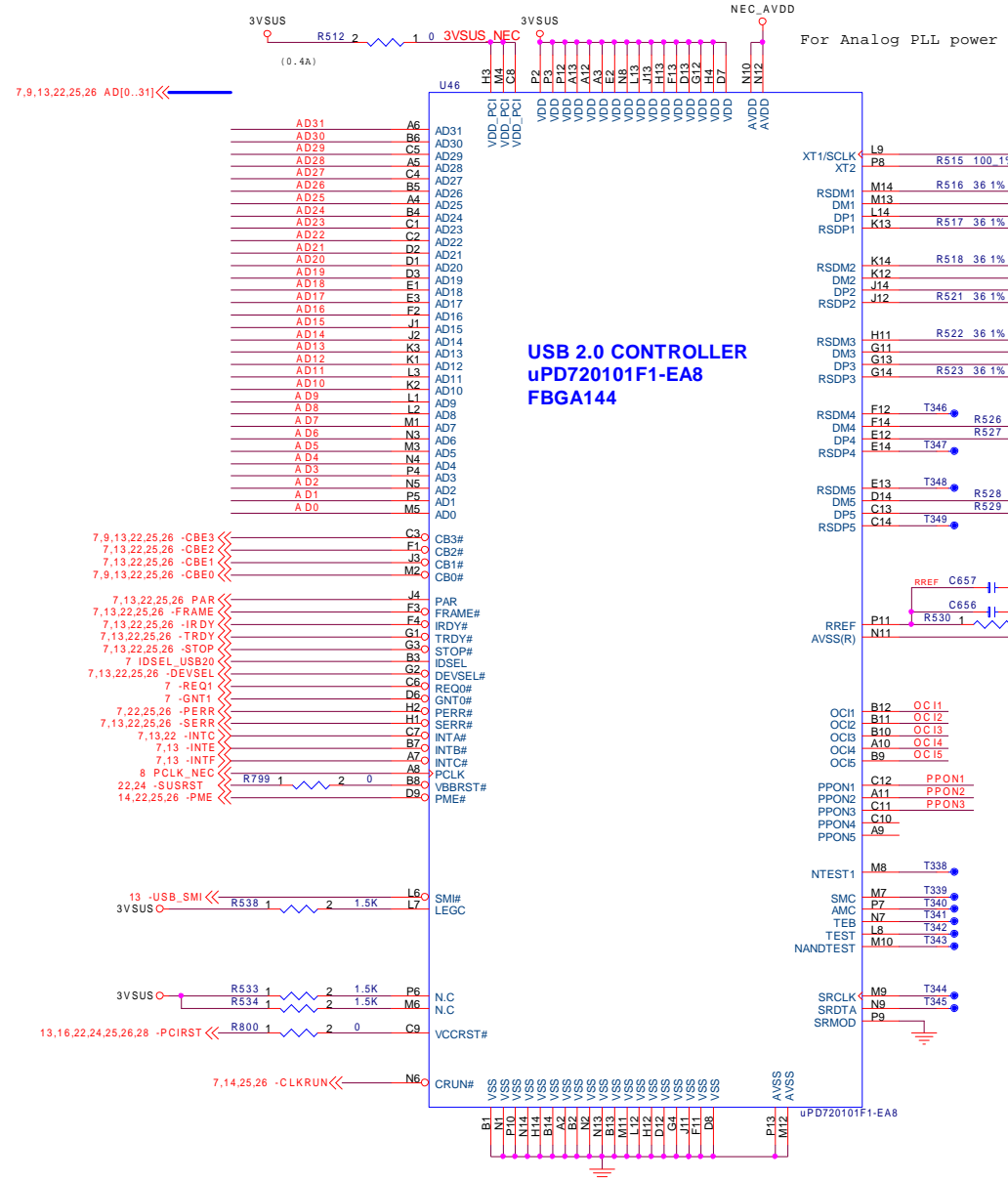
Date: Tuesday, August 31, 2004 Sheet 13 of 36



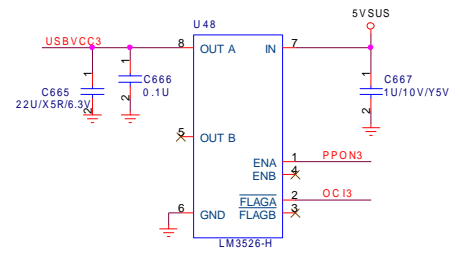
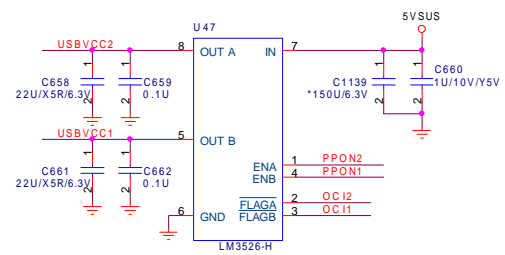
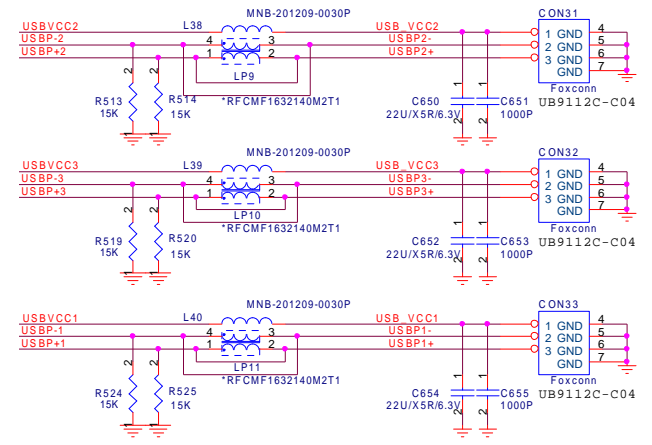
	High	Low
VCC3	R266 2.2K T_ALERT	Internal KEY PS2 MODE
VCC3	R267 4.7K -RTS1	Internal KEY AT MODE
VCC3	R268 1K CLK32K0	Enable Internal KEY
VCC3	R269 1K -SB_SPLED	Disable Internal KEY
VCC3	R270 1K -SB_SQWO	SIO Base ADDR 0x3F0
VCC3	R271 10K -SB_PCS0	SIO Base ADDR 0x3F6
VCC3	R272 10K -SB_PCS1	32K CLK Test Mode Enable
VCC3	R273 10K -SB_PCS1	32K CLK Test Mode Disable
VCC3	R274 10K	AT power Mode
VCC3	R275 10K -SB_GPO34	ATX power Mode
VCC3	R276 10K -SB_GPO34	Pentium II mode
VCC3	R277 1K -SB_LB	Pentium/K7 mode
VCC3	R278 10K -SB_GPO29	Disable 4Mb Flash ROM mode
VCC3	R279 10K AEN	Enable 4Mb Flash ROM mode
VCC3	R280 10K -ROMCS	GP124, GP030 functions
VCC3	R281 10K -SB_GPO35	10CHRDY, AEN Functions
		Disable Chip test mode
		Enable Chip test mode
		Active high topower
		Active low topower
		"H", "H Normal operation"
		Flash ROM mode selection
		L,L Reserve
		L,H Enable 8Mb Flash ROMsize
		L,L Disable all FUNC in this CONFIG
		L,L Enable LPC Flash ROM



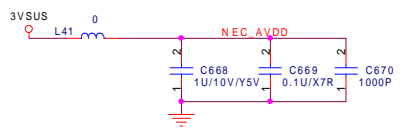
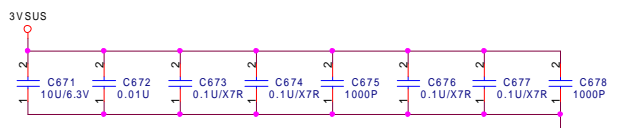
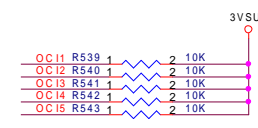
Level 1 environment-related substances should NEVER be used.  
 Molding resin, ink, paint and covered wiring material should be procured from the supplier approved as a Sony's green partner.



**USB 2.0 CONTROLLER**  
**uPD720101F1-EA8**  
**FBGA144**



The chip is power switch for USB power .



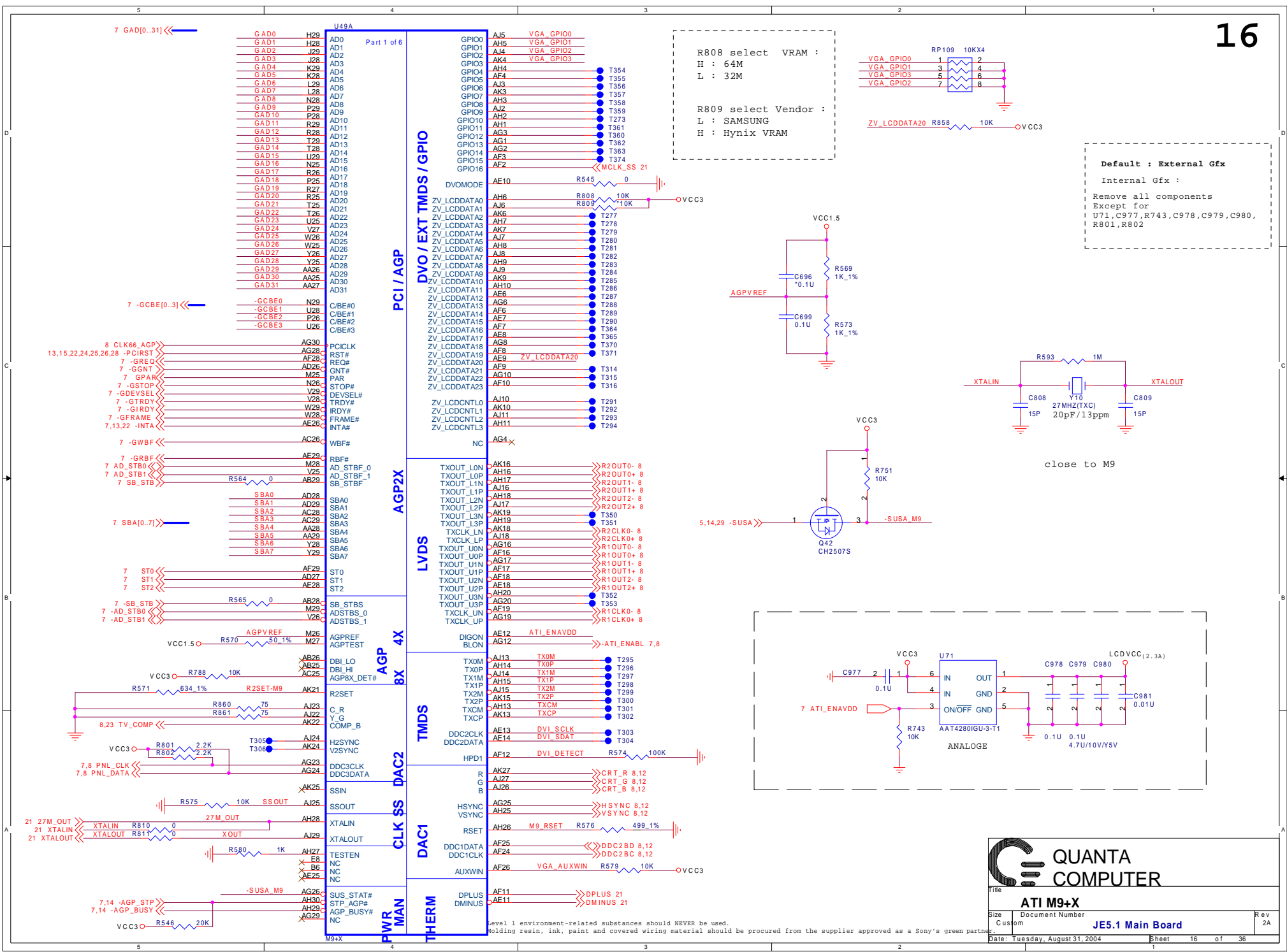
Level 1 environment-related substances should NEVER be used.  
 Molding resin, ink, paint and covered wiring material should be procured from the supplier approved as a Sony's green partner.

**QUANTA**  
COMPUTER

Title: **USB Controller NEC-uPD720101**

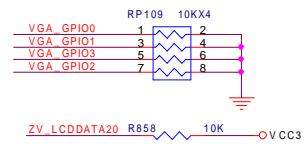
Size: Custom Document Number: **JE5.1 Main Board** Rev: 3A

Date: Tuesday, August 31, 2004 Sheet: 15 of 36



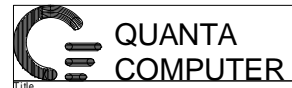
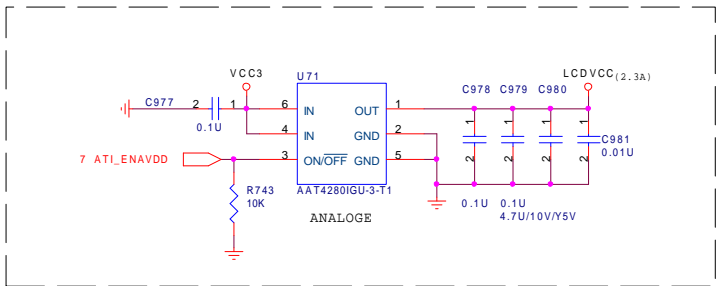
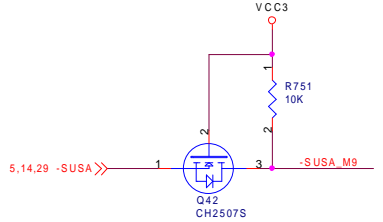
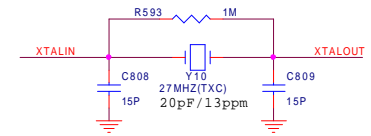
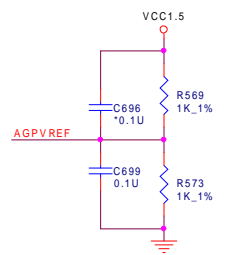
R808 select VRAM :  
H : 64M  
L : 32M

R809 select Vendor :  
L : SAMSUNG  
H : Hynix VRAM

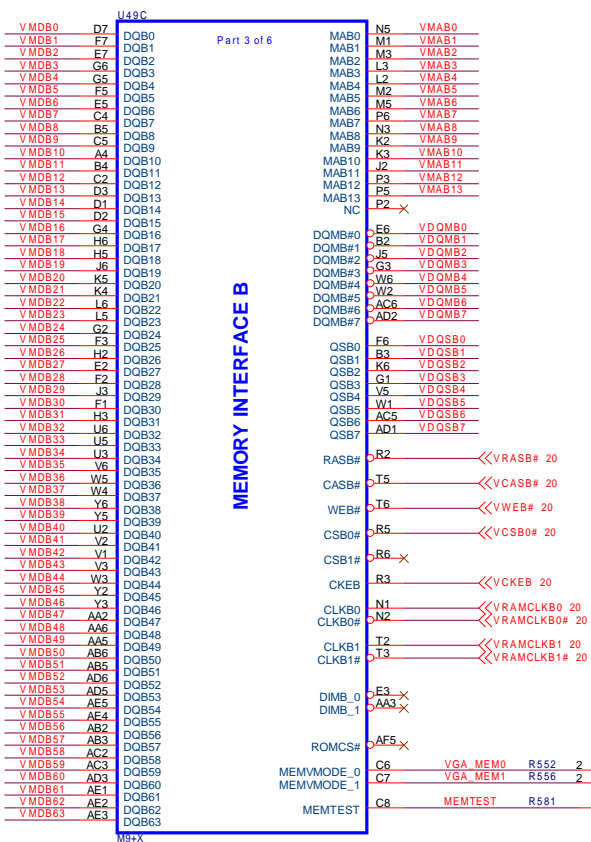
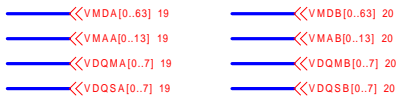
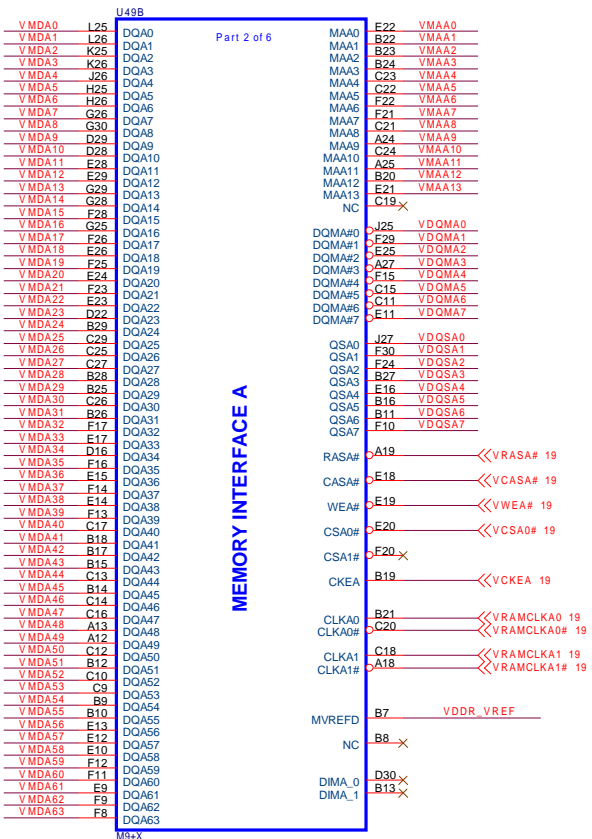


Default : External Gfx

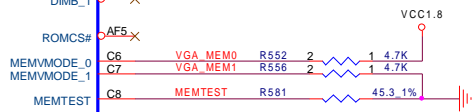
Internal Gfx :  
Remove all components  
Except for  
U71,C977,R743,C978,C979,C980,  
R801,R802



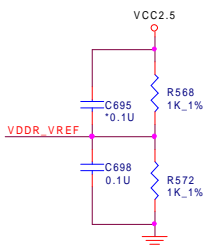




Default : External Gfx  
Internal Gfx :  
Remove all components .

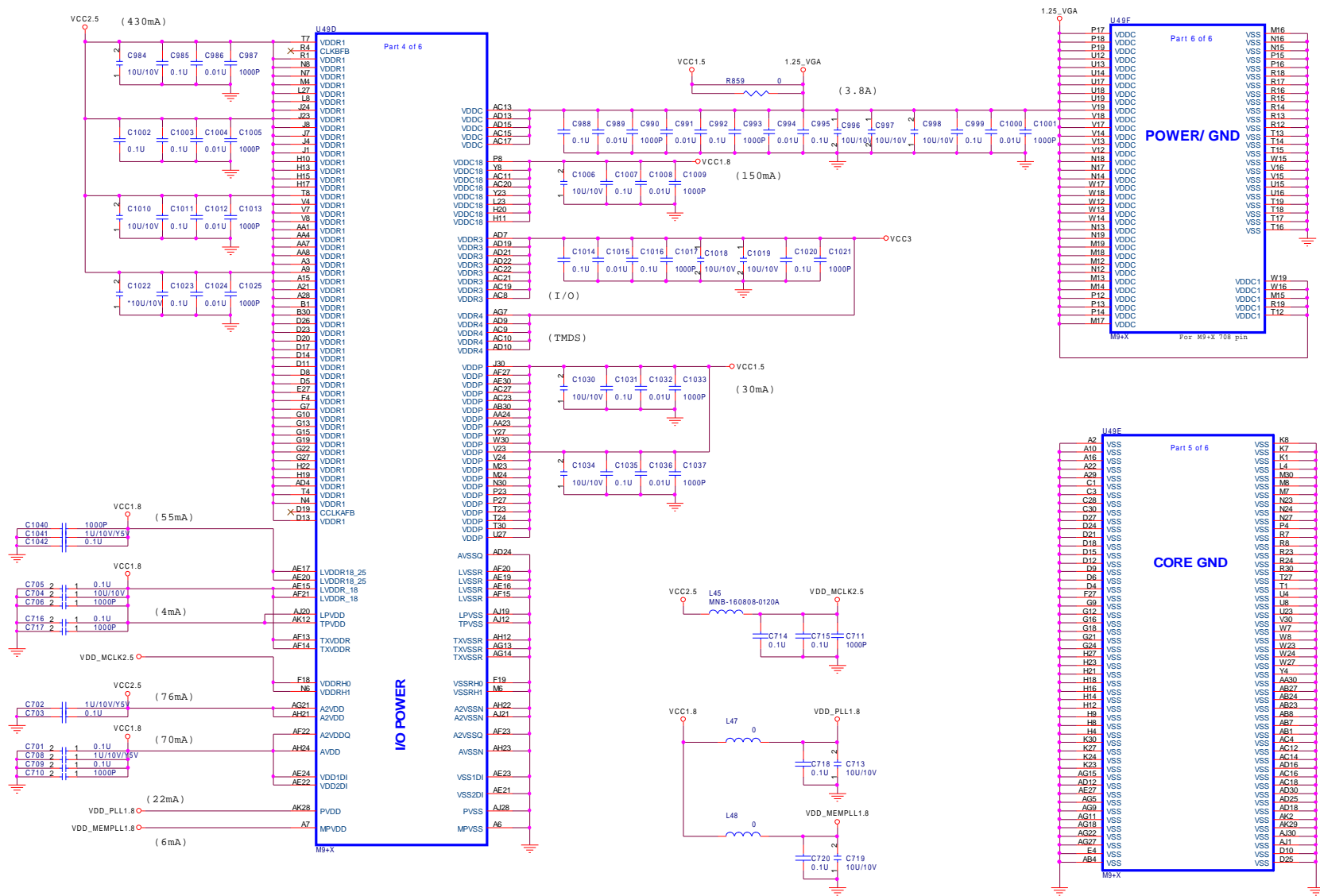


MEMORY IO VOLTAGE = 2.5V



ATI M9+X		Rev 1A
Size Custom	Document Number	
JE5.1 Main Board		
Date: Tuesday, August 31, 2004	Sheet 17 of 36	

Level 1 environment-related substances should NEVER be used.  
Molding resin, ink, paint and covered wiring material should be procured from the supplier approved as a Sony's green partner.

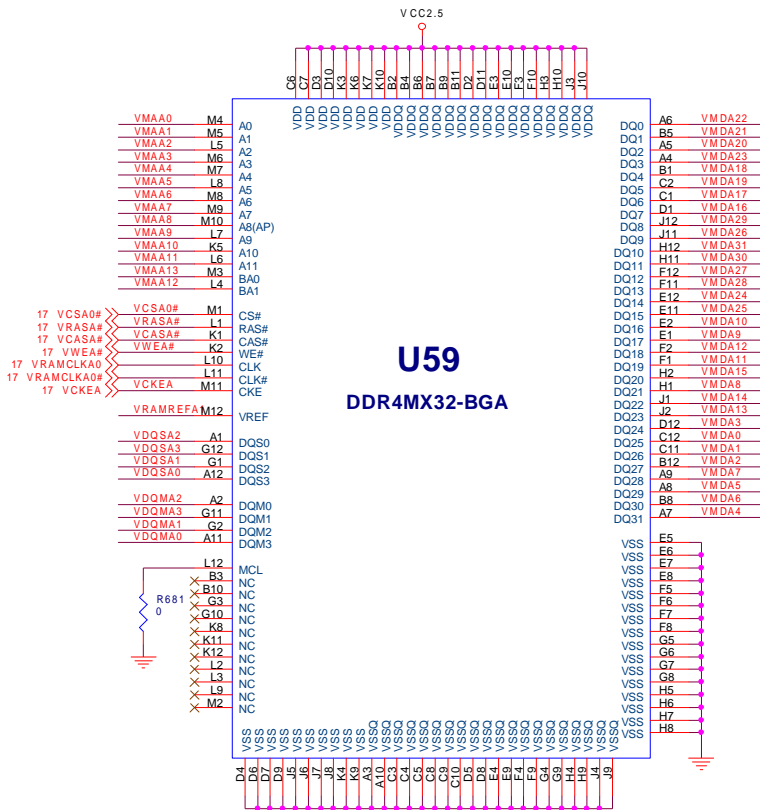


Default : External Gfx  
 Internal Gfx :  
 Remove all of components

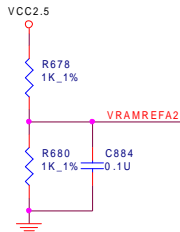
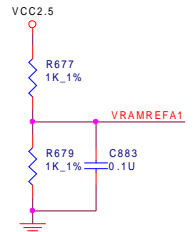
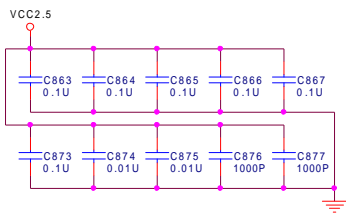


Level 1 environment-related substances should NEVER be used.  
 Molding resin, ink, paint and covered wiring material should be procured from the supplier approved as a Sony's green partner.

<<VMDA[0..63] 17  
<<VMAA[0..13] 17  
<<VDQMA[0..7] 17  
<<VDQSA[0..7] 17

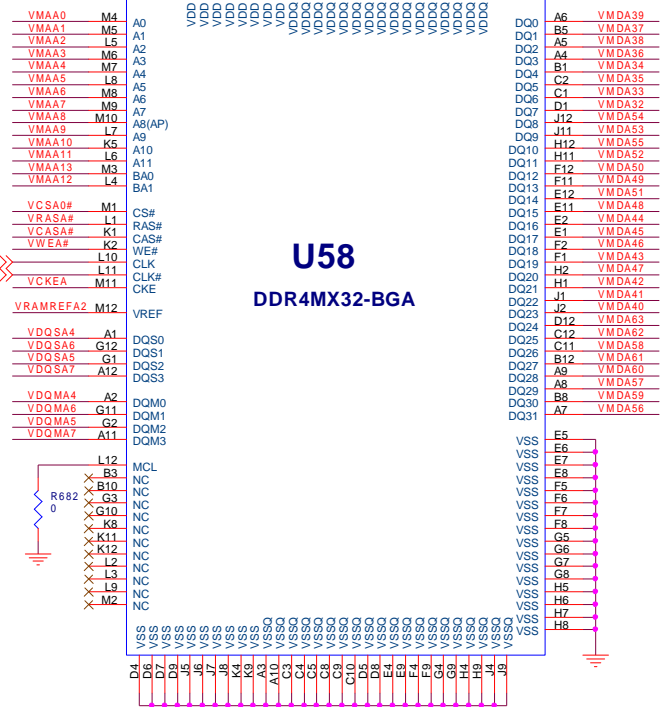


Samsung K4D263238E-VC33  
(Hynix -HY5DU28322AF(P))

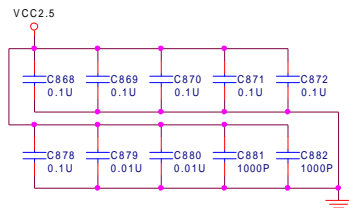


<<VMDA[0..63] 17  
<<VMAA[0..13] 17  
<<VDQMA[0..7] 17  
<<VDQSA[0..7] 17

17 VRAMCLKA1  
17 VRAMCLKA1#



Samsung K4D263238E-VC33  
(Hynix -HY5DU28322AF(P))



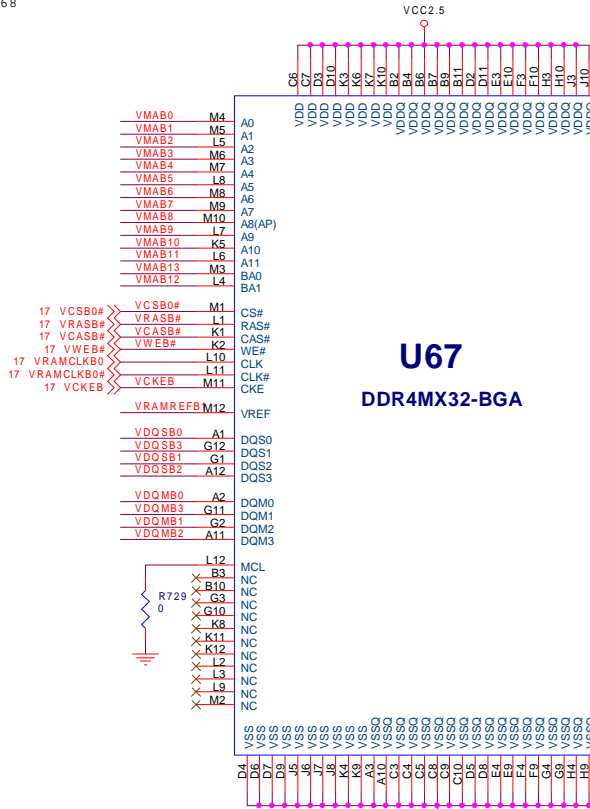
Default : External Gfx  
Internal Gfx :  
Remove all components .



Level 1 environment-related substances should NEVER be used.  
Molding resin, ink, paint and covered wiring material should be procured from the supplier approved as a Sony's green partner.

If 32M VRAM of M9+X only install U67, U68

VMDB[0..63] 17  
VMAB[0..13] 17  
VDQMB[0..7] 17  
VDQSB[0..7] 17



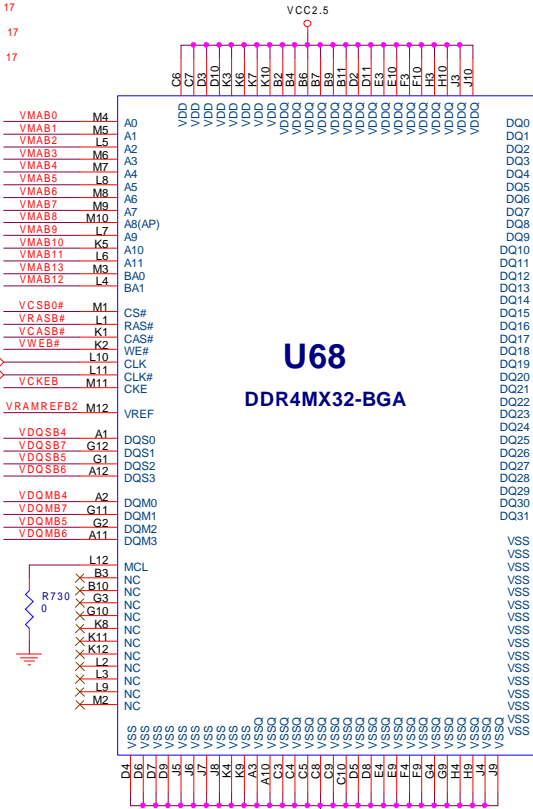
U67  
DDR4MX32-BGA

17 VRAMCLKB1#  
17 VRAMCLKB1#

VRAMREFB2 M12

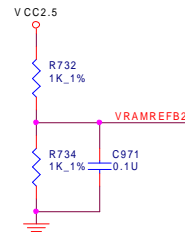
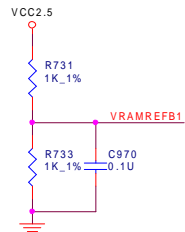
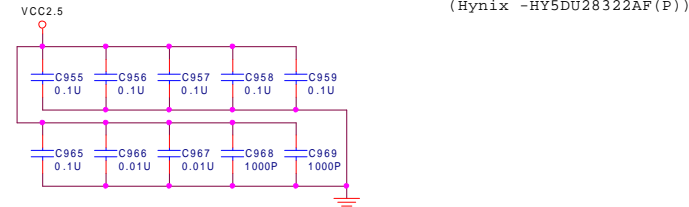
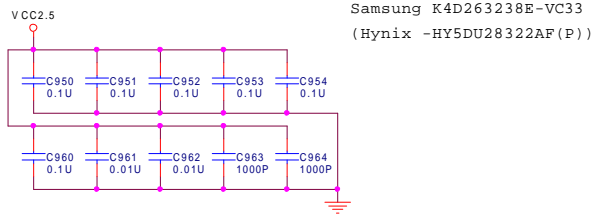
VDQSB4 A1  
VDQSB7 G12  
VDQSB8 K1  
VDQSB5 G1  
VDQSB5 A12

VDQMB4 A2  
VDQMB7 G11  
VDQMB5 G2  
VDQMB6 A11



U68  
DDR4MX32-BGA

Samsung K4D263238E-VC33  
(Hynix -HY5DU28322AF(P))



Default : External Gfx  
Internal Gfx :  
Remove all components .

Level 1 environment-related substances should NEVER be used. Molding resin, ink, paint and covered wiring material should be procured from the supplier approved as a Sony's green partner.

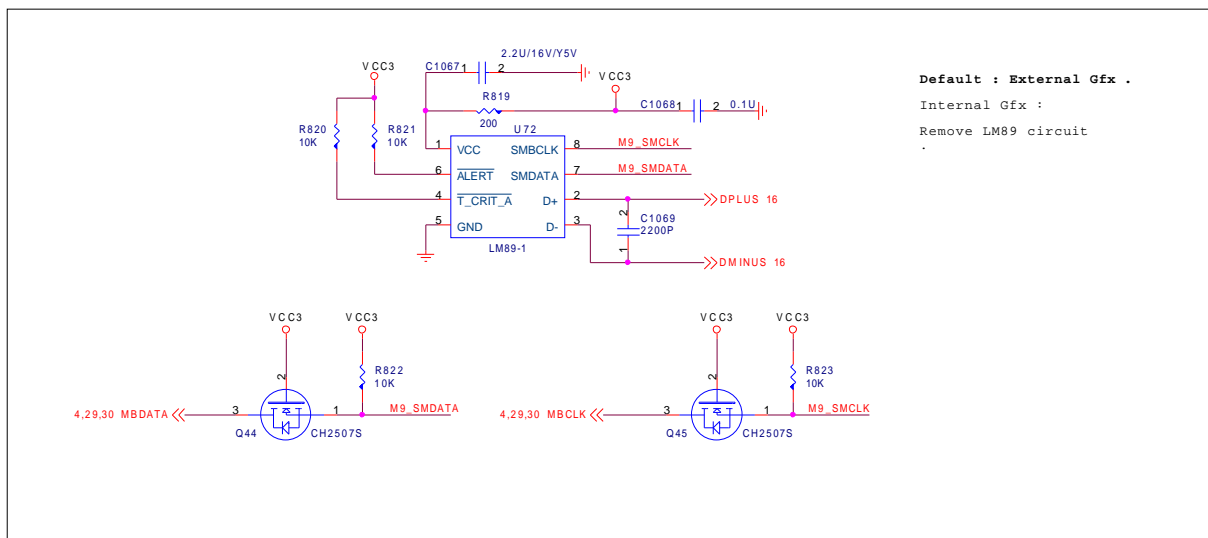
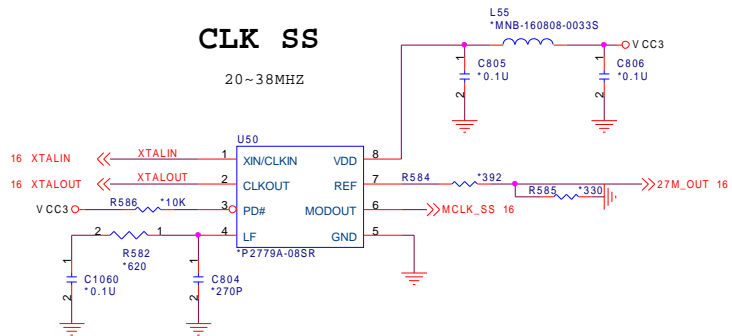
**QUANTA COMPUTER**

File: **VGA DDR VRAMx2 2/2**

Size	Document Number	Rev
Custom	<b>JE5.1 Main Board</b>	1A

Date: Tuesday, August 31, 2004 Sheet 20 of 36

MCLK (230MHZ) SPREAD SPECTRUM LOGIC

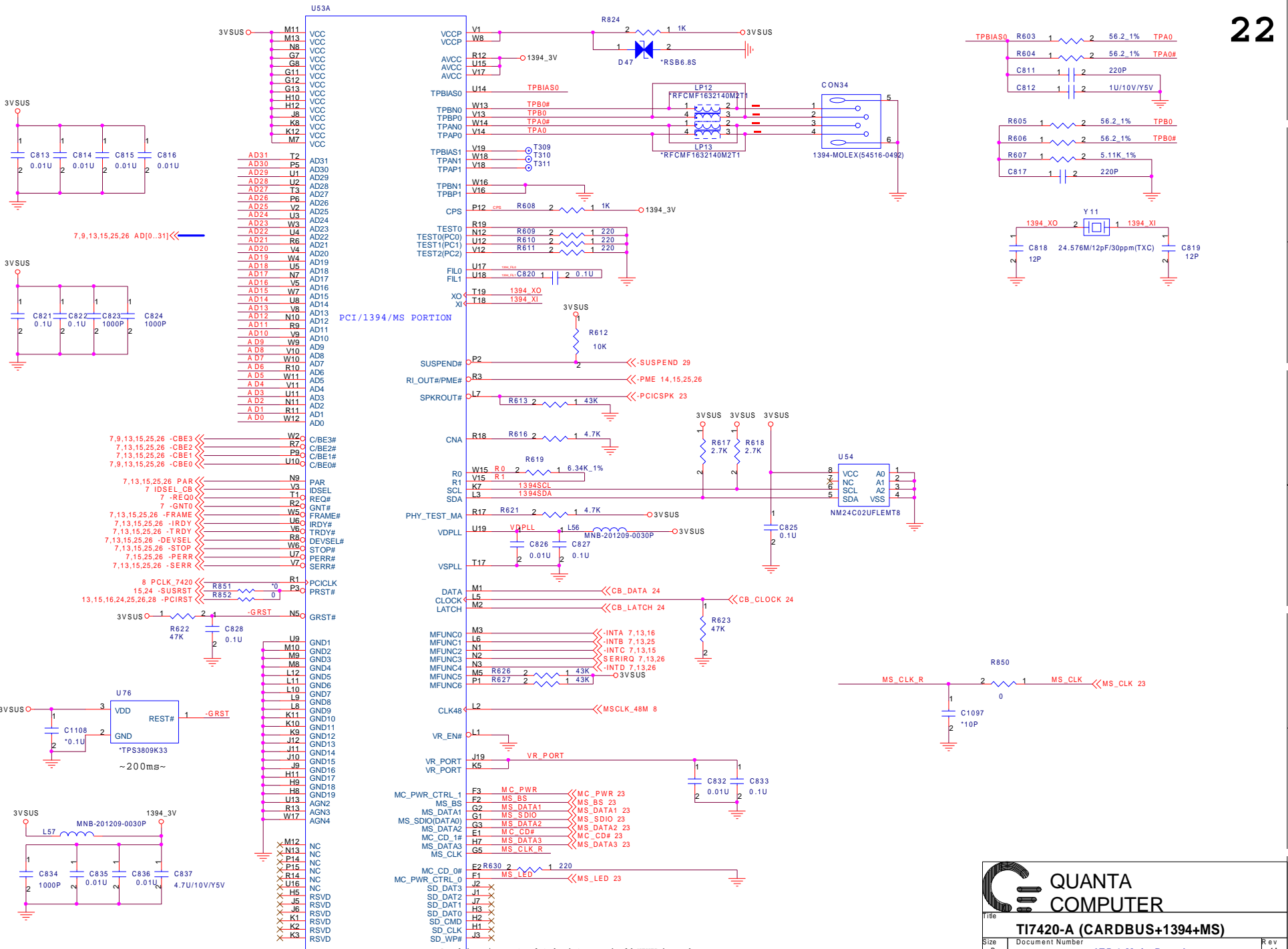


Level 1 environment-related substances should NEVER be used.  
 Molding resin, ink, paint and covered wiring material should be procured from the supplier approved as a Sony's green partner.

**QUANTA COMPUTER**

File: **AGP SSC**

Size: Custom	Document Number: <b>JE5.1 Main Board</b>	Rev: 1A
Date: Tuesday, August 31, 2004		Sheet 21 of 36



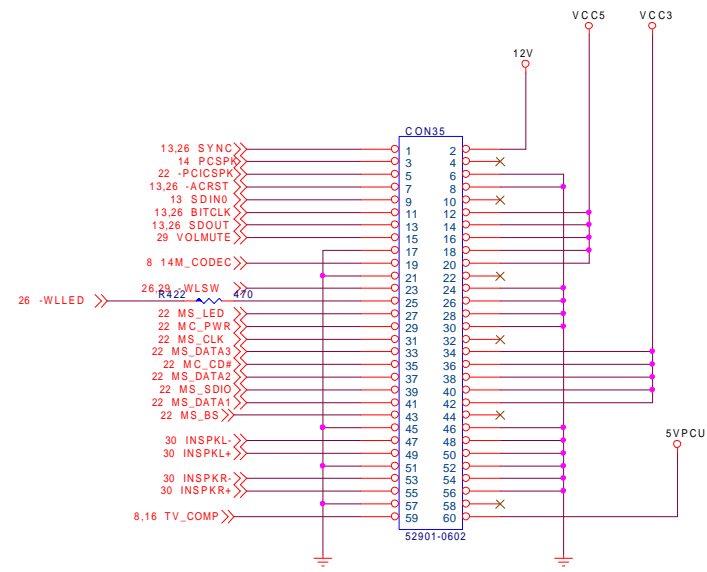
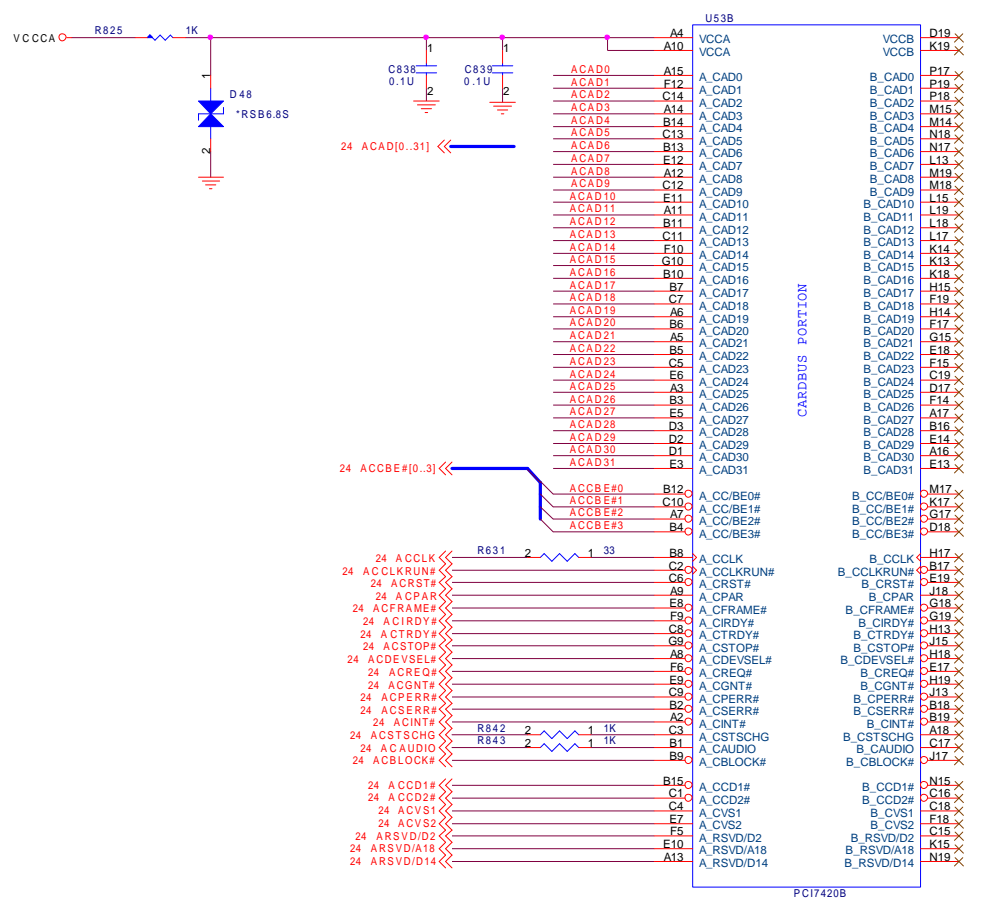
**QUANTA COMPUTER**

TI7420-A (CARDBUS+1394+MS)

JE5.1 Main Board

Date: Tuesday, August 31, 2004 Sheet 22 of 36

Level 1 environment-related substances should NEVER be used.  
 Molding resin, ink, paint and covered wiring material should be procured from the supplier approved as a Sony's green partner.



Level 1 environment-related substances should NEVER be used.  
 Molding resin, ink, paint and covered wiring material should be procured from the supplier approved as a Sony's green partner.

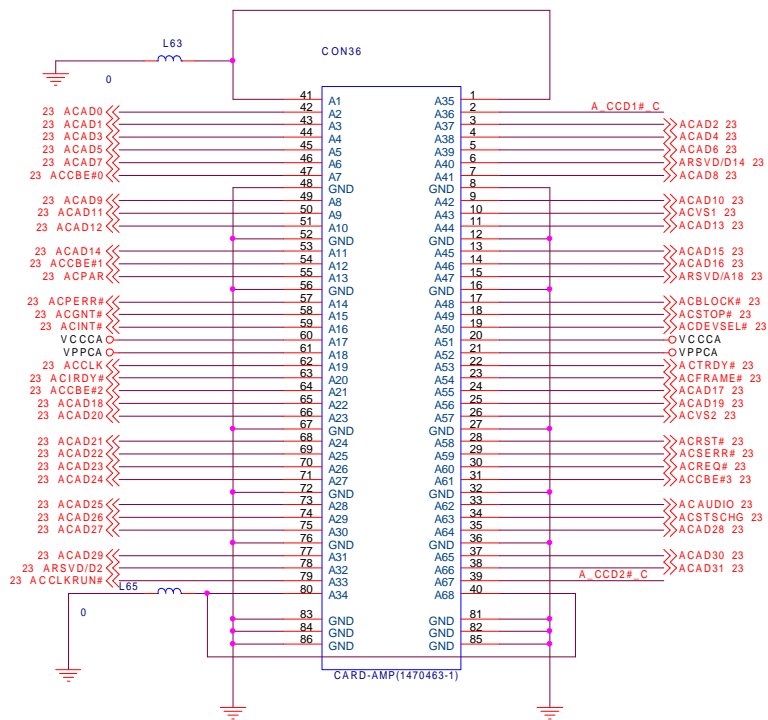
**QUANTA COMPUTER**

TI7420-A (CARDBUS+1394+MS)

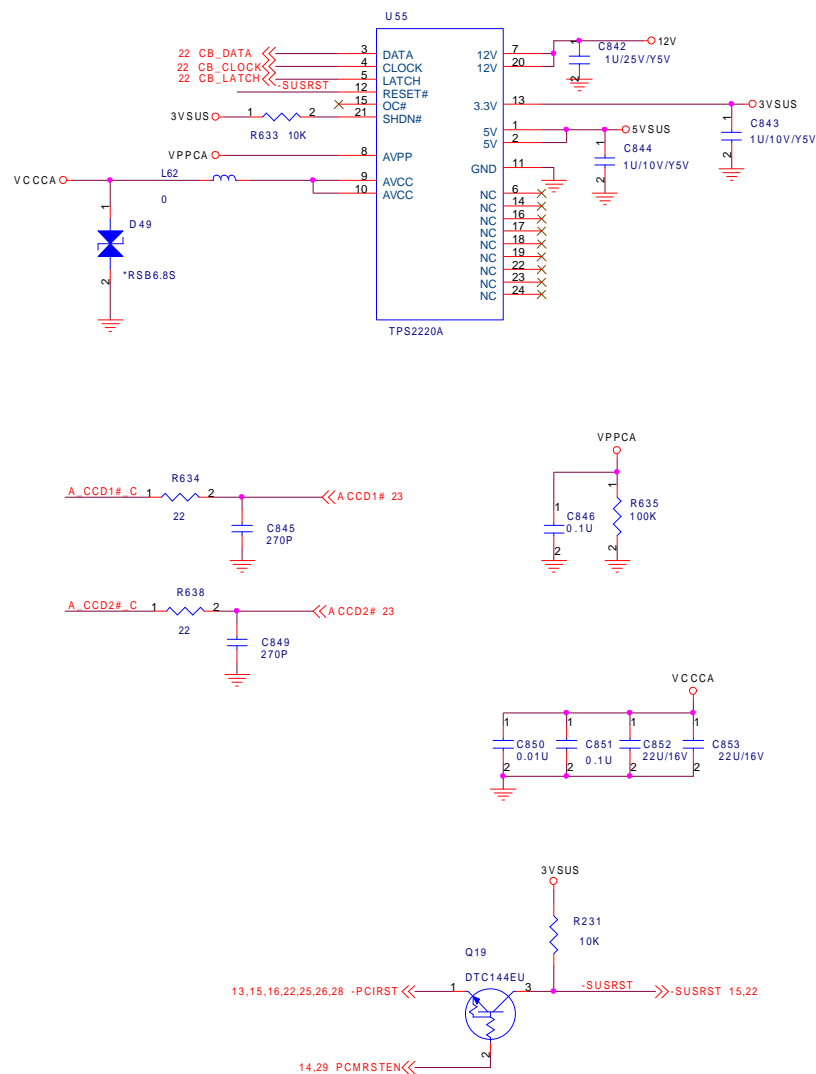
Size Custom Document Number **JE5.1 Main Board** Rev 1A

Date: Tuesday, August 31, 2004 Sheet 23 of 36

23 ACAD[0..31] <<   
 23 ACCBE#[0..3] << 



CardBus power switch chip

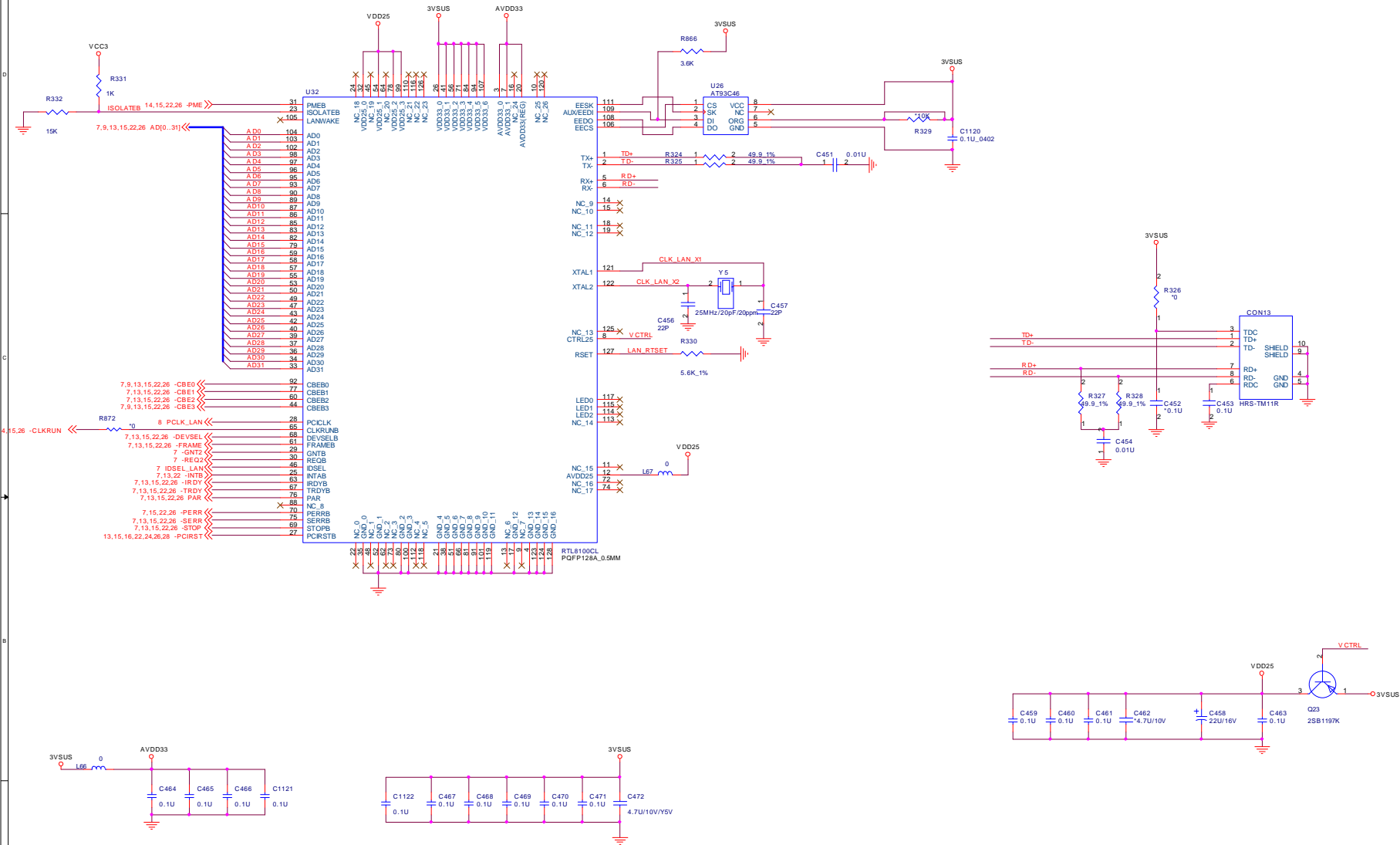


Level 1 environment-related substances should NEVER be used.  
 Molding resin, ink, paint and covered wiring material should be procured from the supplier approved as a Sony's green partner.

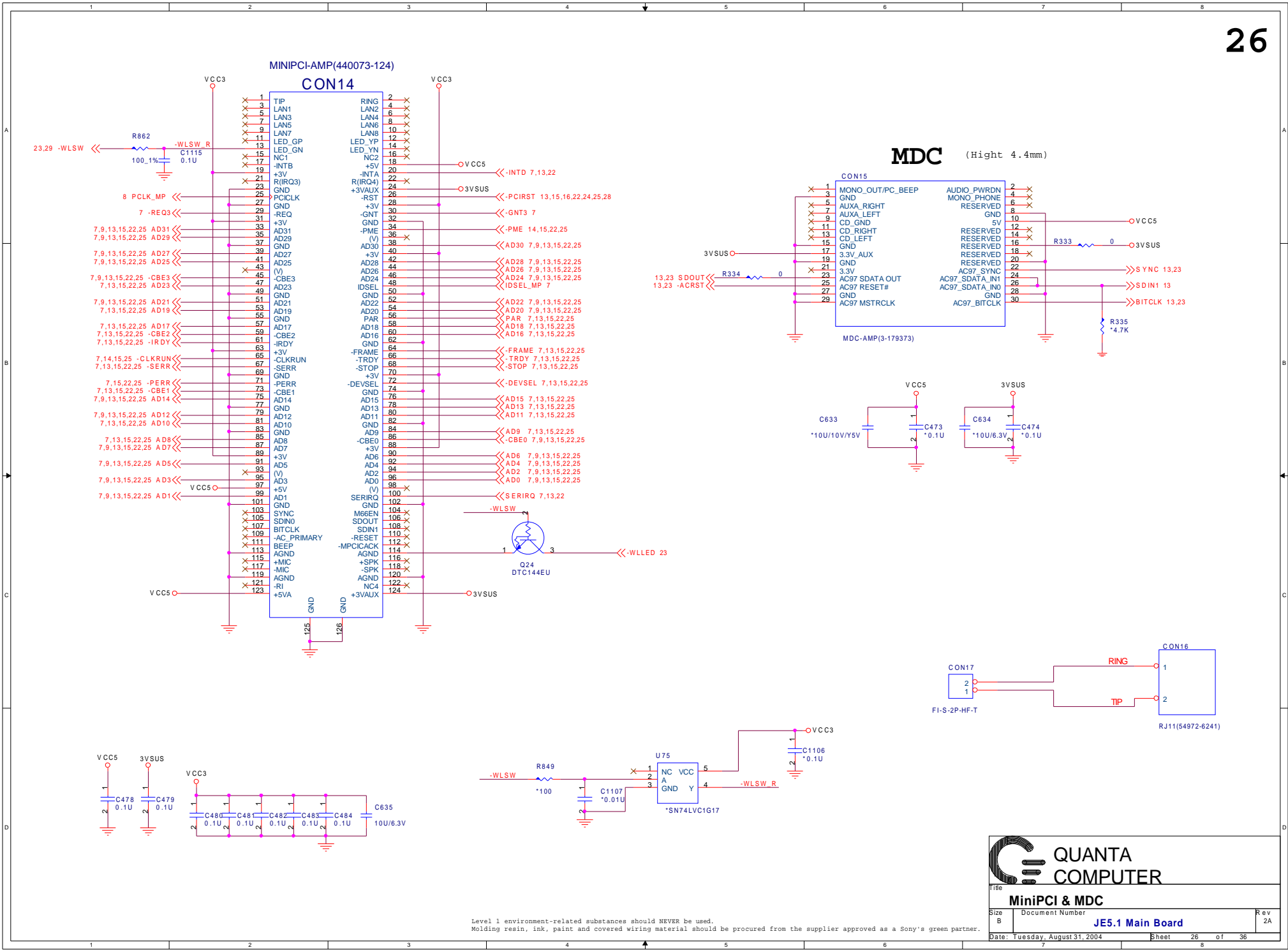


Title		
CARDBUS SLOT		
Size	Document Number	Rev
B	JE5.1 Main Board	1A
Date:	Tuesday, August 31, 2004	Sheet 24 of 36





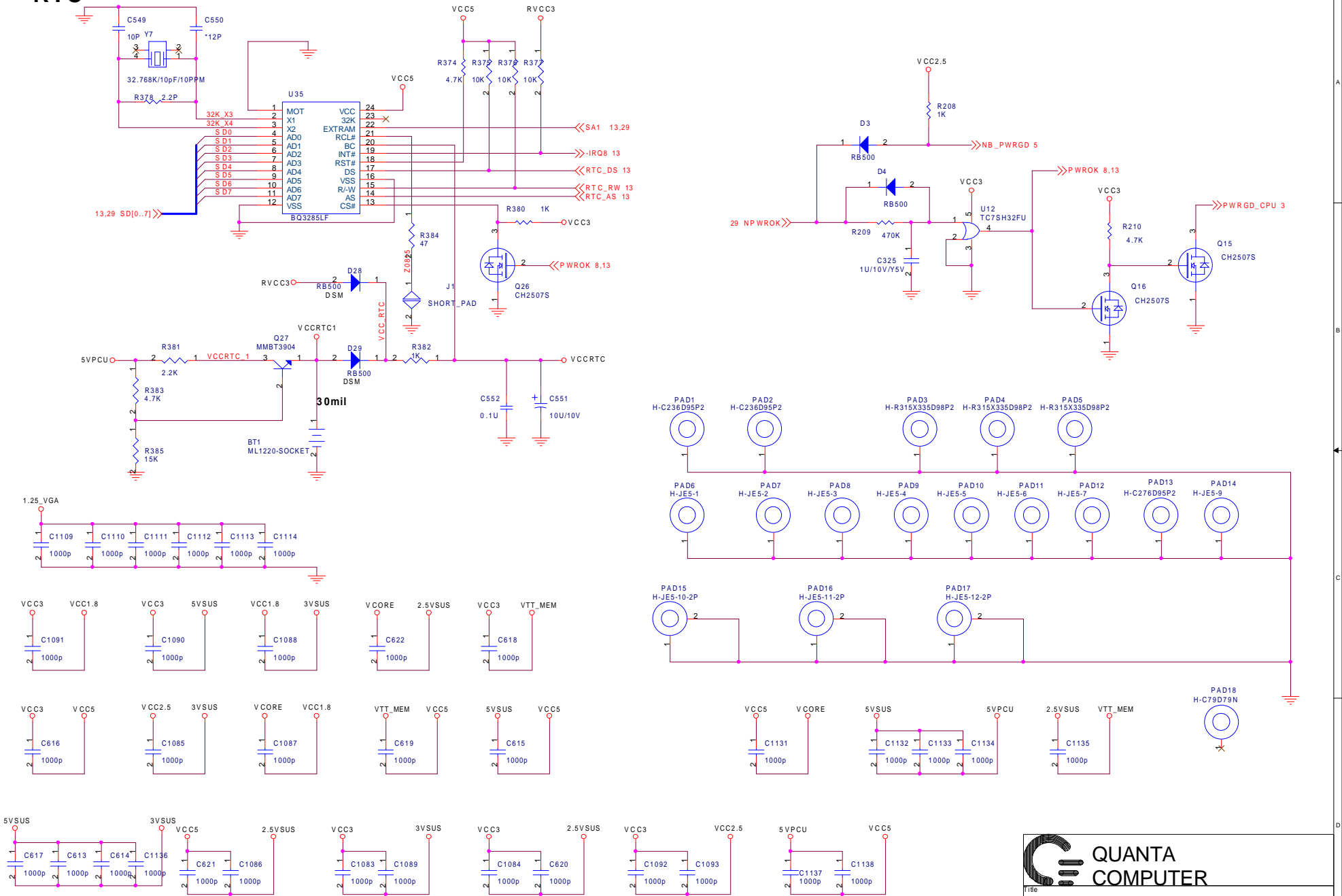
Level 1 environment-related substances should NEVER be used.  
Molding resin, ink, paint and covered wiring material should be procured from the supplier approved as a Sony's green partner.



MiniPCI & MDC		
Size B	Document Number	Rev 2A
Date: Tuesday, August 31, 2004		Sheet 26 of 36

Level 1 environment-related substances should NEVER be used.  
 Molding resin, ink, paint and covered wiring material should be procured from the supplier approved as a Sony's green partner.

RTC



Level 1 environment-related substances should NEVER be used.  
 Molding resin, ink, paint and covered wiring material should be procured from the supplier approved as a Sony's green partner.

**QUANTA COMPUTER**

RTC/POWER GOOD LOGIC

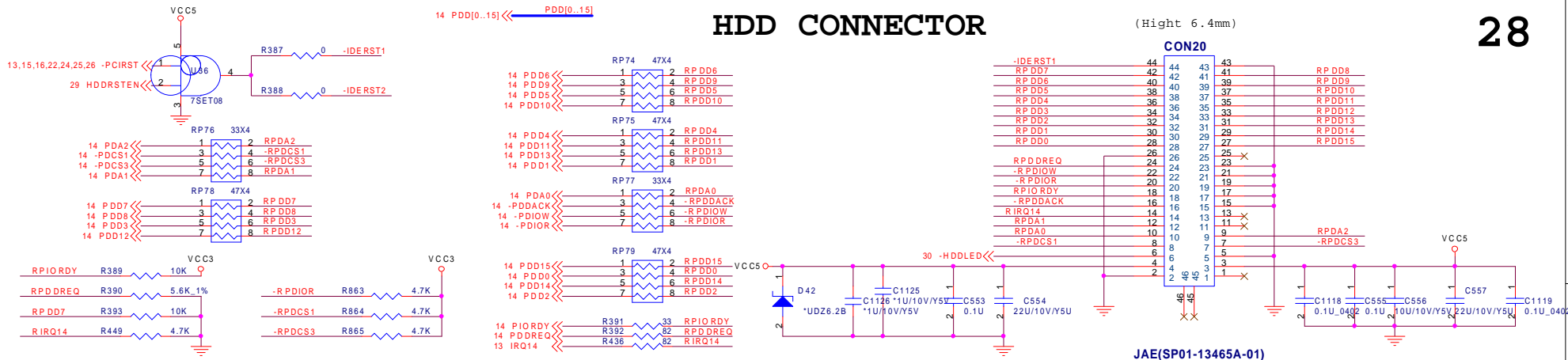
Size B	Document Number	<b>JE5.1 Main Board</b>	Rev 2A
--------	-----------------	-------------------------	--------

Date: Tuesday, August 31, 2004 Sheet 27 of 36

# HDD CONNECTOR

(Hight 6.4mm)

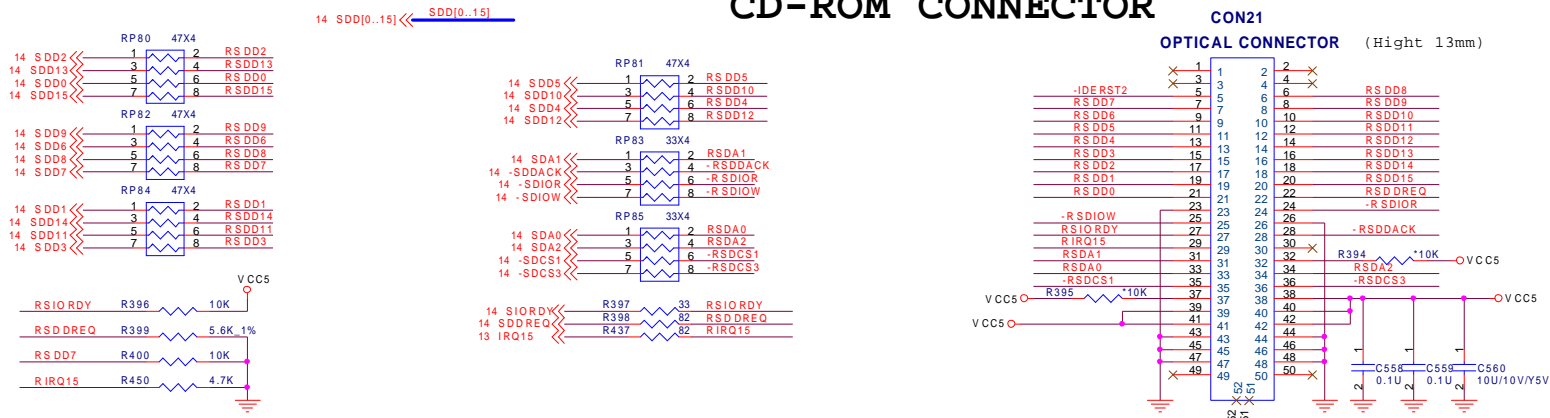
28



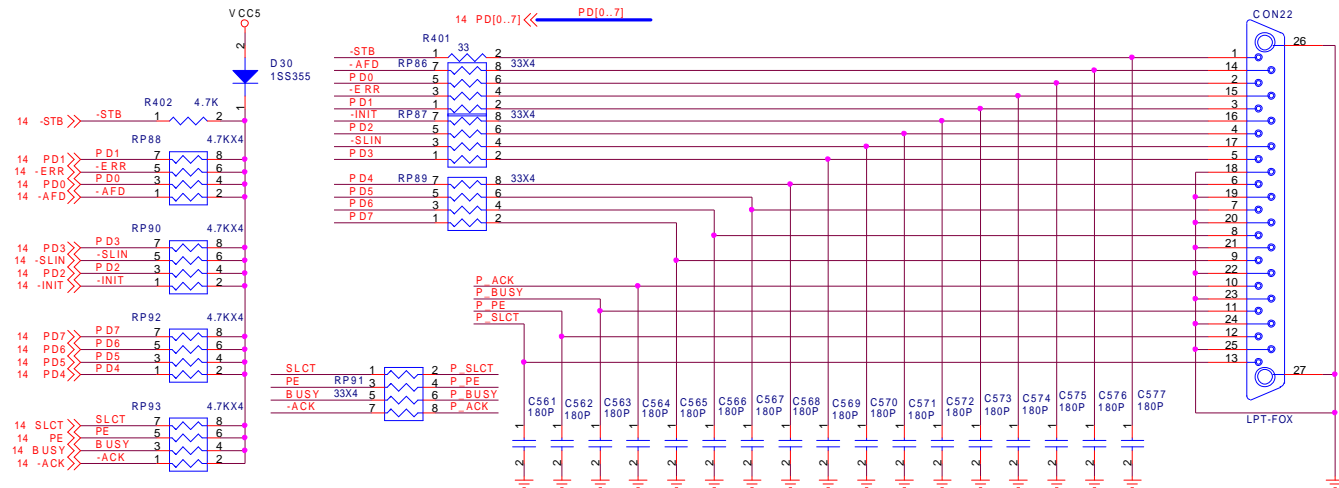
# CD-ROM CONNECTOR

CON21

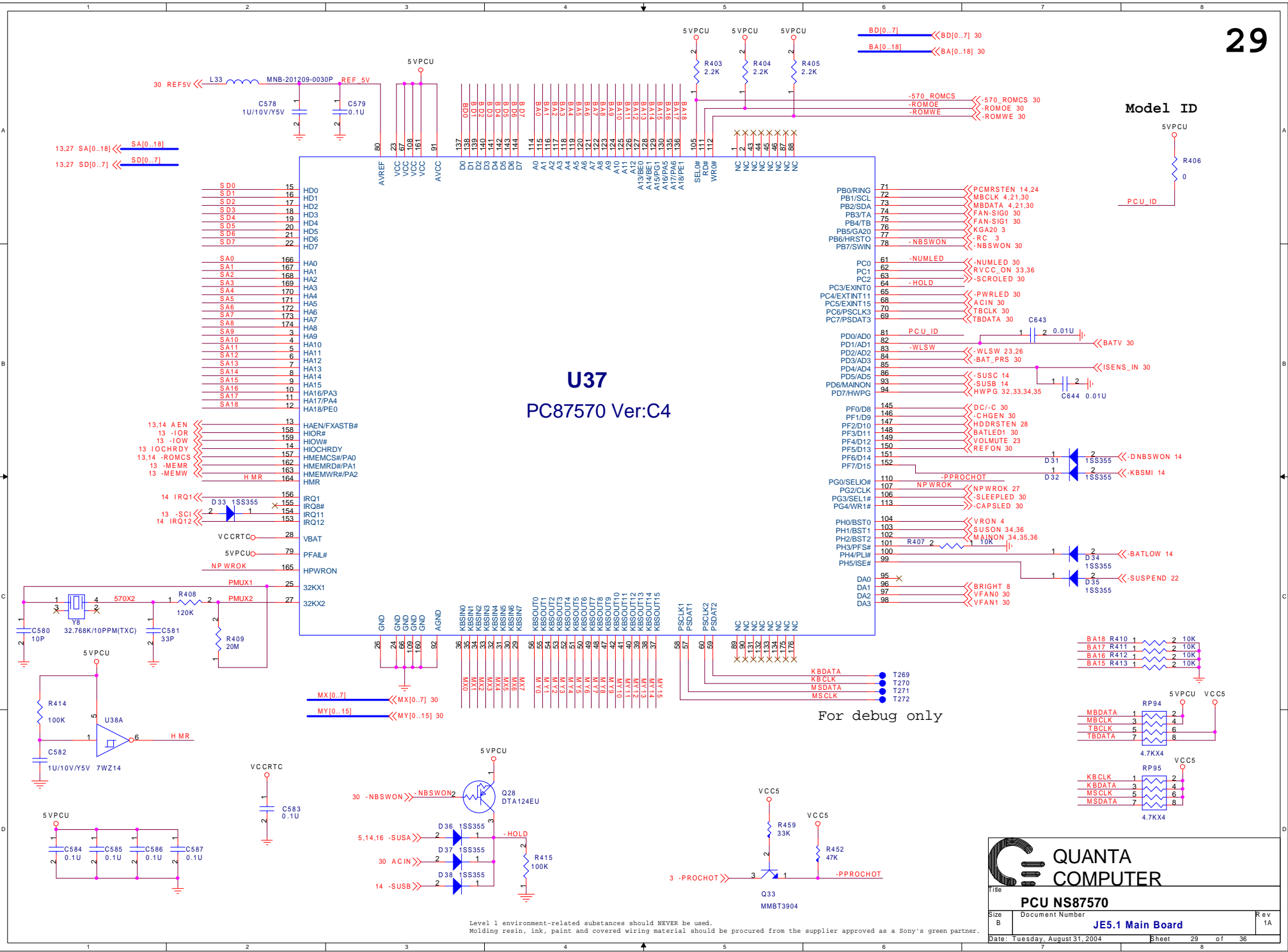
OPTICAL CONNECTOR (Hight 13mm)



# PRINTER port

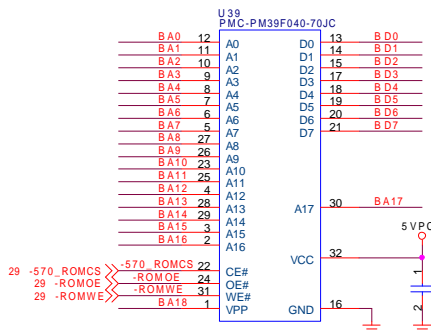
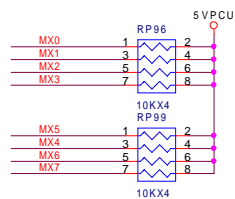
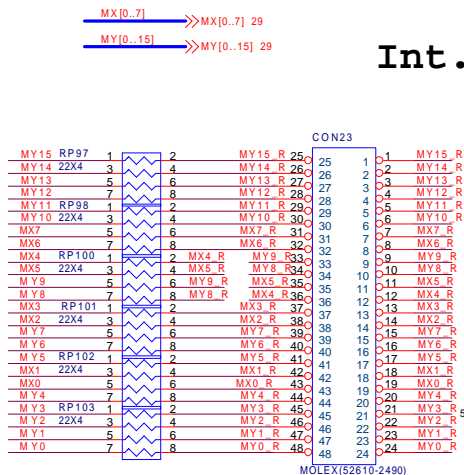


Level 1 environment-related substances should NEVER be used.  
Molding resin, ink, paint and covered wiring material should be procured from the supplier approved as a Sony's green partner.

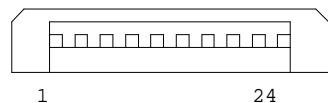
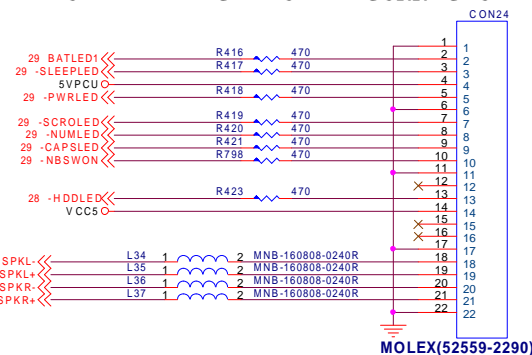


### Int. keyboard

### BIOS



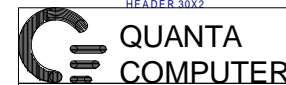
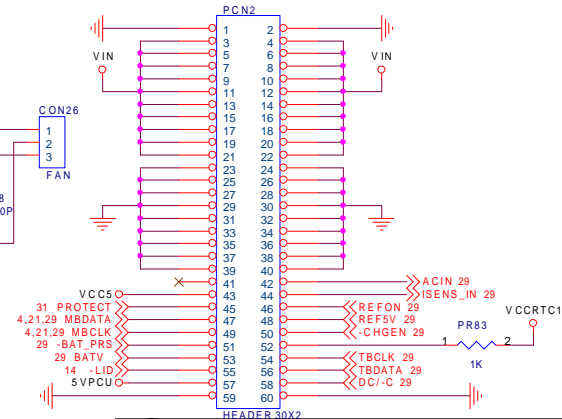
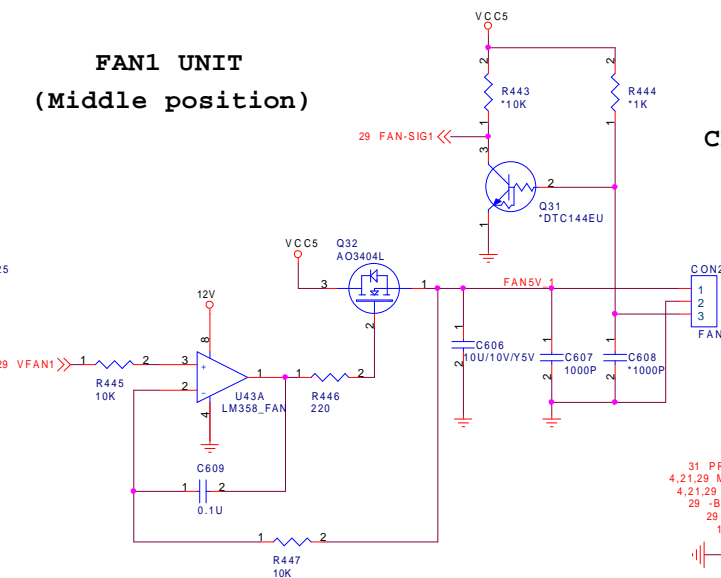
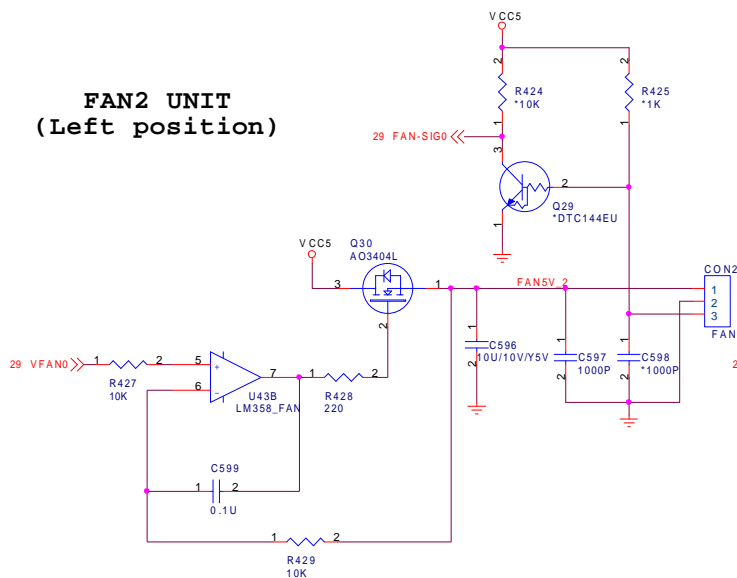
### POWER SWITCH BOARD CONNECTOR



### FAN2 UNIT (Left position)

### FAN1 UNIT (Middle position)

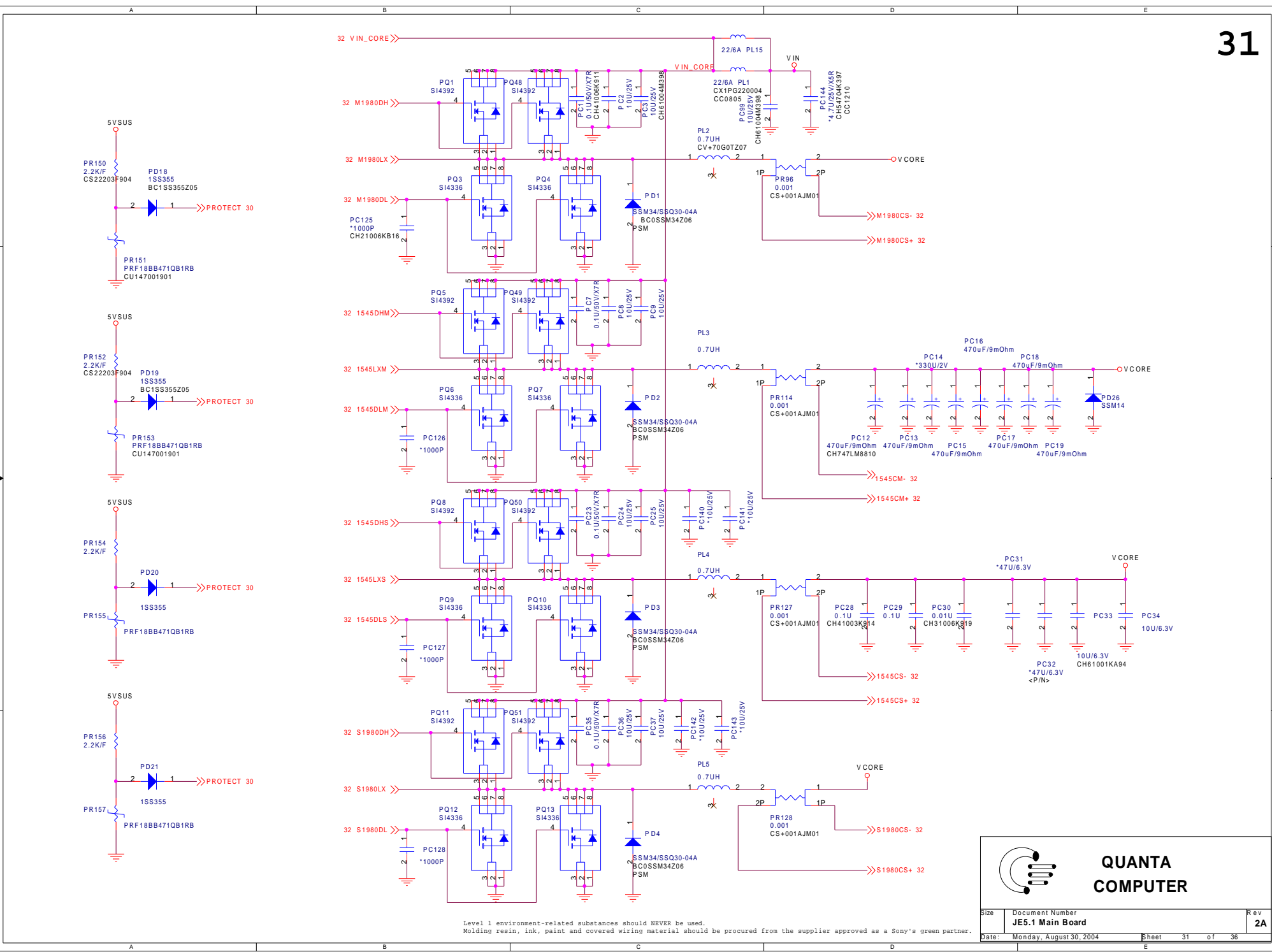
### CHARGER BOARD CONNECTOR



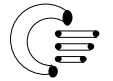
INT. KEYBOARD/FAN

Title	Document Number	Rev
Size	JE5.1 Main Board	1A
Custom		
Date: Tuesday, August 31, 2004	Sheet 30 of 36	

Level 1 environment-related substances should NEVER be used.  
Molding resin, ink, paint and covered wiring material should be procured from the supplier approved as a Sony's green partner.

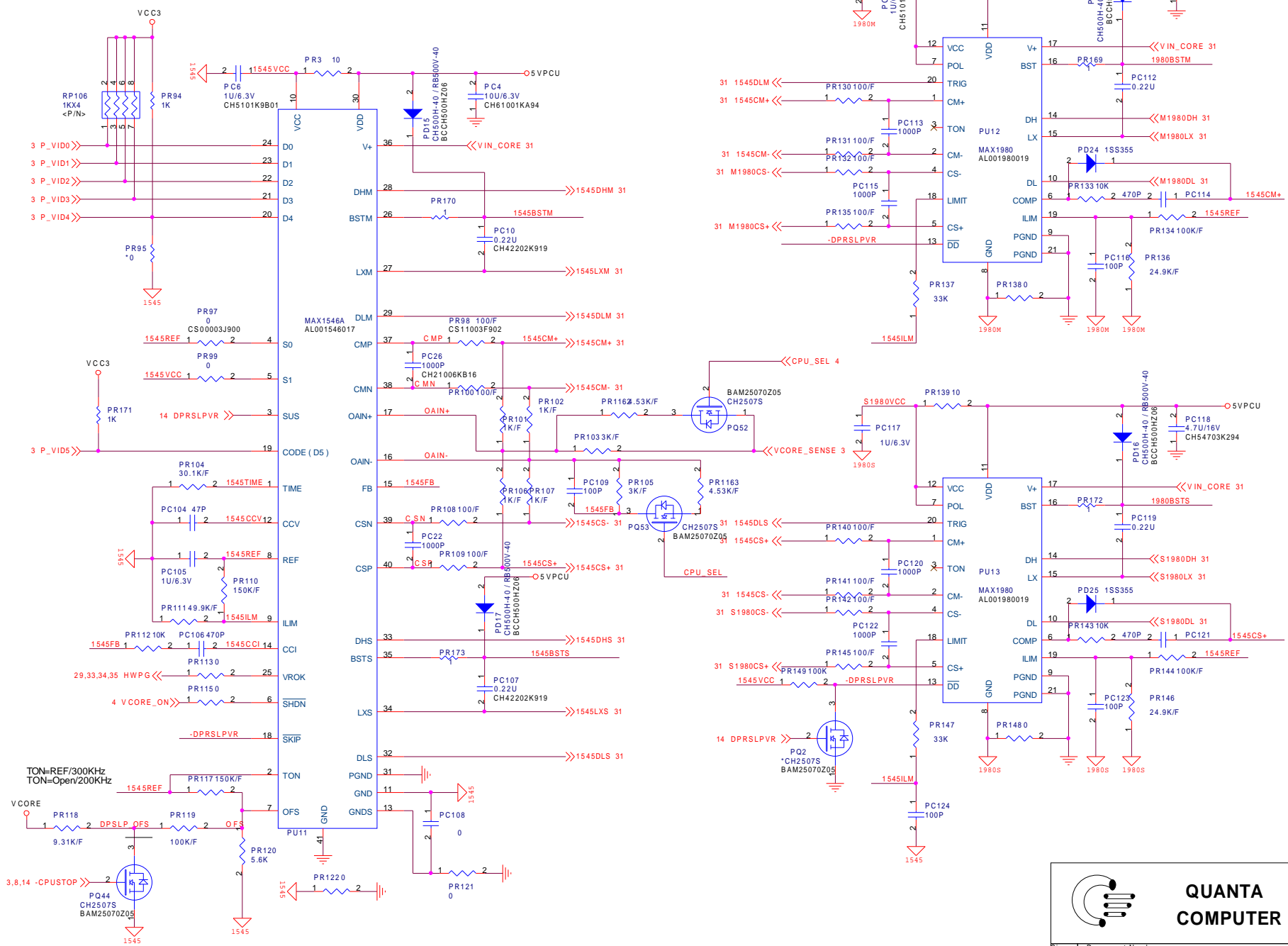


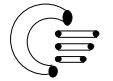
Level 1 environment-related substances should NEVER be used.  
 Molding resin, ink, paint and covered wiring material should be procured from the supplier approved as a Sony's green partner.



**QUANTA  
COMPUTER**

Size	Document Number	Rev
	<b>JE5.1 Main Board</b>	<b>2A</b>
Date:	Monday, August 30, 2004	Sheet 31 of 36



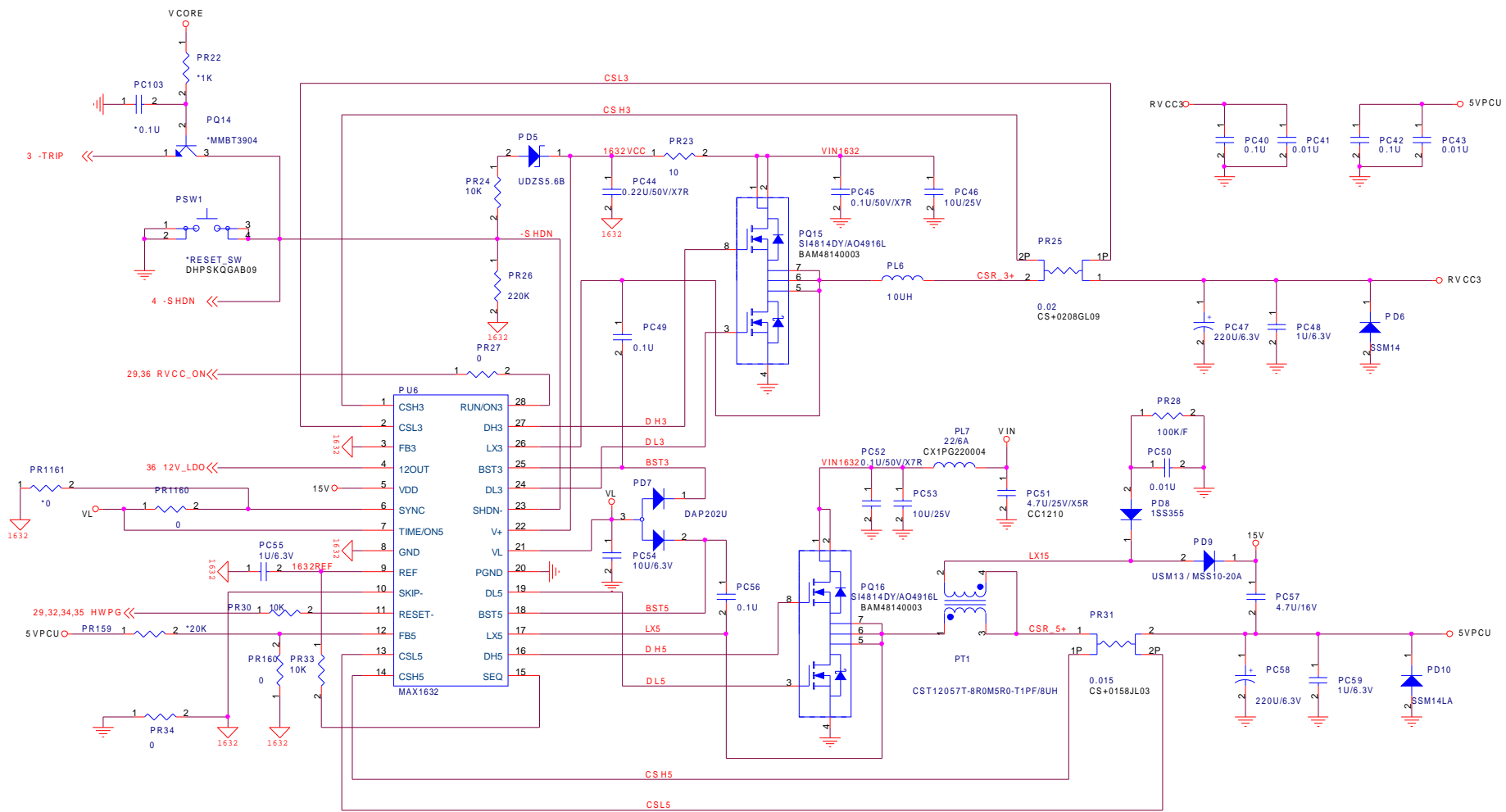



**QUANTA  
COMPUTER**

Size	Document Number	Rev
	<b>JE5.1 Main Board</b>	<b>2A</b>
Date:	Monday, August 30, 2004	Sheet 32 of 36

Level 1 environment-related substances should NEVER be used.  
 Molding resin, ink, paint and covered wiring material should be procured from the supplier approved as a Sony's green partner.



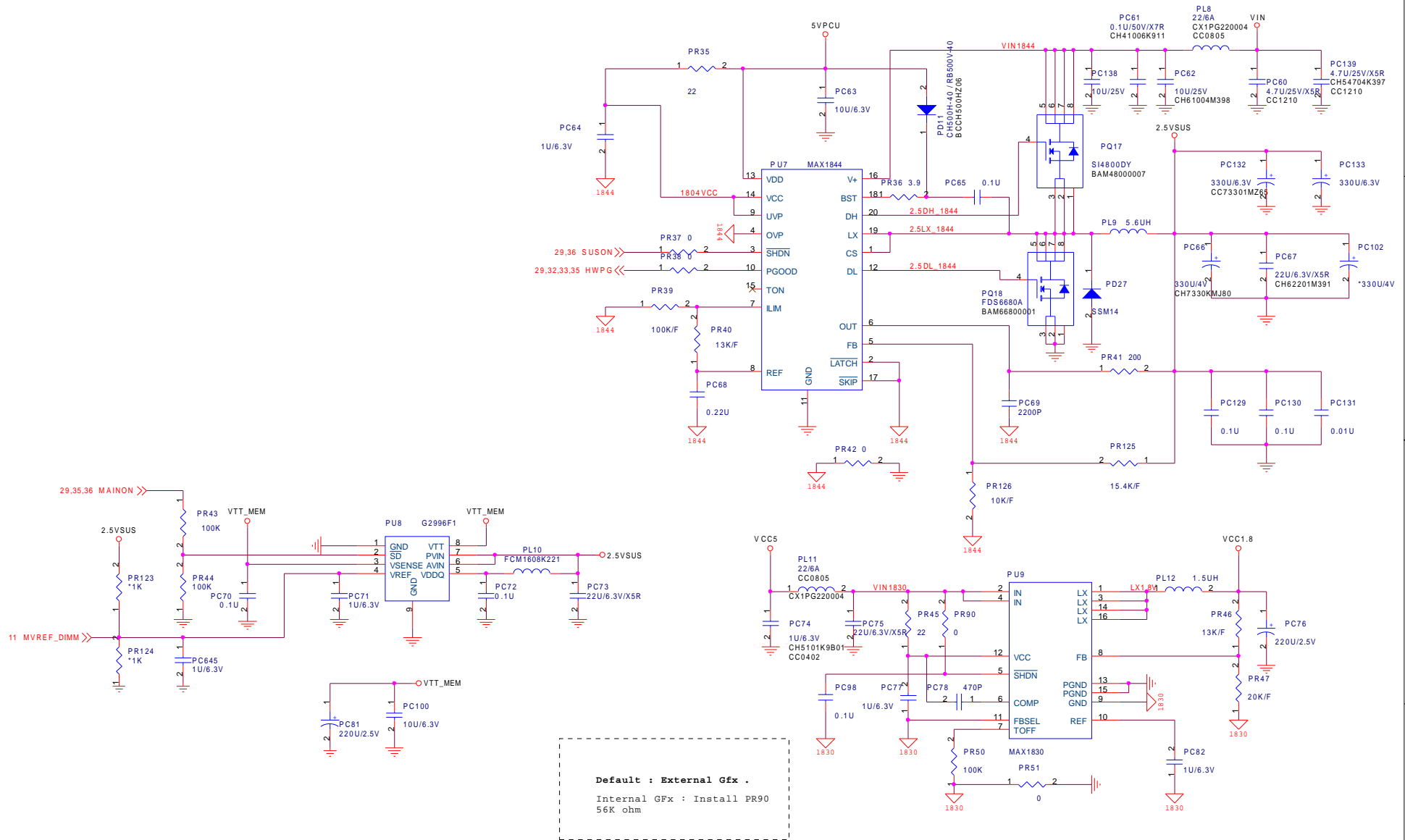




**QUANTA  
COMPUTER**

Size	Document Number	Rev
	<b>JE5.1 Main Board</b>	<b>2A</b>
Date:	Monday, August 30, 2004	Sheet 33 of 36

Level 1 environment-related substances should NEVER be used.  
Molding resin, ink, paint and covered wiring material should be procured from the supplier approved as a Sony's green partner.

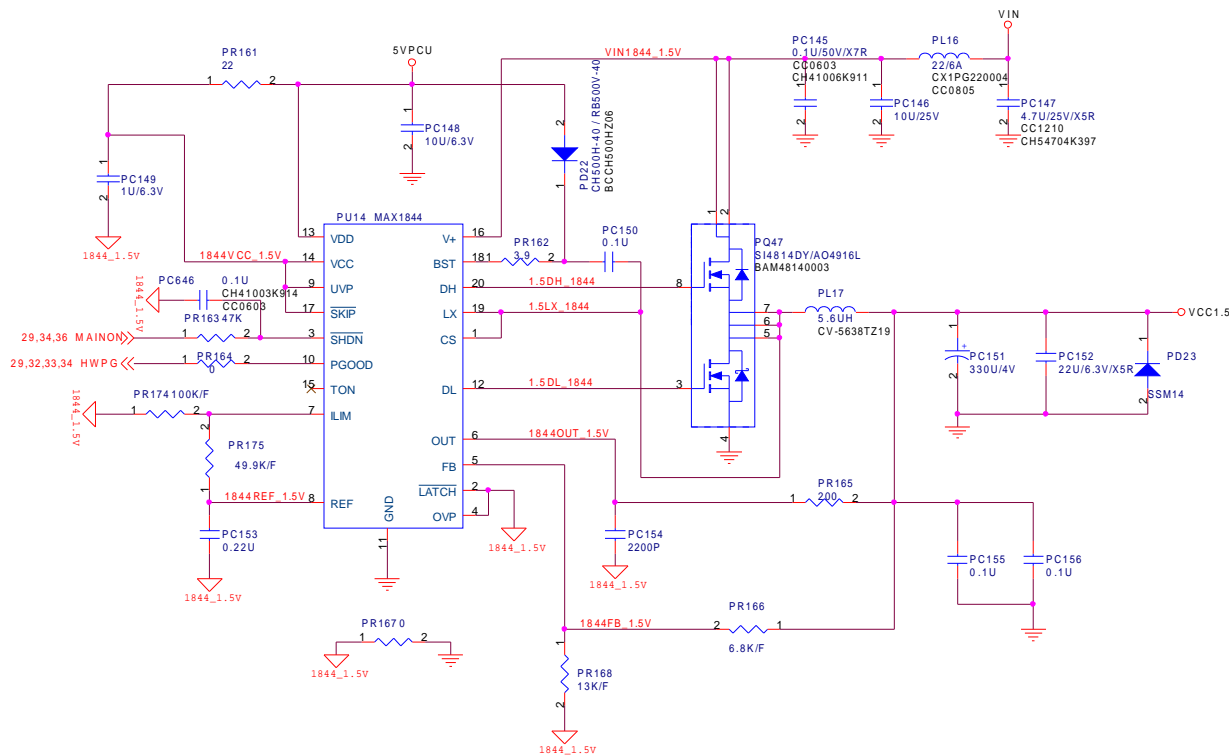


Default : External Gfx .  
 Internal Gfx : Install PR90  
 56K ohm

**QUANTA COMPUTER**

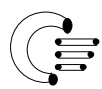
Size	Document Number	Rev
	<b>JE5.1 Main Board</b>	<b>2A</b>
Date:	Monday, August 30, 2004	Sheet 34 of 36

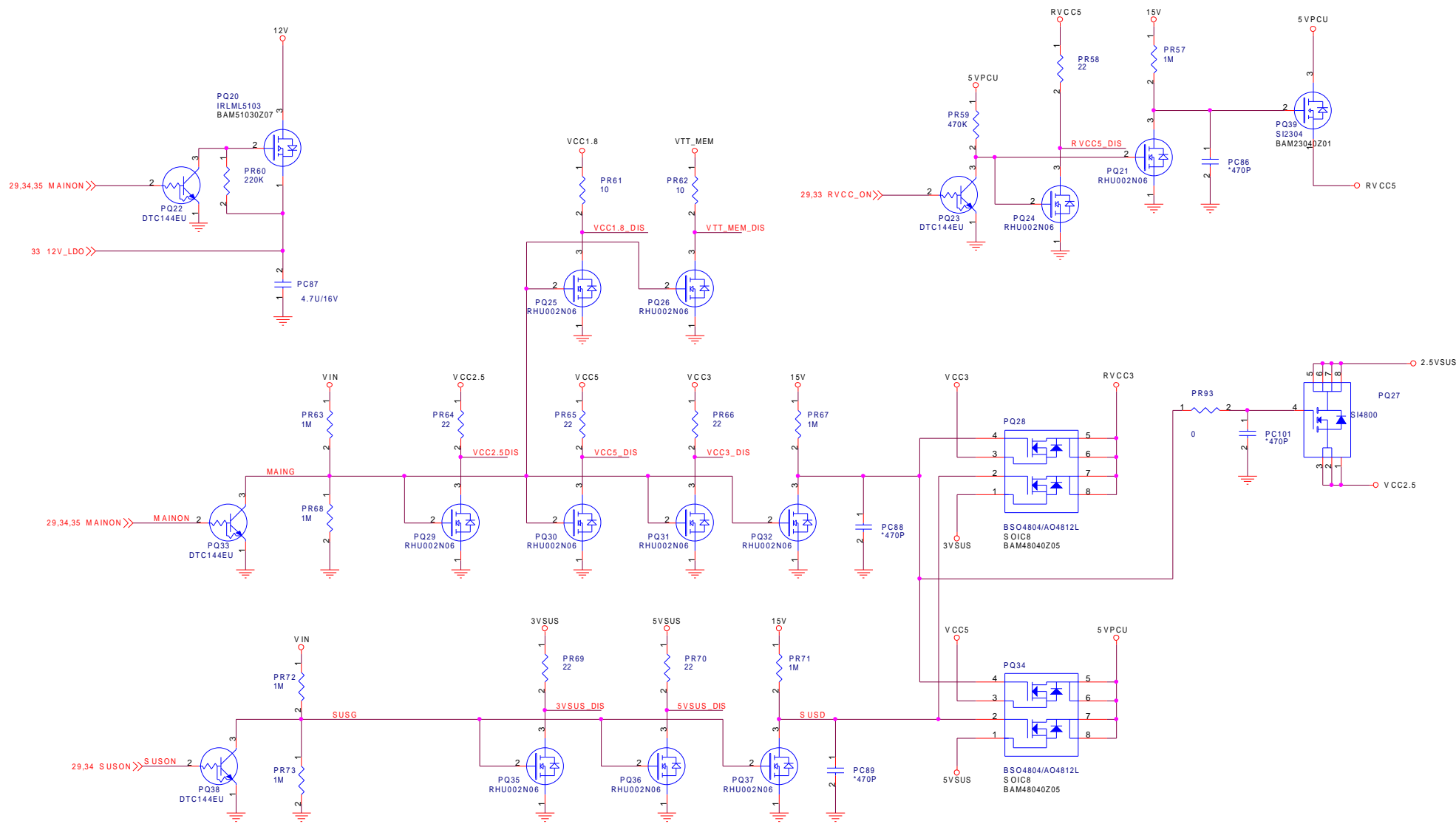
Level 1 environment-related substances should NEVER be used.  
 Molding resin, ink, paint and covered wiring material should be procured from the supplier approved as a Sony's green partner.




Default : External Gfx  
 Internal Gfx :  
 Remove all of components

Level 1 environment-related substances should NEVER be used.  
 Molding resin, ink, paint and covered wiring material should be procured from the supplier approved as a Sony's green partner.

		Size	Document Number	Rev
			JES.1 Main Board	2A
Date:	Monday, August 30, 2004	Sheet	35 of 36	



Level 1 environment-related substances should NEVER be used.  
Molding resin, ink, paint and covered wiring material should be procured from the supplier approved as a Sony's green partner.



**QUANTA  
COMPUTER**

Size	Document Number	Rev
	<b>JE5.1 Main Board</b>	<b>1A</b>
Date:	Monday, August 30, 2004	Sheet 36 of 36