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P. Leader	Check by	Design by
陳奇凱	南海	南海

Project Code & Schematics Subject: MS72 Main Board

PCB P/N: (FUBAI) 1P-006B100-60SA
(NAN YA) 1P-006B200-60SA
(HANSTAR) 1P-006B500-60SA

FOXCONN HON HAI Precision Ind Co., Ltd
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Project Code & Schematics Subject: MS72 Main Board

PCB P/N:

(FUBAI)	1P-006B100-60SA
(NAN YA)	1P-006B200-60SA
(HANSTAR)	1P-006B500-60SA

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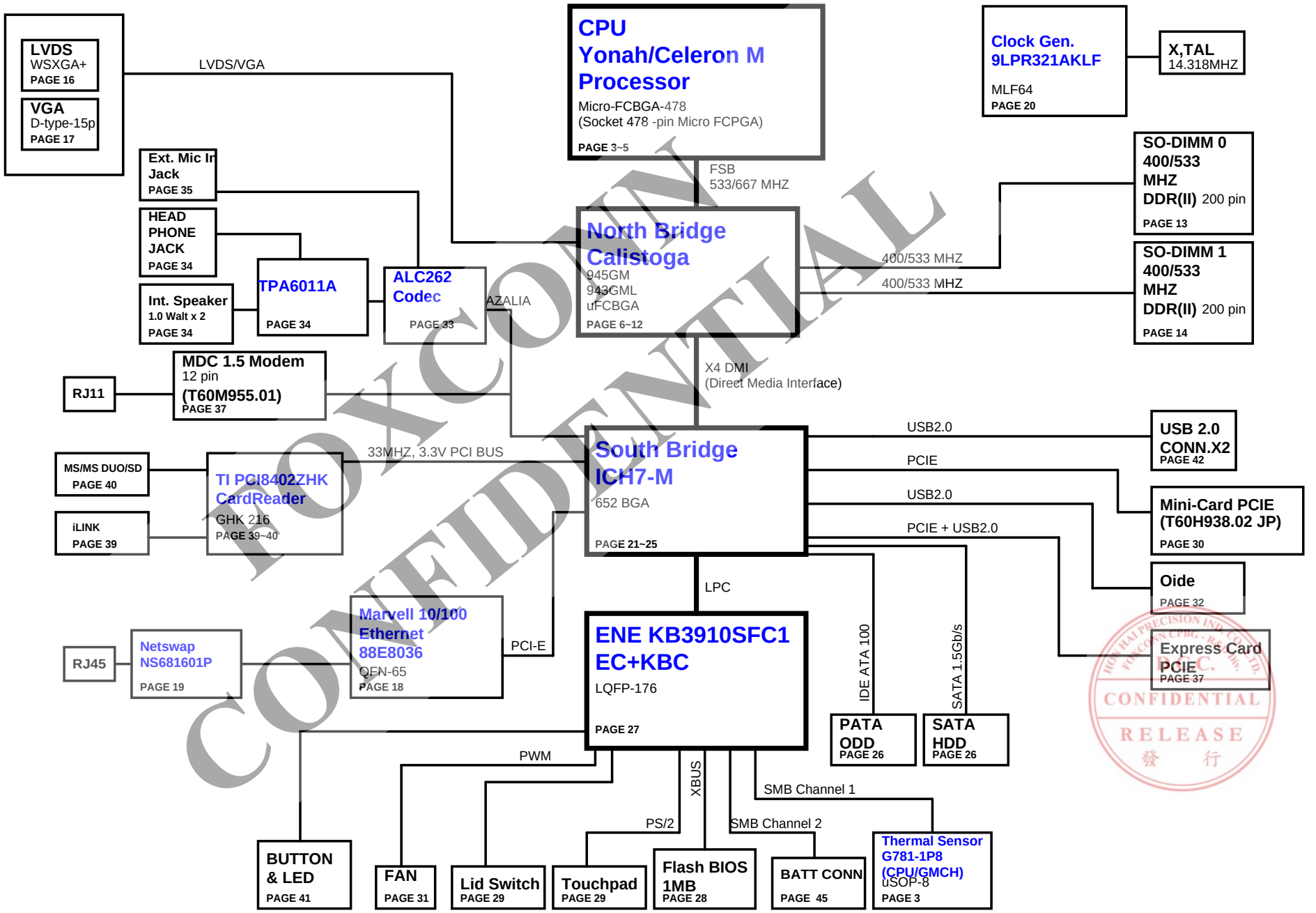
RAPTOR3/MS72(CALISTOGA GM/GML Block Diagram)



FOXCONN	HON HAI Precision Ind. Co., Ltd. CCPBG - R&D Division	
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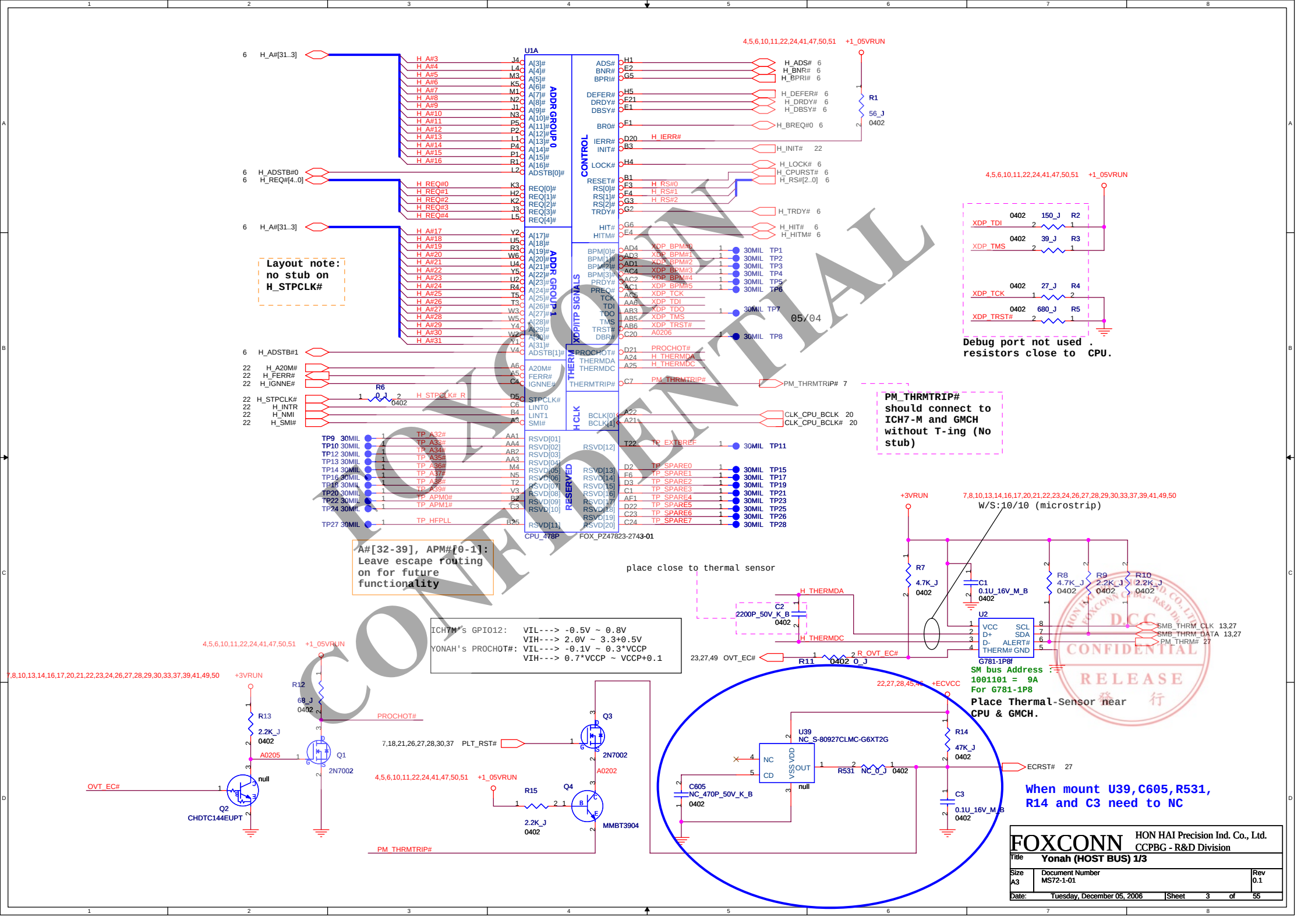
U1A
ADDR GROUP 0
ADDR GROUP 1
CONTROL
XDP/ITP SIGNALS
THERM
H CLK
RESERVED
CPU_478P
FOX_PZ47823-2743-01
Layout note:
no stub on
H_STPCLK#
Debug port not used .
resistors close to CPU.
PM_THRMTRIP# should connect to ICH7-M and GMCH without T-ing (No stub)
A#[32-39], APM#[0-1]:
Leave escape routing
on for future
functionality
place close to thermal sensor
W/S:10/10 (microstrip)
ICH7M's GPIO12: VIL---> -0.5V ~ 0.8V
VIH---> 2.0V ~ 3.3+0.5V
YONAH's PROCHOT#: VIL---> -0.1V ~ 0.3*VCCP
VIH---> 0.7*VCCP ~ VCCP+0.1
SM bus Address :
1001101 = 9A
For G781-1P8
Place Thermal-Sensor near
CPU & GMCH.
When mount U39,C605,R531,
R14 and C3 need to NC
05/04
CONFIDENTIAL
RELEASE
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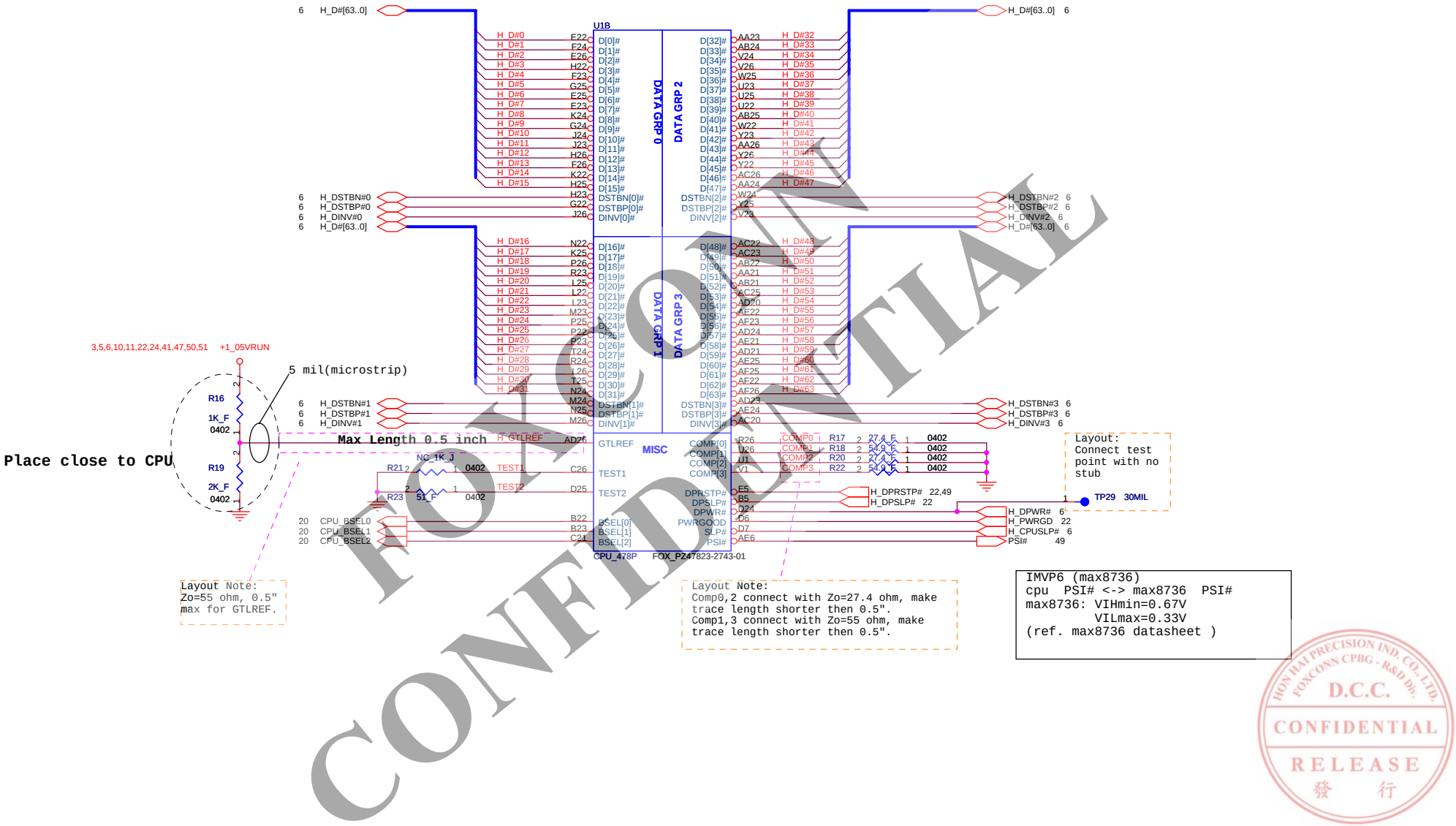

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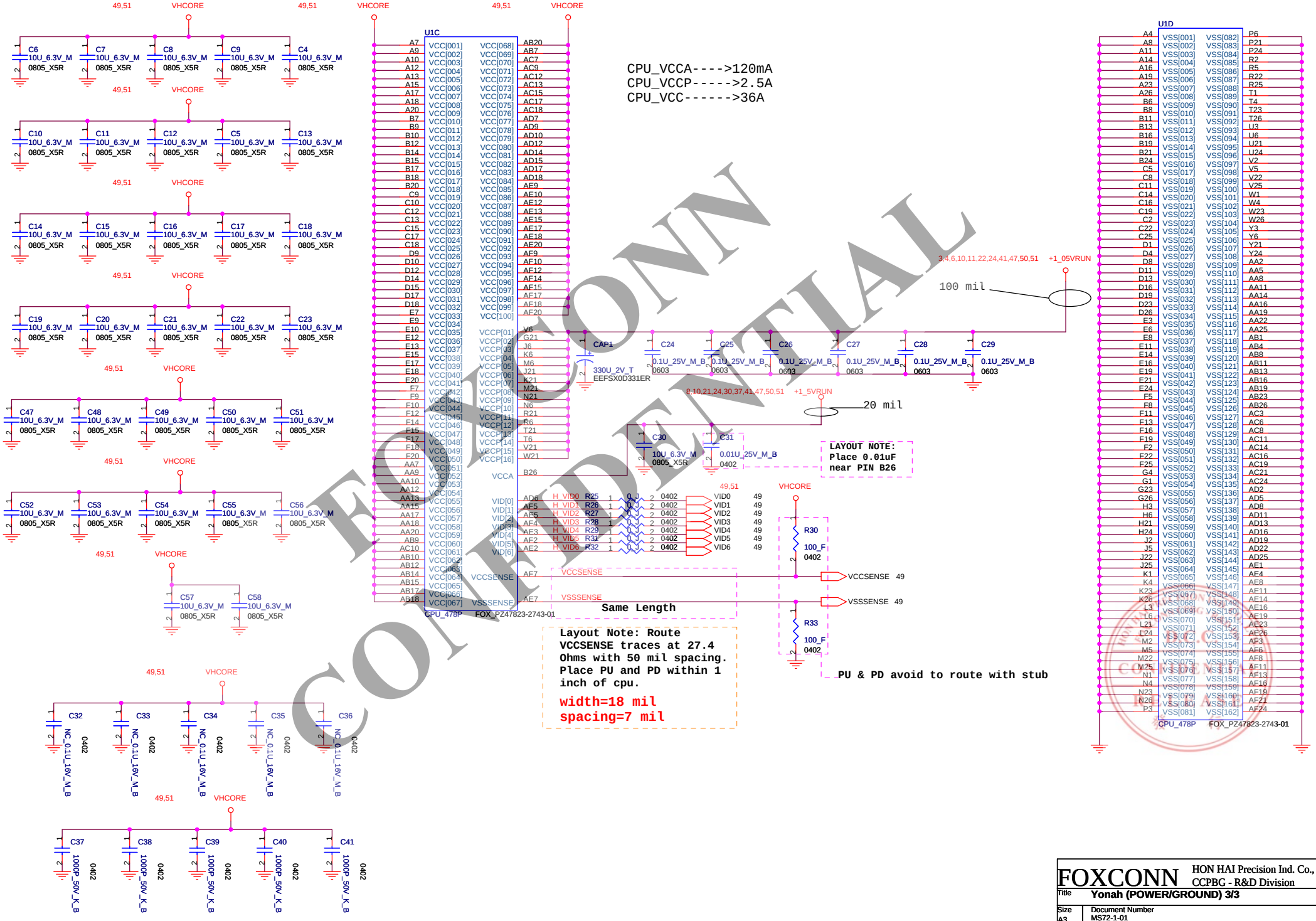

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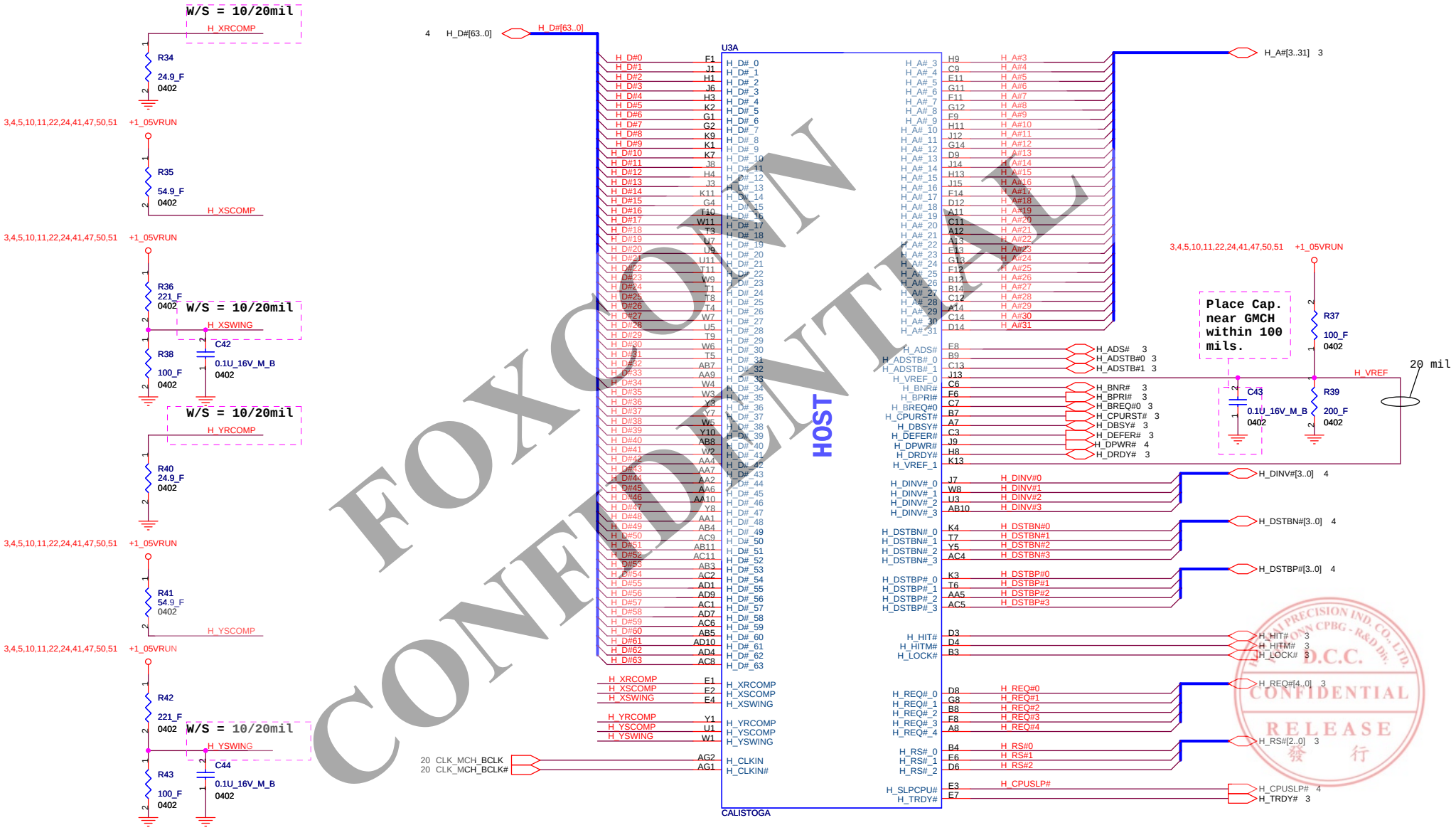

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Title	CALISTOGA (HOST) 1 of 7		
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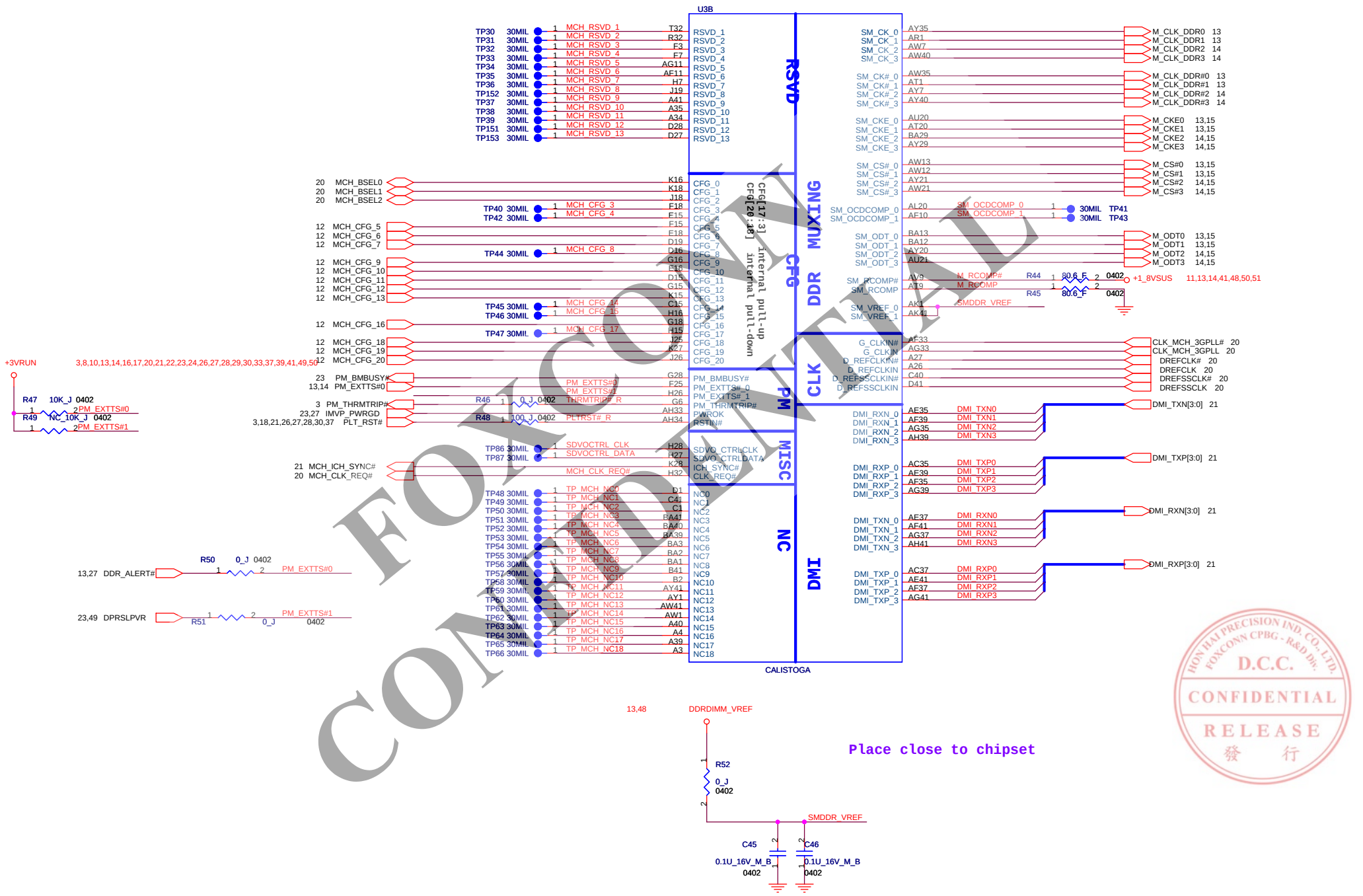

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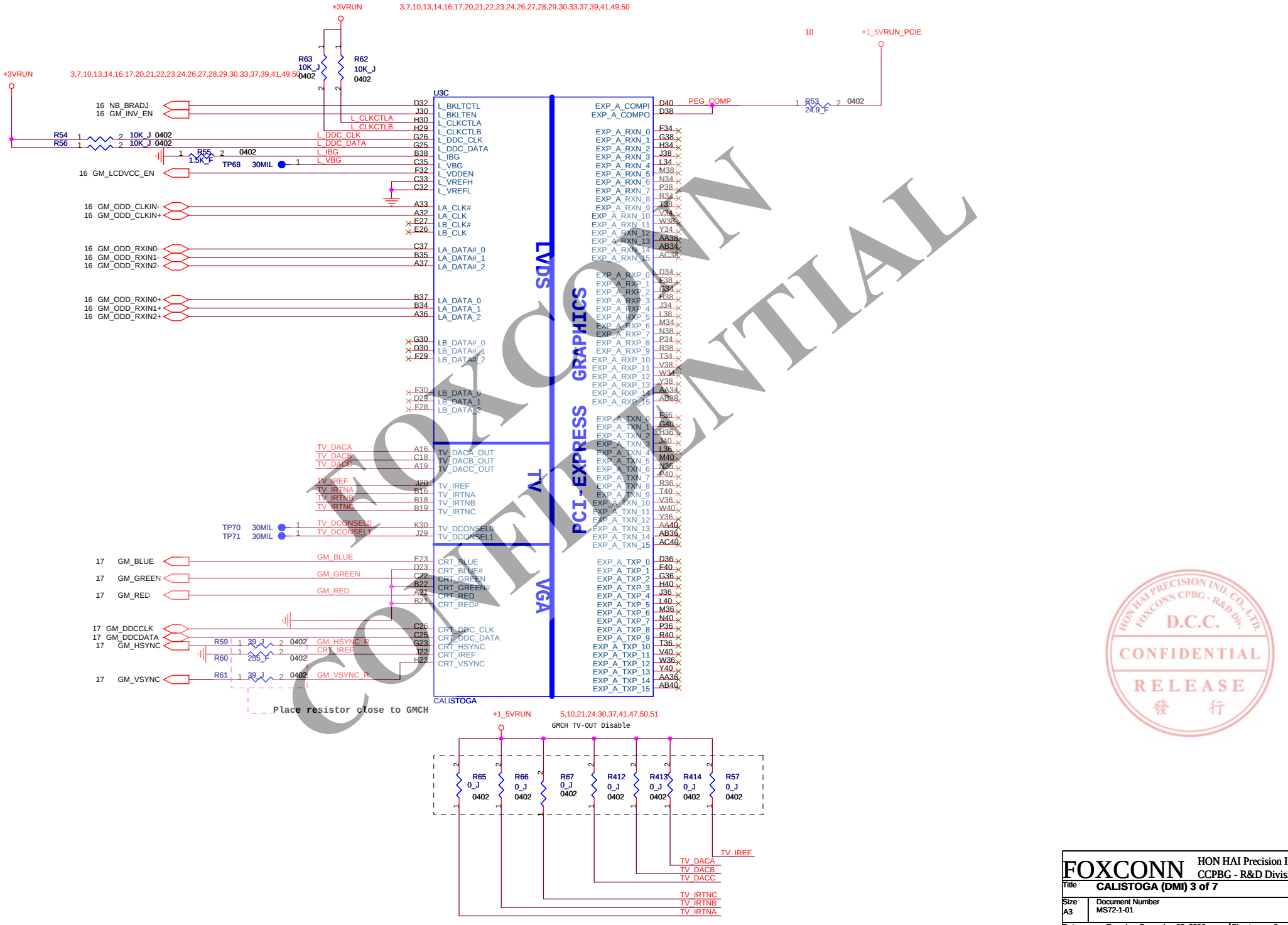

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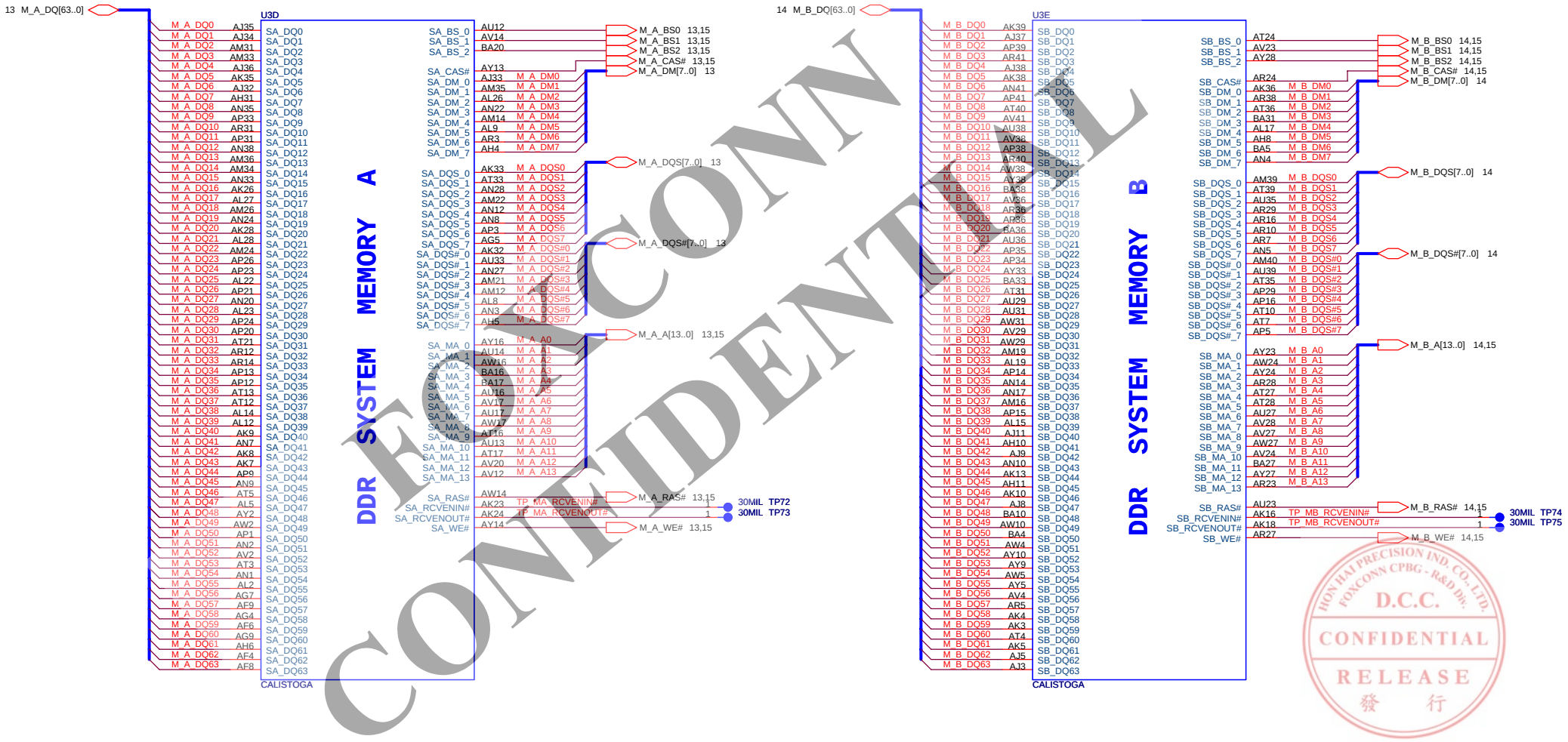

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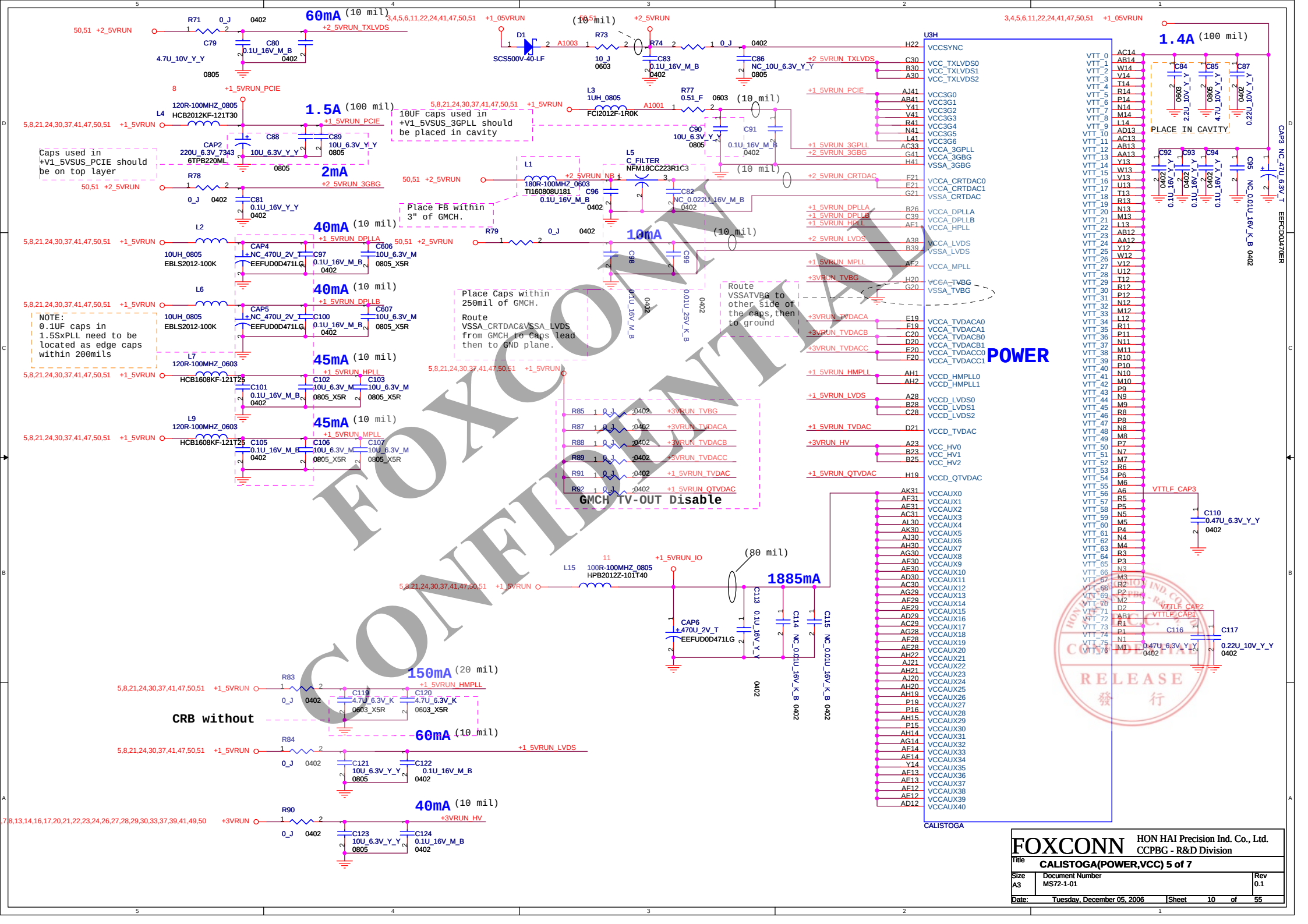
60mA (10 mil)
1.5A (100 mil)
2mA
40mA (10 mil)
40mA (10 mil)
45mA (10 mil)
45mA (10 mil)
10mA
(10 mil)
(80 mil)
1885mA
150mA (20 mil)
60mA (10 mil)
40mA (10 mil)
1.4A (100 mil)
Caps used in +V1_5VSUS_PCIE should be on top layer
10UF caps used in +V1_5VSUS_3GPLL should be placed in cavity
Place FB within 3" of GMCH.
Place Caps within 250mil of GMCH.
Route VSSA_CRTDAC&VSSA_LVDS from GMCH to caps lead then to GND plane.
Route VSSATVBG to other side of the caps,then to ground
NOTE: 0.1UF caps in 1.5SxPLL need to be located as edge caps within 200mils
GMCH TV-OUT Disable
CRB without
PLACE IN CAVITY
POWER
U3H
CALISTOGA
FOXCONN
CONFIDENTIAL
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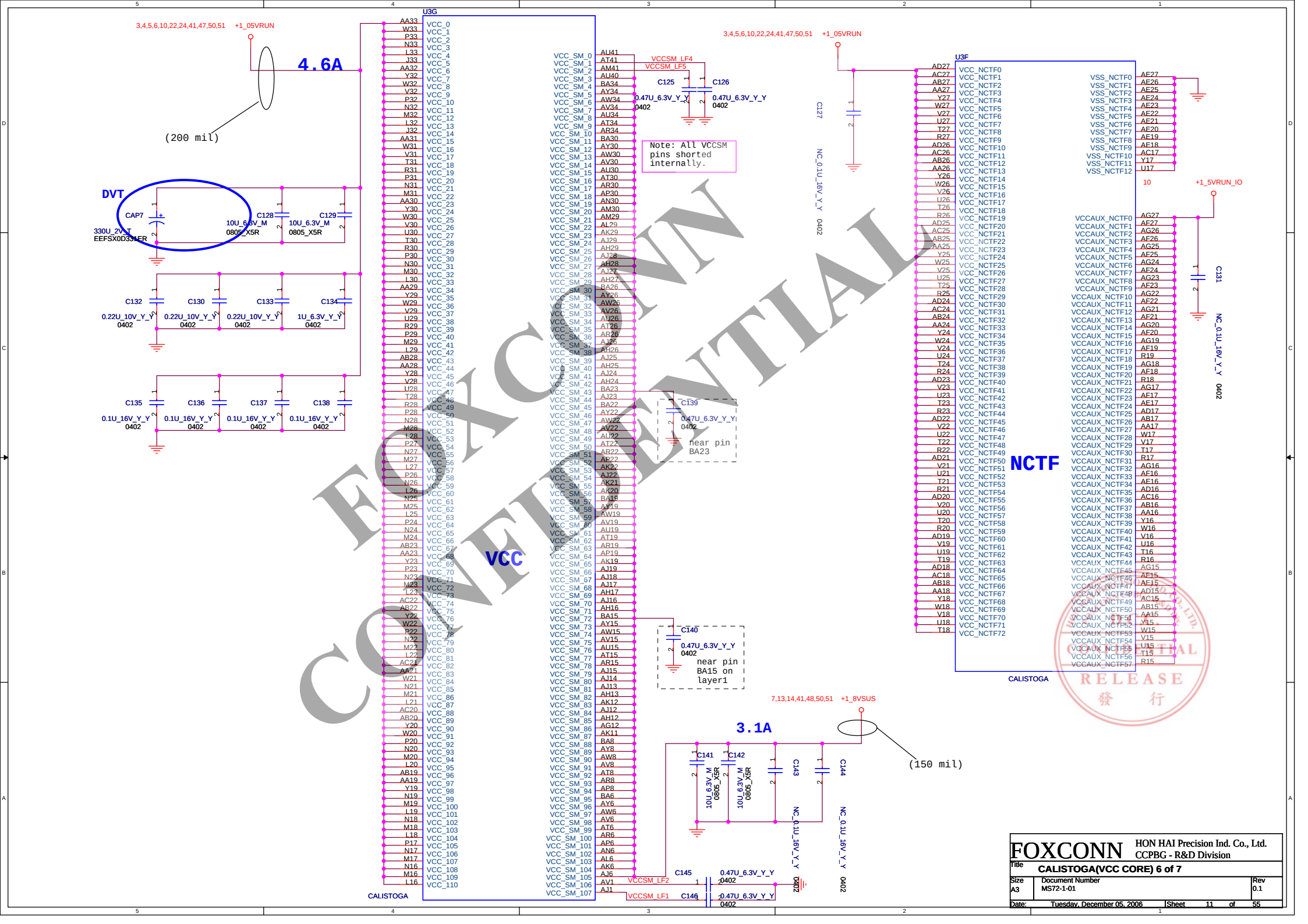

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7 MCH_CFG_5 — 1 — 30MIL TP76

MCH_CFG_5	Low = DMIx2 High = DMIx4

7 MCH_CFG_6 — 1 — 30MIL TP77

MCH_CFG_6	Low = Moby Dick High = Calistoga

DDR2 select (default high)

7 MCH_CFG_7 — 1 — 30MIL TP78

MCH_CFG_7 (CPU Strap)	Low = RSVD High = Mobile Yonah processor

7 MCH_CFG_9 — 1 — 30MIL TP81

MCH_CFG_9 (PCIE Graphics Lane)	Low = Reverse Lane High = Normal operation

For layout convenience

7 MCH_CFG_10 — 1 — 30MIL TP82

MCH_CFG_10 (HOST PLL VCC SELECT)	Low = RESERVED High = MOBILITY

7 MCH_CFG_11 — 1 — R93 NC_2.2K_J 0402 — 2

MCH_CFG_11 (PSB 4x CLK ENABLE)	Low = Calistoga High = Reserved

7 MCH_CFG_12 — 1 — 30MIL TP84
7 MCH_CFG_13 — 1 — 30MIL TP85

MCH_CFG_[13:12] (XOR/ALLZ)	00=Partial Clock Gating Disable 01=XOR Mode Enable 10=All-Z Mode Enable 11=Normal Operation(Default)

7 MCH_CFG_16 — 1 — 30MIL TP160

MCH_CFG_16 (FSB Dynamic ODT)	Low = Dynamic ODT Disabled High = Dynamic ODT Enable

MCH_CFG_18 (VCC_CORE Select)	Low = 1.05V(default) High = 1.5V

7 MCH_CFG_18 — 1 — 30MIL TP79

MCH_CFG_19 (DMI LANE REVERSAL)	Low = Normal(default) High = LANES REVERSED

7 MCH_CFG_19 — 1 — 30MIL TP80

MCH_CFG_20 (PCIe Backward Interpoerability mode)	Low = Only SDVO or PCIE x1 is operational (defaults)) High = SDVO and PCIE x1 are operating simultaneously via the PEG port

7 MCH_CFG_20 — 1 — 30MIL TP83

Layout Noe:
Location of all MCH_CFG strap resistors needs to be close to trace to minimize stub

U3I

AC41	VSS_0	VSS_97	AK34		
AA41	VSS_1	VSS_98	AG34		
W41	VSS_2	VSS_99	AF34		
T41	VSS_3	VSS_100	AE34		
P41	VSS_4	VSS_101	AC34		
M41	VSS_5	VSS_102	C34		
J41	VSS_6	VSS_103	AW33		
F41	VSS_7	VSS_104	AV33		
AV40	VSS_8	VSS_105	AR33		
AP40	VSS_9	VSS_106	AE33		
AN40	VSS_10	VSS_107	AB33		
AK40	VSS_11	VSS_108	Y33		
AJ40	VSS_12	VSS_109	V33		
AH40	VSS_13	VSS_110	T33		
AG40	VSS_14	VSS_111	R33		
AF40	VSS_15	VSS_112	M33		
AE40	VSS_16	VSS_113	H33		
B40	VSS_17	VSS_114	G33		
AY39	VSS_18	VSS_115	F33		
AW39	VSS_19	VSS_116	D33		
AV39	VSS_20	VSS_117	B33		
AR39	VSS_21	VSS_118	AH32		
AN39	VSS_22	VSS_119	AG32		
AJ39	VSS_23	VSS_120	AF32		
AC39	VSS_24	VSS_121	AE32		
AB39	VSS_25	VSS_122	AC32		
AA39	VSS_26	VSS_123	AB32		
Y39	VSS_27	VSS_124	G32		
W39	VSS_28	VSS_125	B32		
V39	VSS_29	VSS_126	AY31		
T39	VSS_30	VSS_127	AV31		
R39	VSS_31	VSS_128	AN31		
P39	VSS_32	VSS_129	AJ31		
N39	VSS_33	VSS_130	AG31		
M39	VSS_34	VSS_131	AB31		
L39	VSS_35	VSS_132	Y31		
J39	VSS_36	VSS_133	AR30		
H39	VSS_37	VSS_134	E30		
G39	VSS_38	VSS_135	AT29		
F39	VSS_39	VSS_136	AN29		
D39	VSS_40	VSS_137	AB29		
AT38	VSS_41	VSS_138	T29		
AM38	VSS_42	VSS_139	N29		
AH38	VSS_43	VSS_140	K29		
AG38	VSS_44	VSS_141	G29		
AF38	VSS_45	VSS_142	E29		
AE38	VSS_46	VSS_143	C29		
C38	VSS_47	VSS_144	B29		
AK37	VSS_48	VSS_145	A29		
AH37	VSS_49	VSS_146	BA28		
AB37	VSS_50	VSS_147	AW28		
AA37	VSS_51	VSS_148	AU28		
Y37	VSS_52	VSS_149	AP28		
W37	VSS_53	VSS_150	AM28		
V37	VSS_54	VSS_151	AD28		
T37	VSS_55	VSS_152	AC28		
R37	VSS_56	VSS_153	W28		
P37	VSS_57	VSS_154	J28		
N37	VSS_58	VSS_155	F28		
M37	VSS_59	VSS_156	AP27		
L37	VSS_60	VSS_157	AM27		
J37	VSS_61	VSS_158	AK27		
H37	VSS_62	VSS_159	J27		
G37	VSS_63	VSS_160	G27		
F37	VSS_64	VSS_161	F27		
D37	VSS_65	VSS_162	C27		
AY36	VSS_66	VSS_163	B27		
AW36	VSS_67	VSS_164	AN26		
AN36	VSS_68	VSS_165	M26		
AH36	VSS_69	VSS_166	K26		
AG36	VSS_70	VSS_167	F26		
AF36	VSS_71	VSS_168	D26		
AE36	VSS_72	VSS_169	AK25		
AC36	VSS_73	VSS_170	P25		
C36	VSS_74	VSS_171	K25		
B36	VSS_75	VSS_172	H25		
BA35	VSS_76	VSS_173	F25		
AV35	VSS_77	VSS_174	D25		
AR35	VSS_78	VSS_175	A25		
AH35	VSS_79	VSS_176	BA24		
AB35	VSS_80	VSS_177	AU24		
AA35	VSS_81	VSS_178	AL24		
Y35	VSS_82	VSS_179	AW23		
W35	VSS_83				
V35	VSS_84				
T35	VSS_85				
R35	VSS_86				
P35	VSS_87				
N35	VSS_88				
M35	VSS_89				
L35	VSS_90				
J35	VSS_91				
H35	VSS_92				
G35	VSS_93				
F35	VSS_94				
D35	VSS_95				
AN34	VSS_96				

VSS

CALISTOGA

U3J

AT23	VSS_180	VSS_273	J11
AN23	VSS_181	VSS_274	D11
AM23	VSS_182	VSS_275	B11
AH23	VSS_183	VSS_276	AV10
AC23	VSS_184	VSS_277	AP10
W23	VSS_185	VSS_278	AL10
K23	VSS_186	VSS_279	AJ10
J23	VSS_187	VSS_280	AG10
F23	VSS_188	VSS_281	AC10
C23	VSS_189	VSS_282	W10
AA22	VSS_190	VSS_283	U10
K22	VSS_191	VSS_284	BA9
G22	VSS_192	VSS_285	AW9
F22	VSS_193	VSS_286	AR9
E22	VSS_194	VSS_287	AH9
D22	VSS_195	VSS_288	AB9
A22	VSS_196	VSS_289	Y9
BA21	VSS_197	VSS_290	R9
AV21	VSS_198	VSS_291	G9
AR21	VSS_199	VSS_292	E9
AN21	VSS_200	VSS_293	A9
AL21	VSS_201	VSS_294	AG8
AB21	VSS_202	VSS_295	AD8
Y21	VSS_203	VSS_296	AA8
P21	VSS_204	VSS_297	U8
K21	VSS_205	VSS_298	K8
J21	VSS_206	VSS_299	C8
H21	VSS_207	VSS_300	BA7
C21	VSS_208	VSS_301	AV7
AW20	VSS_209	VSS_302	AP7
AR20	VSS_210	VSS_303	AL7
AM20	VSS_211	VSS_304	AJ7
AA20	VSS_212	VSS_305	AH7
K20	VSS_213	VSS_306	AF7
B20	VSS_214	VSS_307	AC7
A20	VSS_215	VSS_308	R7
AN19	VSS_216	VSS_309	G7
AC19	VSS_217	VSS_310	D7
W19	VSS_218	VSS_311	AG6
K19	VSS_219	VSS_312	AD6
G19	VSS_220	VSS_313	AB6
C19	VSS_221	VSS_314	Y6
AH18	VSS_222	VSS_315	U6
P18	VSS_223	VSS_316	N6
H18	VSS_224	VSS_317	K6
D18	VSS_225	VSS_318	H6
A18	VSS_226	VSS_319	B6
AY17	VSS_227	VSS_320	AV5
AR17	VSS_228	VSS_321	AF5
AP17	VSS_229	VSS_322	AD5
AM17	VSS_230	VSS_323	AY4
AK17	VSS_231	VSS_324	AR4
AV16	VSS_232	VSS_325	AP4
AN16	VSS_233	VSS_326	AL4
AL16	VSS_234	VSS_327	AJ4
J16	VSS_235	VSS_328	Y4
F16	VSS_236	VSS_329	U4
C16	VSS_237	VSS_330	R4
AN15	VSS_238	VSS_331	J4
AM15	VSS_239	VSS_332	F4
AK15	VSS_240	VSS_333	C4
N15	VSS_241	VSS_334	AY3
M15	VSS_242	VSS_335	AW3
L15	VSS_243	VSS_336	AV3
B15	VSS_244	VSS_337	AL3
A15	VSS_245	VSS_338	AH3
BA14	VSS_246	VSS_339	AG3
AT14	VSS_247	VSS_340	AF3
AK14	VSS_248	VSS_341	AD3
AD14	VSS_249	VSS_342	AC3
AA14	VSS_250	VSS_343	AA3
U14	VSS_251	VSS_344	G3
K14	VSS_252	VSS_345	AT2
H14	VSS_253	VSS_346	AR2
E14	VSS_254	VSS_347	AP2
AV13	VSS_255	VSS_348	AK2
AR13	VSS_256	VSS_349	AJ2
AN13	VSS_257	VSS_350	AD2
AM13	VSS_258	VSS_351	AB2
AL13	VSS_259	VSS_352	Y2
AG13	VSS_260	VSS_353	U2
P13	VSS_261	VSS_354	T2
F13	VSS_262	VSS_355	N2
D13	VSS_263	VSS_356	J2
B13	VSS_264	VSS_357	H2
AY12	VSS_265	VSS_358	E2
AC12	VSS_266	VSS_359	C2
K12	VSS_267	VSS_360	AL1
H12	VSS_268		
E12	VSS_269		
AD11	VSS_270		
AA11	VSS_271		
Y11	VSS_272		

VSS

CALISTOGA

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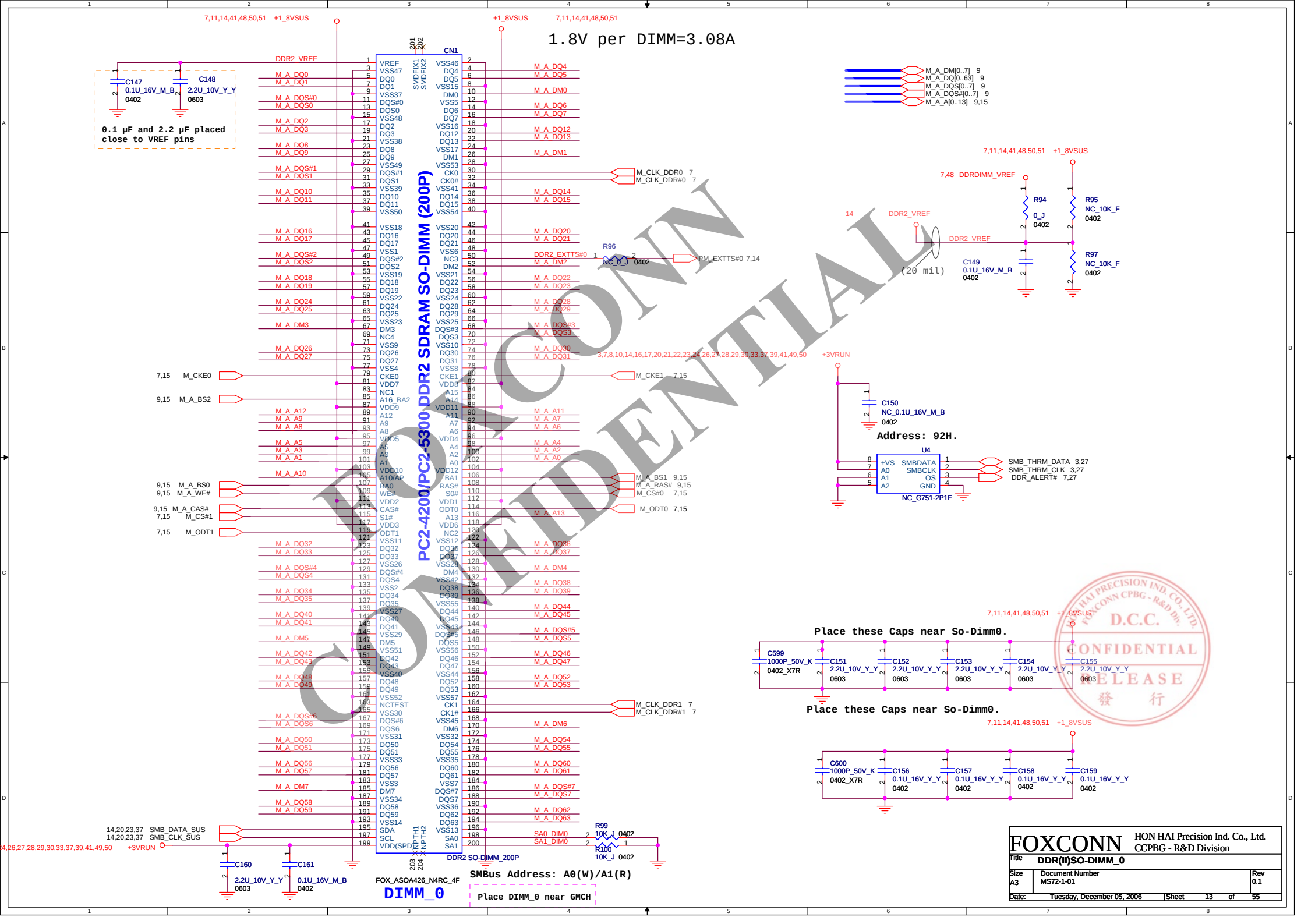
Title	CALISTOGA(VSS) 7 of 7	
Size A3	Document Number MS72-1-01	Rev 0.1
Date: Tuesday, December 05, 2006	Sheet 12 of 55	


1.8V per DIMM=3.08A
PC2-4200/PC2-5300 DDR2 SDRAM SO-DIMM (200P)
DDR2 SO-DIMM_200P
CN1
DIMM_0
FOX_ASOA426_N4RC_4F
SMBus Address: A0(W)/A1(R)
Place DIMM_0 near GMCH
0.1 µF and 2.2 µF placed close to VREF pins
Address: 92H.
Place these Caps near So-Dimm0.
Place these Caps near So-Dimm0.
NC_G751-2P1F
+1_8VSUS
+3VRUN
DDR2_VREF
DDRDIMM_VREF
FOXCONN
HON HAI Precision Ind. Co., Ltd.
CCPBG - R&D Division
DDR(II)SO-DIMM_0
Size A3
Document Number MS72-1-01
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13

DDR2_VREF

7,11,13,41,48,50,51 +1_8VSUS

+1_8VSUS 7,11,13,41,48,50,51

1.8V per DIMM=3.08A

C163 0.1U_16V_M_B 0402

C162 2.2U_10V_Y_Y 0603

0.1 µF and 2.2 µF placed close to VREF pins

CN2

DDR2 SO-DIMM_200P

PC2-4200/PC2-5300 DDR2 SDRAM SO-DIMM (200P)

FOX_ASOA426_N4SC_4F

DIMM_1

M_B_DM[0..7] 9
M_B_DQ[0..63] 9
M_B_DQS[0..7] 9
M_B_DQS#[0..7] 9
M_B_A[0..13] 9,15

M_CLK_DDR3 7
M_CLK_DDR#3 7

R101 NC_0_J 0402

DDR2_EXTTS#1 — PM_EXTTS#0 7,13

M_CKE2 7,15

M_CKE3 7,15

M_B_BS2 9,15

M_B_BS1 9,15
M_B_RAS# 9,15
M_CS#2 7,15

M_ODT2 7,15

M_B_BS0 9,15
M_B_WE# 9,15
M_B_CAS# 9,15
M_CS#3 7,15
M_ODT3 7,15

M_CLK_DDR2 7
M_CLK_DDR#2 7

13,20,23,37 SMB_DATA_SUS
13,20,23,37 SMB_CLK_SUS

+3VRUN 7,8,10,13,16,17,20,21,22,23,24,26,27,28,29,30,33,37,39,41,49,50

R102 10K_J 0402
R103 10K_J 0402

SA0_DIM1
SA1_DIM1

C173 2.2U_10V_Y_Y 0603
C174 0.1U_16V_M_B 0402

SMBus Address: A4(W)/A5(R)

DIMM_1 is placed farther than the GMCH than DIMM_0

Place these Caps near So-Dimm1.

7,11,13,41,48,50,51 +1_8VSUS

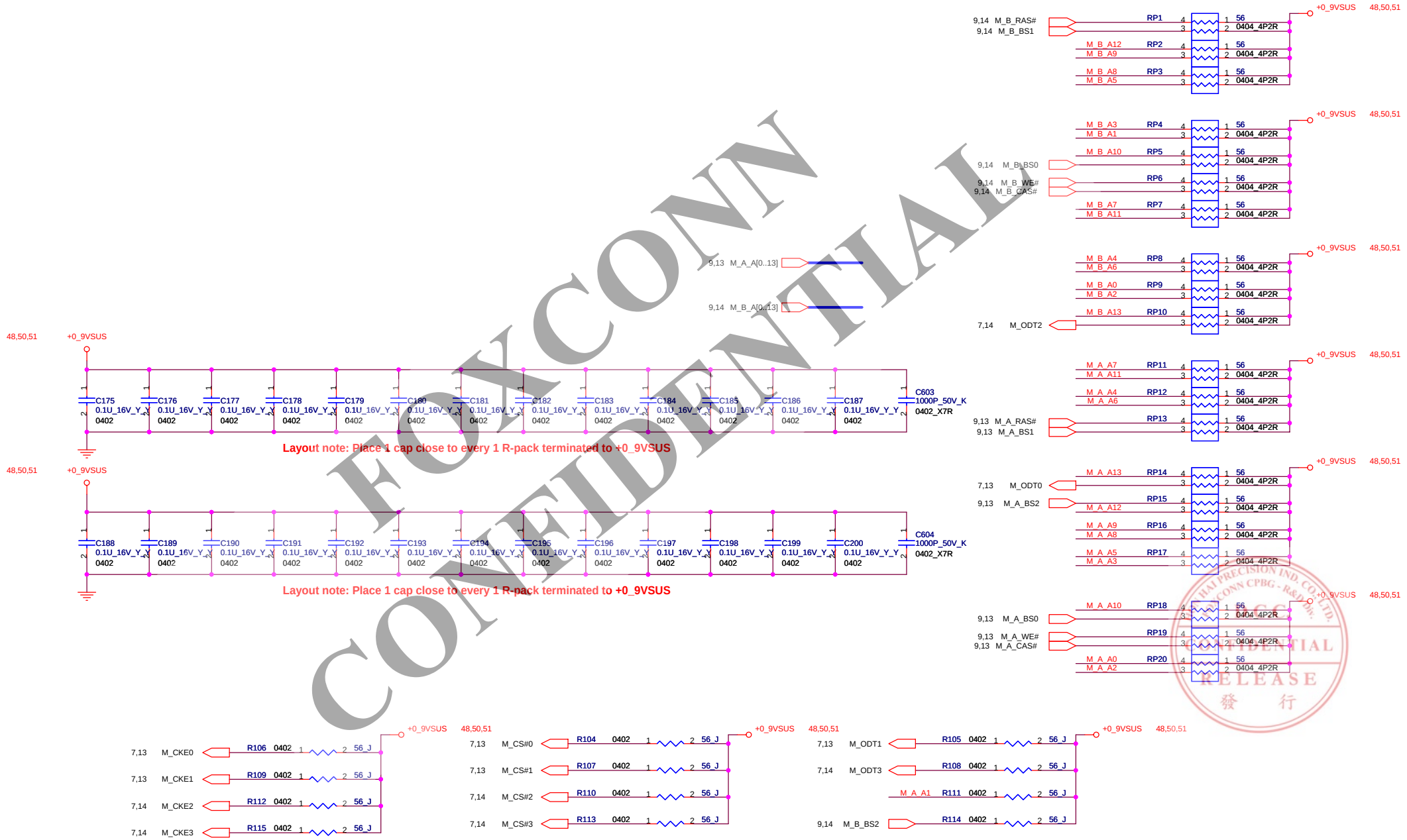
C601 1000P_50V_K 0402_X7R
C164 2.2U_10V_Y_Y 0603
C165 2.2U_10V_Y_Y 0603
C166 2.2U_10V_Y_Y 0603
C167 2.2U_10V_Y_Y 0603
C168 2.2U_10V_Y_Y 0603

Place these Caps near So-Dimm1.

7,11,13,41,48,50,51 +1_8VSUS

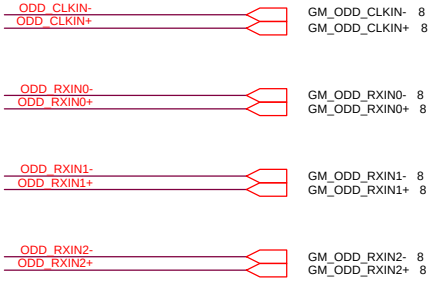
C602 1000P_50V_K 0402_X7R
C169 0.1U_16V_Y_Y 0402
C170 0.1U_16V_Y_Y 0402
C171 0.1U_16V_Y_Y 0402
C172 0.1U_16V_Y_Y 0402

FOXCONN	HON HAI Precision Ind. Co., Ltd. CCPBG - R&D Division	
Title	DDR(II)SO-DIMM_1	
Size A3	Document Number MS72-1-01	Rev 0.1
Date: Tuesday, December 05, 2006	Sheet 14 of 55	

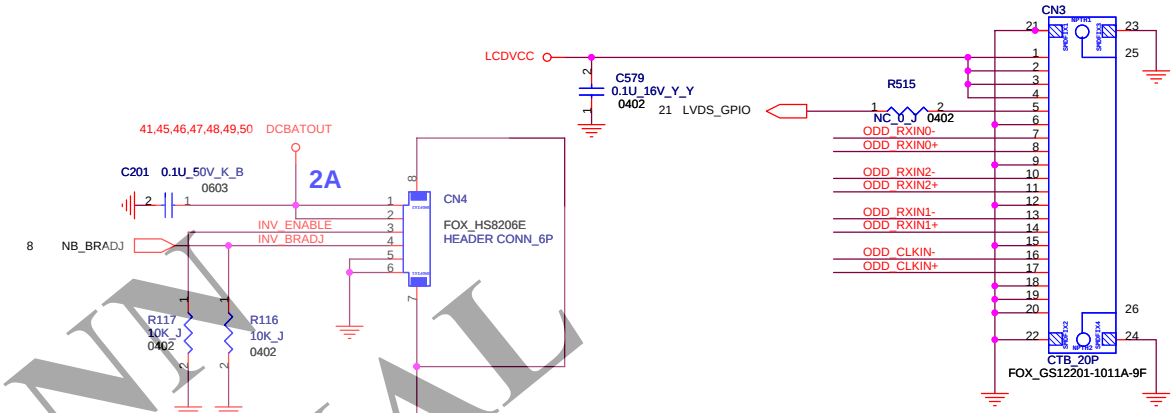


FOXCONN	HON HAI Precision Ind. Co., Ltd. CCPBG - R&D Division	
Title: DDR(II)Termination		
Size: A3	Document Number: MS72-1-01	Rev: 0.1
Date: Tuesday, December 05, 2006	Sheet 15 of 55	

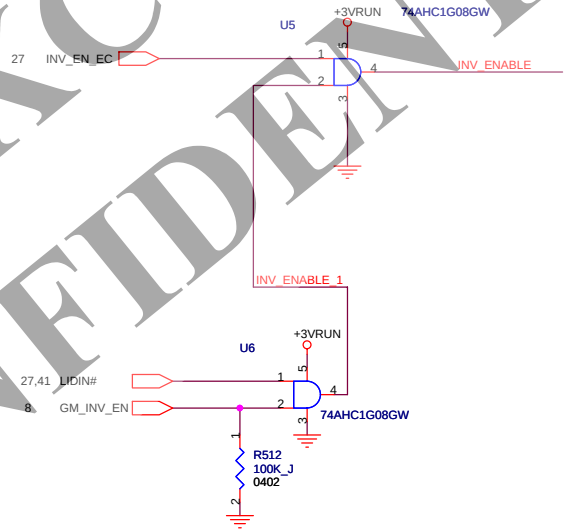
LVDS



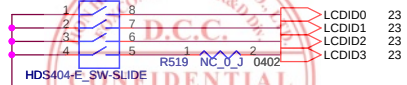
LVDS CONNECTOR



INVERTER CONNECTOR

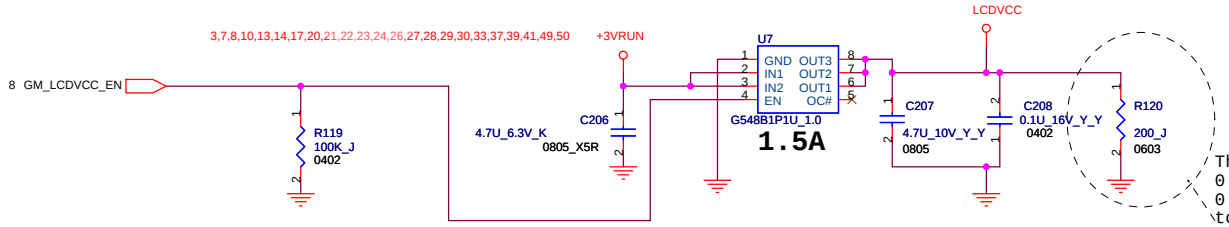


PANEL ID



Type	WXGA	WXGA	WSXGA
Size	15.4" wide	15.4" wide	15.4" wide
Vender	CPT	QDI	InnoLux
Device Name	CLAA154WA05AN	QDI15TL0703	TBD
Panel ID Check[3..0]	1001	1010	1011

ON=0



DISCHARGE

The R461 will consume about 0.054 Watt (3.3x3.3/200 = 0.054W). We changed resistor to 0603 size (1/8 Watt)

FOXCONN HON HAI Precision Ind. Co., Ltd.
CCPBG - R&D Division

Title: LVDS

Size: A3 | Document Number: MS72-1-01 | Rev: 0.1

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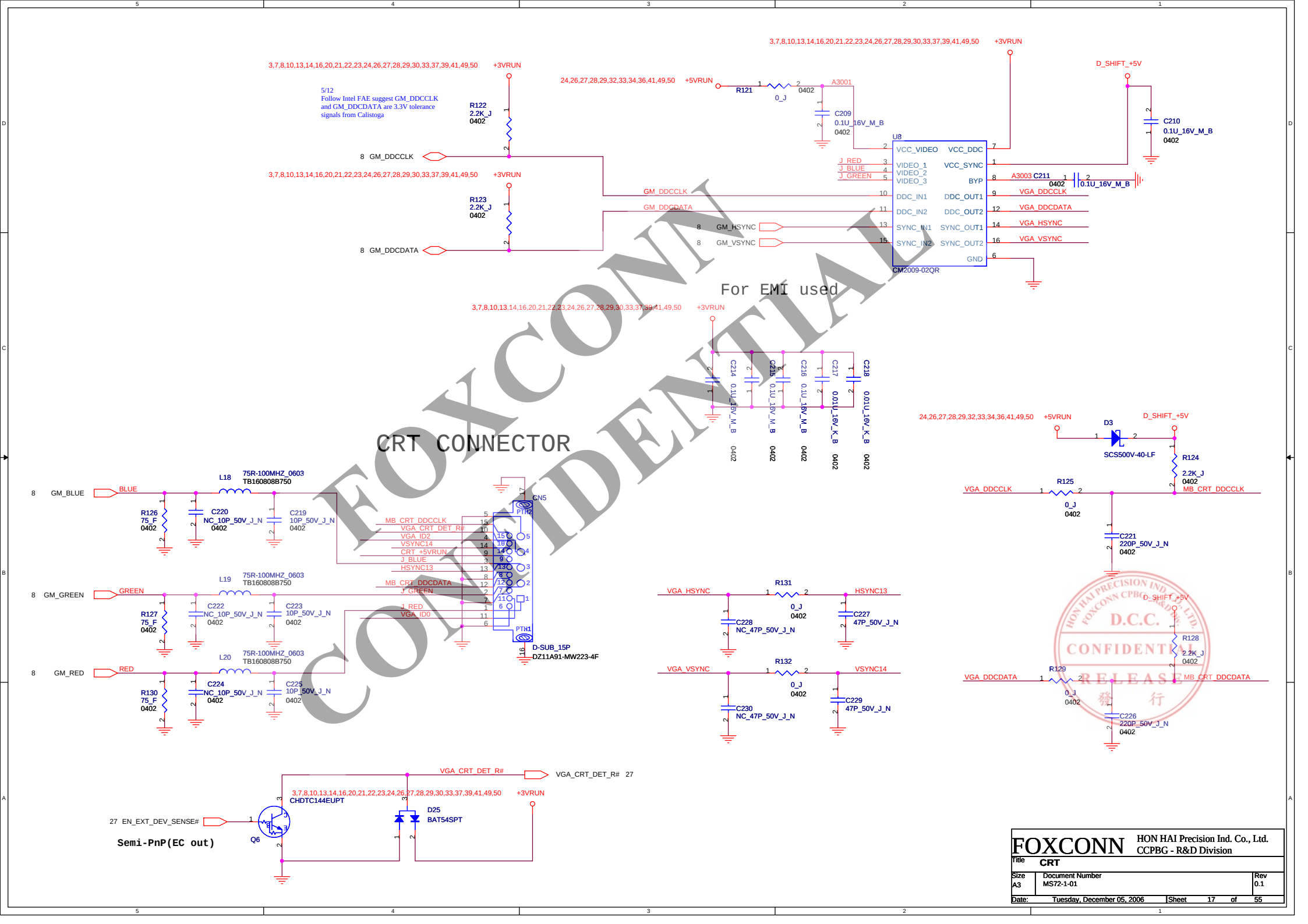
CRT CONNECTOR
For EMI used
FOXCONN CONFIDENTIAL
5/12
Follow Intel FAE suggest GM_DDCCLK and GM_DDCDATA are 3.3V tolerance signals from Calistoga
U8
CM2009-02QR
CN5
D-SUB_15P
DZ11A91-MW223-4F
Semi-PnP(EC out)
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FOXCONN
HON HAI Precision Ind. Co., Ltd.
CCPBG - R&D Division
Title CRT
Size A3
Document Number MS72-1-01
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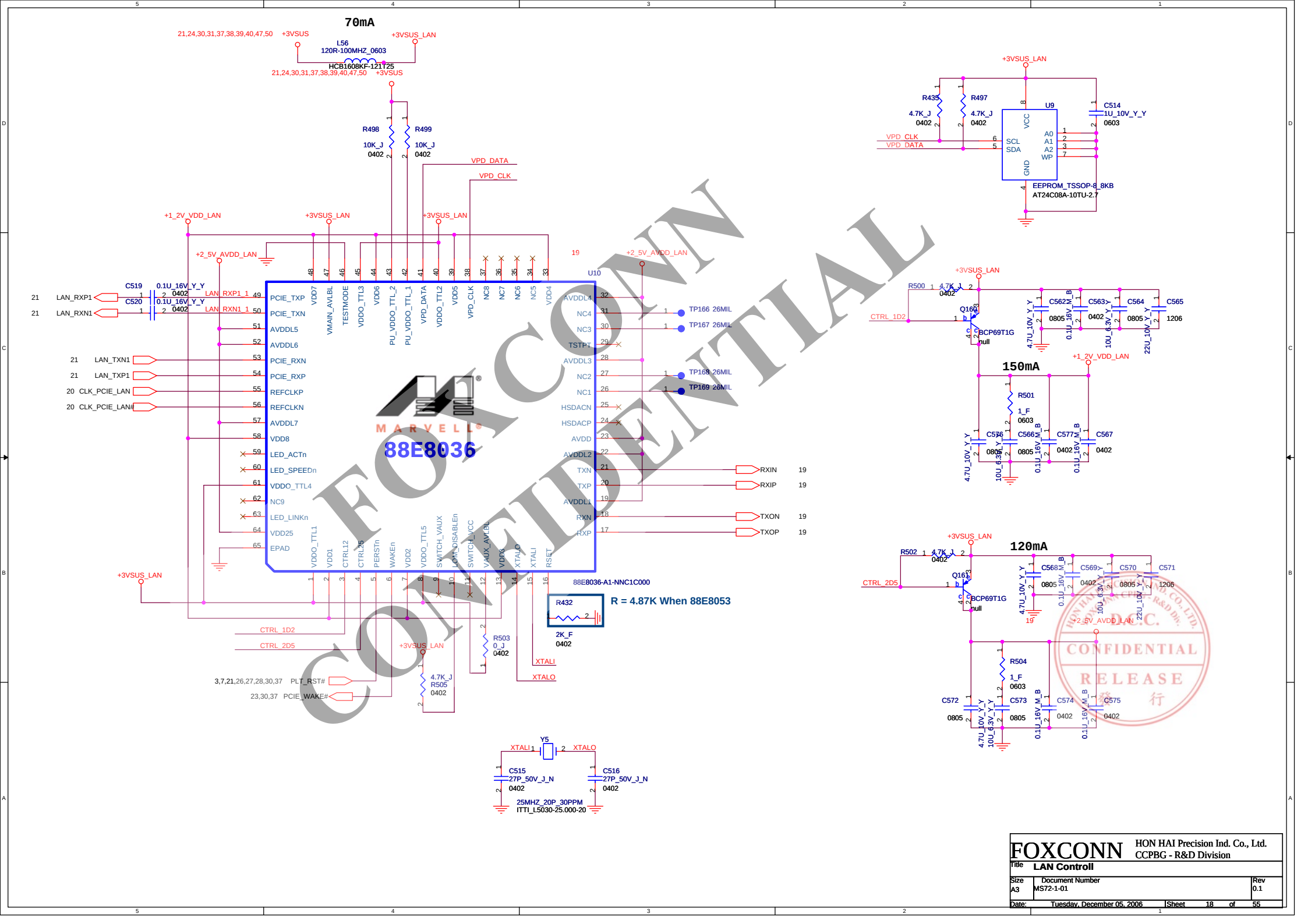
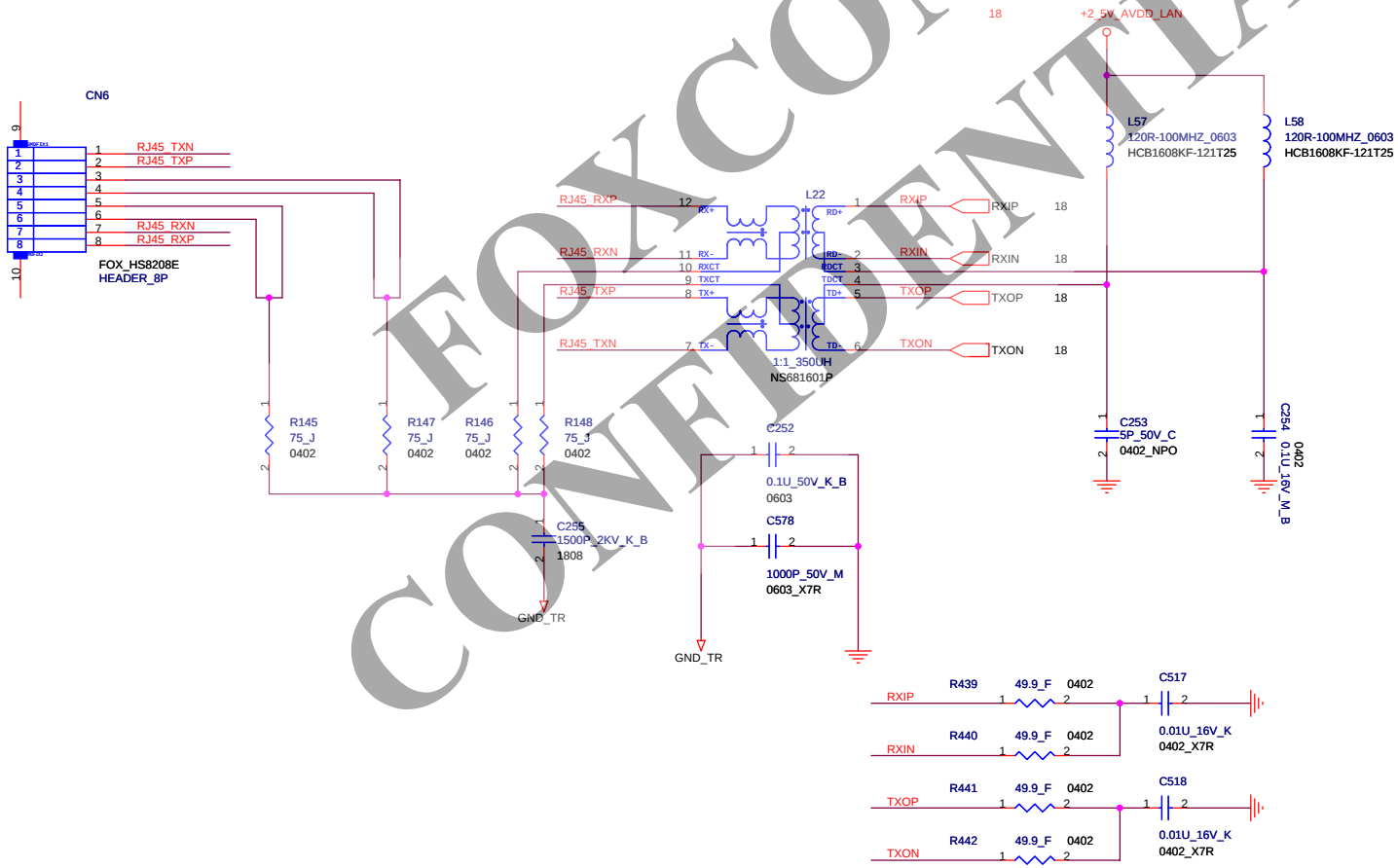
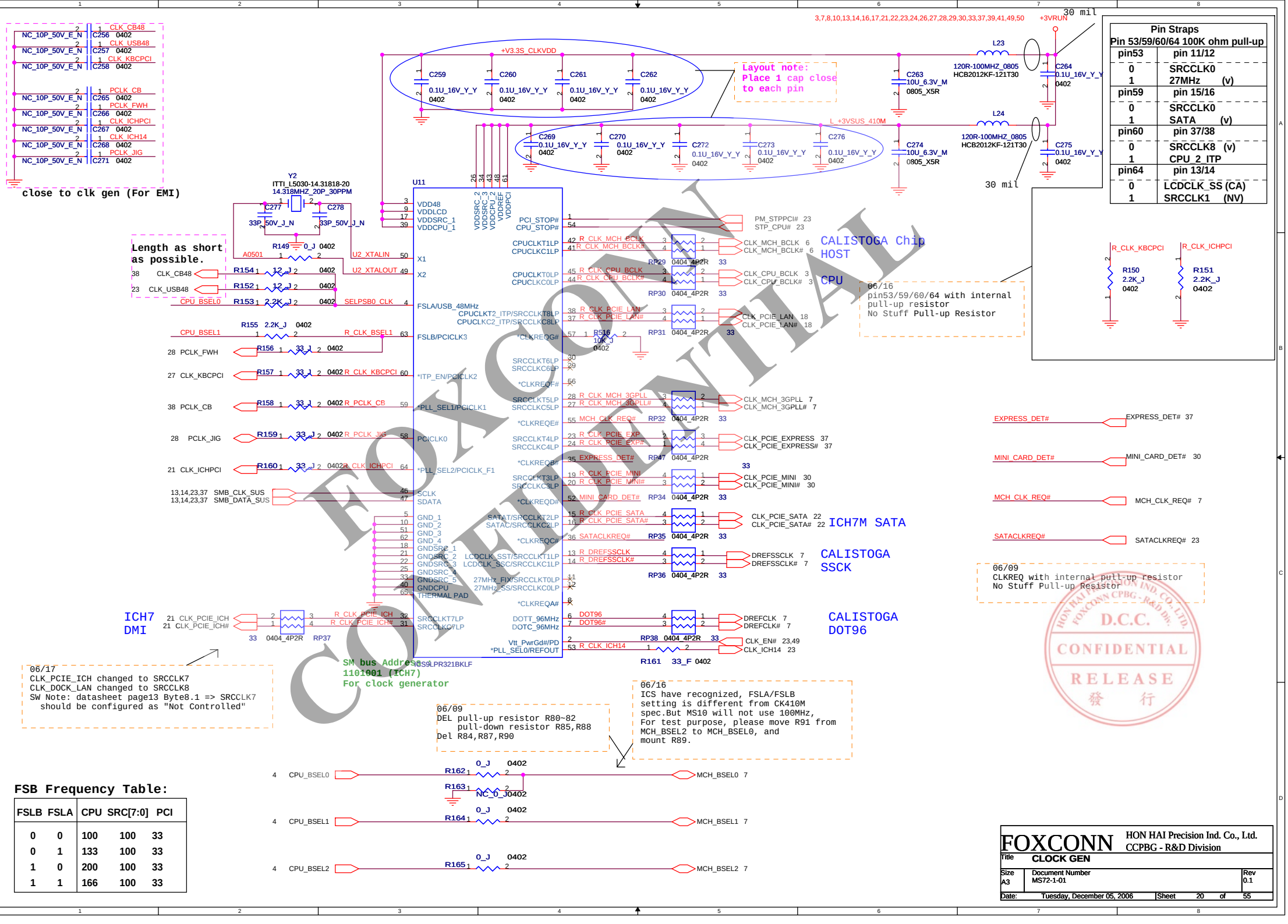


FOXCONN	HON HAI Precision Ind. Co., Ltd. CCPBG - R&D Division	
Title	LAN Controll	
Size A3	Document Number MS72-1-01	Rev 0.1
Date: Tuesday, December 05, 2006	Sheet 18 of 55	



FOXCONN	HON HAI Precision Ind. Co., Ltd. CCPBG - R&D Division	
Title: LAN Transformer		
Size: A3	Document Number: MS72-1-01	Rev: 0.1
Date: Tuesday, December 05, 2006	Sheet 19 of 55	


close to clk gen (For EMI)
Length as short as possible.
Layout note: Place 1 cap close to each pin
Pin Straps
Pin 53/59/60/64 100K ohm pull-up
pin53 pin 11/12
0 SRCCLK0 27MHz (v)
1 SRCCLK0 SATA (v)
pin59 pin 15/16
0 SRCCLK8 (v)
1 CPU_2_ITP
pin60 pin 37/38
0 LCDCLK_SS (CA)
1 SRCCLK1 (NV)
pin64 pin 13/14
CALISTOGA Chip HOST CPU
ICH7M SATA
CALISTOGA SSCK
CALISTOGA DOT96
ICH7 DMI
SM bus Address 1101001 (ICH7)
For clock generator
06/16 pin53/59/60/64 with internal pull-up resistor No Stuff Pull-up Resistor
06/09 CLKREQ with internal pull-up resistor No Stuff Pull-up Resistor
06/17 CLK_PCIE_ICH changed to SRCCLK7 CLK_DOCK_LAN changed to SRCCLK8 SW Note: datasheet page13 Byte8.1 => SRCCLK7 should be configured as "Not Controlled"
06/09 DEL pull-up resistor R80~82 pull-down resistor R85,R88 Del R84,R87,R90
06/16 ICS have recognized, FSLA/FSLB setting is different from CK410M spec.But MS10 will not use 100MHz, For test purpose, please move R91 from MCH_BSEL2 to MCH_BSEL0, and mount R89.
FSB Frequency Table:
FSLB FSLA CPU SRC[7:0] PCI
0 0 100 100 33
0 1 133 100 33
1 0 200 100 33
1 1 166 100 33
FOXCONN
HON HAI Precision Ind. Co., Ltd.
CCPBG - R&D Division
Title CLOCK GEN
Size A3
Document Number MS72-1-01
Rev 0.1
Date: Tuesday, December 05, 2006
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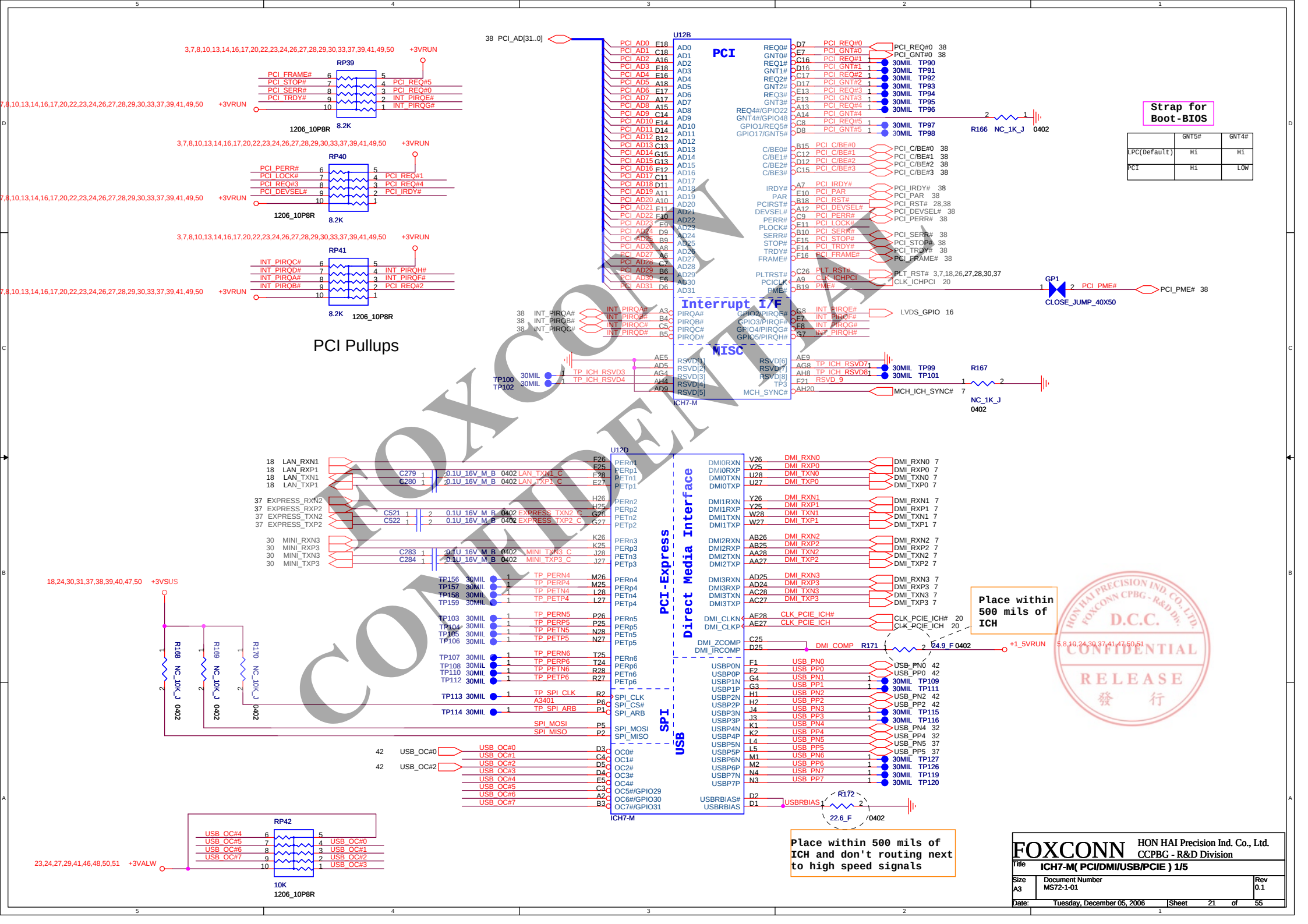
PCI Pullups
Strap for Boot-BIOS
GNT5# GNT4#
LPC(Default) H1 H1
PCI H1 LOW
Place within 500 mils of ICH
Place within 500 mils of ICH and don't routing next to high speed signals
FOXCONN
HON HAI Precision Ind. Co., Ltd.
CCPBG - R&D Division
Title ICH7-M(PCI/DMI/USB/PCIE) 1/5
Size A3
Document Number MS72-1-01
Rev 0.1
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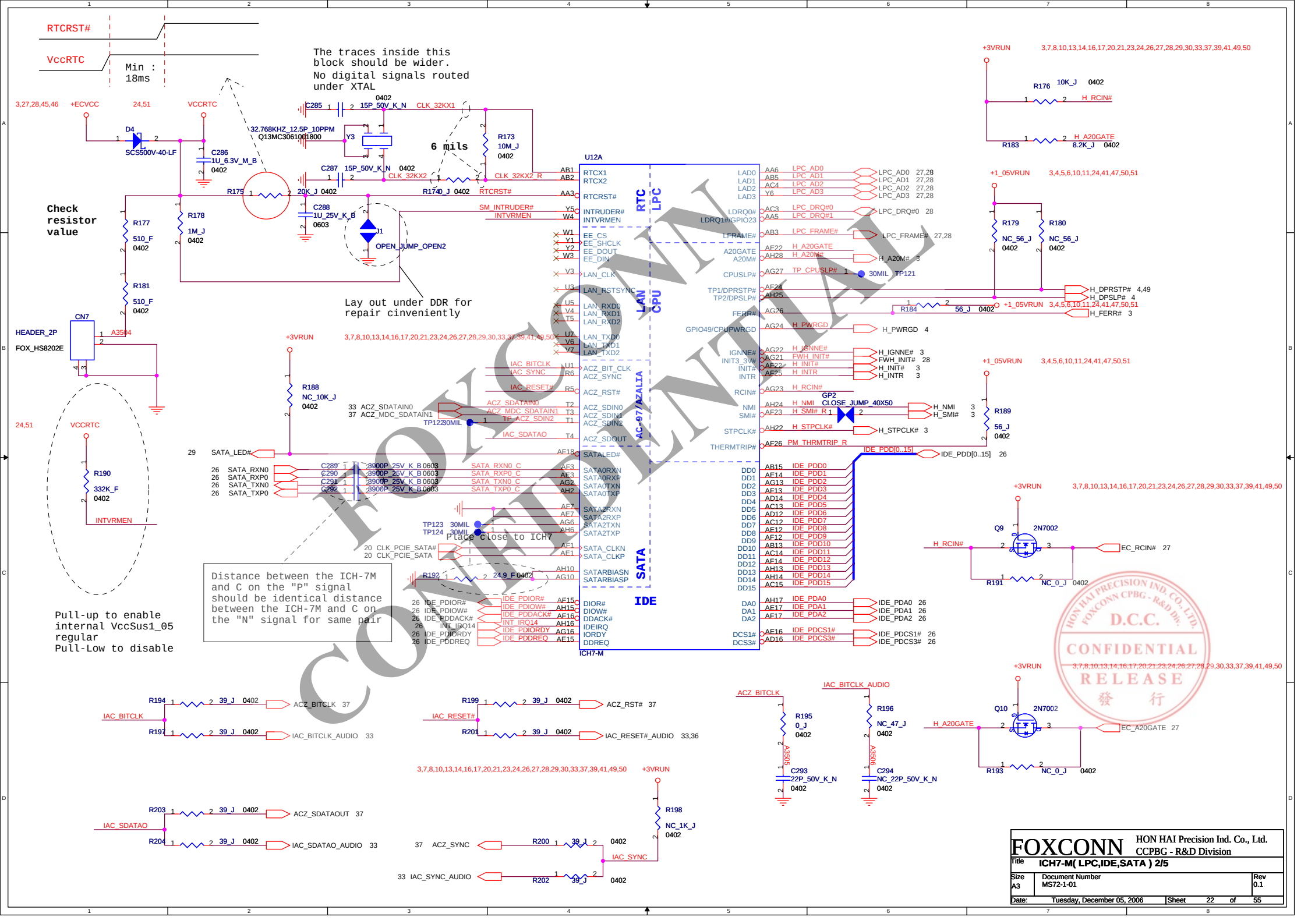

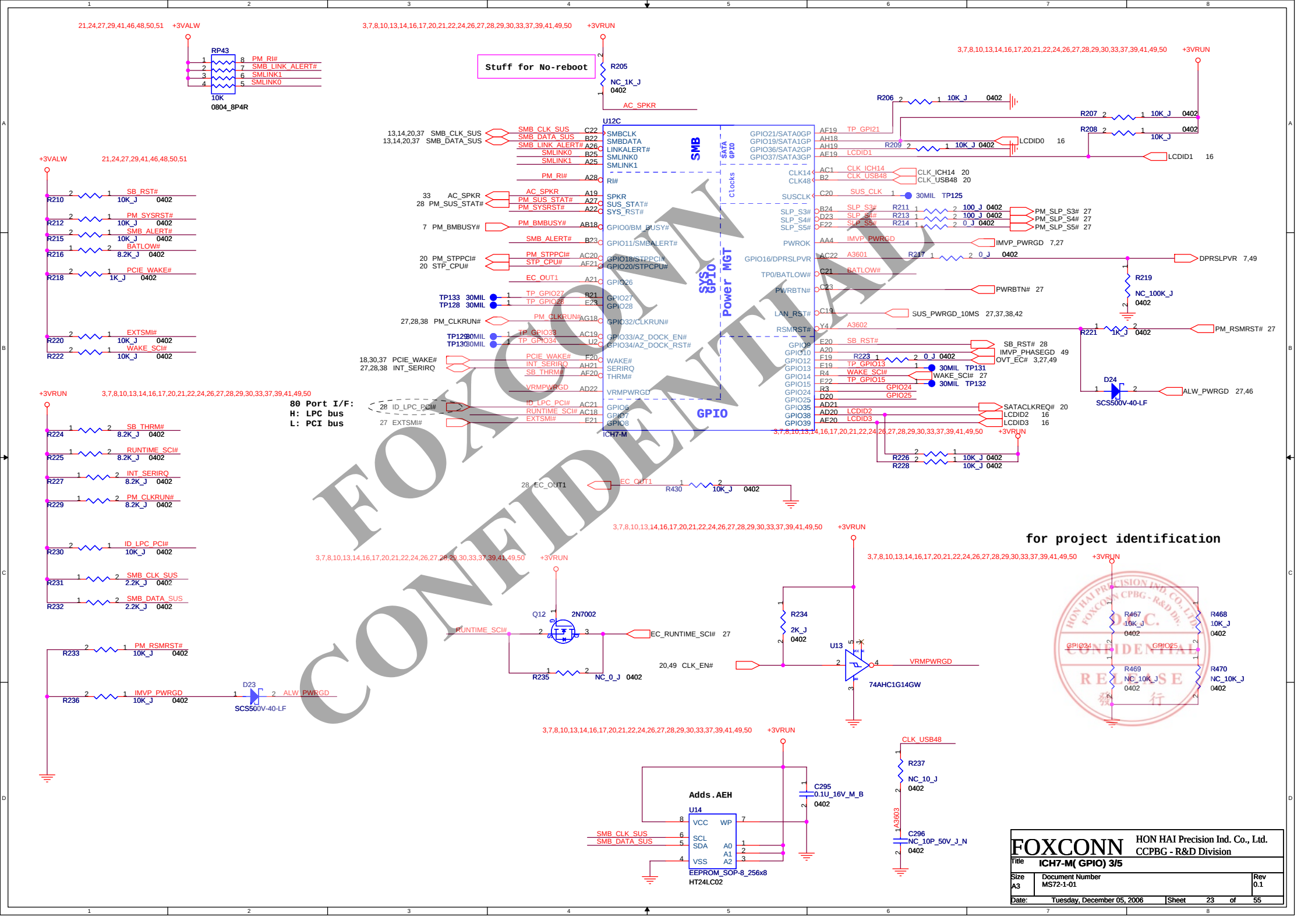

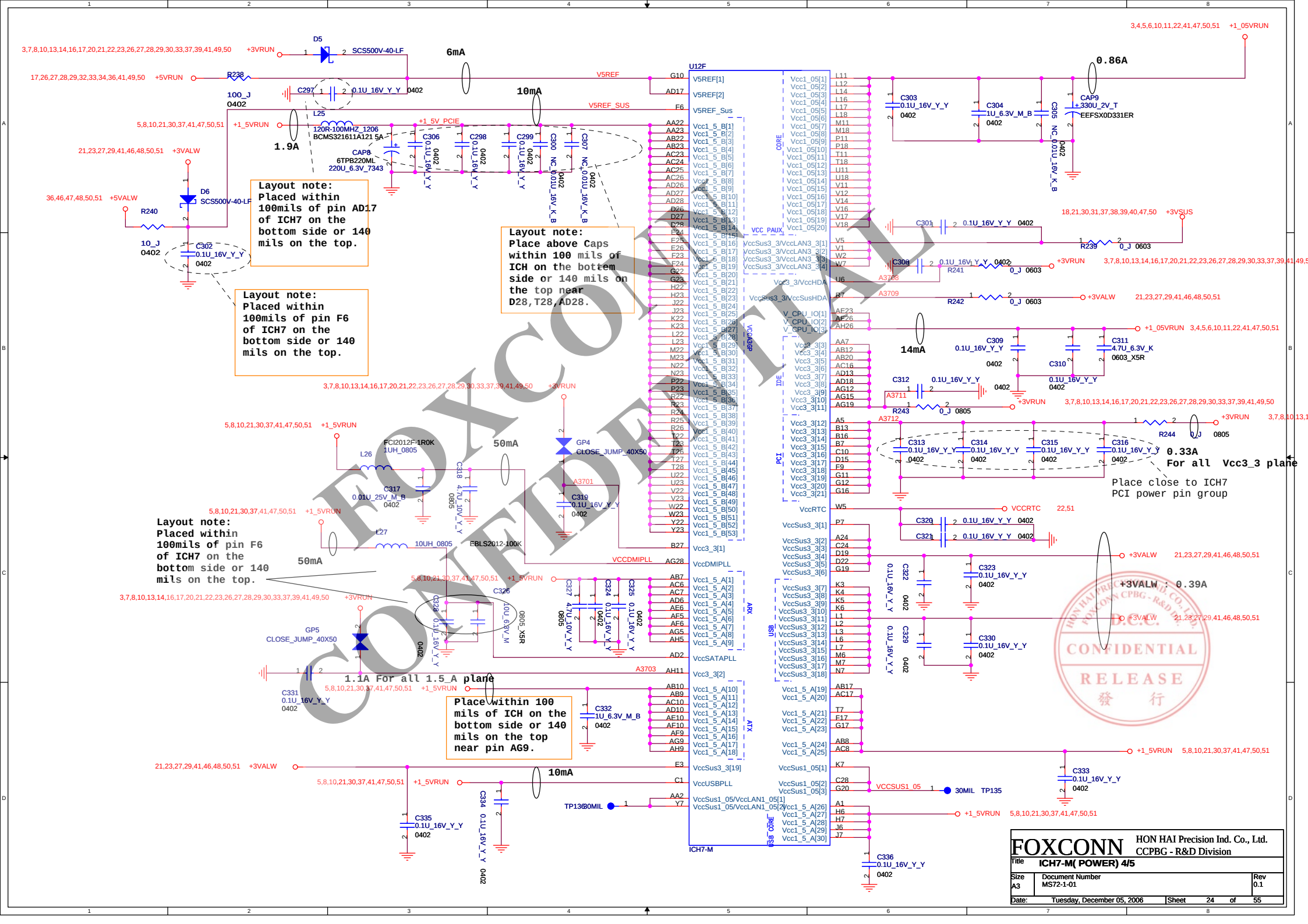
U12F
ICH7-M
Layout note: Placed within 100mils of pin AD17 of ICH7 on the bottom side or 140 mils on the top.
Layout note: Place above Caps within 100 mils of ICH on the bottem side or 140 mils on the top near D28,T28,AD28.
Layout note: Placed within 100mils of pin F6 of ICH7 on the bottom side or 140 mils on the top.
Layout note: Placed within 100mils of pin F6 of ICH7 on the bottom side or 140 mils on the top.
Place within 100 mils of ICH on the bottom side or 140 mils on the top near pin AG9.
1.1A For all 1.5_A plane
0.33A For all Vcc3_3 plane
Place close to ICH7 PCI power pin group
+3VALW : 0.39A
FOXCONN
HON HAI Precision Ind. Co., Ltd.
CCPBG - R&D Division
ICH7-M(POWER) 4/5
Document Number MS72-1-01
Tuesday, December 05, 2006
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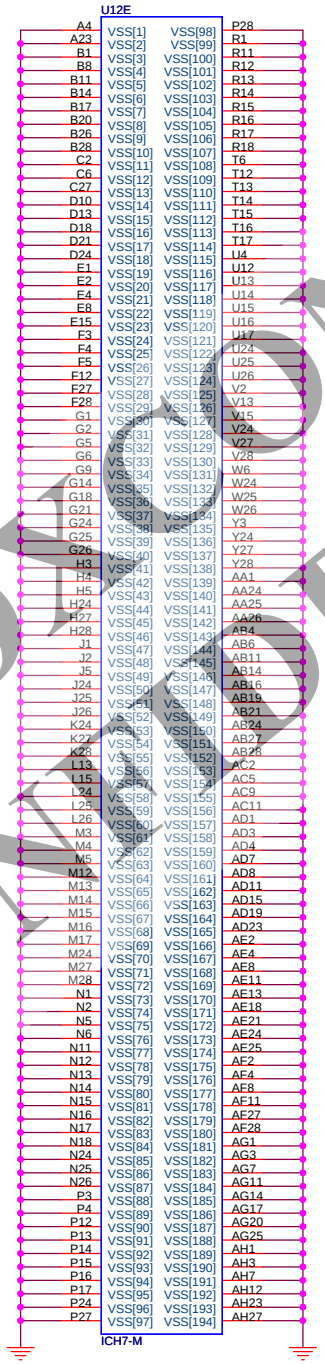
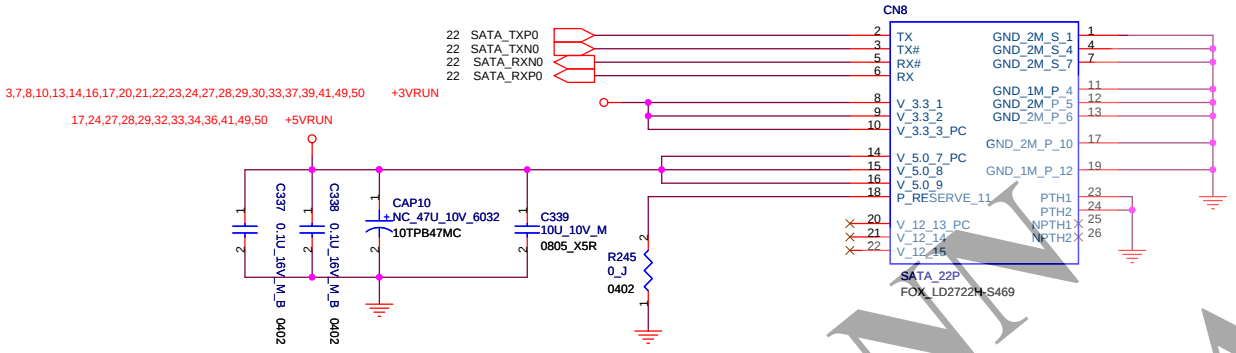
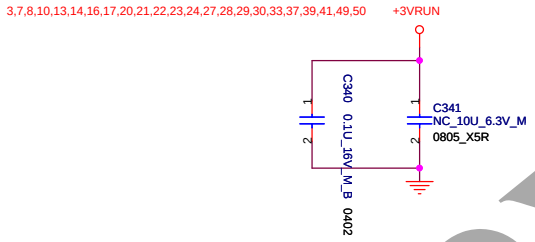
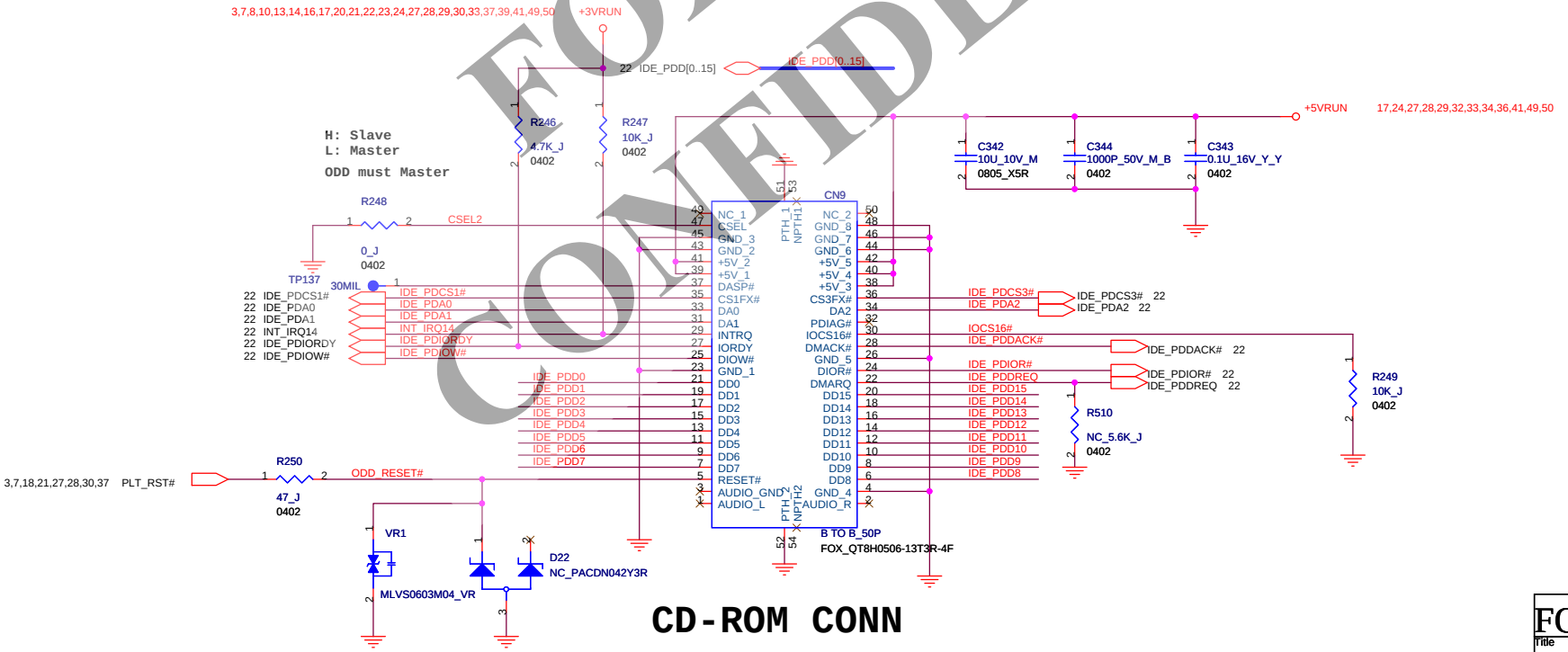


FOXCONN	HON HAI Precision Ind. Co., Ltd. CCPBG - R&D Division	
Title	ICH7-M(GND) 5/5	
Size A3	Document Number MS72-1-01	Rev 0.1
Date: Tuesday, December 05, 2006	Sheet 25 of 55	

SATA HDD CONN



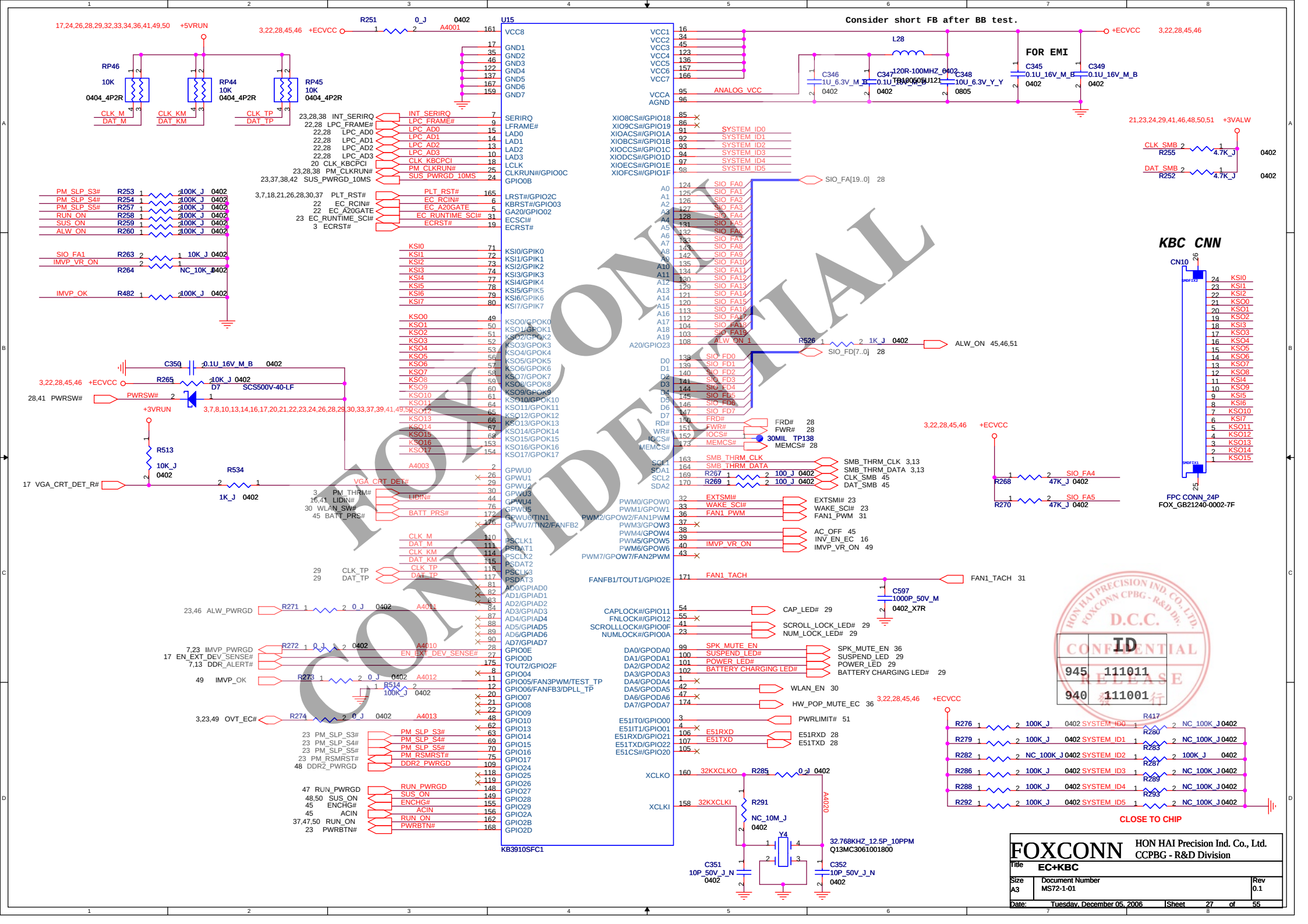


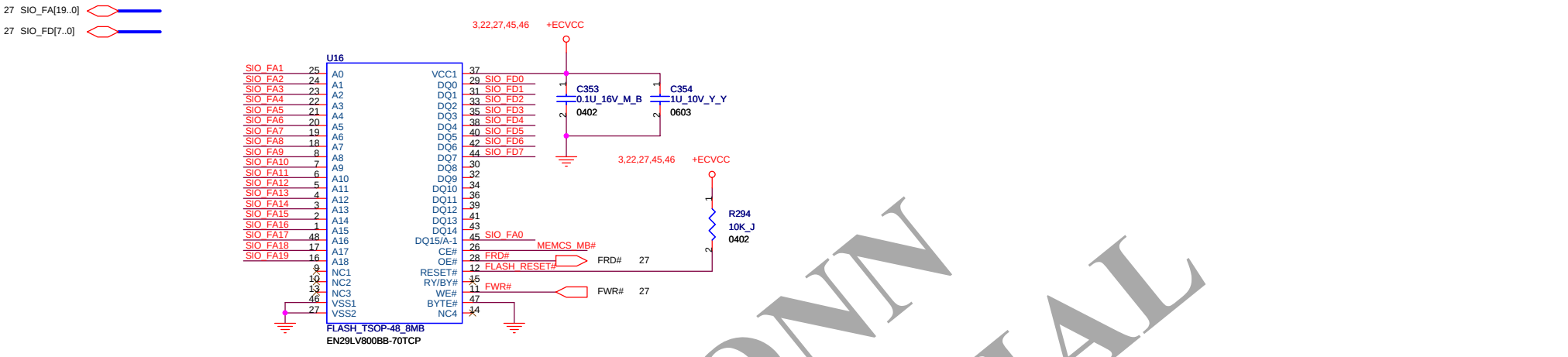


CD-ROM CONN

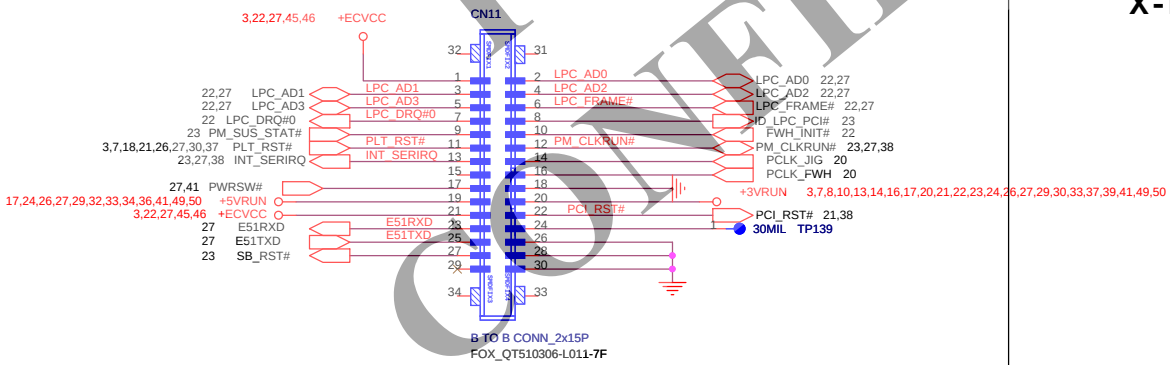
Follow Adoi san suggest ODD: Master/HDD:Slave

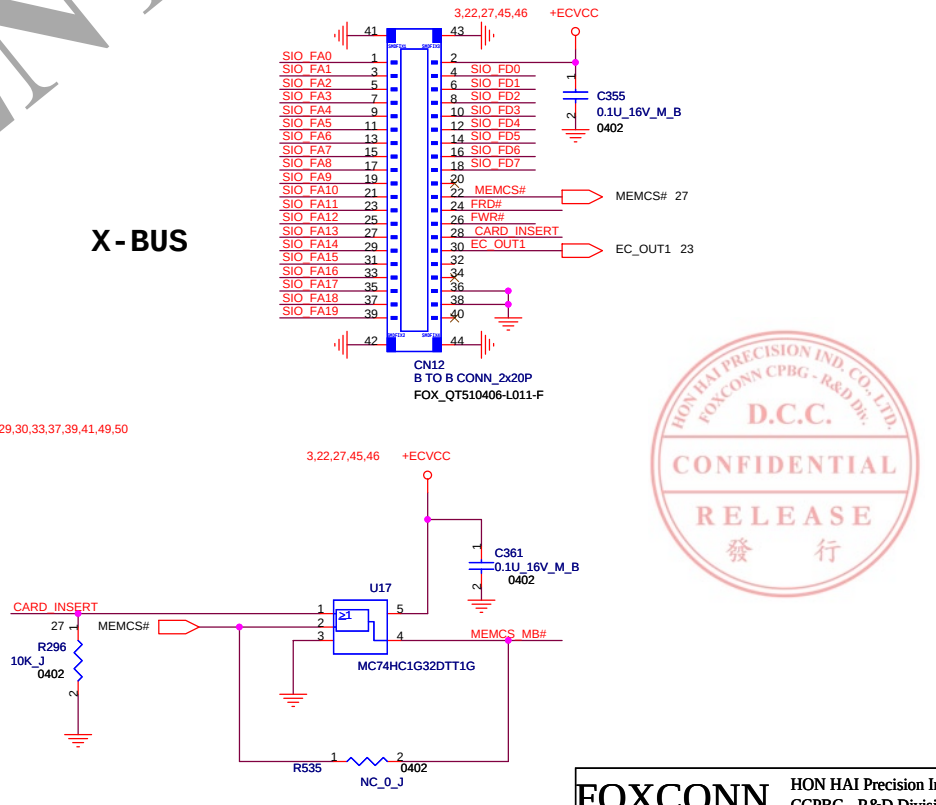
FOXCONN	HON HAI Precision Ind. Co., Ltd. CCPBG - R&D Division	
Title	SATA HDD/CD-ROM	
Size A3	Document Number MS72-1-01	Rev 0.1
Date: Tuesday, December 05, 2006	Sheet 26 of 55	





FLASH BIOS





FOXCONN	HON HAI Precision Ind. Co., Ltd. CCPBG - R&D Division	
Title	Flash ROM/X-Bus/LID SW#	
Size A3	Document Number MS72-1-01	Rev 0.1
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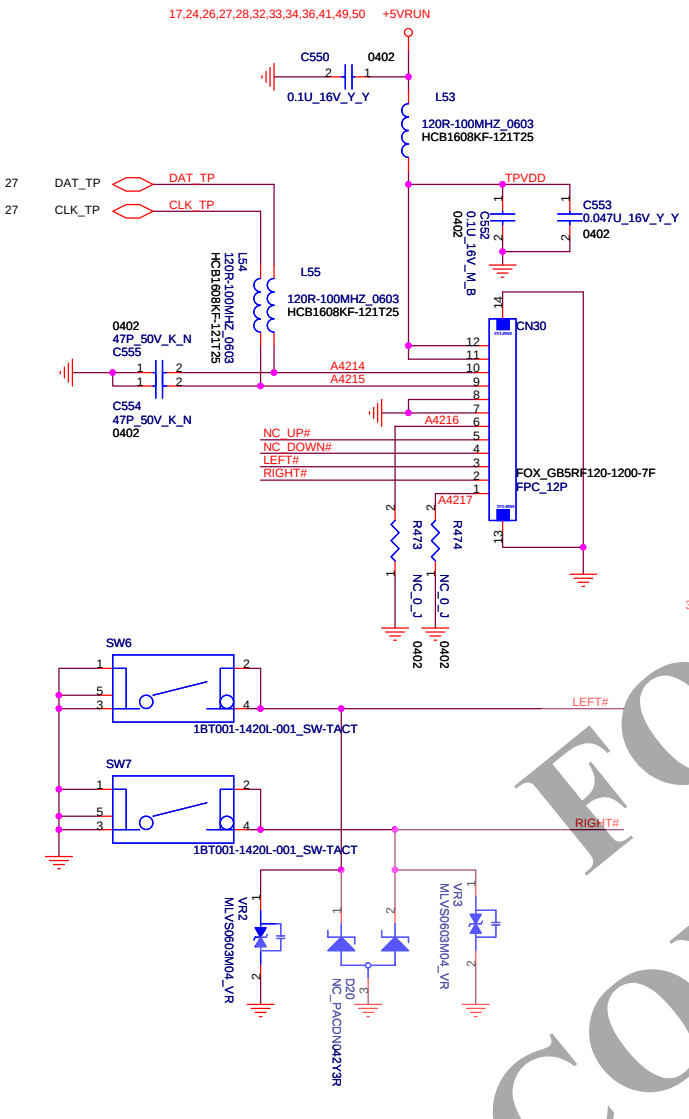
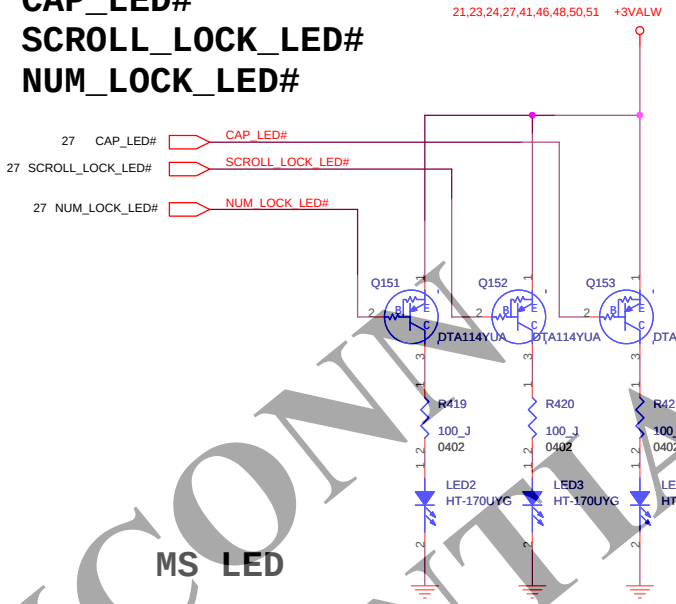
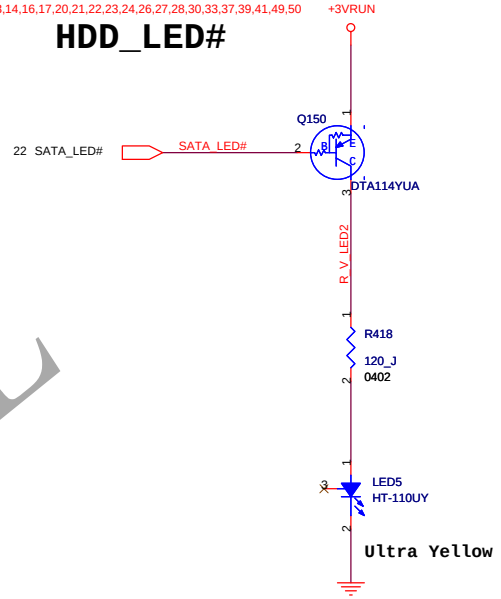
Touch Pad Board



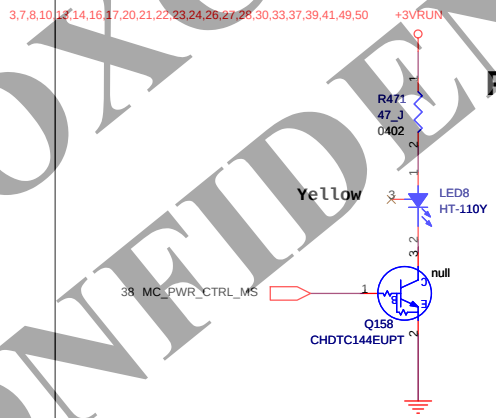
CAP_LED#
SCROLL_LOCK_LED#
NUM_LOCK_LED#



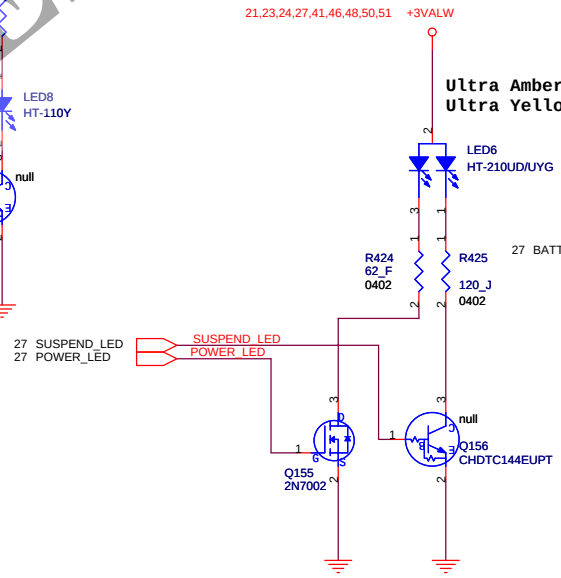
HDD_LED#



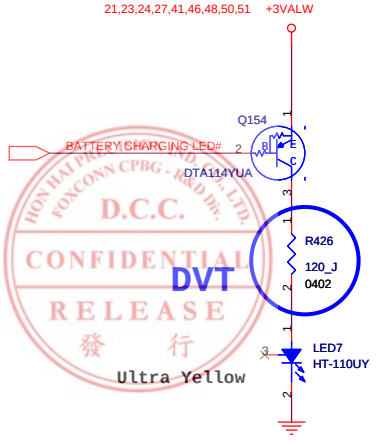
MS LED



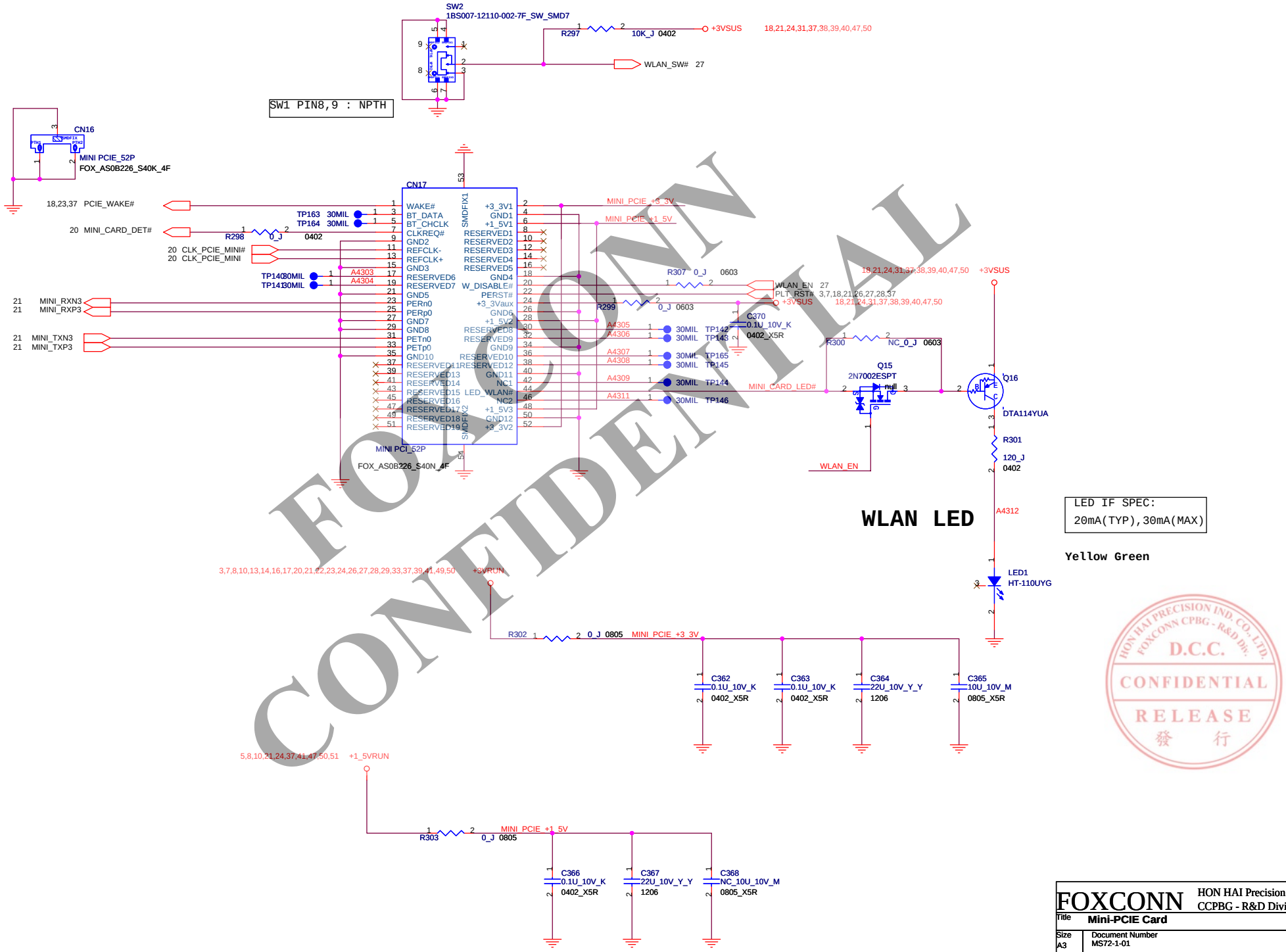
POWER/SUSPEND LED



BATTERY CHARGING LED#

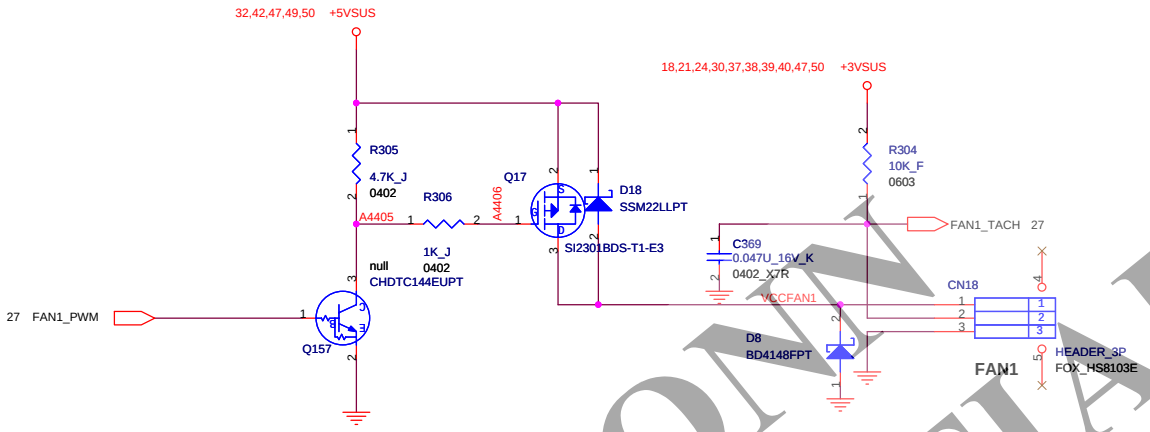


FOXCONN	HON HAI Precision Ind. Co., Ltd. CCPBG - R&D Division	
Title: LED/Touch PAD		
Size: A3	Document Number: MS72-1-01	Rev: 0.1
Date: Tuesday, December 05, 2006	Sheet 29 of 55	

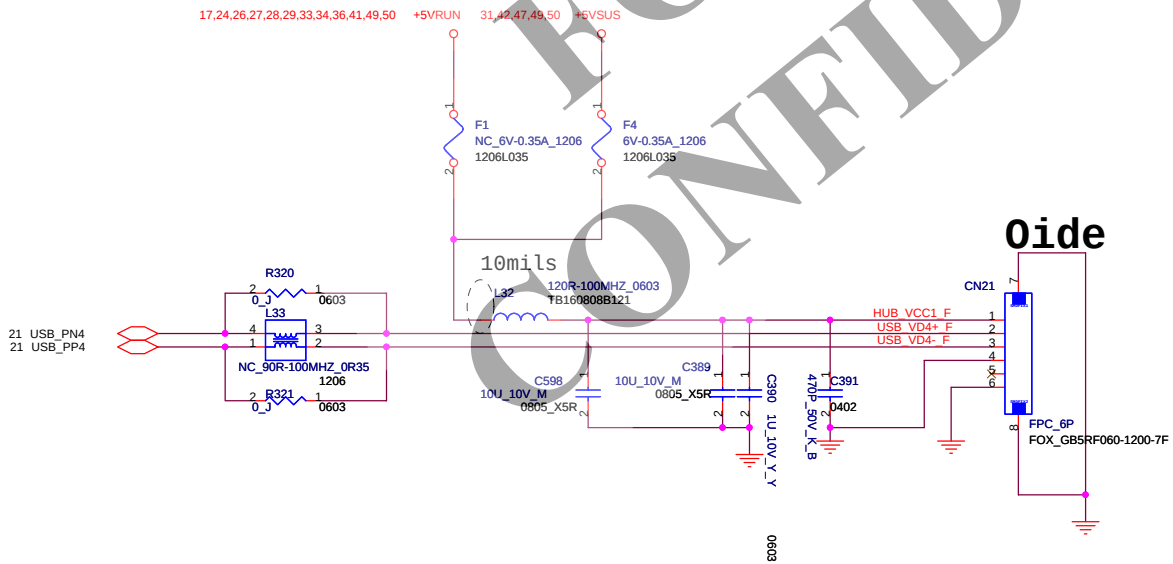


FOXCONN	HON HAI Precision Ind. Co., Ltd. CCPBG - R&D Division		
Title: Mini-PCIE Card			
Size: A3	Document Number: MS72-1-01		Rev: 0.1
Date: Tuesday, December 05, 2006		Sheet 30 of 55	

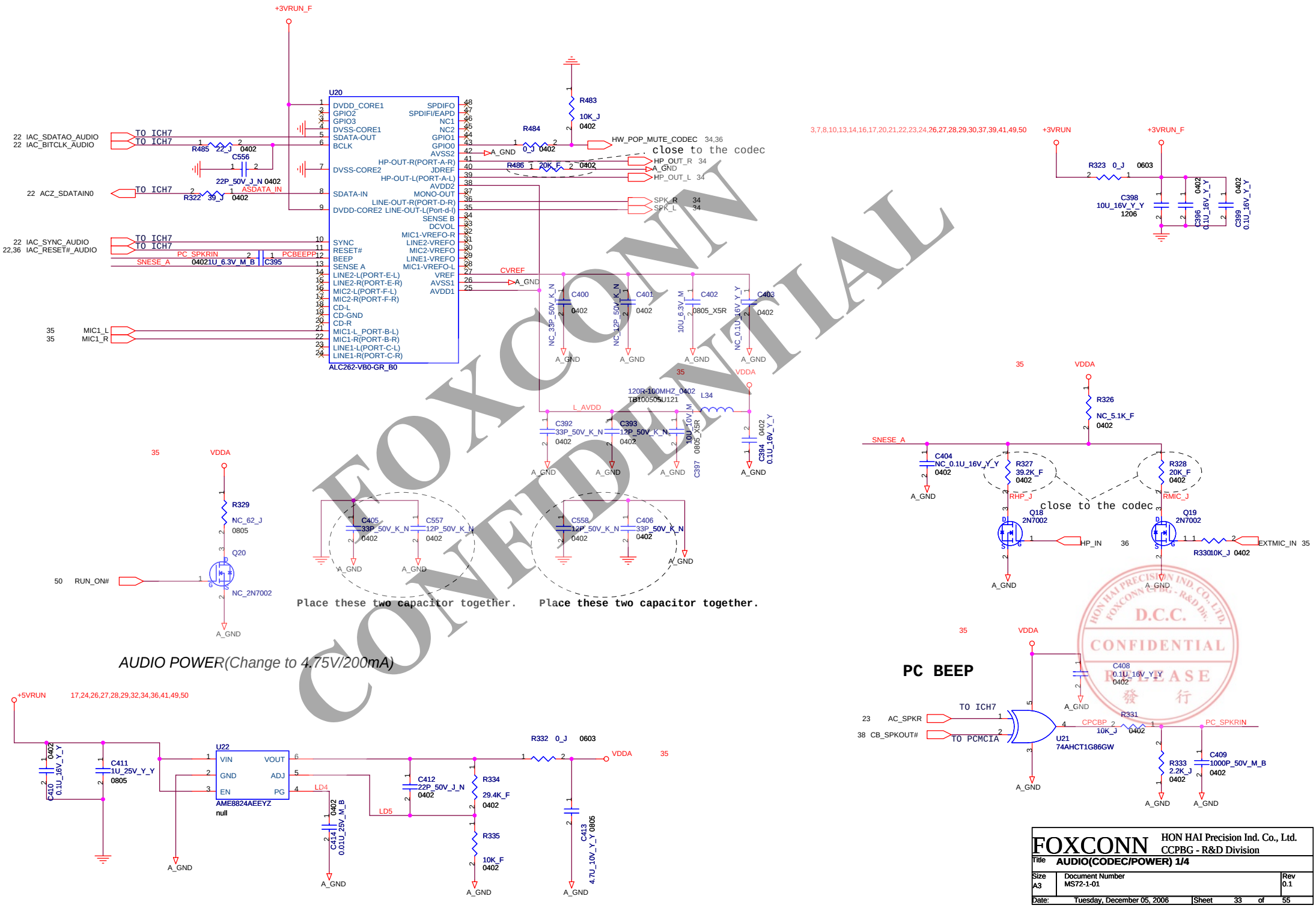
FAN1



FOXCONN	HON HAI Precision Ind. Co., Ltd. CCPBG - R&D Division	
Title: FAN		
Size: A3	Document Number: MS72-1-01	Rev: 0.1
Date: Tuesday, December 05, 2006	Sheet 31 of 55	



FOXCONN	HON HAI Precision Ind. Co., Ltd. CCPBG - R&D Division	
Title: OIDE		
Size: A3	Document Number: MS72-1-01	Rev: 0.1
Date: Tuesday, December 05, 2006	Sheet 32 of 55	

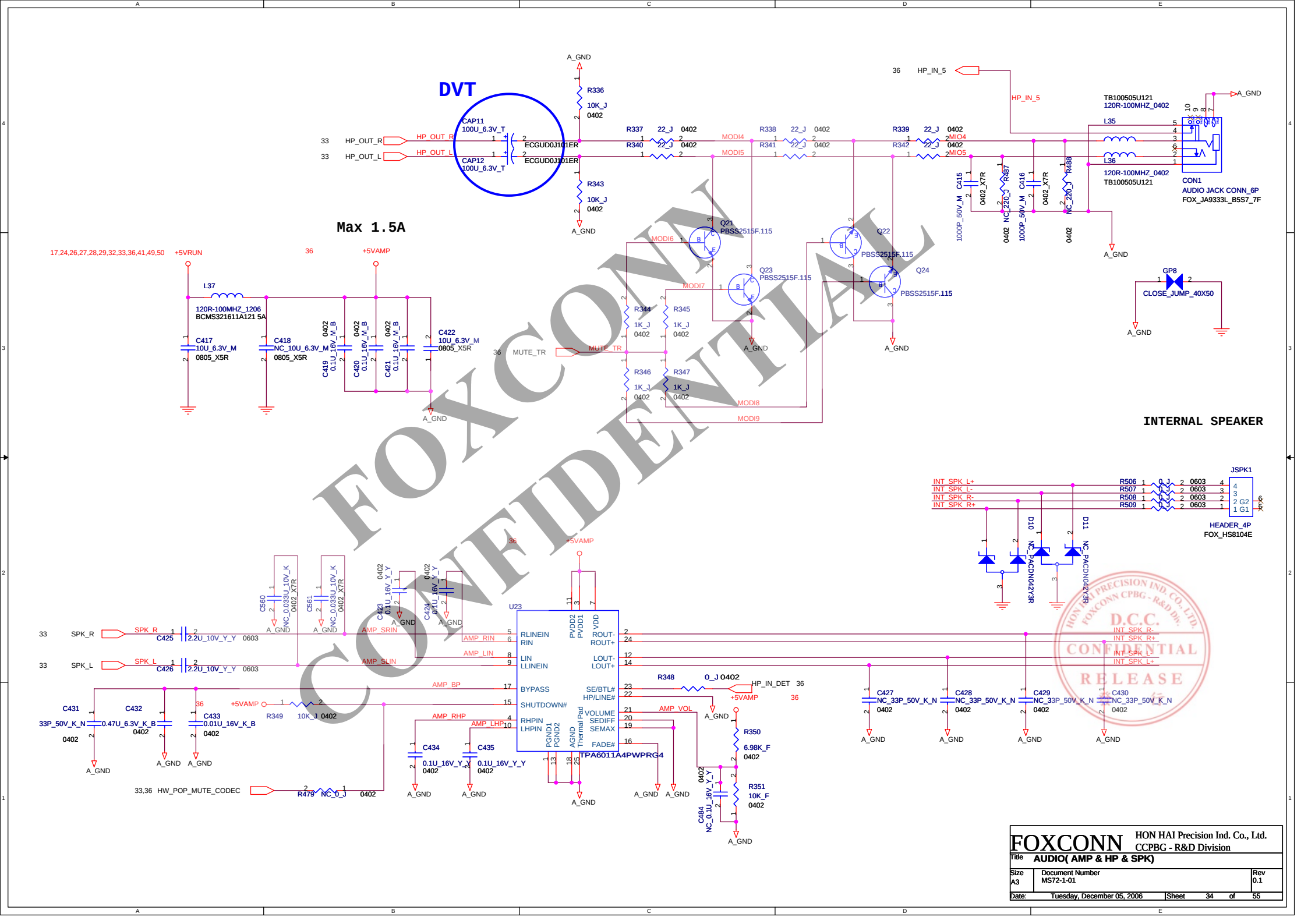


FOXCONN
HON HAI Precision Ind. Co., Ltd.
CCPBG - R&D Division

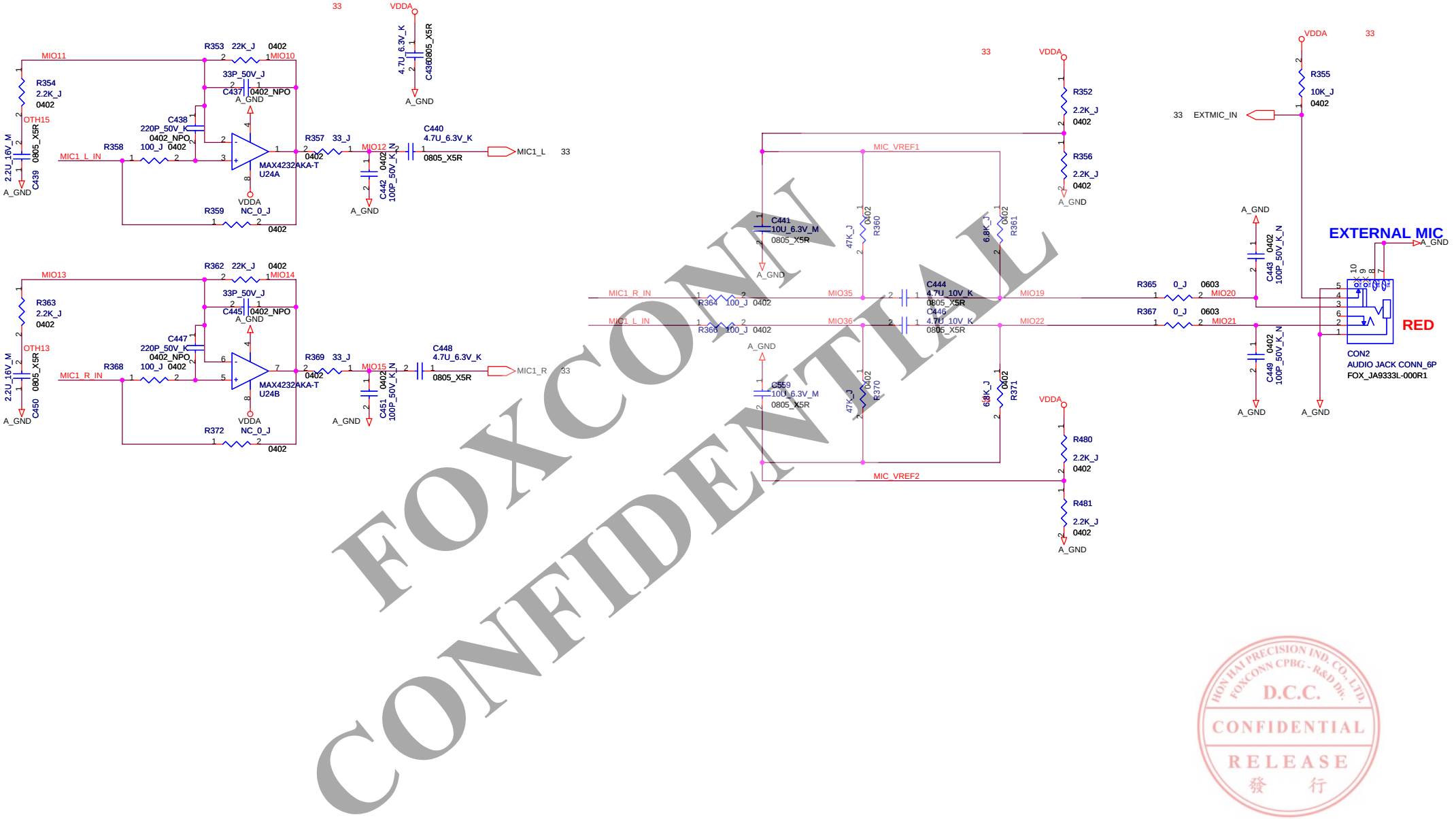
Title: AUDIO(CODEC/POWER) 1/4

Size	Document Number	Rev
A3	MS72-1-01	0.1

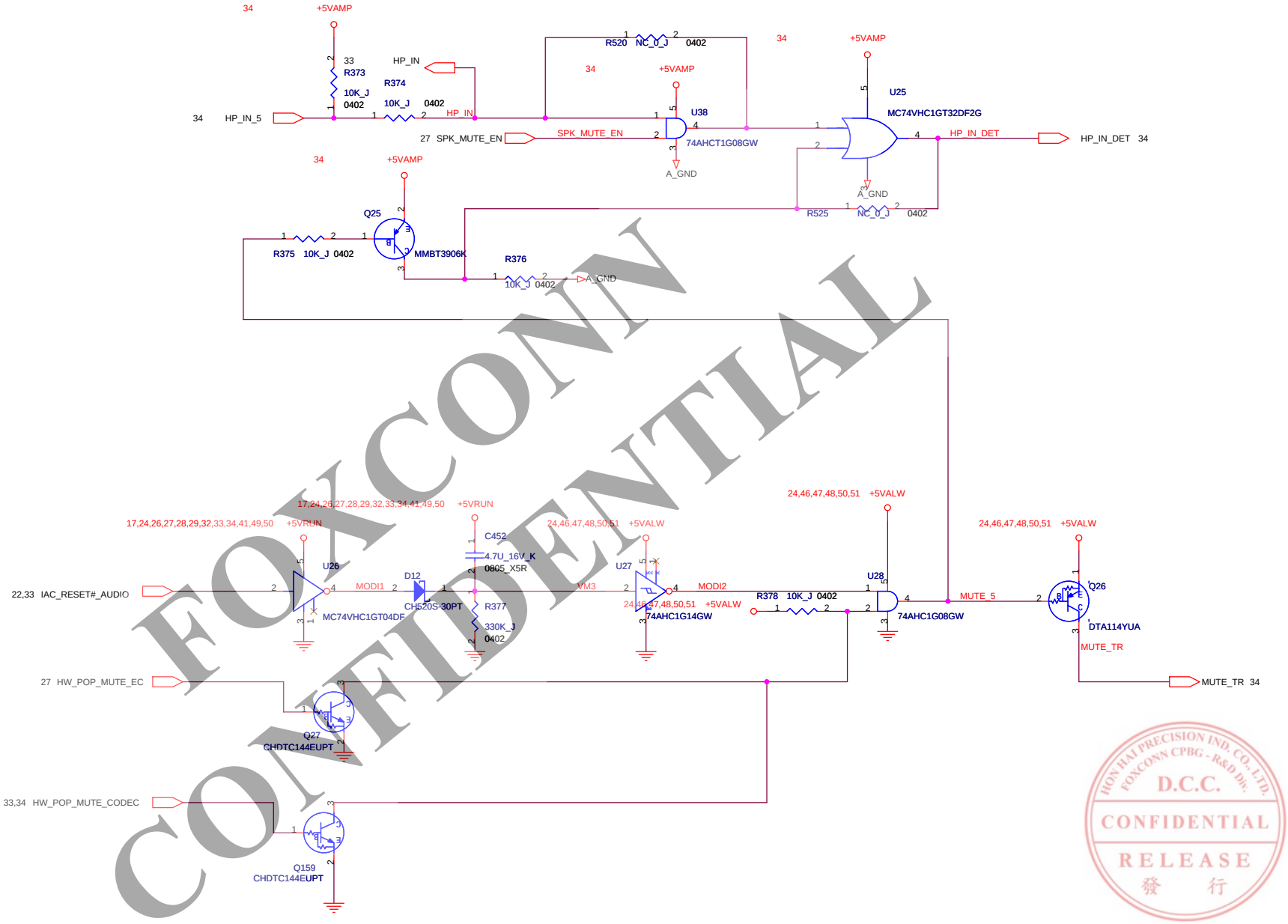
Date: Tuesday, December 05, 2006 | Sheet 33 of 55



FOXCONN	HON HAI Precision Ind. Co., Ltd. CCPBG - R&D Division	
Title	AUDIO(AMP & HP & SPK)	
Size A3	Document Number MS72-1-01	Rev 0.1
Date: Tuesday, December 05, 2006	Sheet 34 of 55	

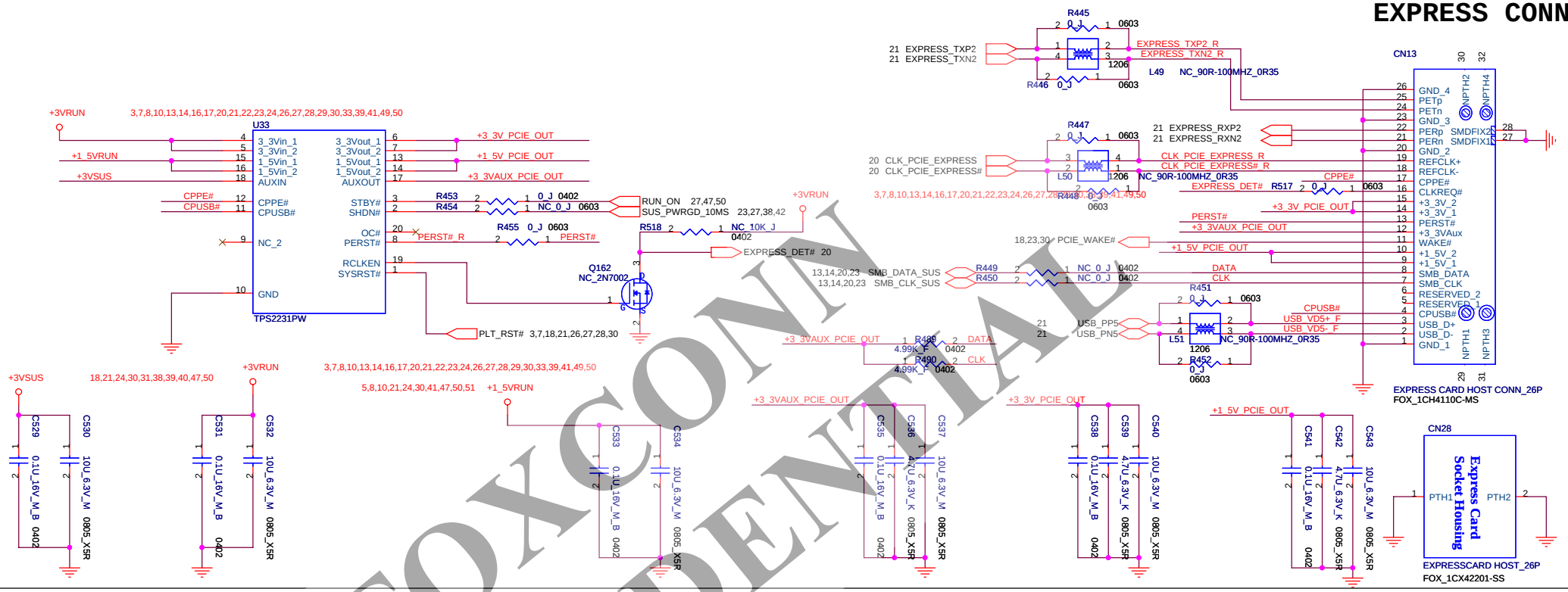


FOXCONN	HON HAI Precision Ind. Co., Ltd. CCPBG - R&D Division	
Title	AUDIO(EXT MIC) 3/4	
Size A3	Document Number MS72-1-01	Rev 0.1
Date: Tuesday, December 05, 2006	Sheet 35 of 55	

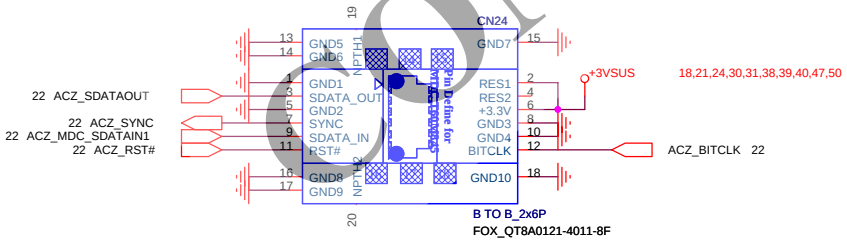


FOXCONN	HON HAI Precision Ind. Co., Ltd. CCPBG - R&D Division	
Title: AUDIO (MUTE) 4/4		
Size: A3	Document Number: MS72-1-01	Rev: 0.1
Date: Tuesday, December 05, 2006	Sheet 36 of 55	

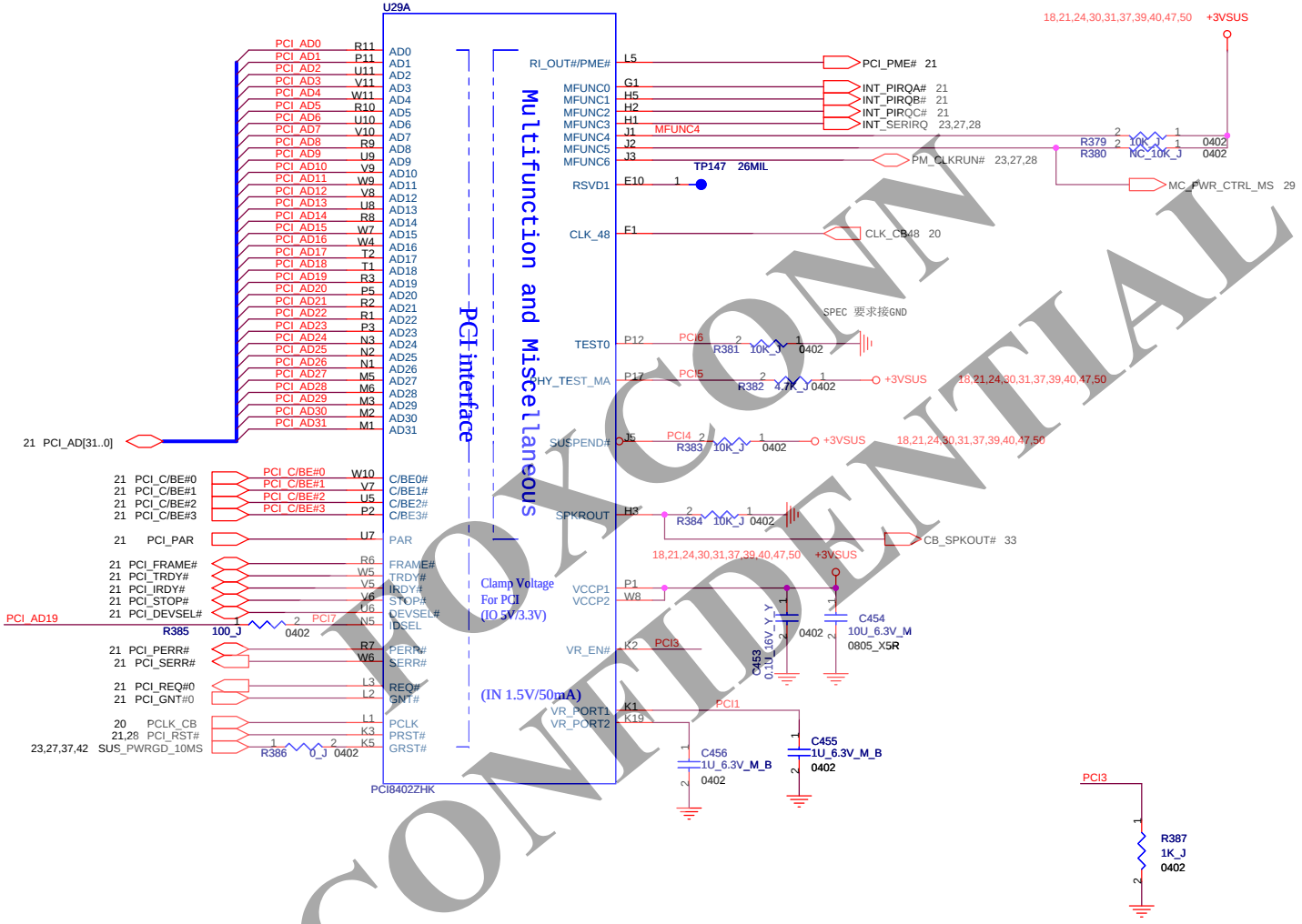
EXPRESS CONN



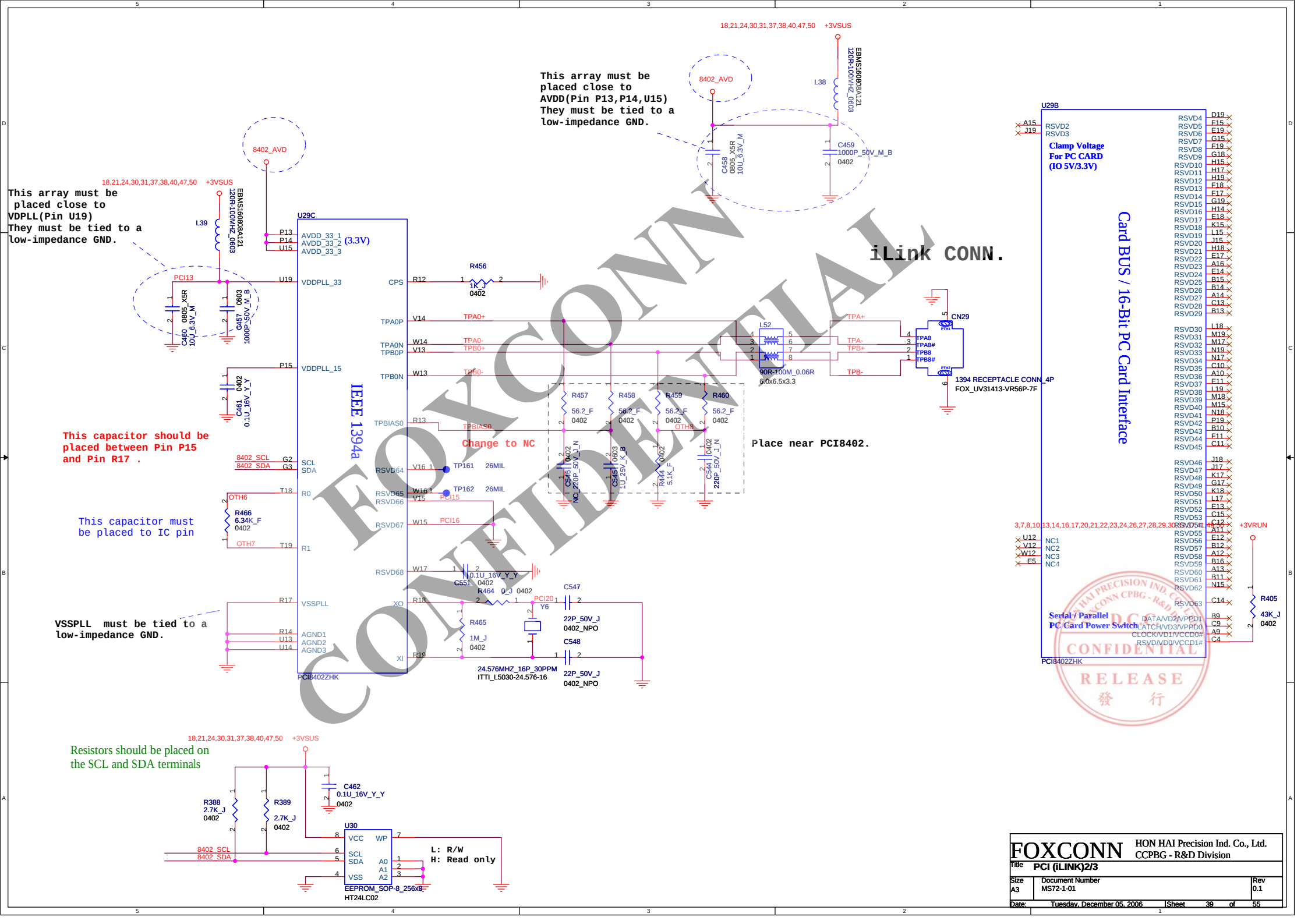
MDC CONN.

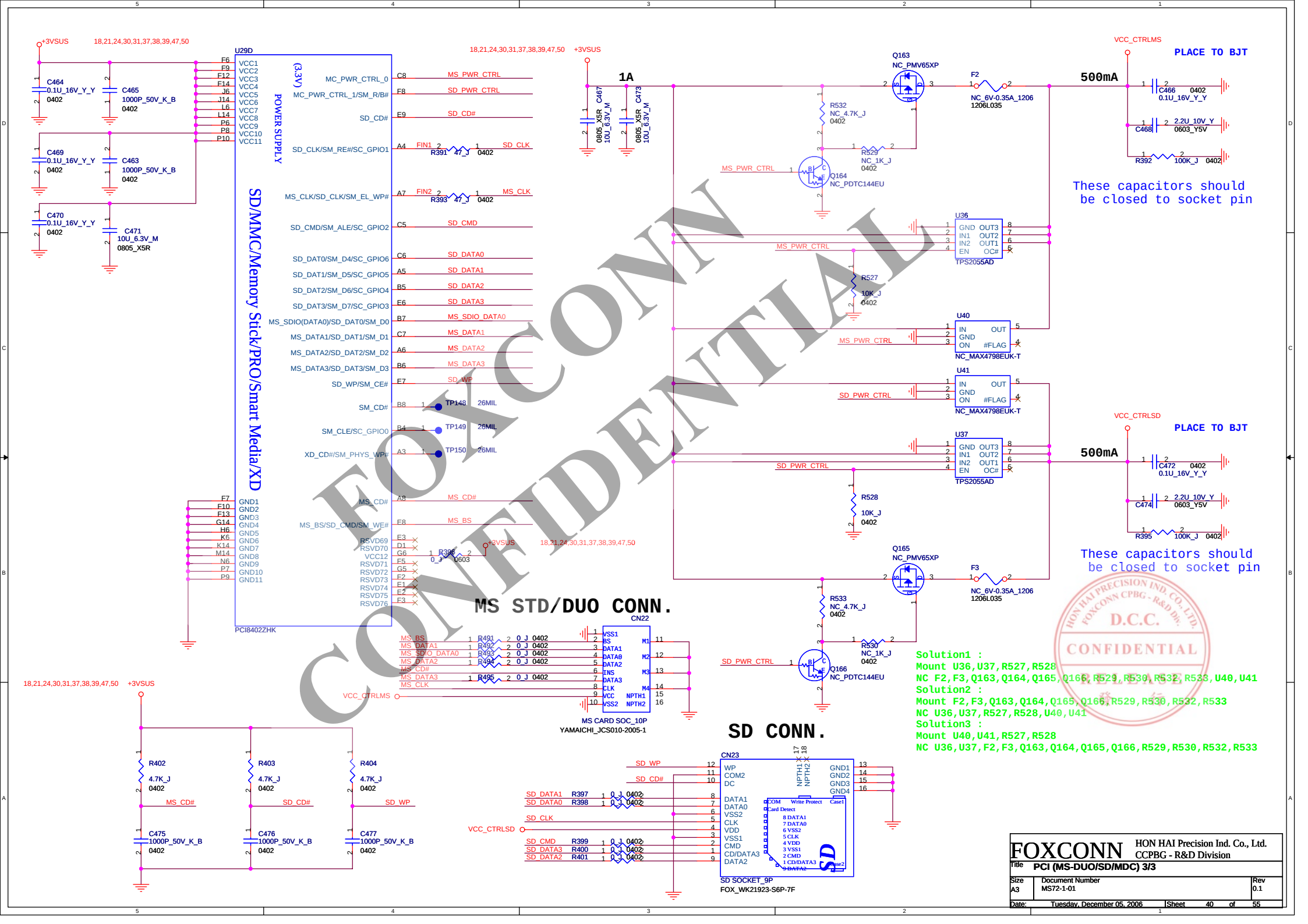


FOXCONN	HON HAI Precision Ind. Co., Ltd. CCPBG - R&D Division	
Title	EXPRESS CARD	
Size A3	Document Number MS72-1-01	Rev 0.1
Date: Tuesday, December 05, 2006	Sheet 37 of 55	



FOXCONN	HON HAI Precision Ind. Co., Ltd. CCPBG - R&D Division	
Title	PCI (PCI BUS) 1/3	
Size A3	Document Number MS72-1-01	Rev 0.1
Date: Tuesday, December 05, 2006	Sheet 38 of 55	




U29D
PCI8402ZHK
SD/MMC/Memory Stick/PRO/Smart Media/XD
POWER SUPPLY
(3.3V)
MS_PWR_CTRL_0
MC_PWR_CTRL_1/SM_R/B#
SD_CD#
SD_CLK/SM_RE#/SC_GPIO1
MS_CLK/SD_CLK/SM_EL_WP#
SD_CMD/SM_ALE/SC_GPIO2
SD_DAT0/SM_D4/SC_GPIO6
SD_DAT1/SM_D5/SC_GPIO5
SD_DAT2/SM_D6/SC_GPIO4
SD_DAT3/SM_D7/SC_GPIO3
MS_SDIO(DATA0)/SD_DAT0/SM_D0
MS_DATA1/SD_DAT1/SM_D1
MS_DATA2/SD_DAT2/SM_D2
MS_DATA3/SD_DAT3/SM_D3
SD_WP/SM_CE#
SM_CD#
SM_CLE/SC_GPIO0
XD_CD#/SM_PHYS_WP#
MS_CD#
MS_BS/SD_CMD/SM_WE#
MS_PWR_CTRL
SD_PWR_CTRL
SD_CD#
SD_CLK
MS_CLK
SD_CMD
SD_DATA0
SD_DATA1
SD_DATA2
SD_DATA3
MS_SDIO_DATA0
MS_DATA1
MS_DATA2
MS_DATA3
SD_WP
MS_BS
TP148
TP149
TP150
26MIL
+3VSUS
18,21,24,30,31,37,38,39,47,50
1A
C464 0.1U_16V_Y_Y 0402
C465 1000P_50V_K_B 0402
C469 0.1U_16V_Y_Y 0402
C463 1000P_50V_K_B 0402
C470 0.1U_16V_Y_Y 0402
C471 10U_6.3V_M 0805_X5R
C467 0805_X5R 10U_6.3V_M
C473 0805_X5R 10U_6.3V_M
R391 47_J 0402
R393 47_J 0402
R396 0_J 0603
Q163 NC_PMV65XP
Q164 NC_PDTC144EU
Q165 NC_PMV65XP
Q166 NC_PDTC144EU
R532 NC_4.7K_J 0402
R529 NC_1K_J 0402
R533 NC_4.7K_J 0402
R530 NC_1K_J 0402
F2 NC_6V-0.35A_1206 1206L035
F3 NC_6V-0.35A_1206 1206L035
500mA
U36 TPS2055AD
U40 NC_MAX4798EUK-T
U41 NC_MAX4798EUK-T
U37 TPS2055AD
R527 10K_J 0402
R528 10K_J 0402
VCC_CTRLMS
VCC_CTRLSD
PLACE TO BJT
C466 0402 0.1U_16V_Y_Y
C468 2.2U_10V_Y 0603_Y5V
R392 100K_J 0402
C472 0402 0.1U_16V_Y_Y
C474 2.2U_10V_Y 0603_Y5V
R395 100K_J 0402
These capacitors should be closed to socket pin
MS STD/DUO CONN.
CN22
MS CARD SOC_10P
YAMAICHI_JCS010-2005-1
R491 0_J 0402
R492 0_J 0402
R493 0_J 0402
R494 0_J 0402
R495 0_J 0402
SD CONN.
CN23
SD SOCKET_9P
FOX_WK21923-S6P-7F
R397 0_J 0402
R398 0_J 0402
R399 0_J 0402
R400 0_J 0402
R401 0_J 0402
R402 4.7K_J 0402
R403 4.7K_J 0402
R404 4.7K_J 0402
C475 1000P_50V_K_B 0402
C476 1000P_50V_K_B 0402
C477 1000P_50V_K_B 0402
Solution1 :
Mount U36,U37,R527,R528
NC F2,F3,Q163,Q164,Q165,Q166,R529,R530,R532,R533,U40,U41
Solution2 :
Mount F2,F3,Q163,Q164,Q165,Q166,R529,R530,R532,R533
NC U36,U37,R527,R528,U40,U41
Solution3 :
Mount U40,U41,R527,R528
NC U36,U37,F2,F3,Q163,Q164,Q165,Q166,R529,R530,R532,R533
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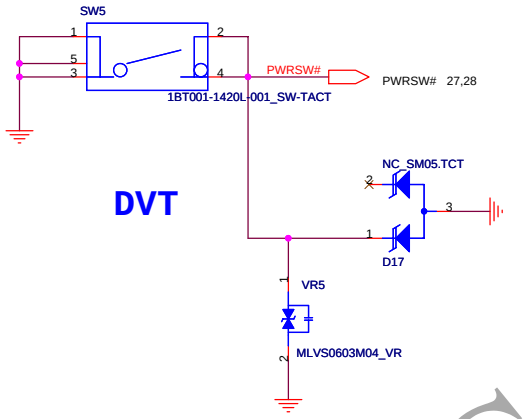
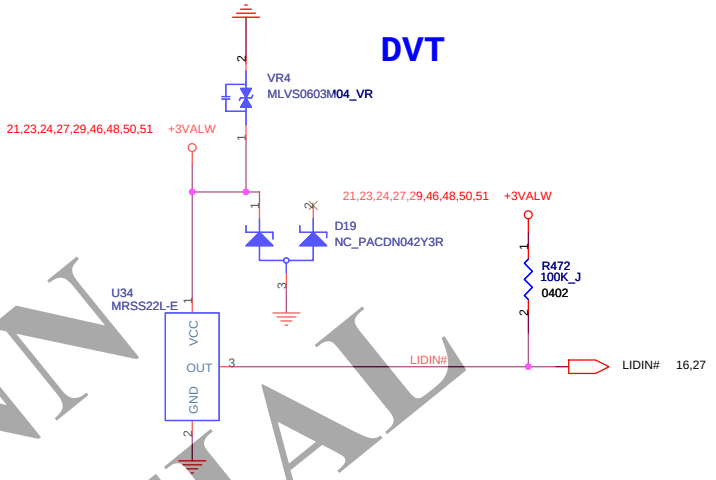
HON HAI PRECISION IND. CO., LTD.
FOXCONN CPBG - R&D Div.
D.C.C.
CONFIDENTIAL
HON HAI Precision Ind. Co., Ltd.
CCPBG - R&D Division
Title PCI (MS-DUO/SD/MDC) 3/3
Size A3
Document Number MS72-1-01
Rev 0.1
Date: Tuesday, December 05, 2006
Sheet 40 of 55

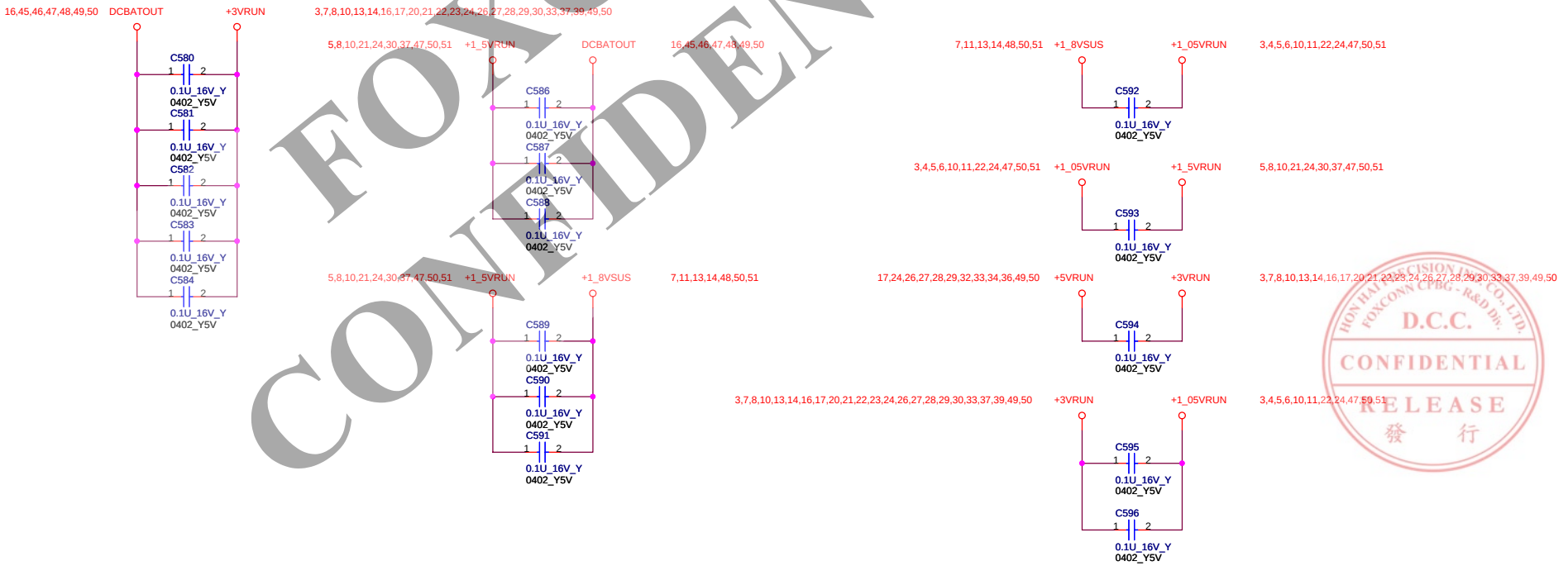
POWER BUTTON



LID Switch

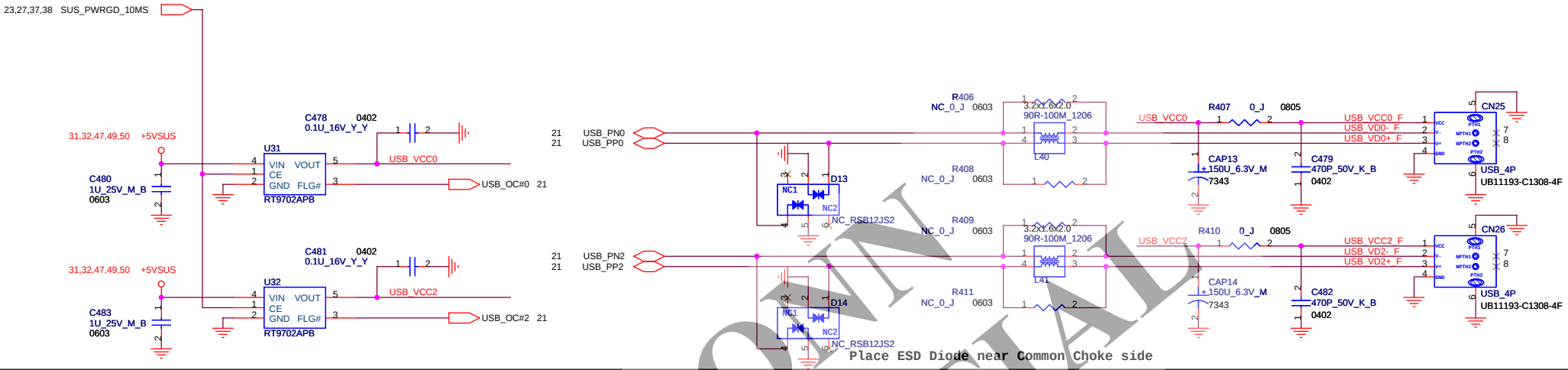


EMI CAP



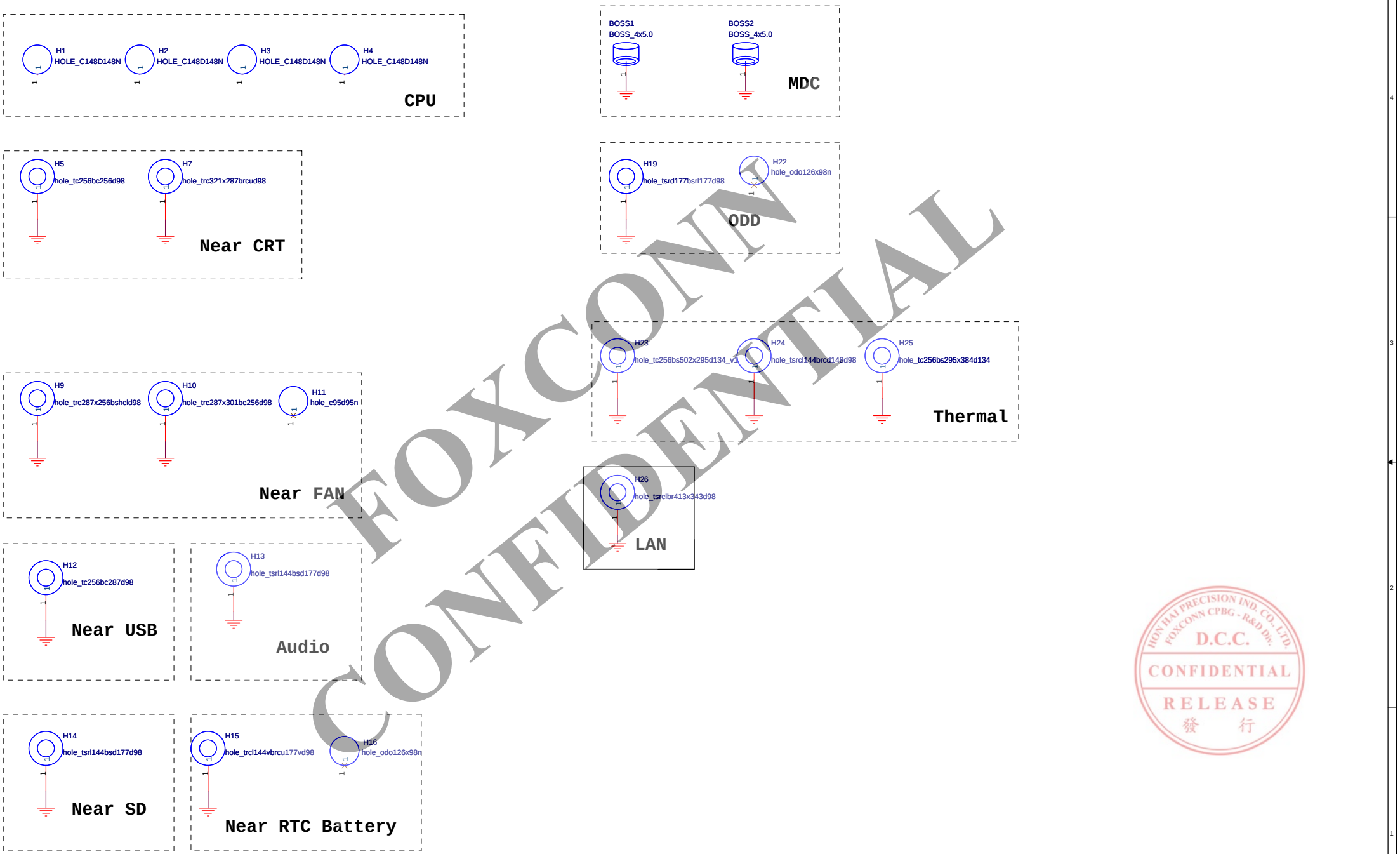
FOXCONN	HON HAI Precision Ind. Co., Ltd. CCPBG - R&D Division		
Title	Button/LID Switch/EMI CAP		
Size A3	Document Number MS72-1-01		Rev 0.1
Date: Tuesday, December 05, 2006	Sheet 41 of 55		

USB CONN X 2

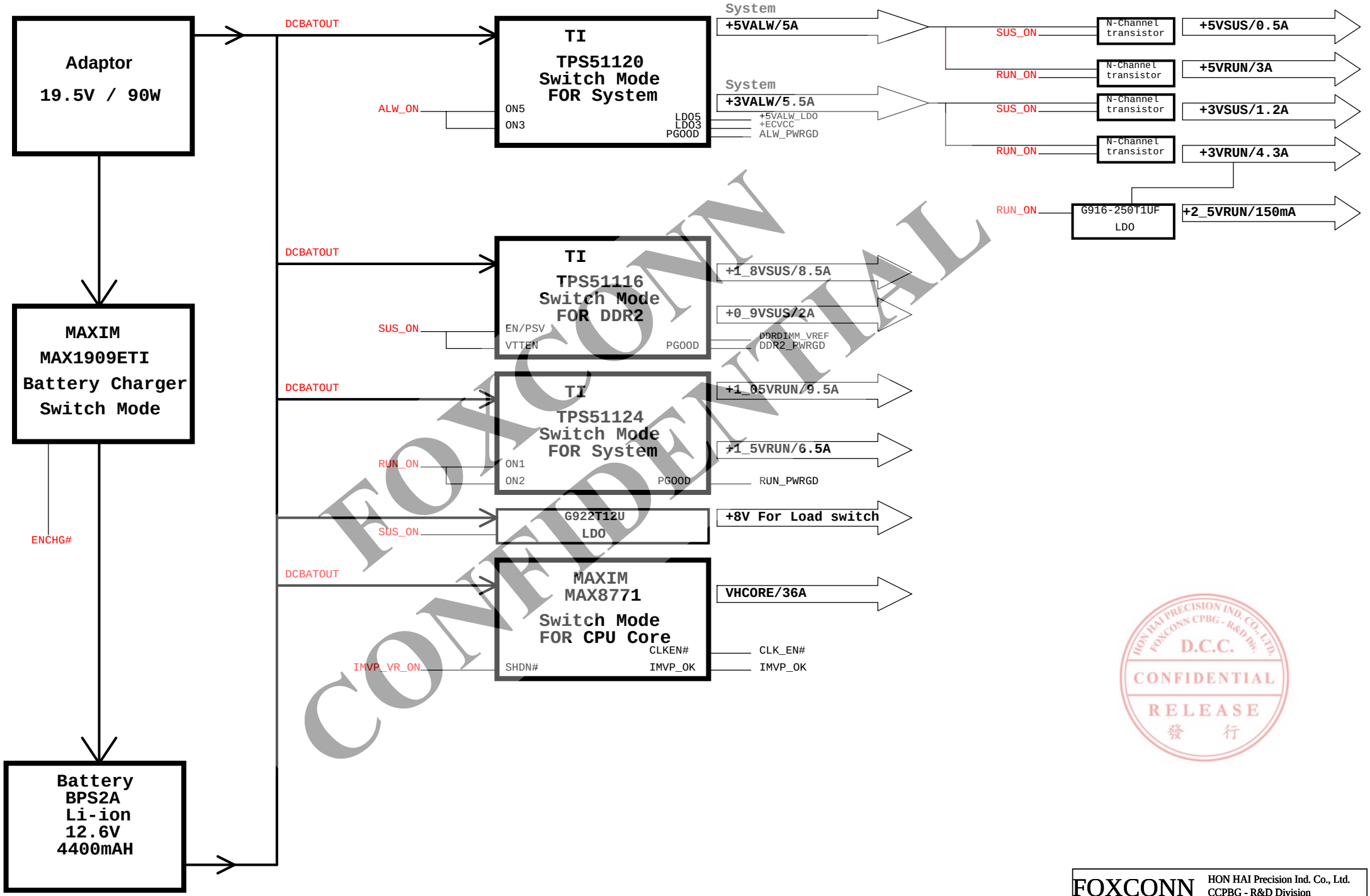


Place ESD Diode near Common Choke side

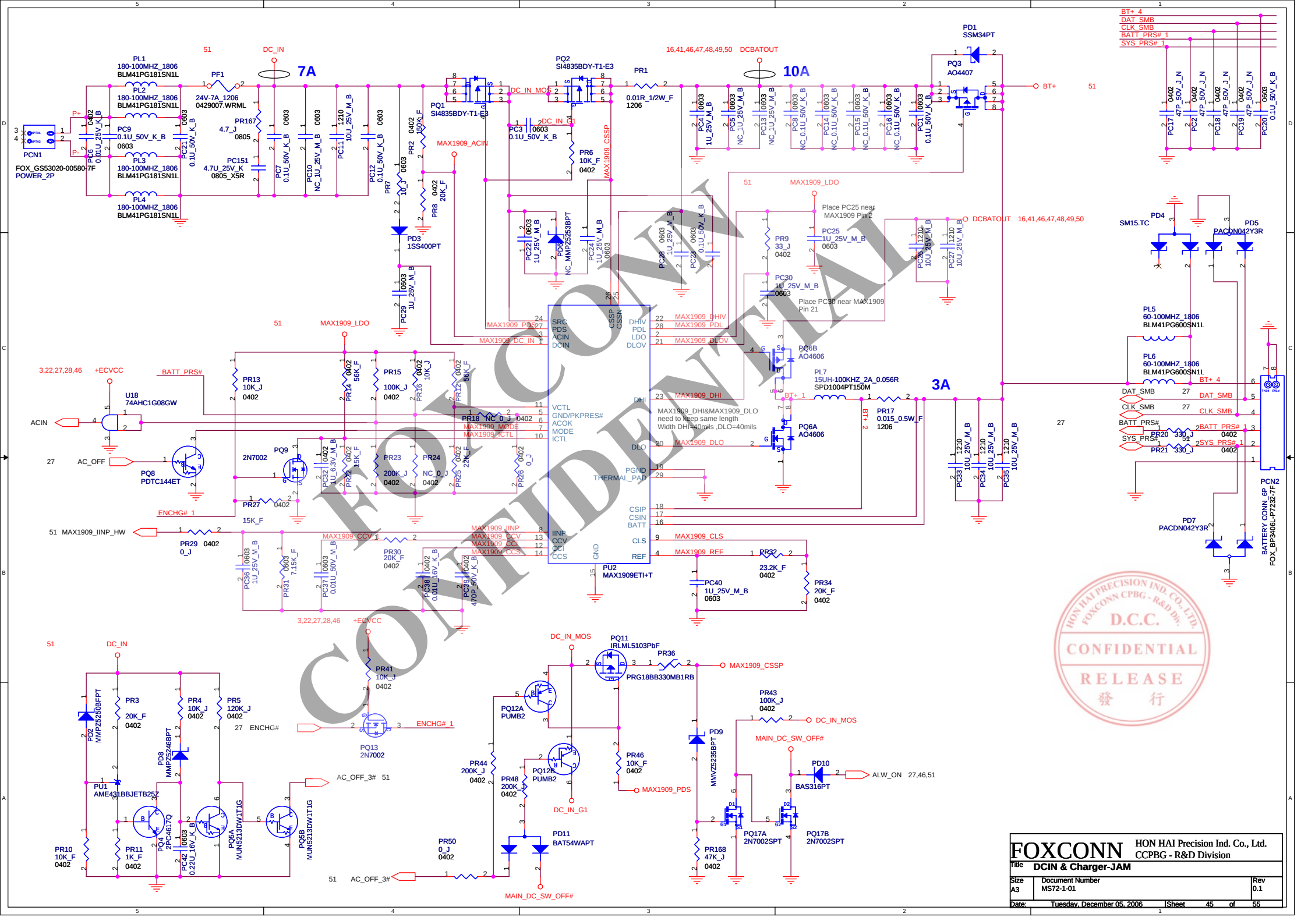
FOXCONN	HON HAI Precision Ind. Co., Ltd. CCPBG - R&D Division	
Title: USB2.0		
Size: A3	Document Number: MS72-1-01	Rev: 0.1
Date: Tuesday, December 05, 2006	Sheet 42 of 55	

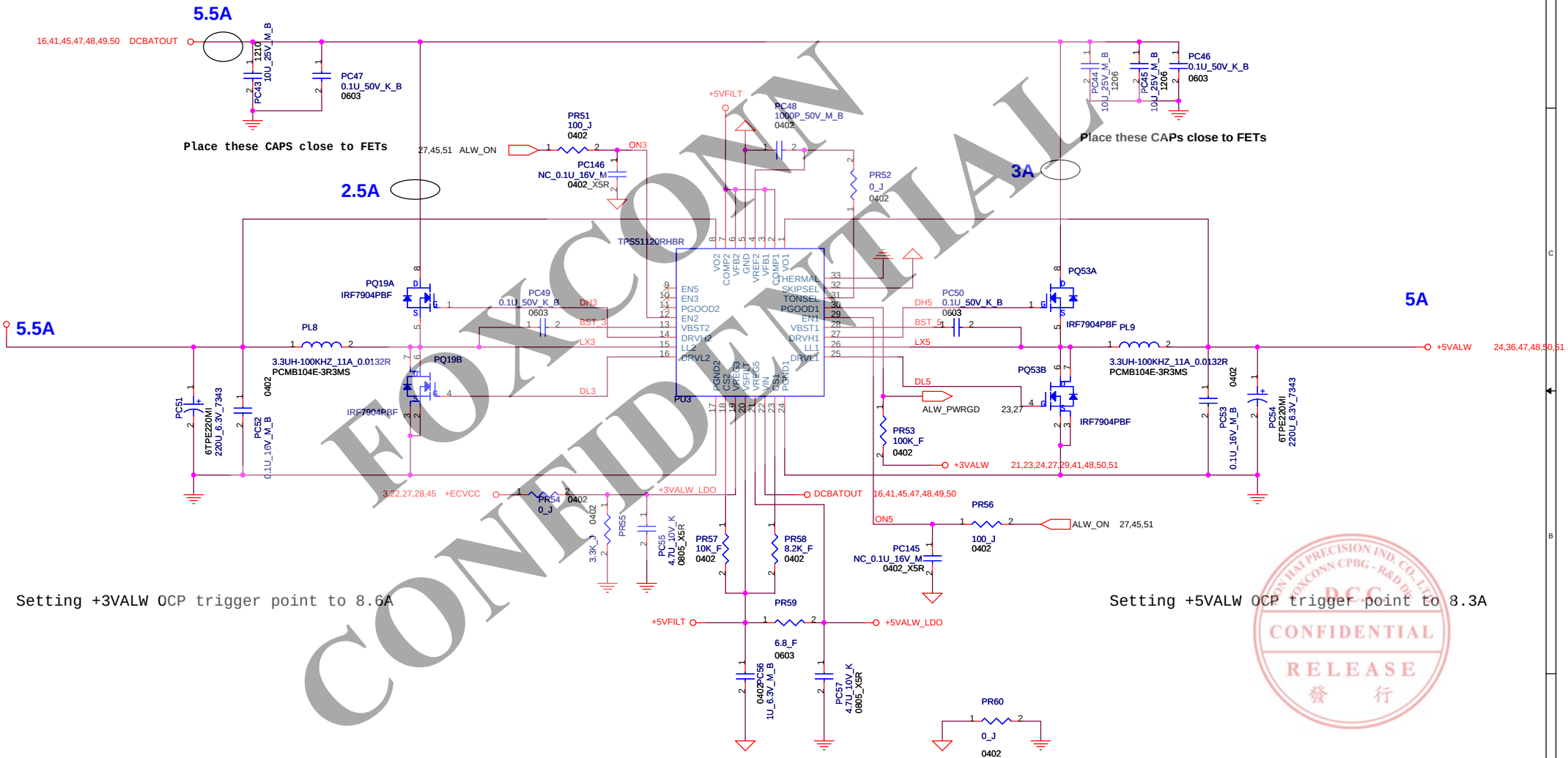


FOXCONN	HON HAI Precision Ind. Co., Ltd. CCPBG - R&D Division	
Title	HOLE	
Size A3	Document Number MS72-1-01	Rev 0.1
Date: Tuesday, December 05, 2006	Sheet 43 of 55	

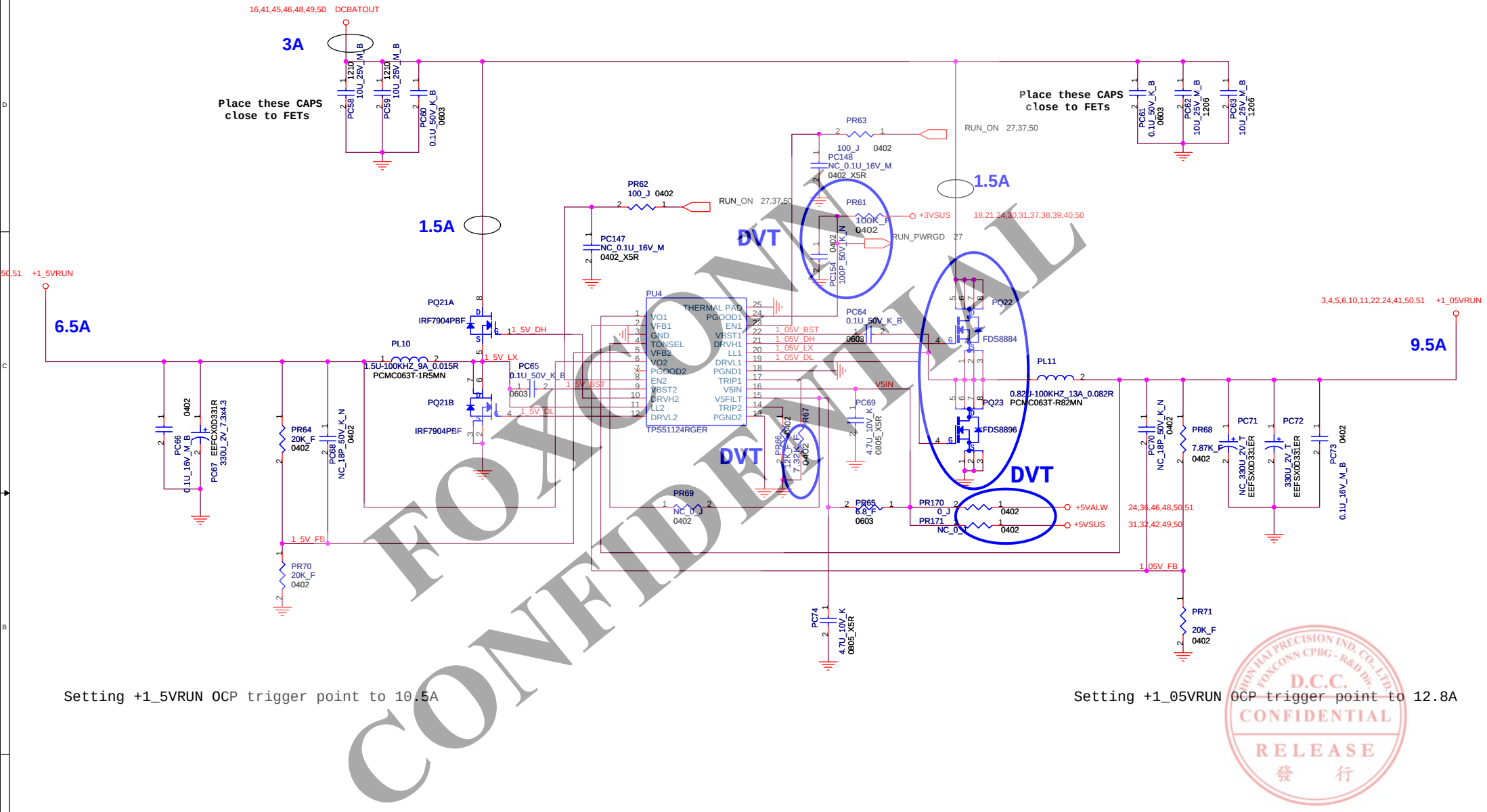


FOXCONN	HON HAI Precision Ind. Co., Ltd. CCPBG - R&D Division	
Title: Power Design Diagram-ZG		
Size: A3	Document Number: MS72-1-01	Rev: 0.1
Date: Tuesday, December 05, 2006	Sheet 44 of 55	





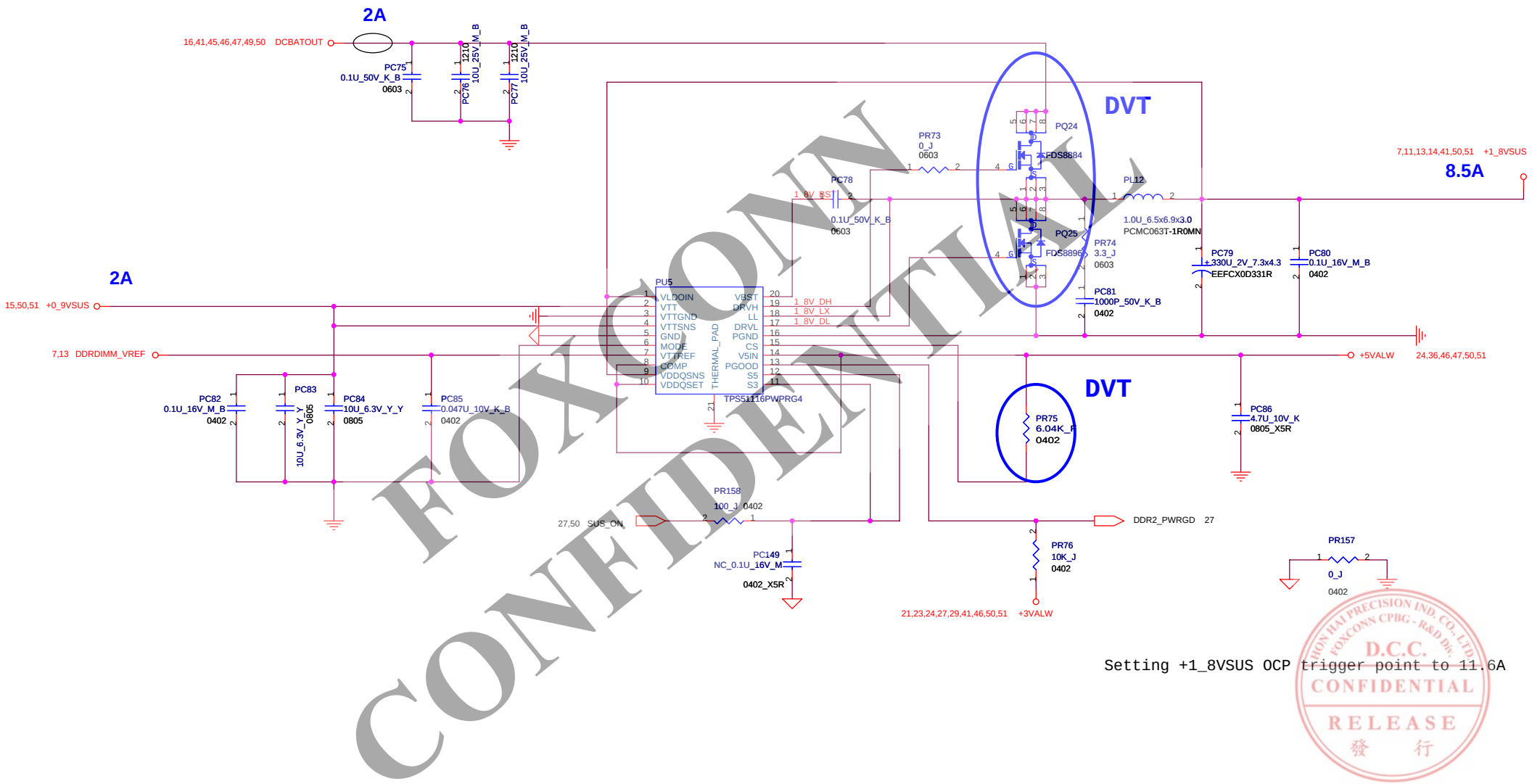
FOXCONN	HON HAI Precision Ind. Co., Ltd. CCPBG - R&D Division	
Title	D/D Power-JAM	
Size A3	Document Number MS72-1-01	Rev 0.1
Date: Tuesday, December 05, 2006	Sheet 46 of 55	



Setting +1_5VRUN OCP trigger point to 10.5A

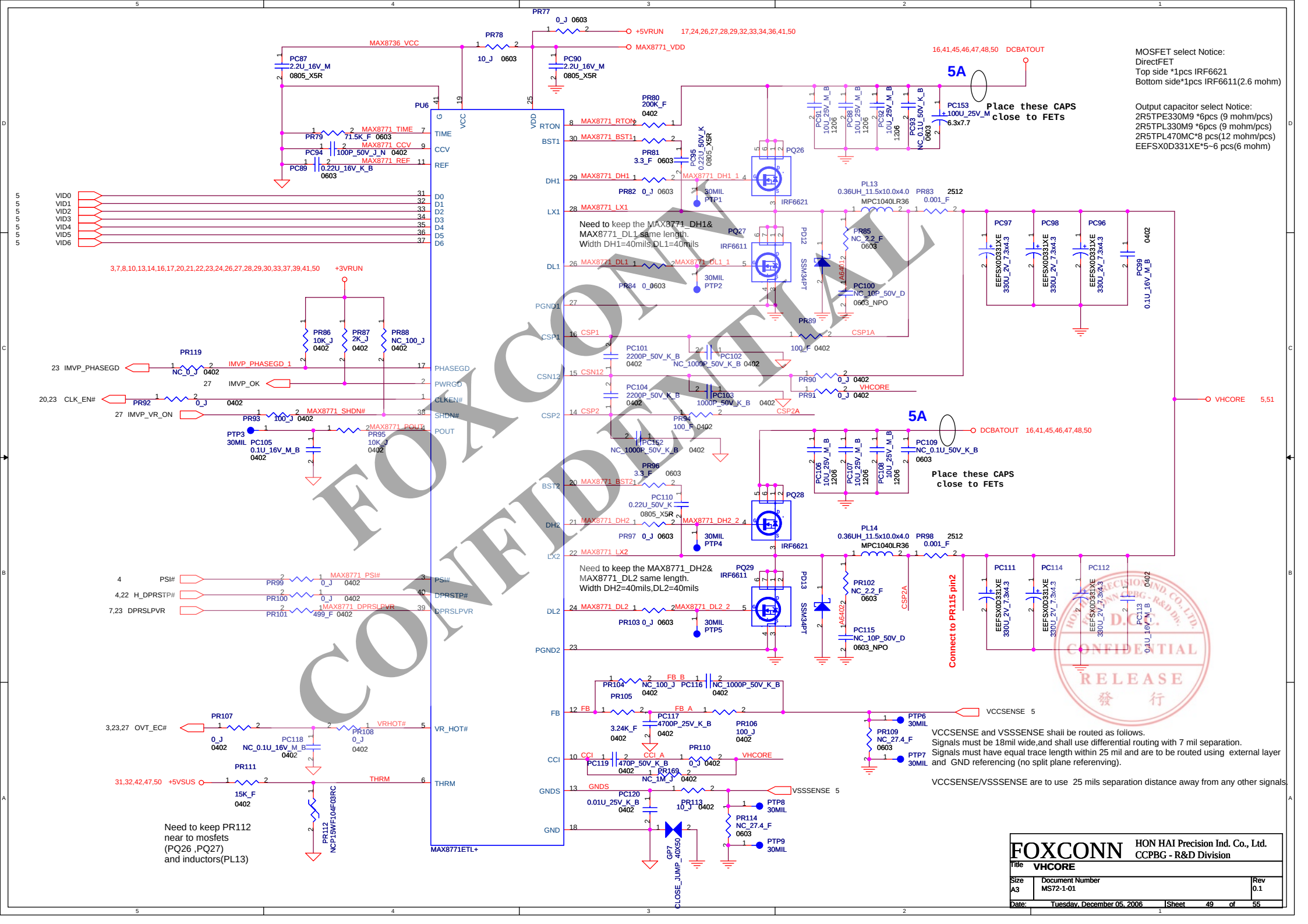
Setting +1_05VRUN OCP trigger point to 12.8A

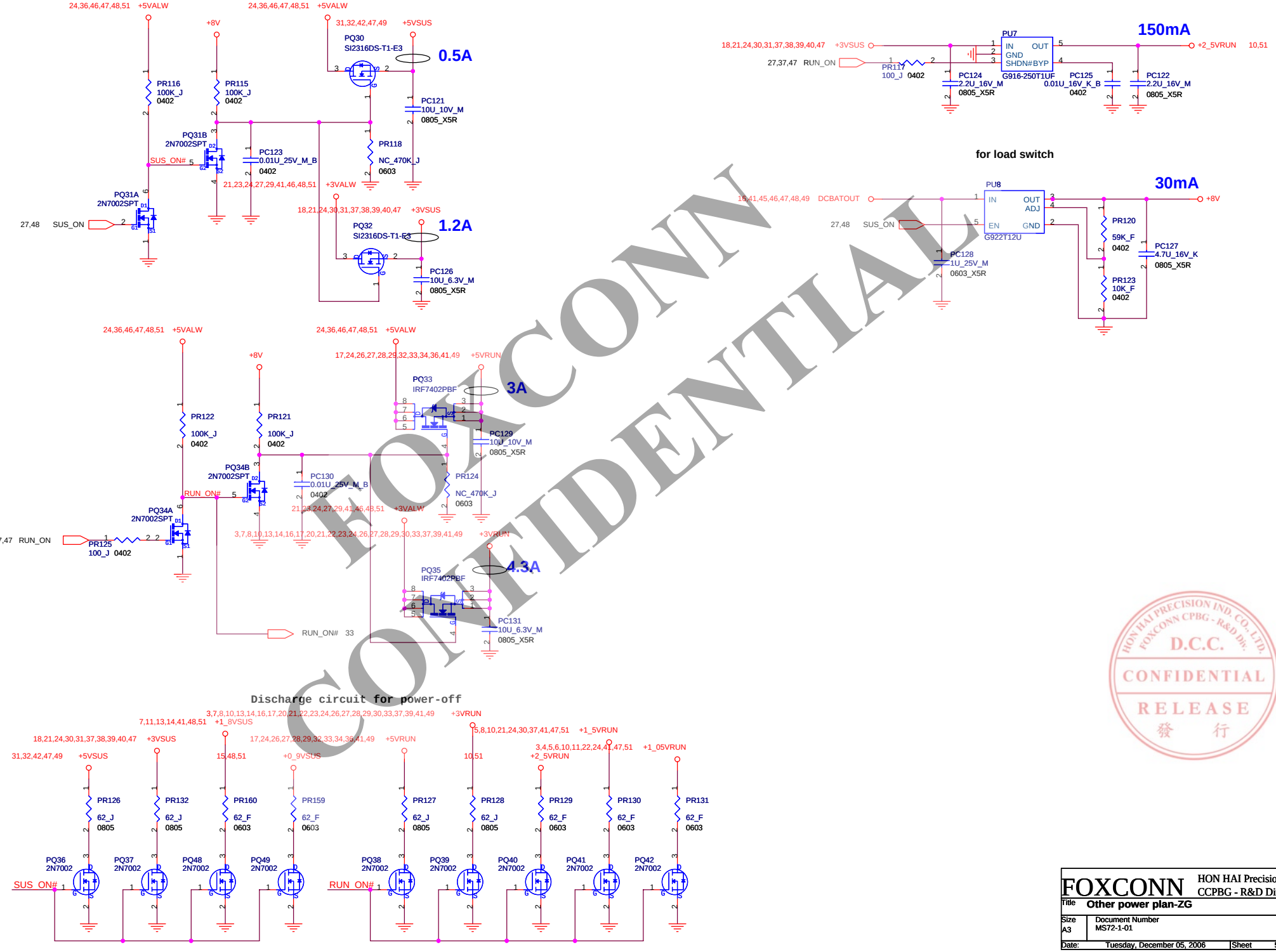
FOXCONN	HON HAI Precision Ind. Co., Ltd. CCPBG - R&D Division	
Title	1.5V/1.05V-JAM	
Size A3	Document Number MS72-1-01	Rev 0.1
Date: Tuesday, December 05, 2006	Sheet 47 of 55	



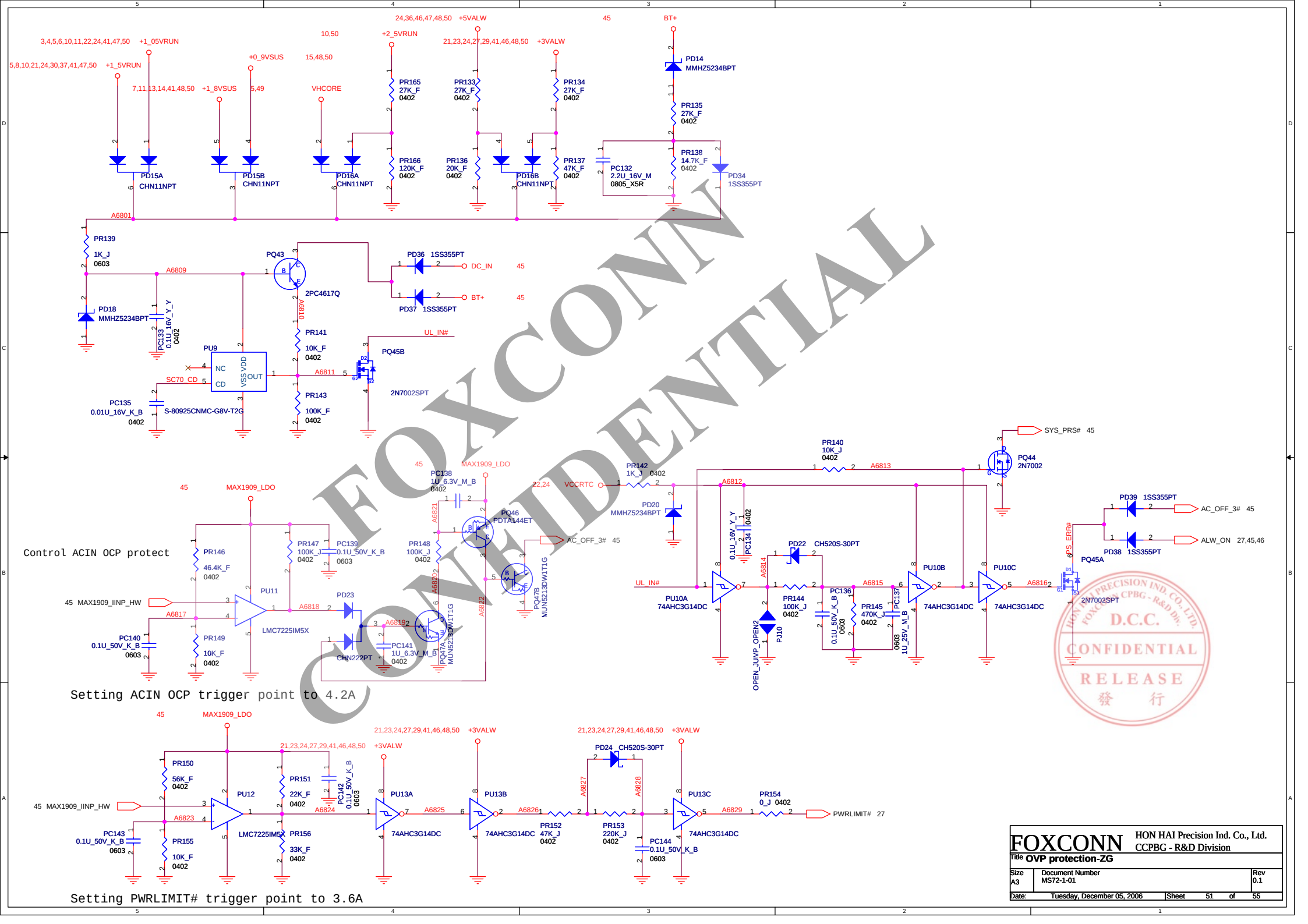
Setting +1_8VSUS OCP trigger point to 11.6A

FOXCONN	HON HAI Precision Ind. Co., Ltd. CCPBG - R&D Division	
Title	1.8V/0.9V-CWK	
Size A3	Document Number MS72-1-01	Rev 0.1
Date: Tuesday, December 05, 2006	Sheet 48 of 55	





FOXCONN	HON HAI Precision Ind. Co., Ltd. CCPBG - R&D Division	
Title: Other power plan-ZG		
Size: A3	Document Number: MS72-1-01	Rev: 0.1
Date: Tuesday, December 05, 2006	Sheet 50 of 55	


3,4,5,6,10,11,22,24,41,47,50 +1_05VRUN
5,8,10,21,24,30,37,41,47,50 +1_5VRUN
7,11,13,14,41,48,50 +1_8VSUS
+0_9VSUS
5,49
10,50
15,48,50
VHCORE
+2_5VRUN
24,36,46,47,48,50 +5VALW
21,23,24,27,29,41,46,48,50 +3VALW
45 BT+
PD15A CHN11NPT
PD15B CHN11NPT
PD16A CHN11NPT
PD16B CHN11NPT
PR165 27K_F 0402
PR166 120K_F 0402
PR133 27K_F 0402
PR136 20K_F 0402
PR134 27K_F 0402
PR137 47K_F 0402
PD14 MMHZ5234BPT
PR135 27K_F 0402
PR138 14.7K_F 0402
PD34 1SS355PT
PC132 2.2U_16V_M 0805_X5R
A6801
PR139 1K_J 0603
A6809
PD18 MMHZ5234BPT
PC133 0.1U_16V_Y_Y 0402
PQ43 2PC4617Q
A6810
PD36 1SS355PT
DC_IN 45
PD37 1SS355PT
BT+ 45
PR141 10K_F 0402
PU9
NC
CD
VSS VDD
OUT
SC70_CD
PC135 0.01U_16V_K_B 0402
S-80925CNMC-G8V-T2G
A6811
PR143 100K_F 0402
UL_IN#
PQ45B
2N7002SPT
45 MAX1909_LDO
PC138 1U_6.3V_M_B 0402
PQ46 PDTA144ET
22,24 VCCRTC
PR142 1K_J 0402
PD20 MMHZ5234BPT
AC_OFF_3# 45
PR140 10K_J 0402
A6812
A6813
PQ44 2N7002
SYS_PRS# 45
PD39 1SS355PT
AC_OFF_3# 45
PD38 1SS355PT
ALW_ON 27,45,46
PQ45A
2N7002SPT
GPS_ERR#
Control ACIN OCP protect
PR146 46.4K_F 0402
PR147 100K_J 0402
PC139 0.1U_50V_K_B 0603
PR148 100K_J 0402
A6821
A6822
PU11
LMC7225IM5X
45 MAX1909_IINP_HW
A6817
A6818
PD23
CHN222PT
PR149 10K_F 0402
PC140 0.1U_50V_K_B 0603
PC141 1U_6.3V_M_B 0402
PQ47A MUN5213DW1T1G
PQ47B MUN5213DW1T1G
PD22 CH520S-30PT
A6814
A6815
PU10A 74AHC3G14DC
PU10B 74AHC3G14DC
PU10C 74AHC3G14DC
A6816
PC136 0.1U_50V_K_B 0603
PR144 100K_J 0402
PR145 470K_J 0402
PC137 0.1U_25V_M_B 0603
PJ10 OPEN_JUMP_OPEN2
Setting ACIN OCP trigger point to 4.2A
45 MAX1909_LDO
21,23,24,27,29,41,46,48,50 +3VALW
PR150 56K_F 0402
PR151 22K_F 0402
PC142 0.1U_50V_K_B 0603
PU12
LMC7225IM5X
45 MAX1909_IINP_HW
A6823
A6824
PR156 33K_F 0402
PR155 10K_F 0402
PC143 0.1U_50V_K_B 0603
PU13A 74AHC3G14DC
A6825
PU13B 74AHC3G14DC
A6826
PR152 47K_J 0402
PD24 CH520S-30PT
A6827
A6828
PR153 220K_J 0402
PC144 0.1U_50V_K_B 0603
PU13C 74AHC3G14DC
A6829
PR154 0_J 0402
PWRLIMIT# 27
Setting PWRLIMIT# trigger point to 3.6A
FOXCONN
HON HAI Precision Ind. Co., Ltd.
CCPBG - R&D Division
Title OVP protection-ZG
Size A3
Document Number MS72-1-01
Rev 0.1
Date: Tuesday, December 05, 2006
Sheet 51 of 55
FOXCONN CONFIDENTIAL
HON HAI PRECISION IND. CO., LTD. FOXCONN CPBG - R&D Div.
D.C.C.
CONFIDENTIAL
RELEASE
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HISTORY (1)

DVT

(2006/11/14)

P.11 Add C608 and NC CAP7 for purchase convenient.
P.17 Add R544 and NC R121for CRB requirement(refer to Capell_Valley_CRB_Schematics_Rev1_502 page18)
P.33 NC R483 for Page36 had pull low 10k resistor to GND
P.34 Change CAP11,CAP12 from 1C-31T0337-M101 to 1C-42T0107-M100 for application modification
P.36 Add Q167, Q168, R536 and NC U26, U27,U28, D12, C452, R377,R378 for application modification
Change Q27, Q159, from 17-CHDTC14-4E01 to 17-PMBT390-4200 for application modification
Change Q26 from 17-DTA114Y-UA00 to 17-MMBT390-6K00 for application modification
Add R540,R541,R542,R543,R537,R538,R539,C609 for application modification

(2006/11/15)

P.47 Change PQ22 from 17-1RF7807-2000 to 17-FDS8884-0000 for application modification
Change PQ23 from 17-1RF8113-0000 to 17-FDS8896-0000 for application modification
P.48 Change PQ24 from 17-1RF7807-2000 to 17-FDS8884-0000 for application modification
Change PQ25 from 17-1RF8113-0000 to 17-FDS8896-0000 for application modification

(2006/11/20)

P.11 Del C608 and mount CAP7 for MOR requirement
P.17 Del R544 and mount R121 for MOR requirement
P.47 Change PR67 from 1R-0000472-F200 to 1R-0007321-F200 for power PWM OCP application modification
P.48 Change PR75 from 1R-0000562-F200 to 1R-0006041-F200 for power PWM OCP application modification

(2006/11/21)

P.33 Add R483 for MOR requirement
P.36 Del Q167, Q168, R536 and add U26, U27,U28,D12, C452, R377,R378 for MOR requirement
Change Q27, Q159, from 17-PMBT390-42001 to 17-CHDTC14-4E01for MOR requirement
Change Q26 from 17-MMBT390-6K00 to 17-DTA114Y-UA00 for MOR requirement
Del R540,R541,R542,R543,R537,R538,R539,C609 for MOR requirement

(2006/11/22)

P.28 Add R535 0om for X-BUS CONN del on MP
P.47 Add PC154 and change PR61 from 10k to 100k for RUN_PWRGD glitch issue
Change the power supply of PU4 pin16 from +5VSUS to +5VALW for application modification

(2006/11/23)

P.47 Add PR170,PR171 for MOR requirement

HISTORY (2)

FOXCONN	HON HAI Precision Ind. Co., Ltd. CCPBG - R&D Division		
Title: History (2)			
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