

Nozomi-4 SWG SOVP LOGIC SCHEMATICS

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NZM4H-7
VER 7.54
MAR/19/2012

BASE LOGIC :Nozomi-4 SWG SVT
VER 7.03
Jan/25/2012

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- 81.VCCCPUCORE DECOUPLING
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- 84.DC/DC VCC1R05AMT(VT356)
- 85.DC/DC VCC1R5A(VT357)
- 86.DC/DC VCC0R75B(MAX1510)
- 87.DC/DC VCC1R5VIDEO(VT356)
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EC HISTORY

NOZOMI-4 PRE-DV (BASE LOGIC :NZM3H-7 VER 7.00 Nov/30/2010)

VER.0.00 12/08/2010 APPLIED PDV_EC001
 VER.0.01 12/14/2010 APPLIED PDV_EC002-012
 VER.0.02 12/15/2010 APPLIED PDV_EC013-016,018
 VER.0.03 12/16/2010 APPLIED PDV_EC017,019-026
 VER.0.04 12/21/2010 APPLIED PDV_EC027-028
 VER.0.05 12/22/2010 APPLIED PDV_EC030-034
 VER.0.06 12/24/2010 APPLIED PDV_EC035-037
 VER.0.07 12/27/2010 APPLIED PDV_EC038-041,043-046
 VER.0.08 12/29/2010 APPLIED PDV_EC047-049
 VER.0.09 01/06/2011 APPLIED PDV_EC051-058
 VER.0.10 01/07/2011 APPLIED PDV_EC042,050,059-063
 VER.0.11 01/11/2011 APPLIED PDV_EC064-069
 VER.0.12 01/13/2011 APPLIED PDV_EC070-074
 VER.0.13 01/14/2011 APPLIED PDV_EC075-077
 VER.0.14 01/17/2011 APPLIED PDV_EC079,080
 VER.0.15 01/19/2011 APPLIED PDV_EC078,081
 VER.0.16 01/21/2011 APPLIED PDV_EC082-085
 VER.0.17 01/24/2011 APPLIED PDV_EC086,087
 VER.0.18 01/25/2011 APPLIED PDV_EC088-092
 VER.0.19 01/26/2011 APPLIED PDV_EC093-095
 VER.0.20 01/27/2011 APPLIED PDV_EC096,097
 VER.0.21 01/28/2011 APPLIED PDV_EC098-100
 VER.0.22 01/31/2011 APPLIED PDV_EC101,102
 VER.0.23 02/01/2011 APPLIED PDV_EC103-109
 VER.0.24 02/02/2011 APPLIED PDV_EC110-116
 VER.0.25 02/03/2011 APPLIED PDV_EC117,118
 VER.0.26 02/04/2011 APPLIED PDV_EC119-123
 VER.0.27 02/07/2011 APPLIED PDV_EC124-130
 VER.0.28 02/08/2011 APPLIED PDV_EC131-133
 VER.0.29 02/09/2011 APPLIED PDV_EC134-136
 VER.0.30 02/10/2011 APPLIED PDV_EC139,140
 VER.0.31 02/14/2011 APPLIED PDV_EC137,138
 VER.0.32 02/15/2011 APPLIED PDV_EC141-145
 VER.0.33 02/16/2011 APPLIED PDV_EC146-148
 VER.0.34 02/17/2011 APPLIED PDV_EC149-151
 VER.0.35 02/18/2011 APPLIED PDV_EC152-160
 VER.0.36 02/21/2011 APPLIED PDV_EC161-166
 VER.0.37 02/22/2011 APPLIED PDV_EC167-183
 VER.0.38 02/23/2011 APPLIED PDV_EC184-187

NOZOMI-4 SWG SDV (BASE LOGIC :Nozomi-4 SWG Pre-DV VER 0.38 Feb/23/2011)

VER.1.00 02/24/2011 APPLIED SDV_EC001
 VER.1.01 02/25/2011 APPLIED SDV_EC002-007
 VER.1.02 02/28/2011 APPLIED SDV_EC008-033
 VER.1.03 03/01/2011 APPLIED SDV_EC034-052
 VER.1.04 03/02/2011 APPLIED SDV_EC053-061
 VER.1.05 03/03/2011 APPLIED SDV_EC062-076
 VER.1.06 03/04/2011 APPLIED SDV_EC077-079,081-083
 VER.1.07 03/07/2011 APPLIED SDV_EC084-089
 VER.1.08 03/08/2011 APPLIED SDV_EC090-099,101,102
 VER.1.09 03/09/2011 APPLIED SDV_EC103-114
 VER.1.10 03/10/2011 APPLIED SDV_EC115-121
 VER.1.11 03/11/2011 APPLIED SDV_EC122-127
 VER.1.12 03/14/2011 APPLIED SDV_EC128,130
 VER.1.13 03/15/2011 APPLIED SDV_EC131-134
 VER.1.14 03/16/2011 APPLIED SDV_EC129
 VER.1.15 03/17/2011 APPLIED SDV_EC135
 VER.1.16 03/18/2011 APPLIED SDV_EC136-141
 VER.1.17 03/22/2011 APPLIED SDV_EC142-144
 VER.1.18 03/23/2011 APPLIED SDV_EC145-147
 VER.1.19 03/24/2011 APPLIED SDV_EC149-154
 VER.1.20 03/25/2011 APPLIED SDV_EC148
 VER.1.21 03/28/2011 APPLIED SDV_EC155-158
 VER.1.22 03/29/2011 APPLIED SDV_EC159-163
 VER.1.23 03/30/2011 APPLIED SDV_EC164-168
 VER.1.24 03/31/2011 APPLIED SDV_EC169-171
 VER.1.25 04/01/2011 APPLIED SDV_EC172,173
 VER.1.26 04/04/2011 APPLIED SDV_EC174,175
 VER.1.27 04/05/2011 APPLIED SDV_EC176,177
 VER.1.28 04/06/2011 APPLIED SDV_EC178-180
 VER.1.29 04/07/2011 APPLIED SDV_EC181-184
 VER.1.30 04/08/2011 APPLIED SDV_EC185-189
 VER.1.31 04/11/2011 APPLIED SDV_EC190,191

VER.1.32 04/13/2011 APPLIED SDV_EC192-194
 VER.1.33 04/14/2011 APPLIED SDV_EC195
 VER.1.34 04/15/2011 APPLIED SDV_EC196
 VER.1.35 04/18/2011 APPLIED SDV_EC197-199,201
 VER.1.36 04/19/2011 APPLIED SDV_EC202,203
 VER.1.37 04/20/2011 APPLIED SDV_EC205-208
 VER.1.38 04/21/2011 APPLIED SDV_EC209-213
 VER.1.39 04/22/2011 APPLIED SDV_EC214-217
 VER.1.40 04/25/2011 APPLIED SDV_EC218-220,224
 VER.1.41 04/26/2011 APPLIED SDV_EC228-232
 VER.1.42 04/27/2011 APPLIED SDV_EC236,240,242,245,247
 VER.1.43 04/28/2011 APPLIED SDV_EC248,251-254,261
 VER.1.44 05/06/2011 APPLIED SDV_EC262,263
 VER.1.45 05/20/2011 APPLIED SDV_EC264,265

Nozomi-4 SWG MFVT (BASE LOGIC :Nozomi-4 SWG SDV VER 1.45 May/20/2011)

VER.2.00 05/23/2011 APPLIED MFVT_EC001-008
 VER.2.01 05/24/2011 APPLIED MFVT_EC009-014,017
 VER.2.02 05/25/2011 APPLIED MFVT_EC010,015,016,018
 VER.2.03 05/26/2011 APPLIED MFVT_EC020
 VER.2.04 05/27/2011 APPLIED MFVT_EC021,022
 VER.2.05 05/30/2011 APPLIED MFVT_EC023,025,026
 VER.2.06 05/31/2011 APPLIED MFVT_EC028
 VER.2.07 06/01/2011 APPLIED MFVT_EC029,030
 VER.2.08 06/02/2011 APPLIED MFVT_EC031,032
 VER.2.09 06/03/2011 APPLIED MFVT_EC033
 VER.2.10 06/06/2011 APPLIED MFVT_EC037-039,041,042
 VER.2.11 06/07/2011 APPLIED MFVT_EC043,045,047
 VER.2.12 06/08/2011 APPLIED MFVT_EC048,053
 VER.2.13 06/09/2011 APPLIED MFVT_EC055,057,058,060,061
 VER.2.14 06/14/2011 APPLIED MFVT_EC063
 VER.2.15 06/15/2011 APPLIED MFVT_EC066
 VER.2.16 06/21/2011 APPLIED MFVT_EC067

Nozomi-4 SWG FVT (BASE LOGIC :Nozomi-4 SWG MFVT VER 2.16 Jun/21/2011)

VER.3.00 06/23/2011 APPLIED FVT_EC001-003
 VER.3.01 06/29/2011 APPLIED FVT_EC006
 VER.3.02 07/01/2011 APPLIED FVT_EC008
 VER.3.03 07/04/2011 APPLIED FVT_EC004,009-013
 VER.3.04 07/05/2011 APPLIED FVT_EC014-017,019
 VER.3.05 07/06/2011 APPLIED FVT_EC020-024
 VER.3.06 07/07/2011 APPLIED FVT_EC018,026
 VER.3.07 07/08/2011 APPLIED FVT_EC028-031,034,036
 VER.3.08 07/11/2011 APPLIED FVT_EC037
 VER.3.09 07/12/2011 APPLIED FVT_EC041,044
 VER.3.10 07/13/2011 APPLIED FVT_EC045,046
 VER.3.11 07/14/2011 APPLIED FVT_EC048-052,055,056,058-060,063
 VER.3.12 07/15/2011 APPLIED FVT_EC064
 VER.3.13 07/19/2011 APPLIED FVT_EC065-070
 VER.3.14 07/21/2011 APPLIED FVT_EC071-076
 VER.3.15 07/22/2011 APPLIED FVT_EC078
 VER.3.16 07/26/2011 APPLIED FVT_EC079-081

Nozomi-4 SWG SIT (BASE LOGIC :Nozomi-4 SWG FVT VER 3.16 Jul/26/2011)

VER.4.00 08/01/2011 APPLIED SIT_EC001-009
 VER.4.01 08/02/2011 APPLIED SIT_EC010-012
 VER.4.02 08/03/2011 APPLIED SIT_EC013
 VER.4.03 08/04/2011 APPLIED SIT_EC014-019
 VER.4.04 08/05/2011 APPLIED SIT_EC020,021
 VER.4.05 08/17/2011 APPLIED SIT_EC022-031,033,034
 VER.4.06 08/22/2011 APPLIED SIT_EC035,036,039-042
 VER.4.07 08/23/2011 APPLIED SIT_EC044,045
 VER.4.08 08/24/2011 APPLIED SIT_EC046-050
 VER.4.09 08/25/2011 APPLIED SIT_EC037,052-054
 VER.4.10 08/26/2011 APPLIED SIT_EC055,056
 VER.4.11 08/29/2011 APPLIED SIT_EC057
 VER.4.12 09/01/2011 APPLIED SIT_EC059-061
 VER.4.13 09/05/2011 APPLIED SIT_EC062,063
 VER.4.14 09/07/2011 APPLIED SIT_EC064,065,070-079
 VER.4.15 09/08/2011 APPLIED SIT_EC080-089
 VER.4.16 09/09/2011 APPLIED SIT_EC092,095,096
 VER.4.17 09/12/2011 APPLIED SIT_EC099,100
 VER.4.18 09/13/2011 APPLIED SIT_EC101,102

VER.4.19 09/15/2011 APPLIED SIT_EC104-106
 VER.4.20 09/21/2011 APPLIED SIT_EC107

Nozomi-4 SWG SIT-R1 (BASE LOGIC :Nozomi-4 SWG SIT VER 4.20 Sep/21/2011)

VER.5.00 09/27/2011 APPLIED SIT-R_EC001-003
 VER.5.01 10/11/2011 APPLIED SIT-R_EC004-006
 VER.5.02 10/14/2011 APPLIED SIT-R_EC007
 VER.5.03 10/28/2011 APPLIED SIT-R_EC008
 VER.5.04 10/31/2011 APPLIED SIT-R_EC009-012
 VER.5.05 11/01/2011 APPLIED SIT-R_EC014
 VER.5.06 11/02/2011 APPLIED SIT-R_EC015
 VER.5.07 11/04/2011 APPLIED SIT-R_EC017-020,022
 VER.5.08 11/07/2011 APPLIED SIT-R_EC023
 VER.5.09 11/09/2011 APPLIED SIT-R_EC016
 VER.5.10 11/10/2011 APPLIED SIT-R_EC024
 VER.5.11 11/15/2011 APPLIED SIT-R_EC025-026

Nozomi-4 SWG SIT-R2 (BASE LOGIC :Nozomi-4 SWG SIT-R1 VER 5.11 Nov/15/2011)

VER.6.00 11/18/2011 APPLIED SIT-R2_EC001-003
 VER.6.01 11/22/2011 APPLIED SIT-R2_EC006
 VER.6.02 11/25/2011 APPLIED SIT-R2_EC007
 VER.6.03 11/28/2011 APPLIED SIT-R2_EC008
 VER.6.04 11/29/2011 APPLIED SIT-R2_EC009-010
 VER.6.05 11/30/2011 APPLIED SIT-R2_EC011-013
 VER.6.06 12/01/2011 APPLIED SIT-R2_EC016
 VER.6.07 12/05/2011 APPLIED SIT-R2_EC018

Nozomi-4 SWG SVT (BASE LOGIC :Nozomi-4 SWG SIT-R2 VER 6.07 Dec/05/2011)

VER.7.00 12/13/2011 APPLIED SVT_EC001-006
 VER.7.01 12/15/2011 APPLIED SVT_EC007
 VER.7.02 01/17/2012 APPLIED SVT_EC011
 VER.7.03 01/25/2012 APPLIED SVT_EC012

Nozomi-4 SWG SOVP (BASE LOGIC :Nozomi-4 SWG SVT VER 7.03 Jan/25/2012)

VER.7.50 02/20/2012 APPLIED SOVP_EC001-002
 VER.7.51 02/24/2012 APPLIED SOVP_EC003-004
 VER.7.52 03/05/2012 APPLIED SOVP_EC005-006
 VER.7.53 03/08/2012 APPLIED SOVP_EC007
 VER.7.54 03/19/2012 APPLIED SOVP_EC008

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Project Name : NZM-4 SWG SOVP	Title : EC HISTORY
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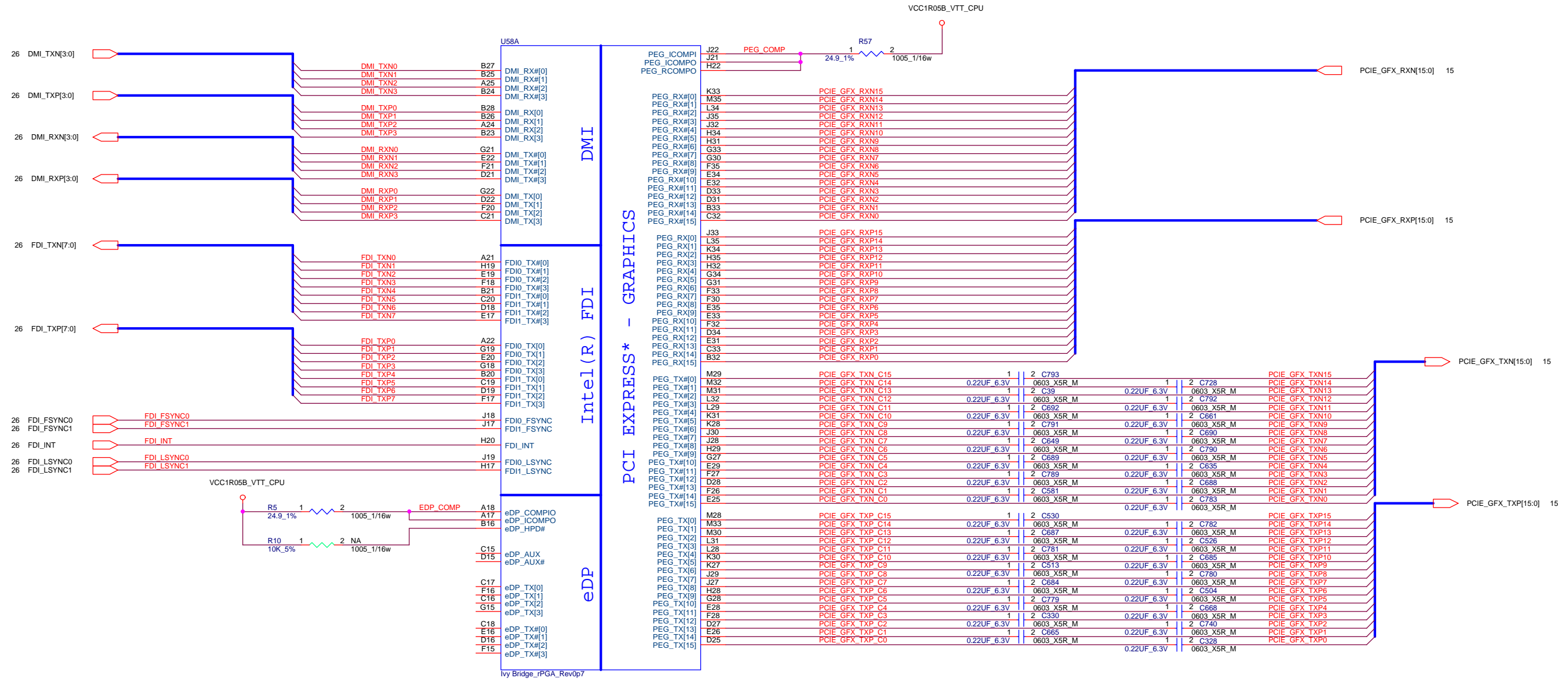
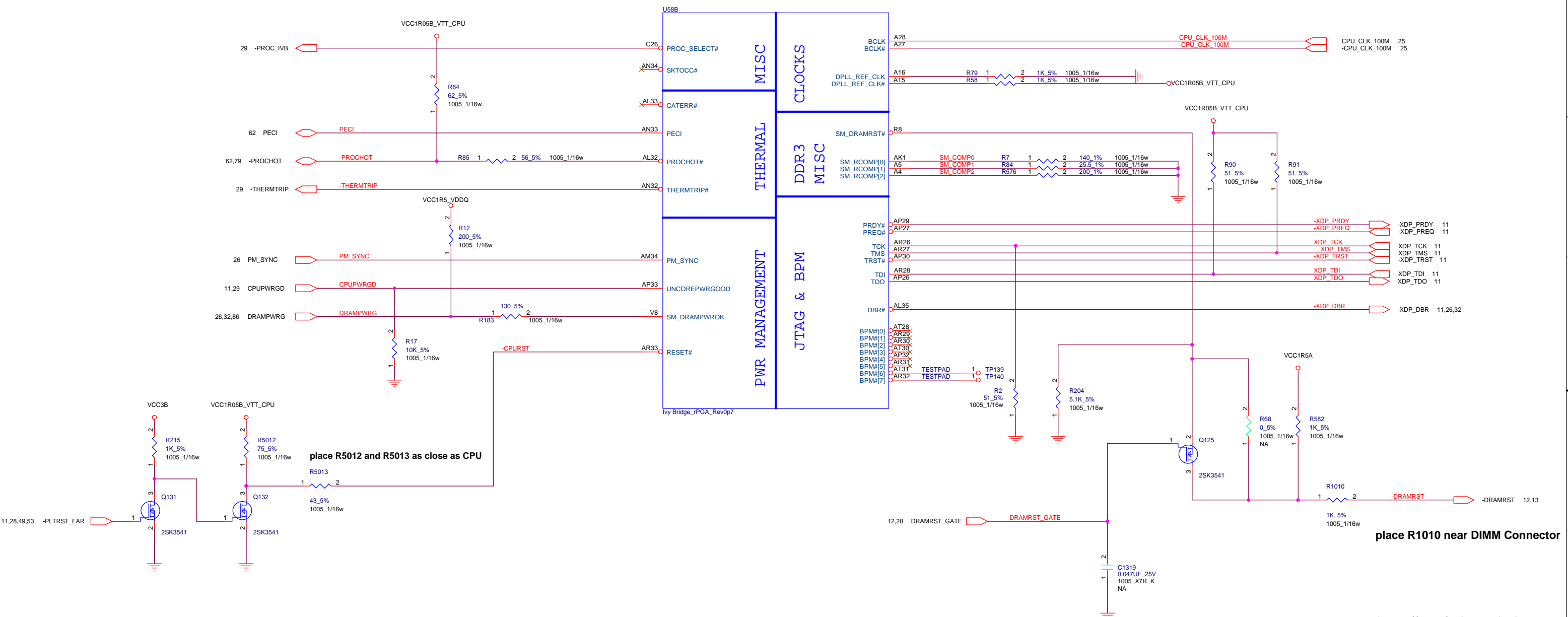
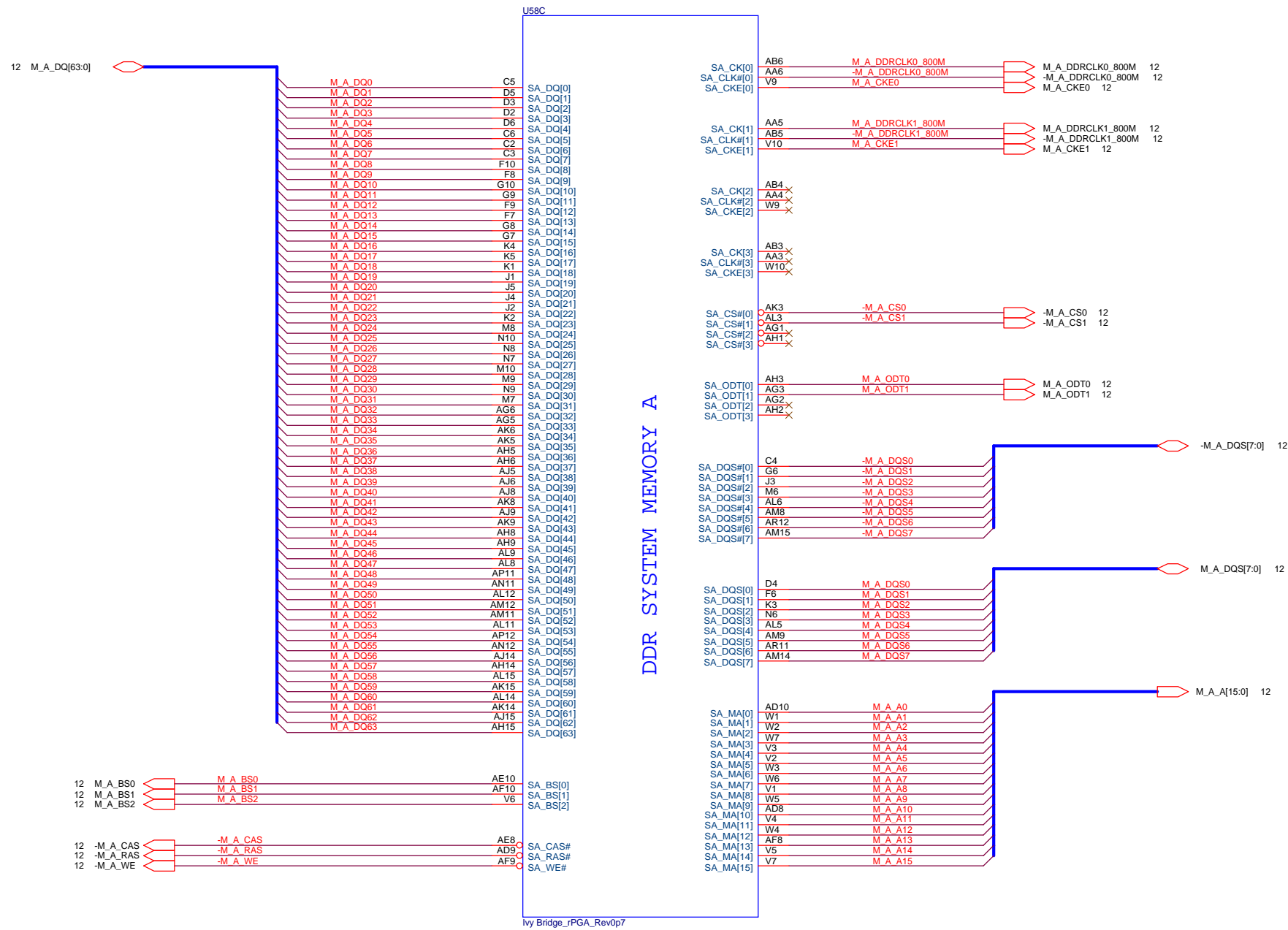
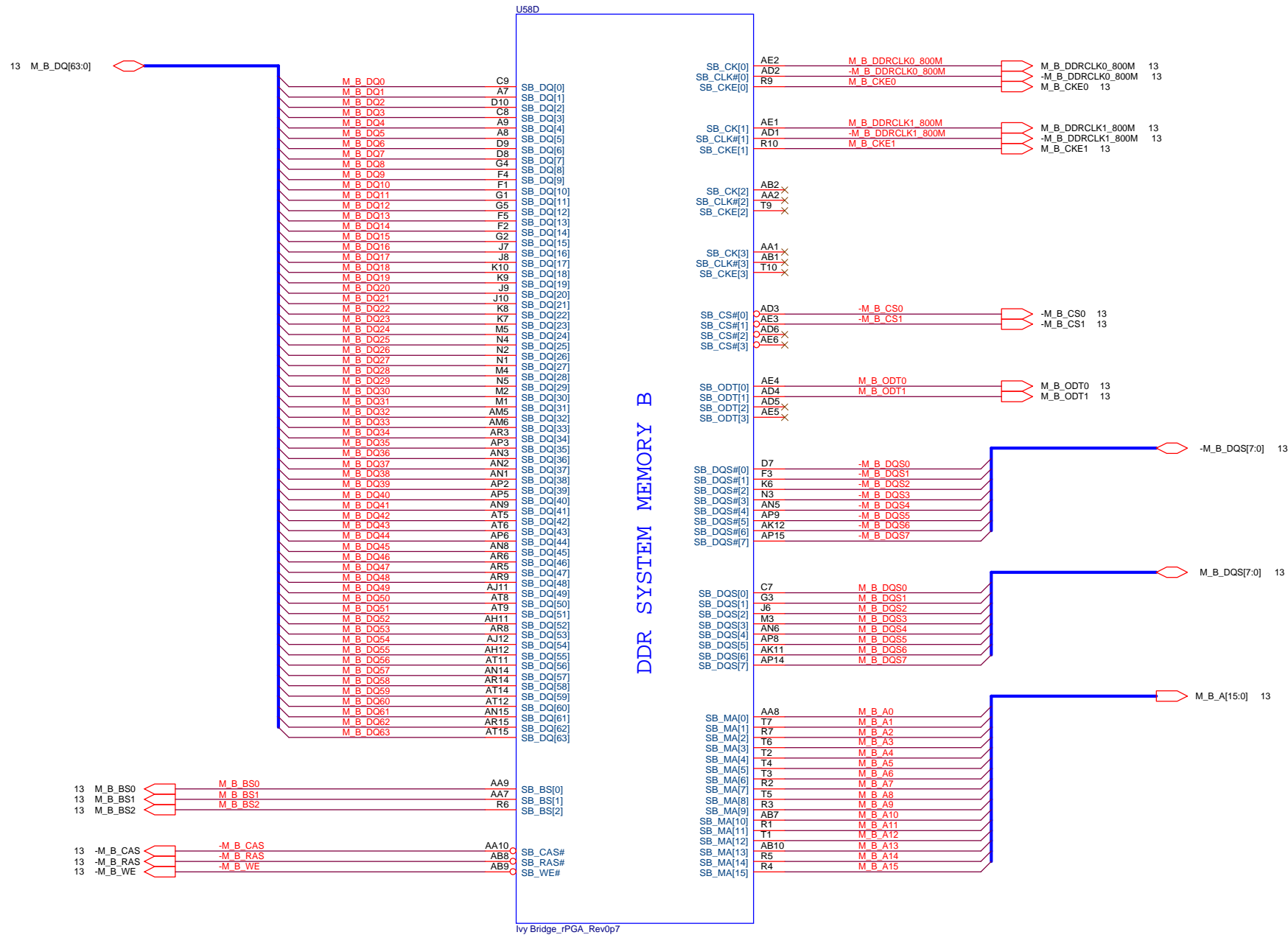


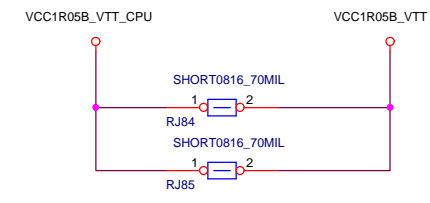
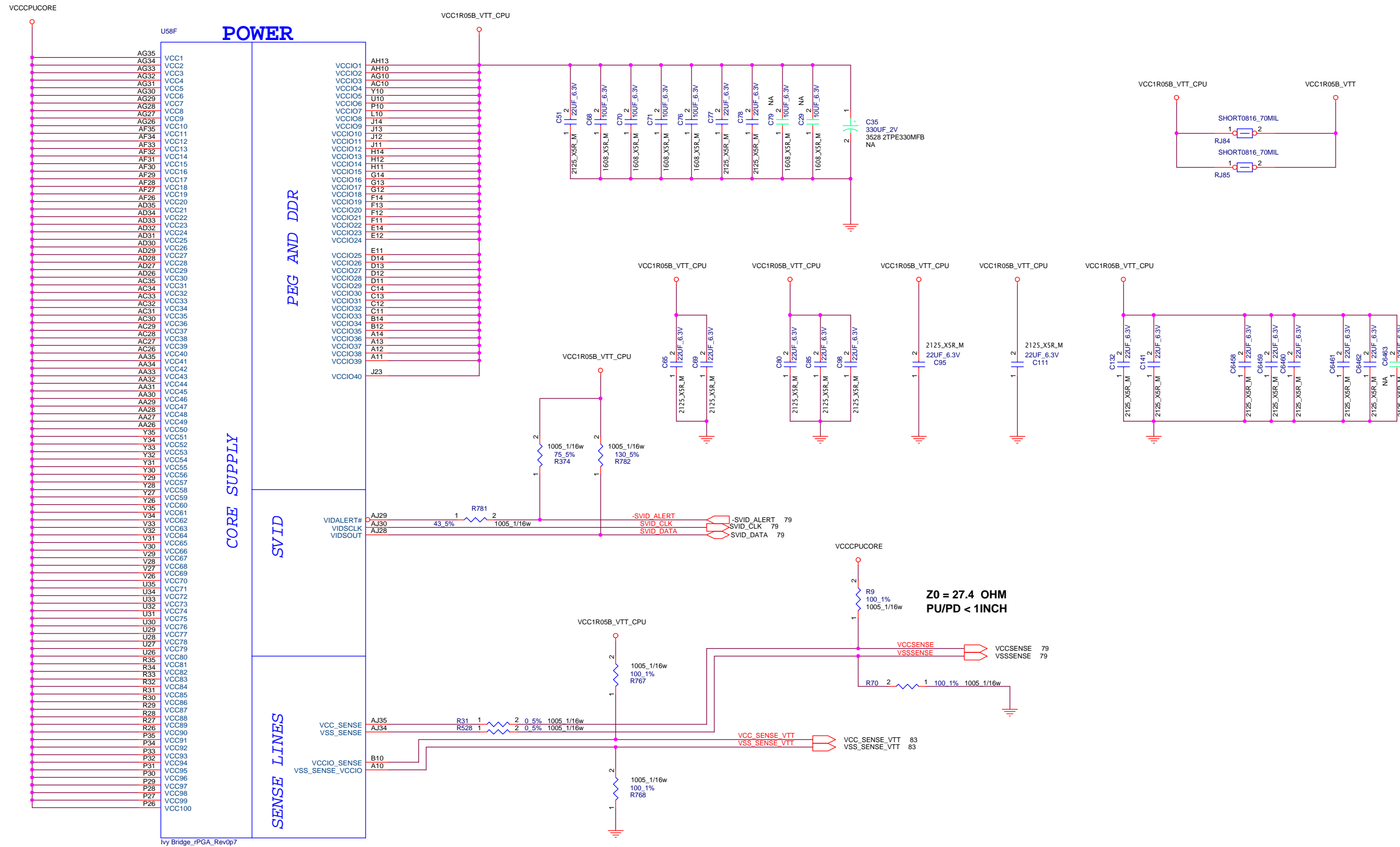
TABLE PROC_SELECT#(-PROC_IVB)

Sandy Bridge	High
Ivy Bridge	Low

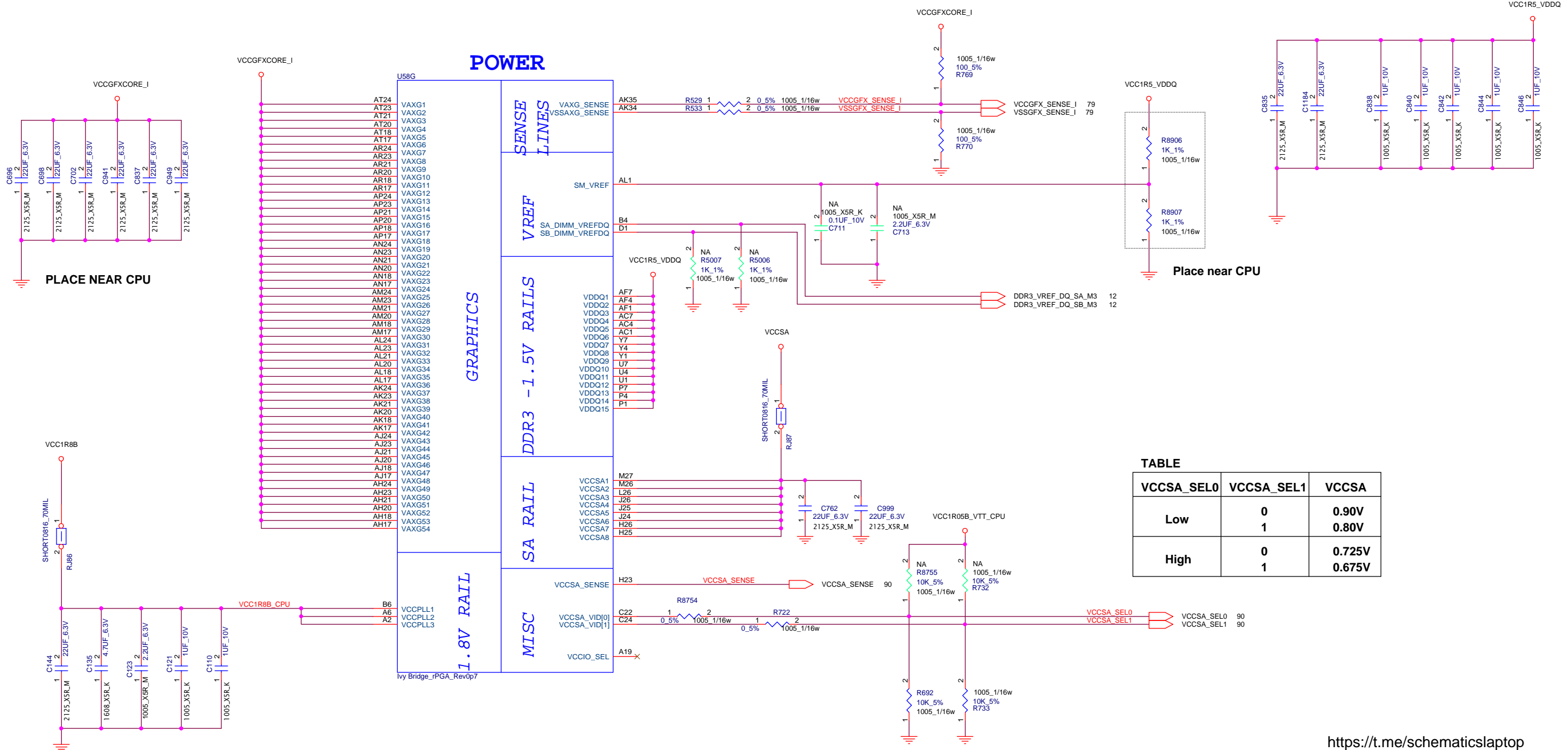






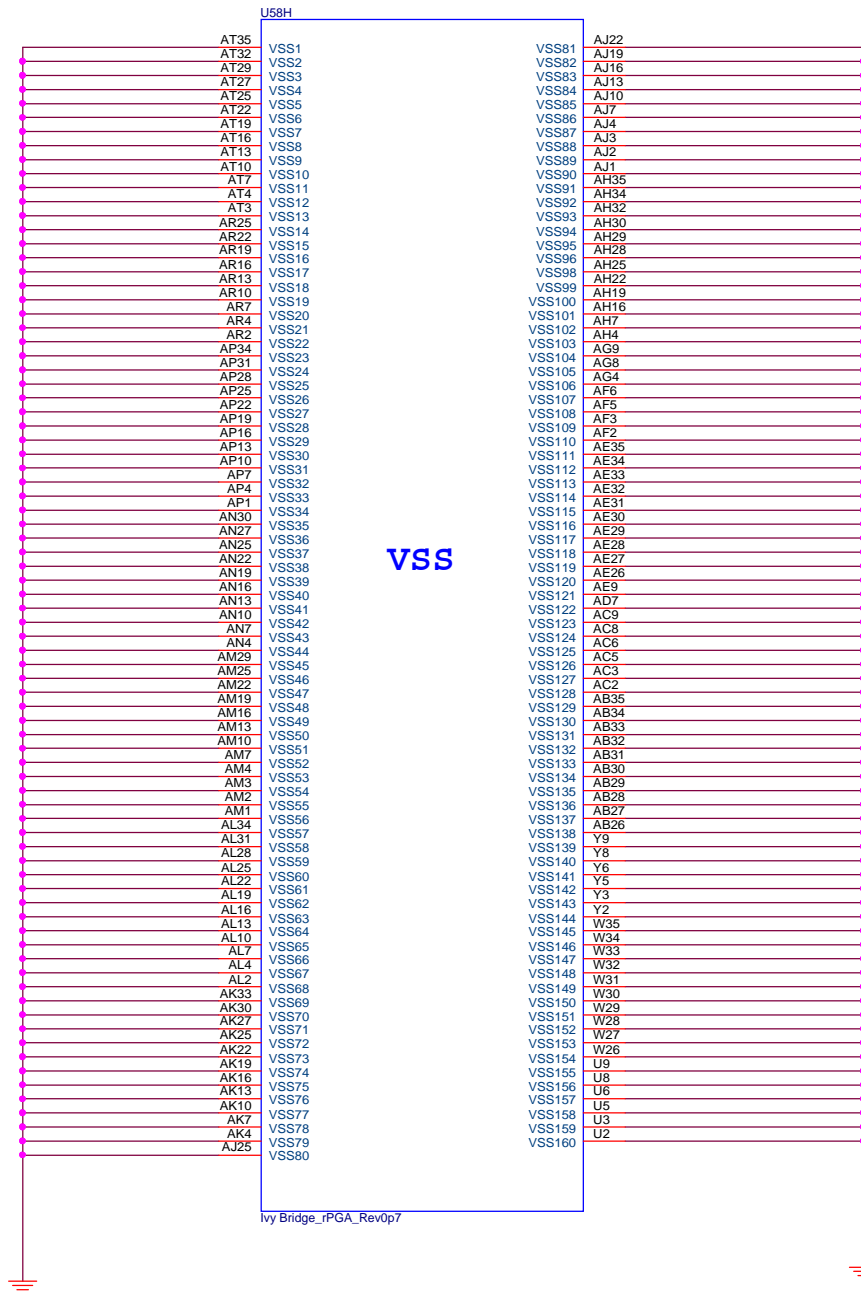
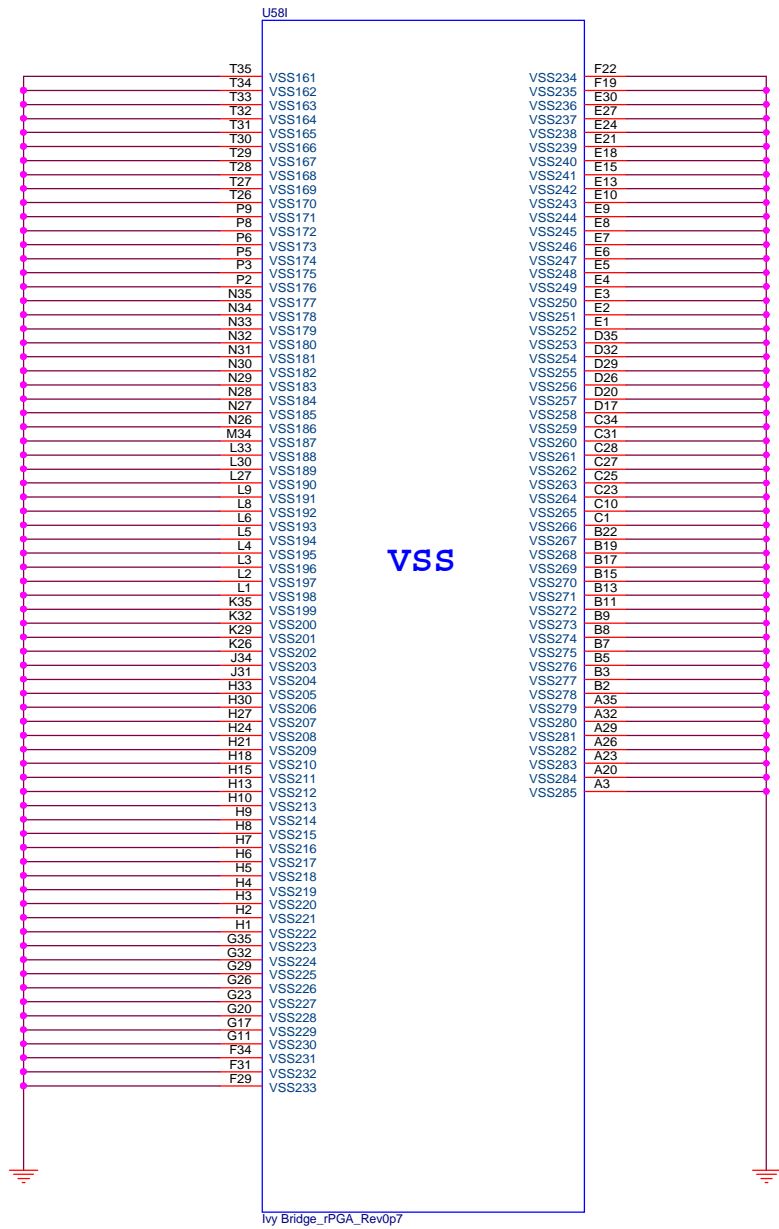


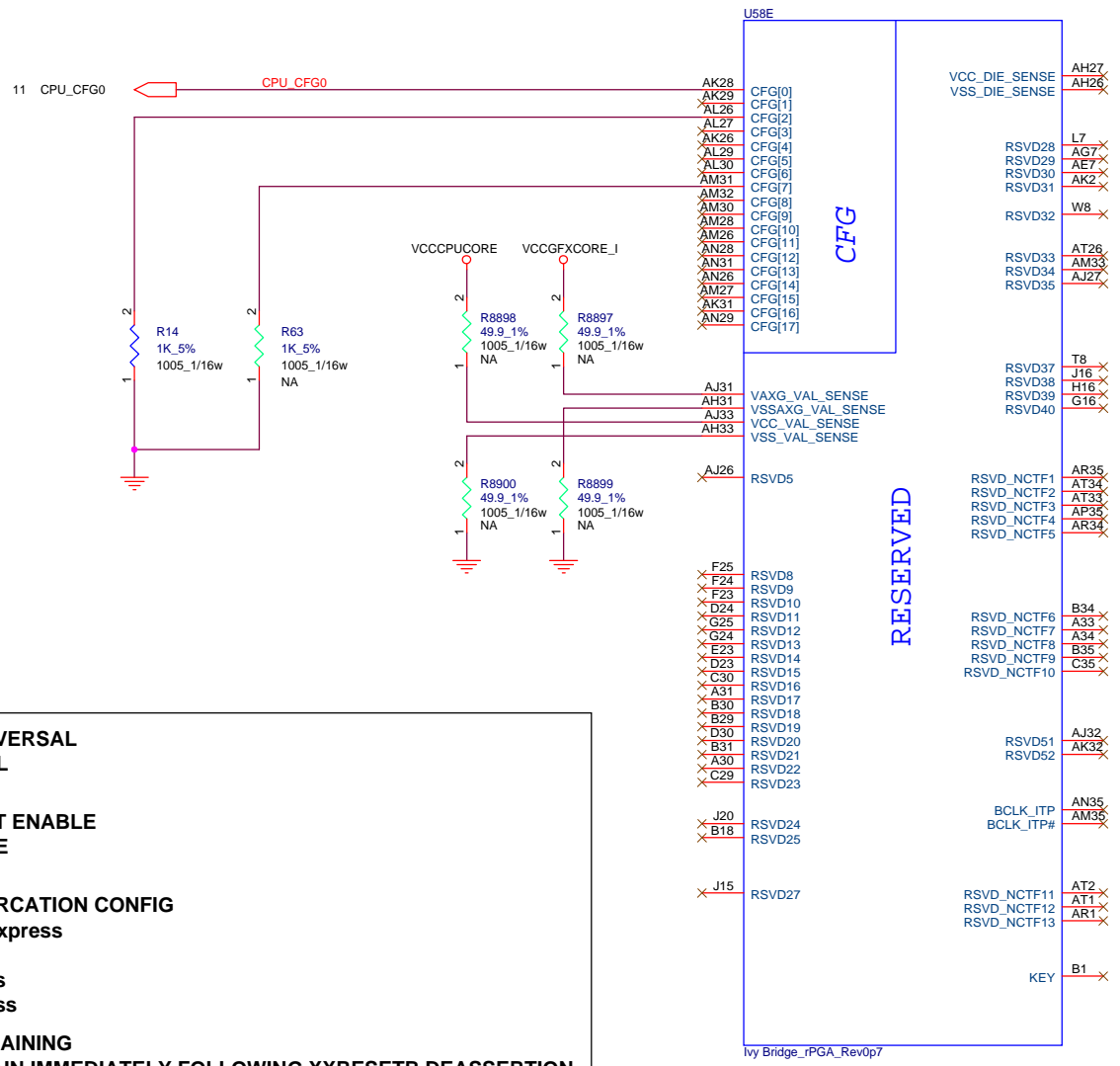
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TABLE

VCCSA_SELO	VCCSA_SEL1	VCCSA
Low	0	0.90V
	1	0.80V
High	0	0.725V
	1	0.675V





TABLE

CFG2 PEG LANE REVERSAL	
1	-NO ASM : NORMAL
0	-ASM : RESVERSE
CFG4 DISPLAY PORT ENABLE	
1	-NO ASM : DISABLE
0	-ASM : ENABLE
CFG[6 : 5] PEG BIFURCATION CONFIG	
00	= 1 x 8, 2 x 4 PCI Express
01	= reserved
10	= 2 x 8 PCI Express
11	= 1 x 16 PCI Express
CFG7 PEG DEFER TRAINING	
1	-NO ASM :PEG TRAIN IMMEDIATELY FOLLOWING XXRESETB DEASSERTION
0	-ASM : PEG WAIT FOR BIOS FOR TRAINING

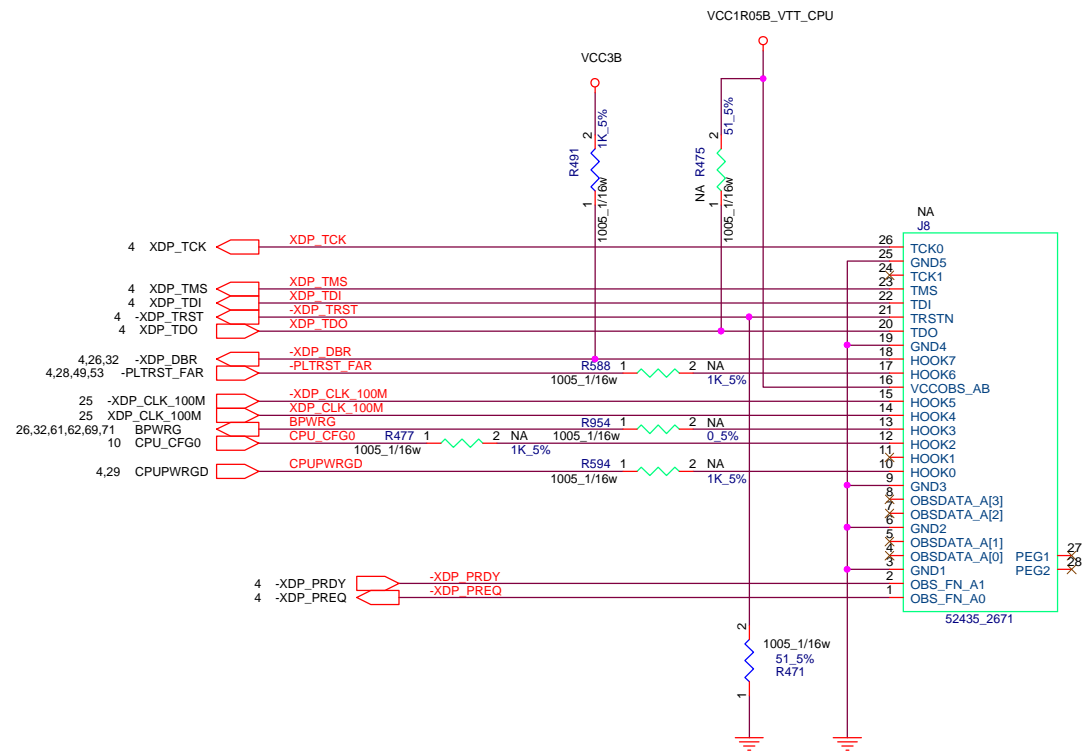


TABLE NOTE: J8 "ASM" FOR PDV/SDV ONLY.

SIGNAL	REF DES	ENABLE	DISABLE
TDO	R475	ASM	NO ASM
TRST#	R471	ASM	ASM
DBRST#	R491	ASM	ASM
RESET#	R588	ASM	NO ASM
CFG0	R477	ASM	NO ASM
PWRGD	R594	ASM	NO ASM
BPWRG	R954	ASM	NO ASM
	J8	ASM	NO ASM

LOGIC

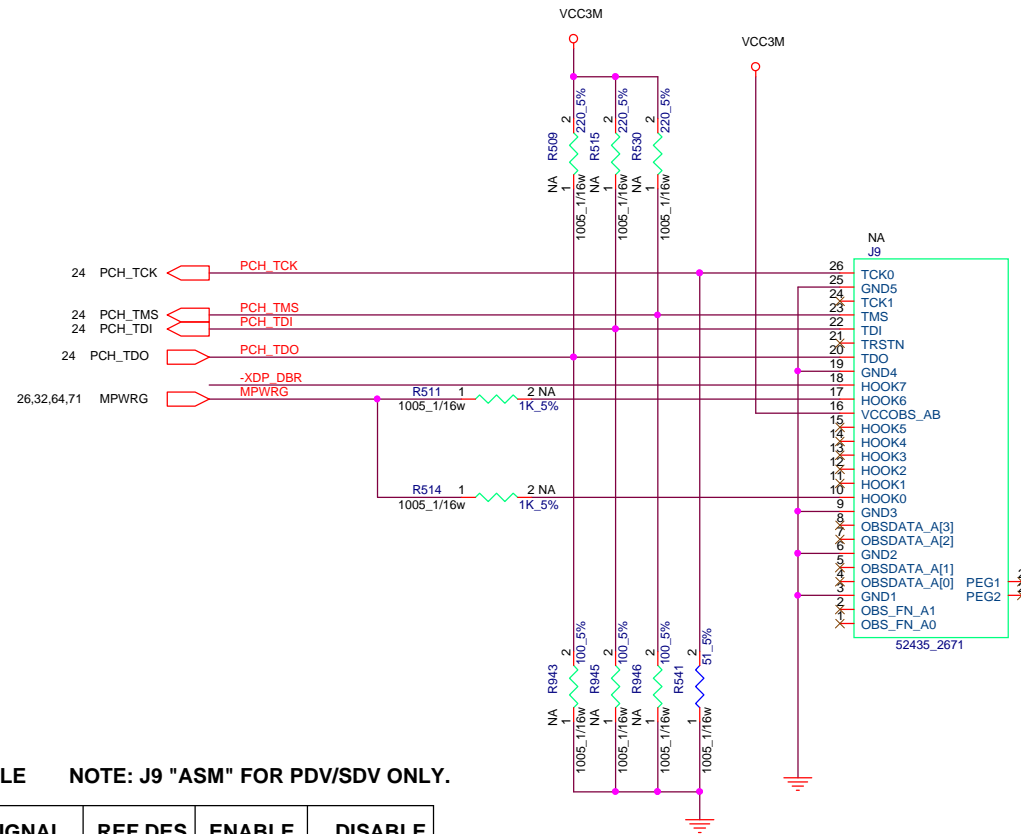
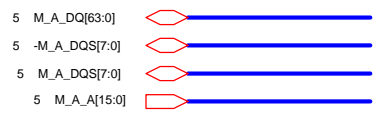


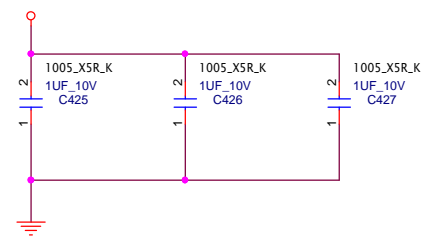
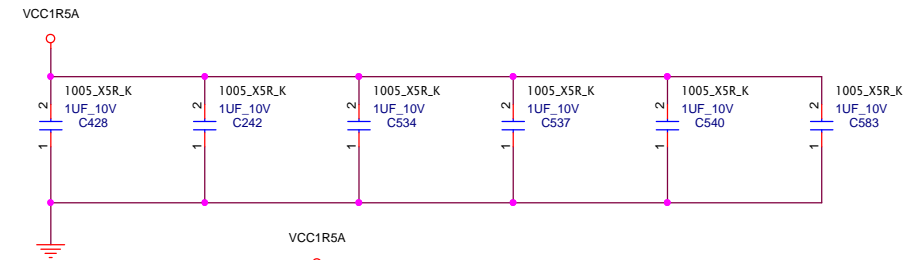
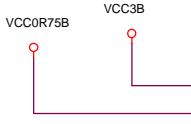
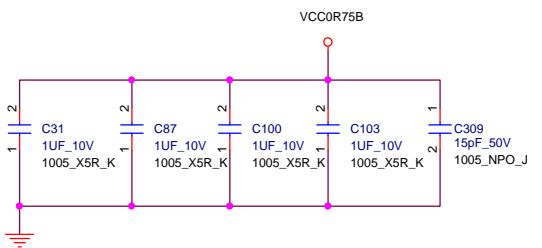
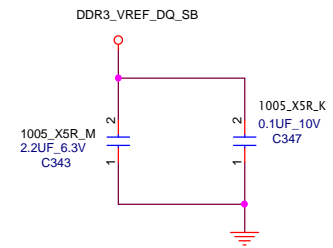
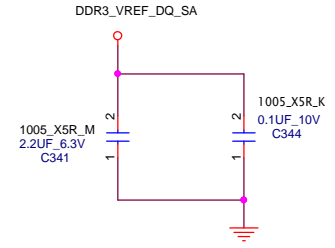
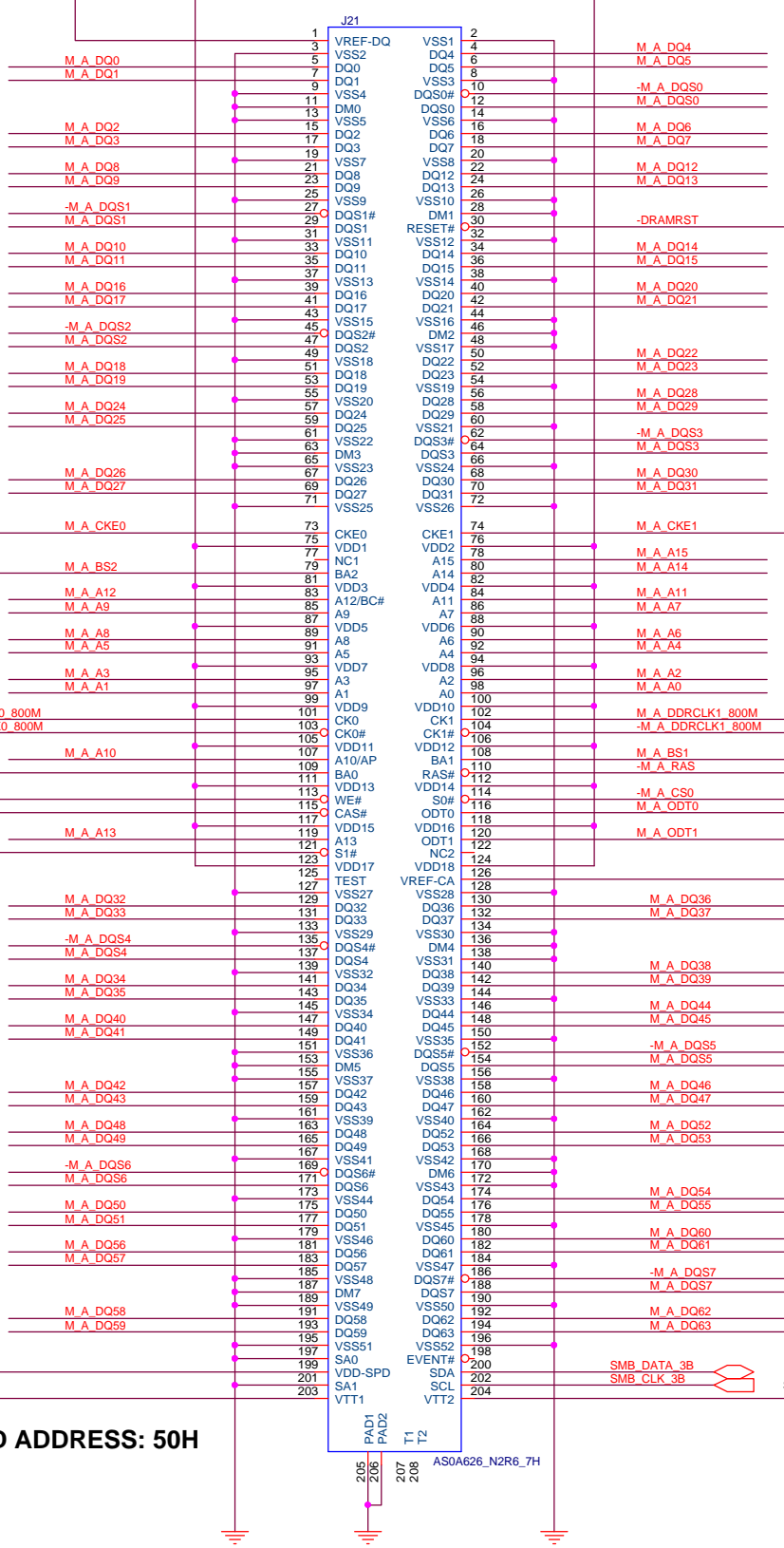
TABLE NOTE: J9 "ASM" FOR PDV/SDV ONLY.

SIGNAL	REF DES	ENABLE	DISABLE
TDO	R509	220	NO ASM
	R943	100	NO ASM
TMS	R530	220	NO ASM
	R946	100	NO ASM
TDI	R515	220	NO ASM
	R945	100	NO ASM
TCK	R541	51	51
MPWRG	R511	ASM	NO ASM
	R514	ASM	NO ASM
	J9	ASM	NO ASM

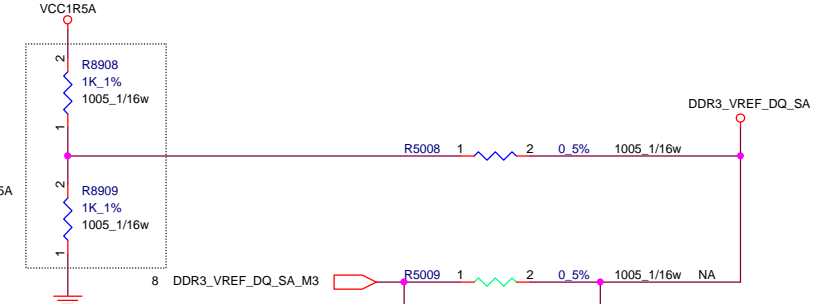
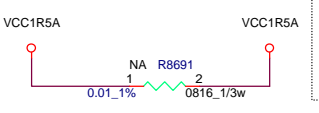
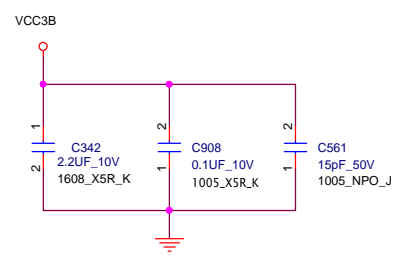
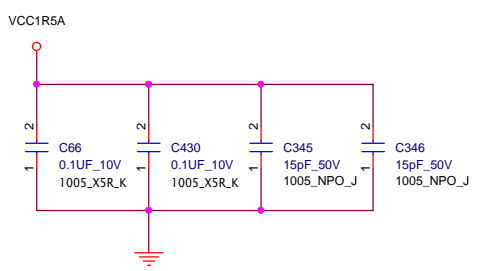
LOGIC



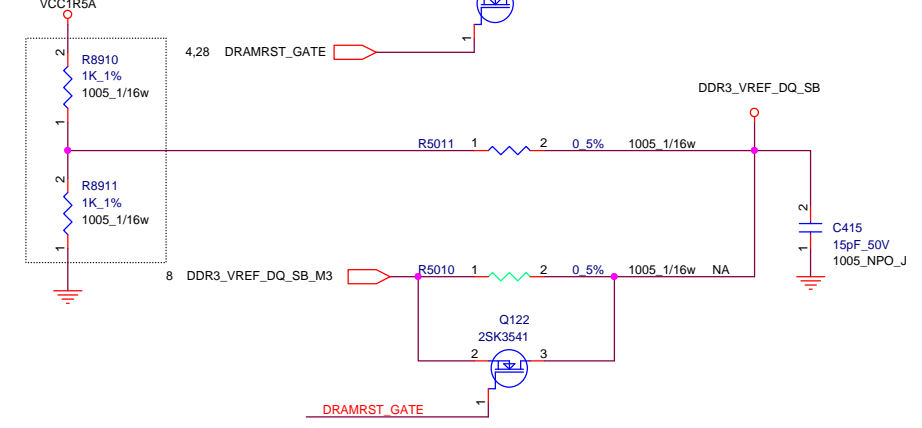
DDR3_VREF_DQ_SA VCC1R5A VCC1R5A



PLACE 1UF NEAR VCC1R5A PIN.

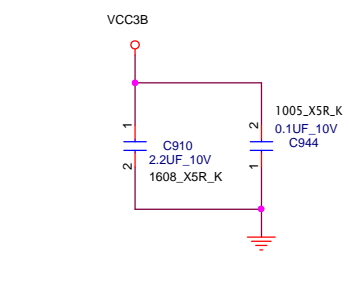
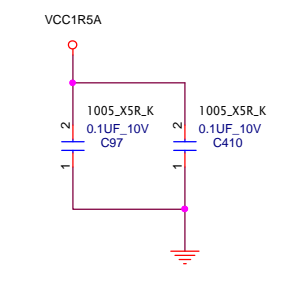
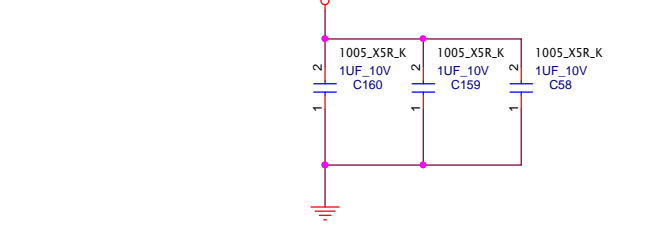
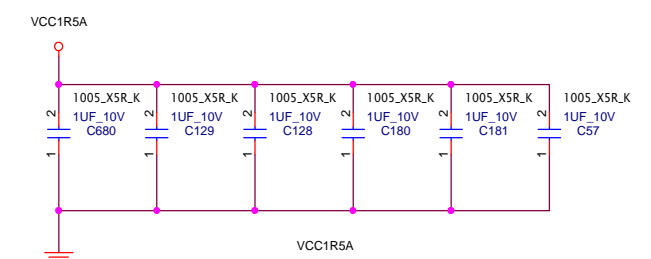
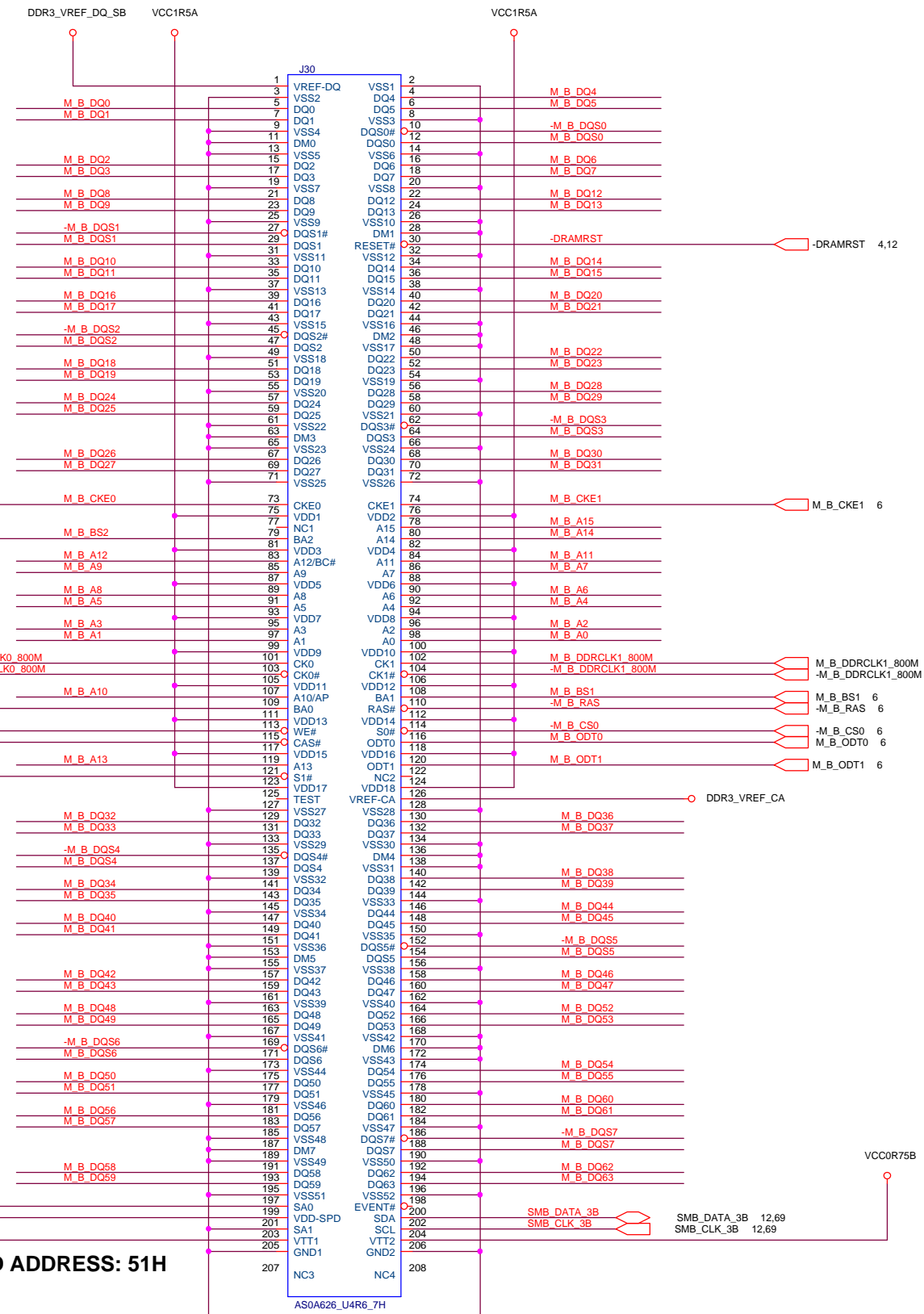
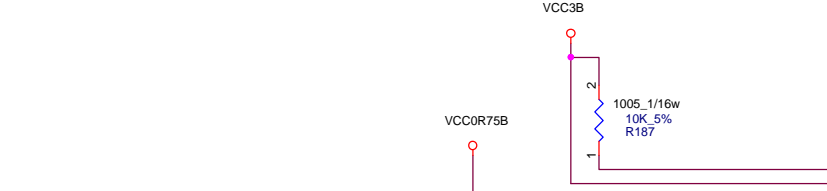
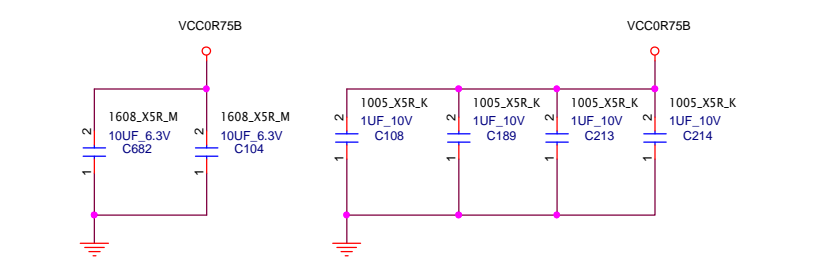
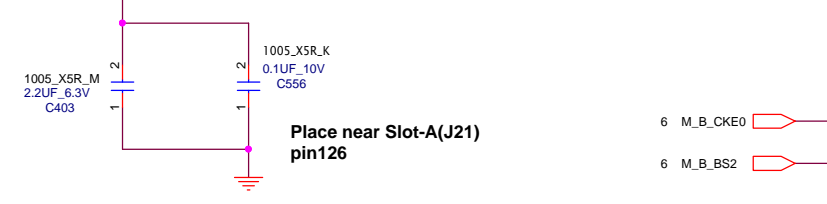
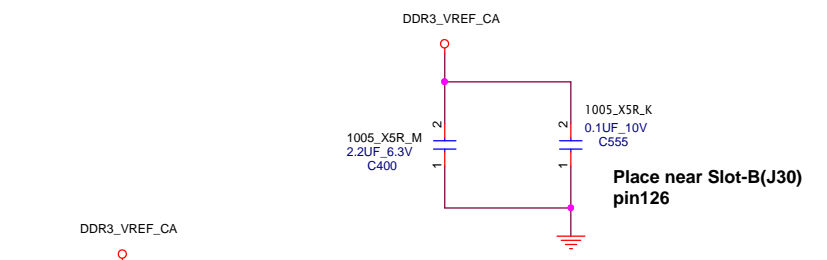
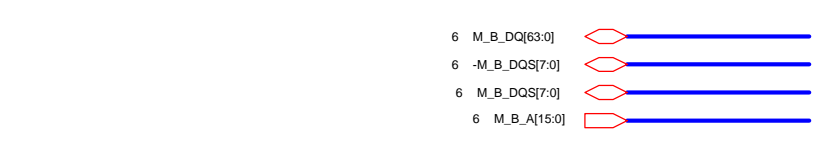


PLACE NEAR DIMM Slot

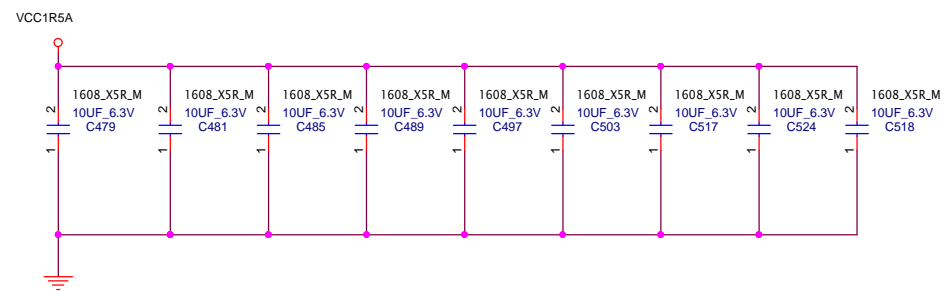
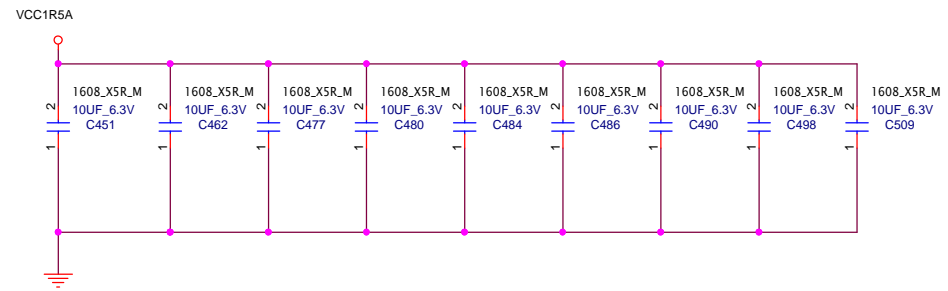
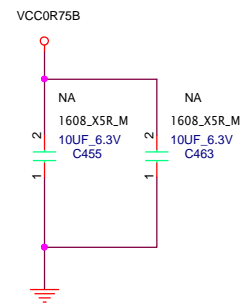


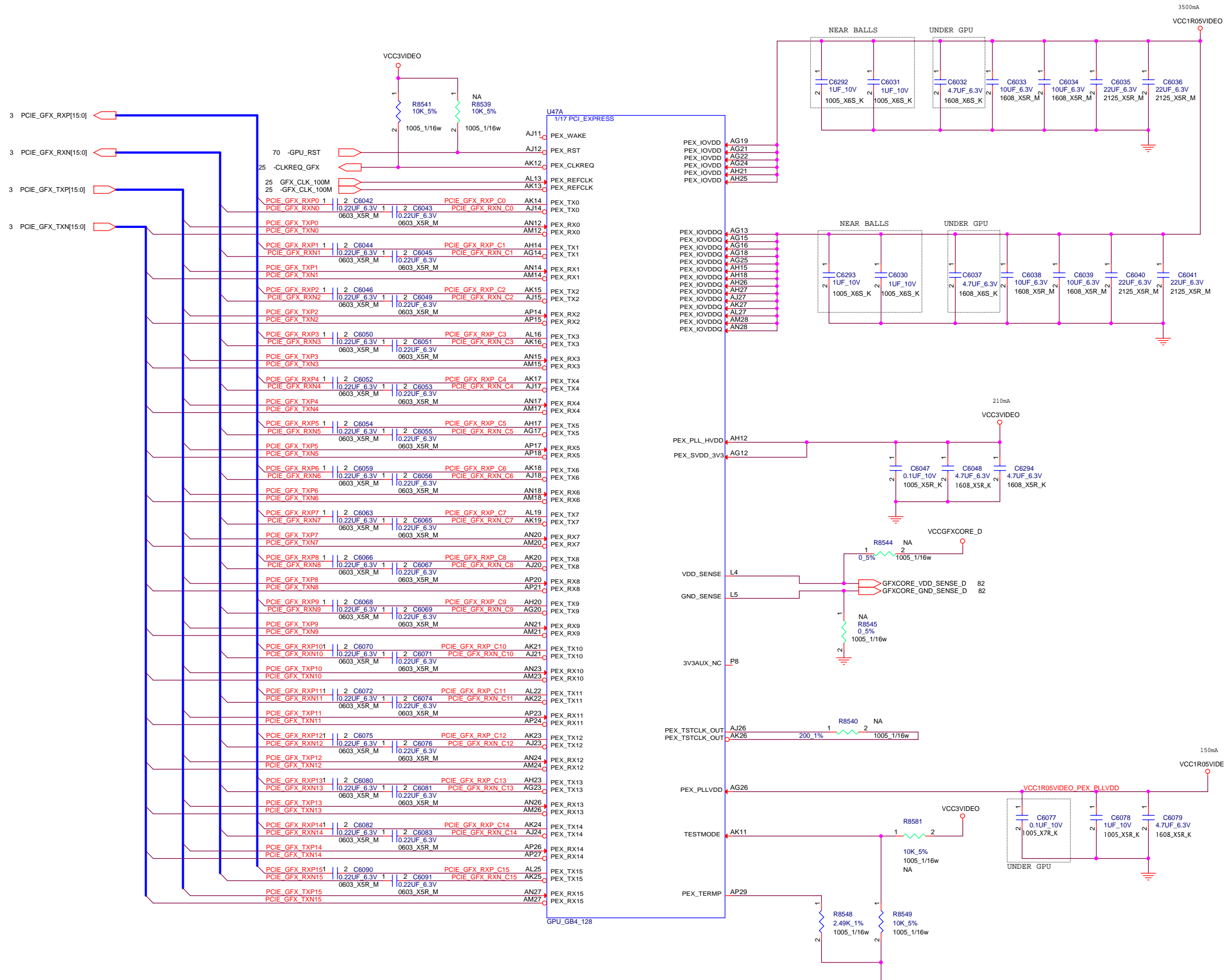
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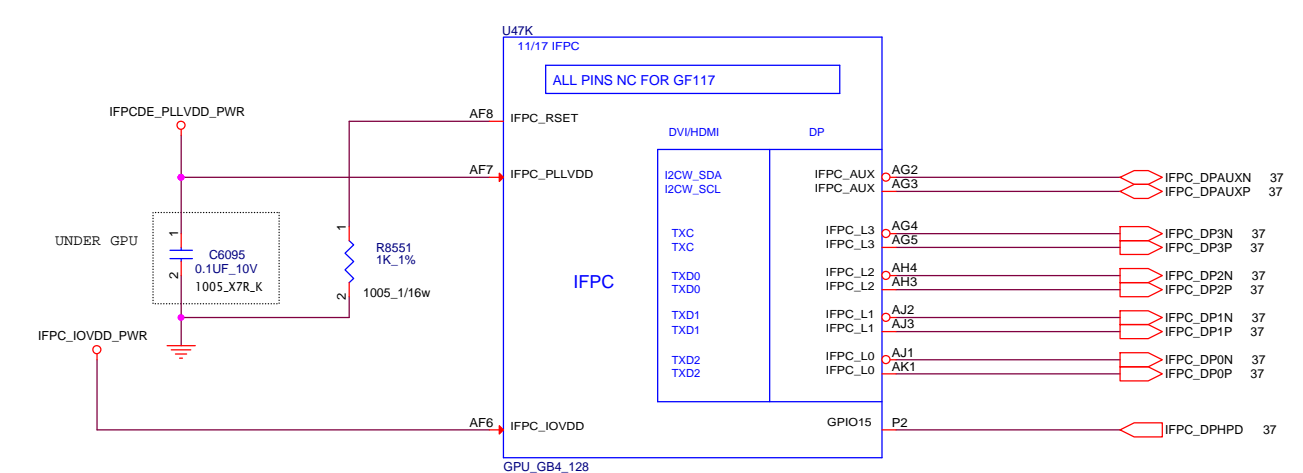
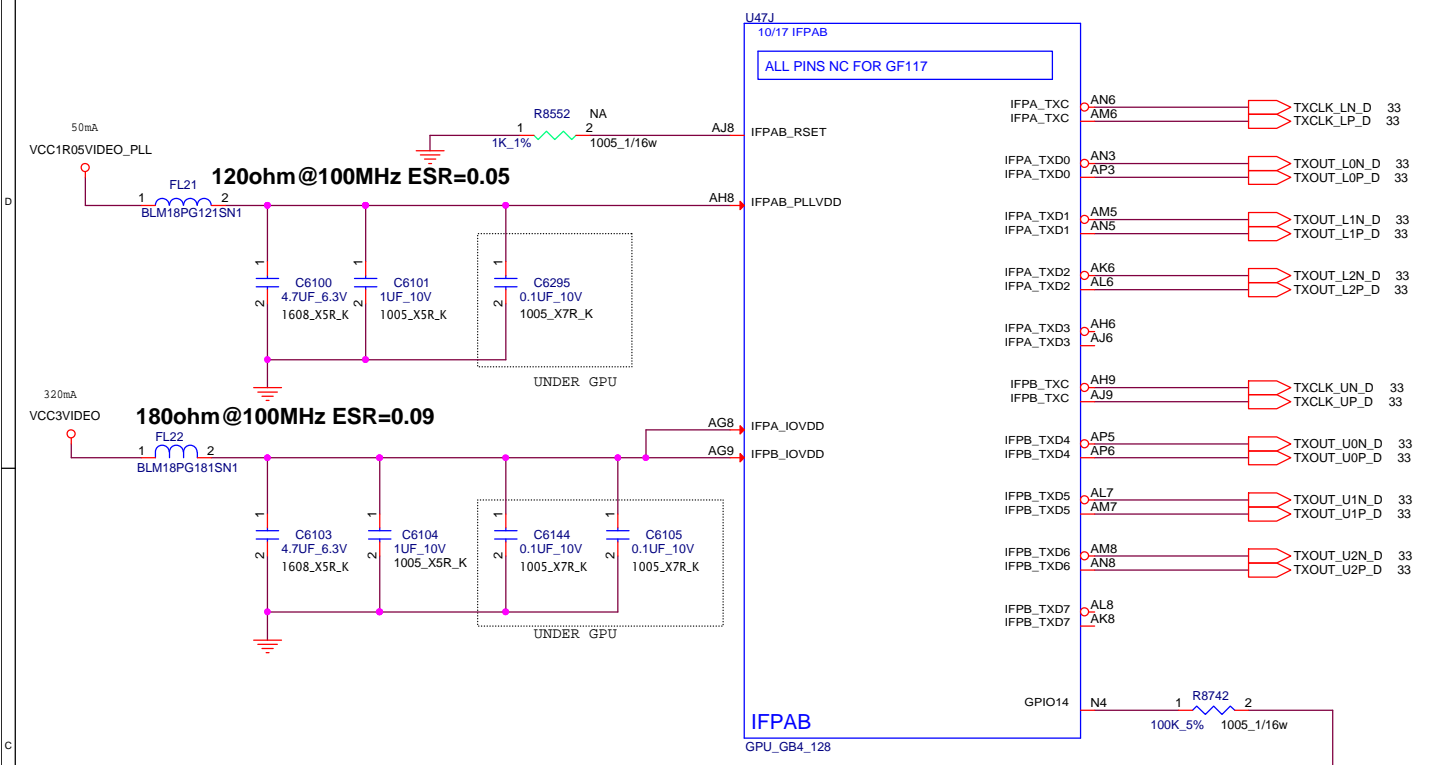
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SMB_CLK_3B 13.69



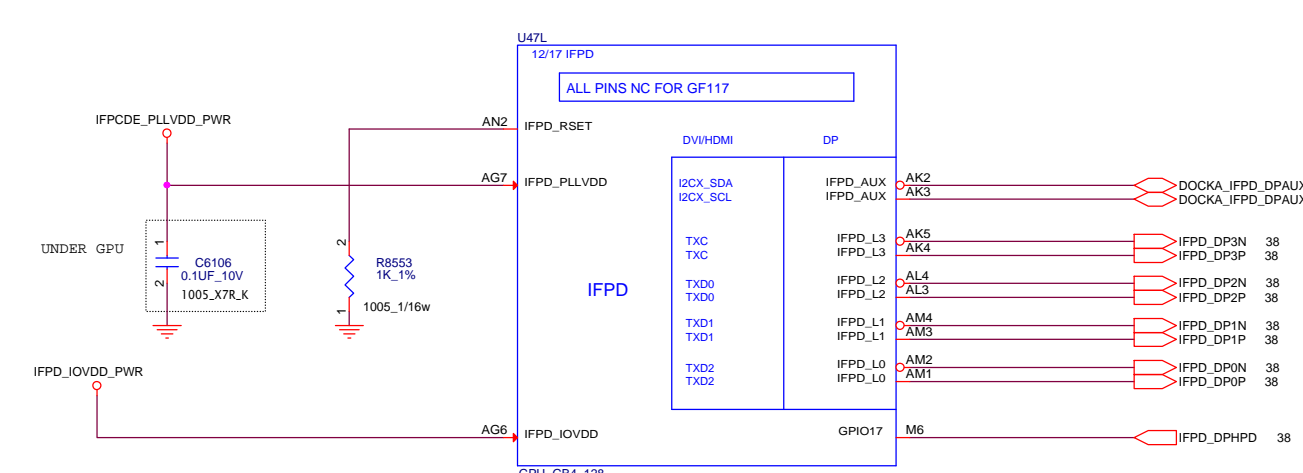
SPD ADDRESS: 51H



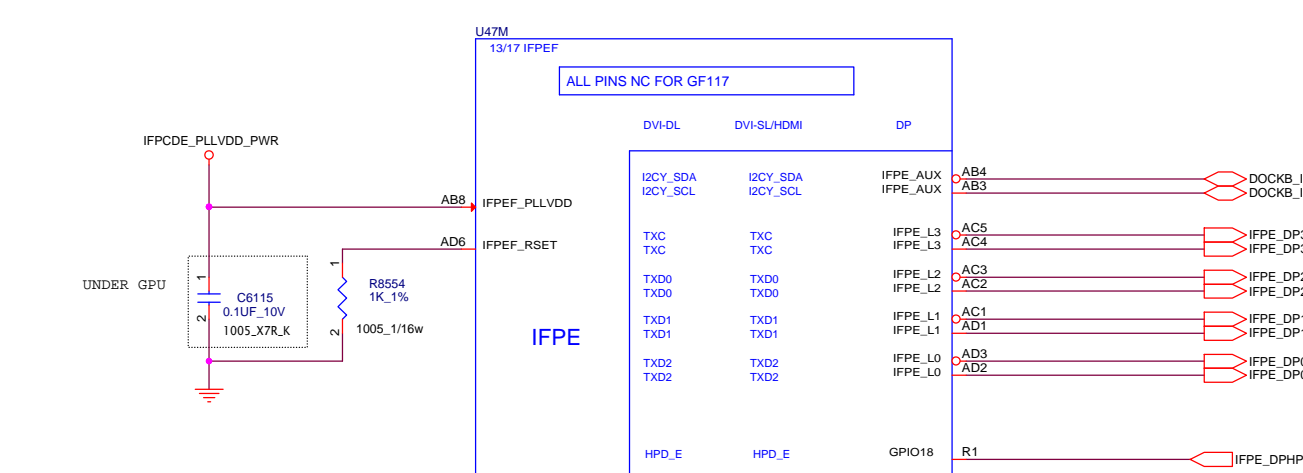
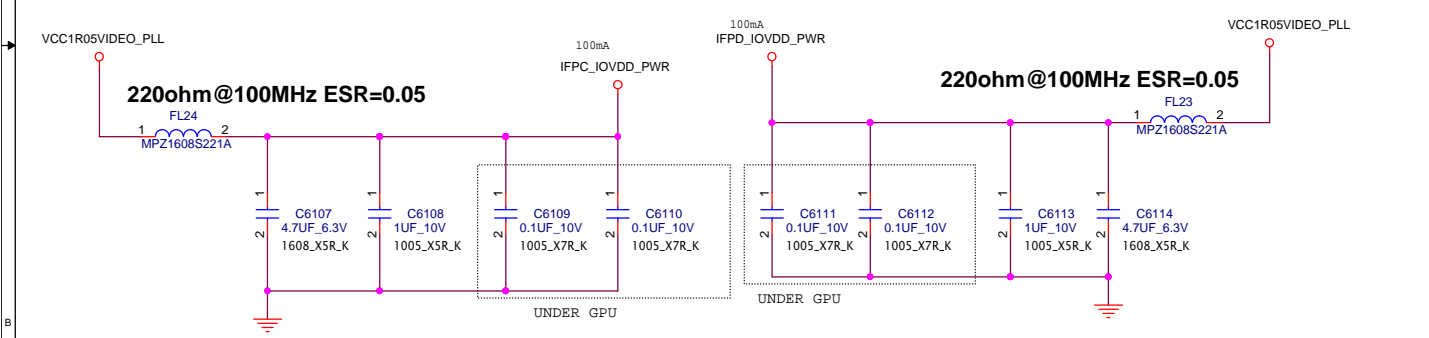




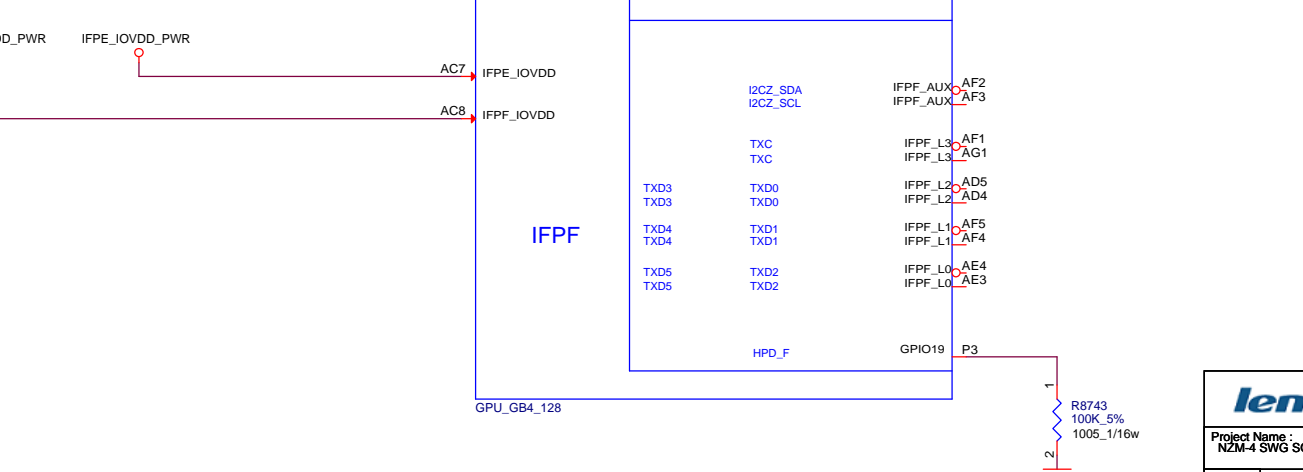
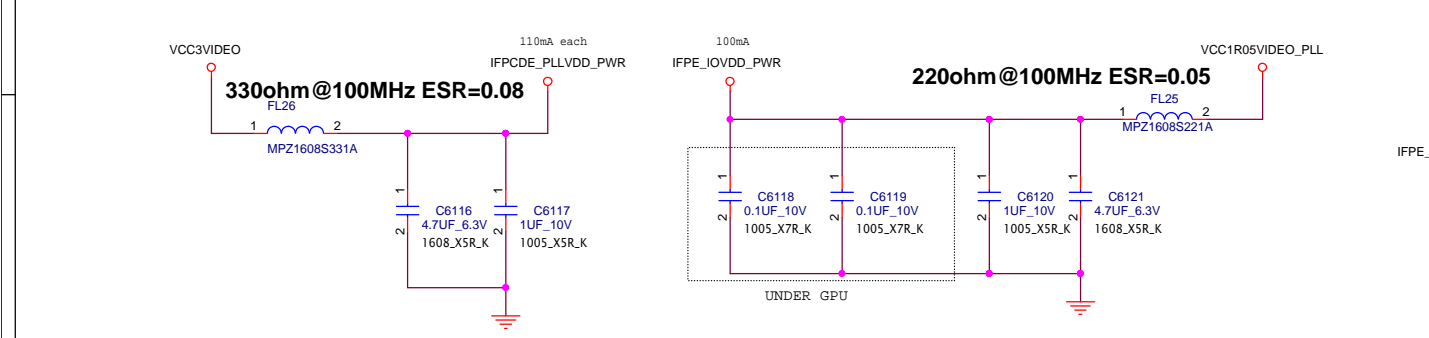
Dual Mode DP Link



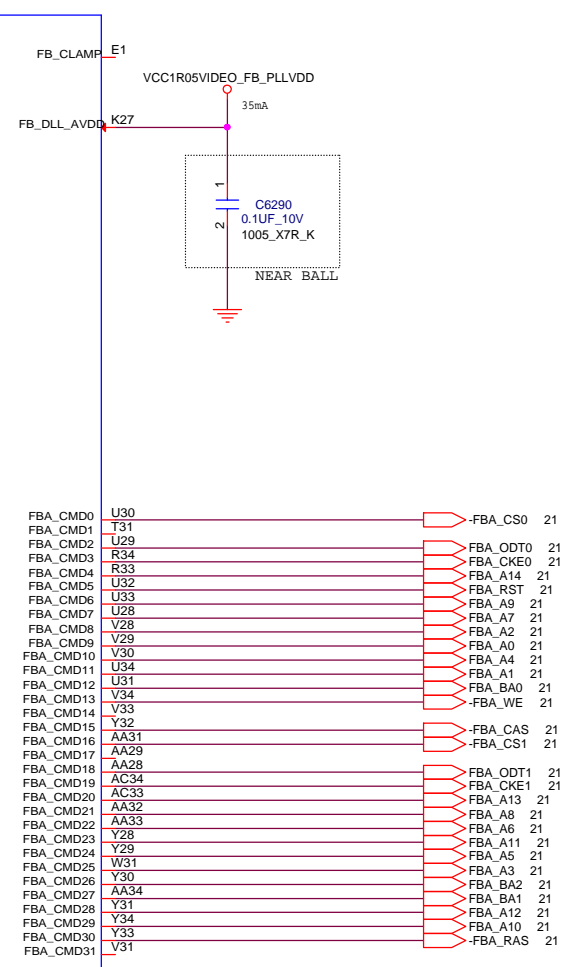
Dual Mode DP Link



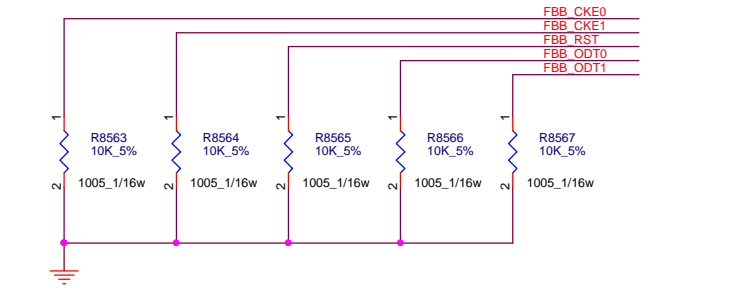
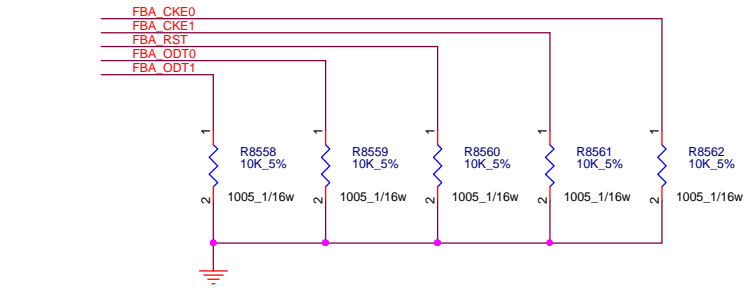
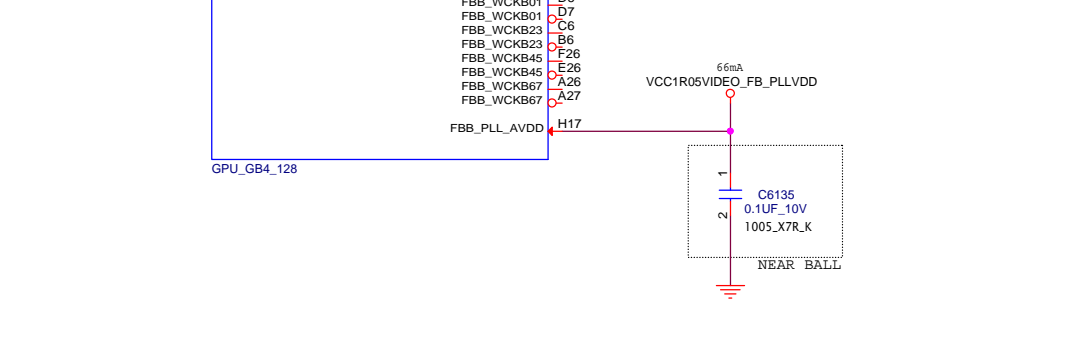
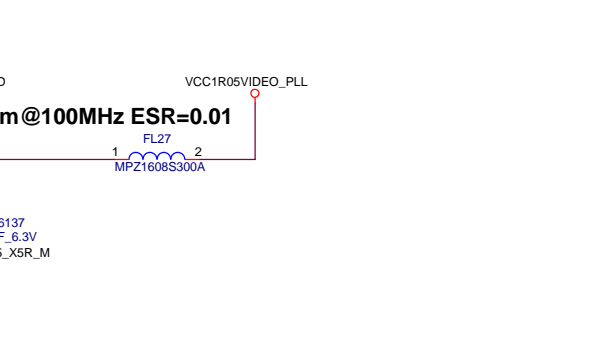
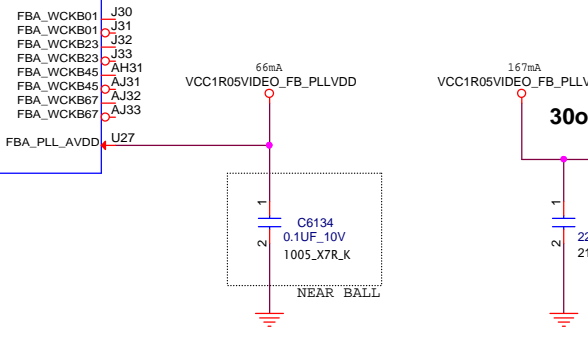
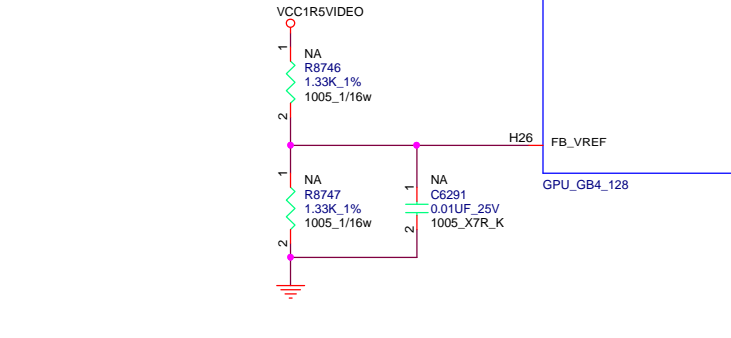
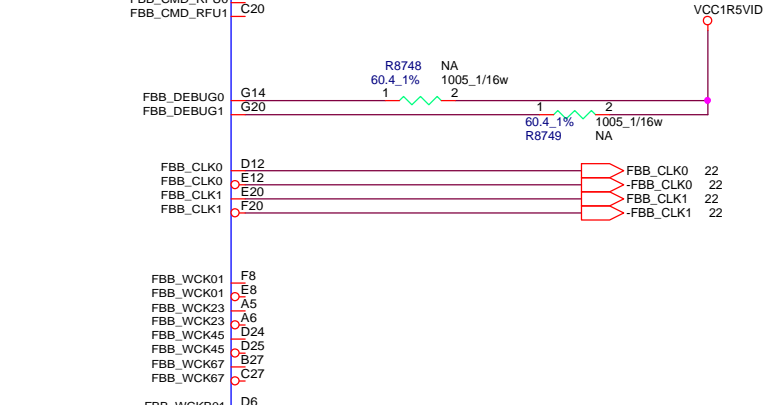
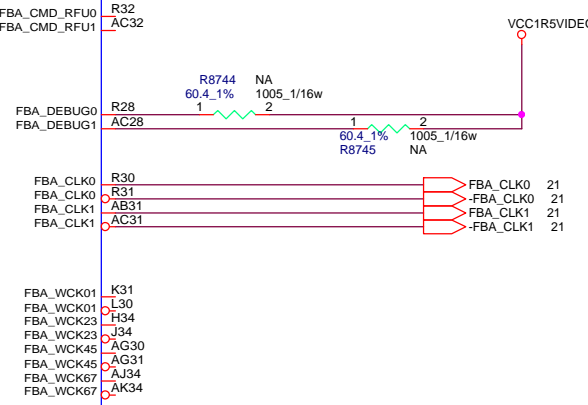
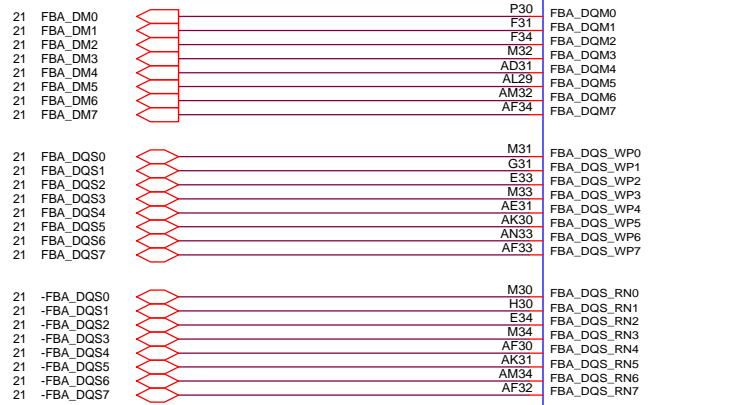
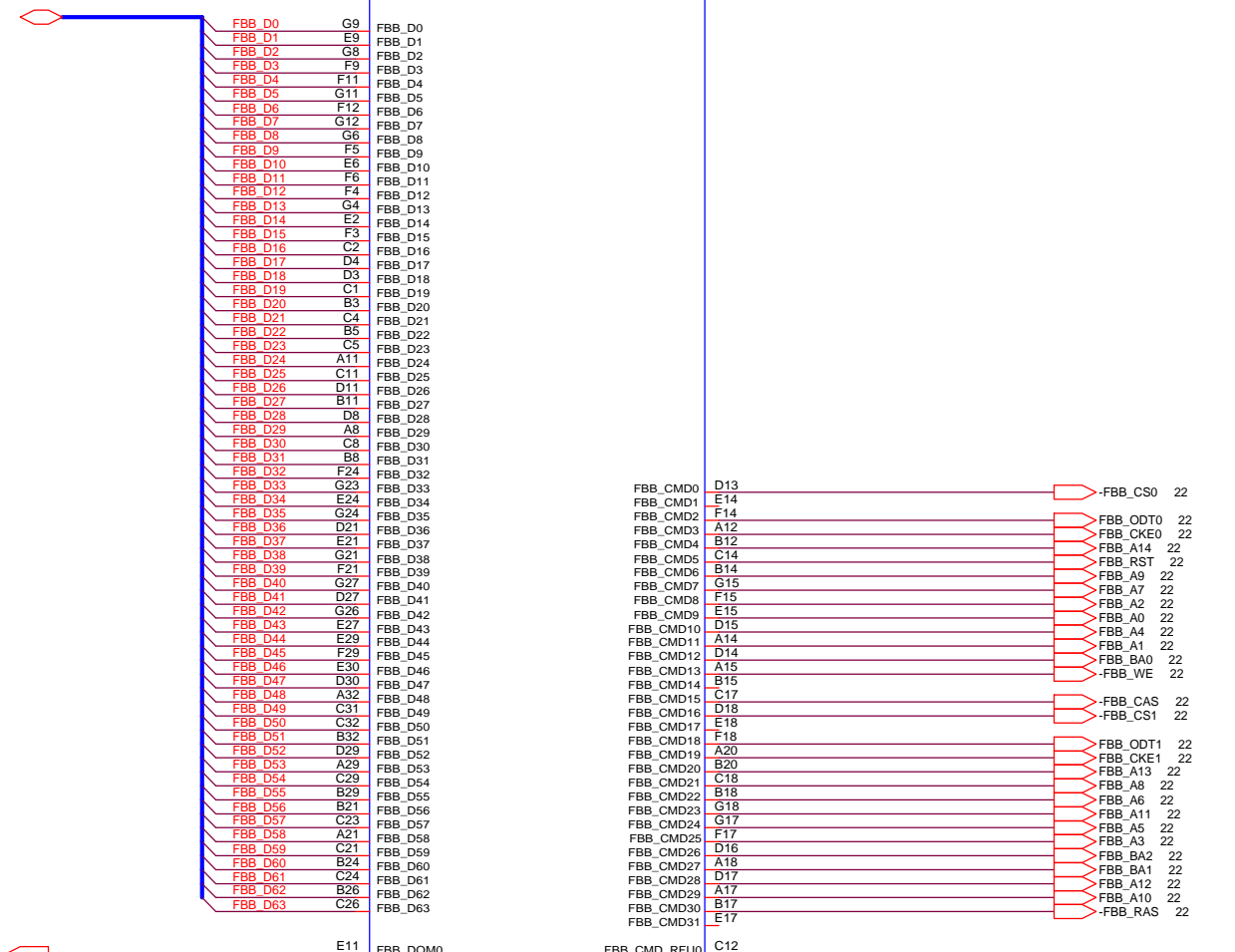
Dual Mode DP Link



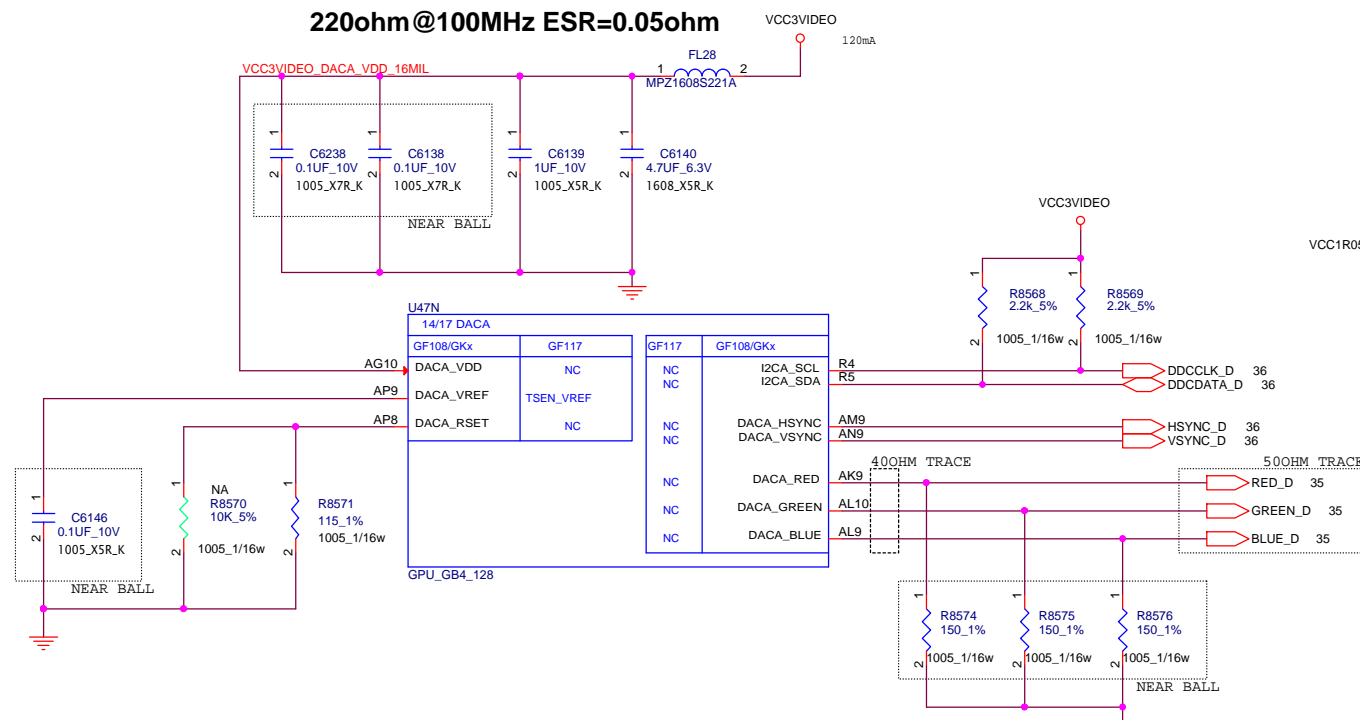
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FBA_D1	M29	FBA_D1
FBA_D2	L29	FBA_D2
FBA_D3	M28	FBA_D3
FBA_D4	N31	FBA_D4
FBA_D5	P29	FBA_D5
FBA_D6	R29	FBA_D6
FBA_D7	F28	FBA_D7
FBA_D8	J28	FBA_D8
FBA_D9	H29	FBA_D9
FBA_D10	J29	FBA_D10
FBA_D11	H28	FBA_D11
FBA_D12	G29	FBA_D12
FBA_D13	E31	FBA_D13
FBA_D14	E32	FBA_D14
FBA_D15	F30	FBA_D15
FBA_D16	C34	FBA_D16
FBA_D17	D32	FBA_D17
FBA_D18	B33	FBA_D18
FBA_D19	D19	FBA_D19
FBA_D20	C33	FBA_D20
FBA_D21	F33	FBA_D21
FBA_D22	F32	FBA_D22
FBA_D23	H33	FBA_D23
FBA_D24	F34	FBA_D24
FBA_D25	P32	FBA_D25
FBA_D26	P31	FBA_D26
FBA_D27	P33	FBA_D27
FBA_D28	L31	FBA_D28
FBA_D29	L34	FBA_D29
FBA_D30	L32	FBA_D30
FBA_D31	L33	FBA_D31
FBA_D32	AG28	FBA_D32
FBA_D33	AF29	FBA_D33
FBA_D34	AG29	FBA_D34
FBA_D35	U29	FBA_D35
FBA_D36	AD30	FBA_D36
FBA_D37	AD29	FBA_D37
FBA_D38	AC29	FBA_D38
FBA_D39	AD28	FBA_D39
FBA_D40	AJ29	FBA_D40
FBA_D41	AK29	FBA_D41
FBA_D42	AJ30	FBA_D42
FBA_D43	AK28	FBA_D43
FBA_D44	AM29	FBA_D44
FBA_D45	AM31	FBA_D45
FBA_D46	AN29	FBA_D46
FBA_D47	AM30	FBA_D47
FBA_D48	AN31	FBA_D48
FBA_D49	AN32	FBA_D49
FBA_D50	AP30	FBA_D50
FBA_D51	AF32	FBA_D51
FBA_D52	AM33	FBA_D52
FBA_D53	AL31	FBA_D53
FBA_D54	AK33	FBA_D54
FBA_D55	AK32	FBA_D55
FBA_D56	AD34	FBA_D56
FBA_D57	AD32	FBA_D57
FBA_D58	AC30	FBA_D58
FBA_D59	AD33	FBA_D59
FBA_D60	AF31	FBA_D60
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FBA_D62	AG32	FBA_D62
FBA_D63	AG33	FBA_D63



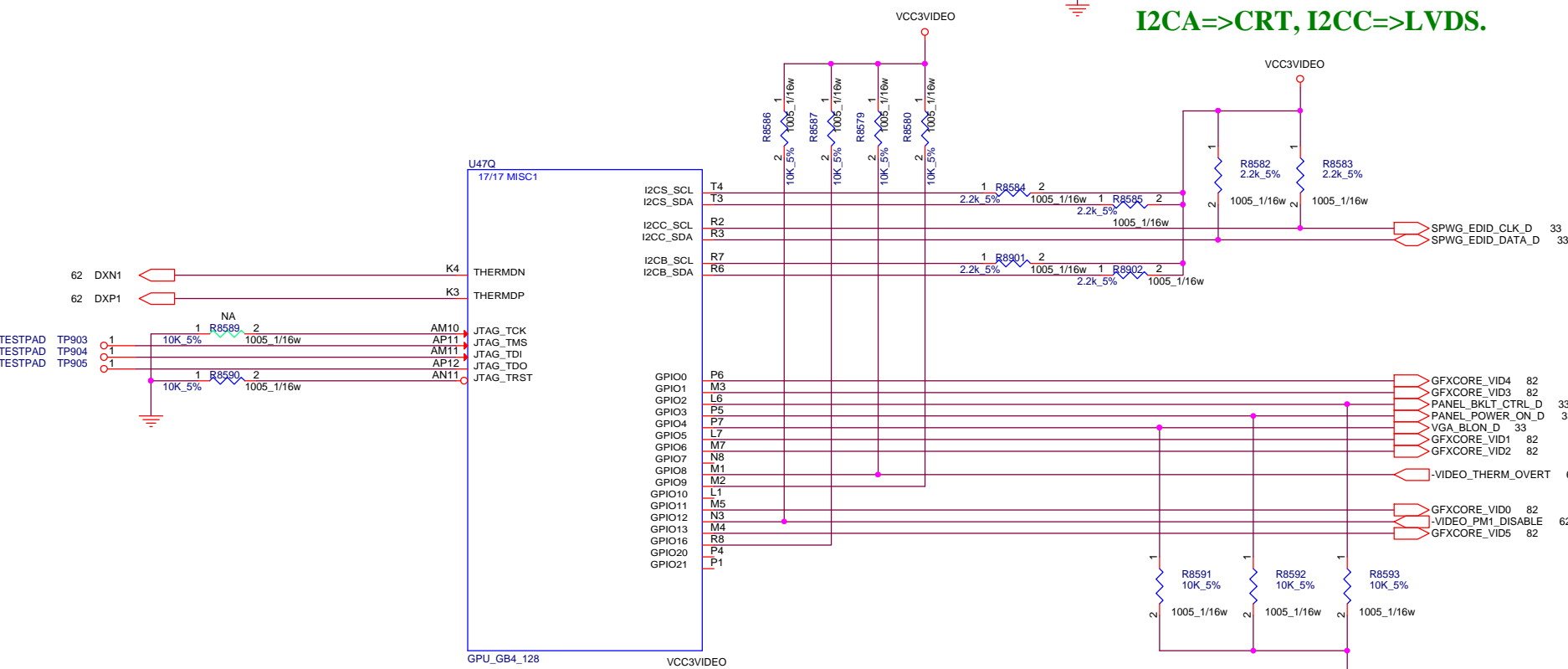
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FBB_D2	G8	FBB_D2
FBB_D3	F9	FBB_D3
FBB_D4	F11	FBB_D4
FBB_D5	G11	FBB_D5
FBB_D6	F12	FBB_D6
FBB_D7	G12	FBB_D7
FBB_D8	G6	FBB_D8
FBB_D9	F5	FBB_D9
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FBB_D16	C2	FBB_D16
FBB_D17	D4	FBB_D17
FBB_D18	D3	FBB_D18
FBB_D19	C1	FBB_D19
FBB_D20	B3	FBB_D20
FBB_D21	C4	FBB_D21
FBB_D22	B5	FBB_D22
FBB_D23	C5	FBB_D23
FBB_D24	A11	FBB_D24
FBB_D25	C11	FBB_D25
FBB_D26	D11	FBB_D26
FBB_D27	B11	FBB_D27
FBB_D28	D8	FBB_D28
FBB_D29	A8	FBB_D29
FBB_D30	C8	FBB_D30
FBB_D31	B8	FBB_D31
FBB_D32	F24	FBB_D32
FBB_D33	G23	FBB_D33
FBB_D34	E24	FBB_D34
FBB_D35	G24	FBB_D35
FBB_D36	D21	FBB_D36
FBB_D37	E21	FBB_D37
FBB_D38	G21	FBB_D38
FBB_D39	F21	FBB_D39
FBB_D40	G27	FBB_D40
FBB_D41	D27	FBB_D41
FBB_D42	G26	FBB_D42
FBB_D43	E27	FBB_D43
FBB_D44	E29	FBB_D44
FBB_D45	F29	FBB_D45
FBB_D46	E30	FBB_D46
FBB_D47	D30	FBB_D47
FBB_D48	A32	FBB_D48
FBB_D49	C31	FBB_D49
FBB_D50	C32	FBB_D50
FBB_D51	B32	FBB_D51
FBB_D52	D29	FBB_D52
FBB_D53	A29	FBB_D53
FBB_D54	C29	FBB_D54
FBB_D55	B29	FBB_D55
FBB_D56	B21	FBB_D56
FBB_D57	C23	FBB_D57
FBB_D58	A21	FBB_D58
FBB_D59	C21	FBB_D59
FBB_D60	B24	FBB_D60
FBB_D61	C24	FBB_D61
FBB_D62	B26	FBB_D62
FBB_D63	C26	FBB_D63



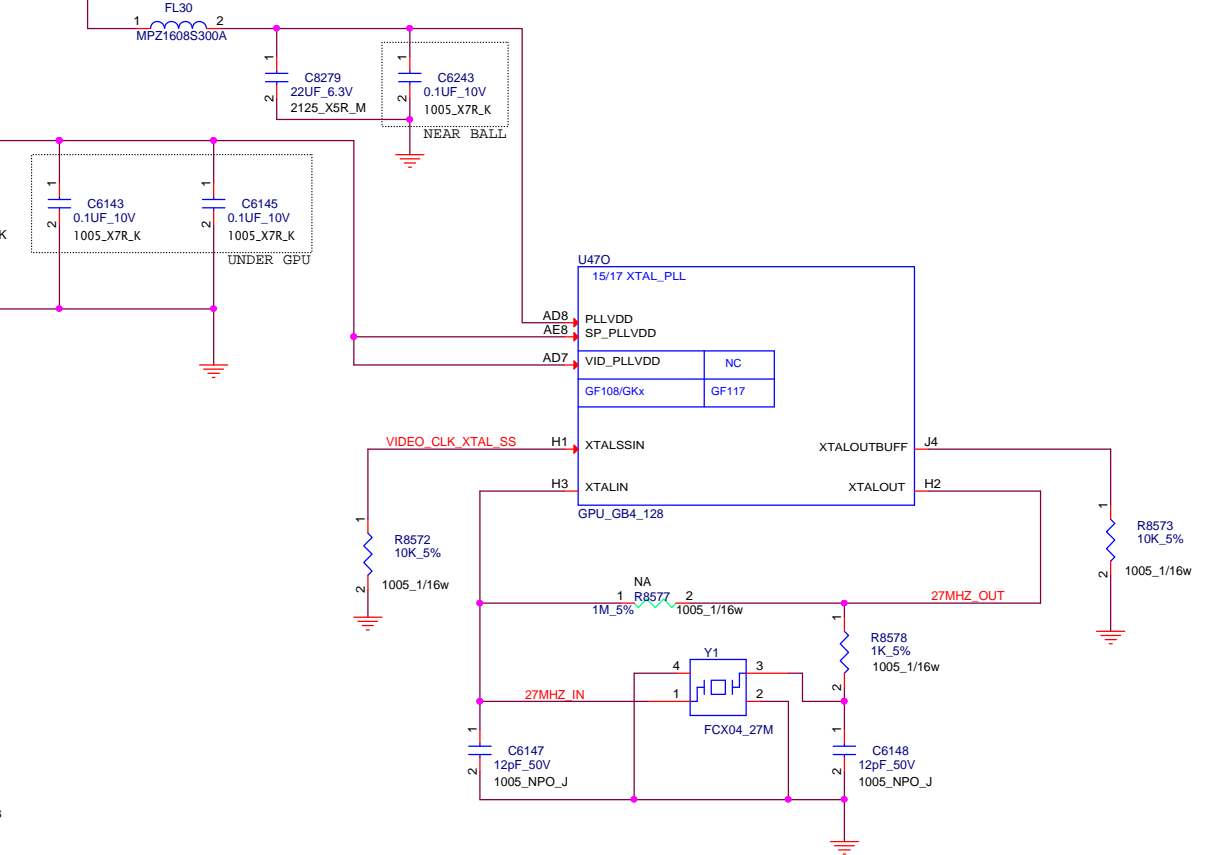
220ohm@100MHz ESR=0.05ohm



I2CA=>CRT, I2CC=>LVDS.



30ohm@100MHz ESR=0.01

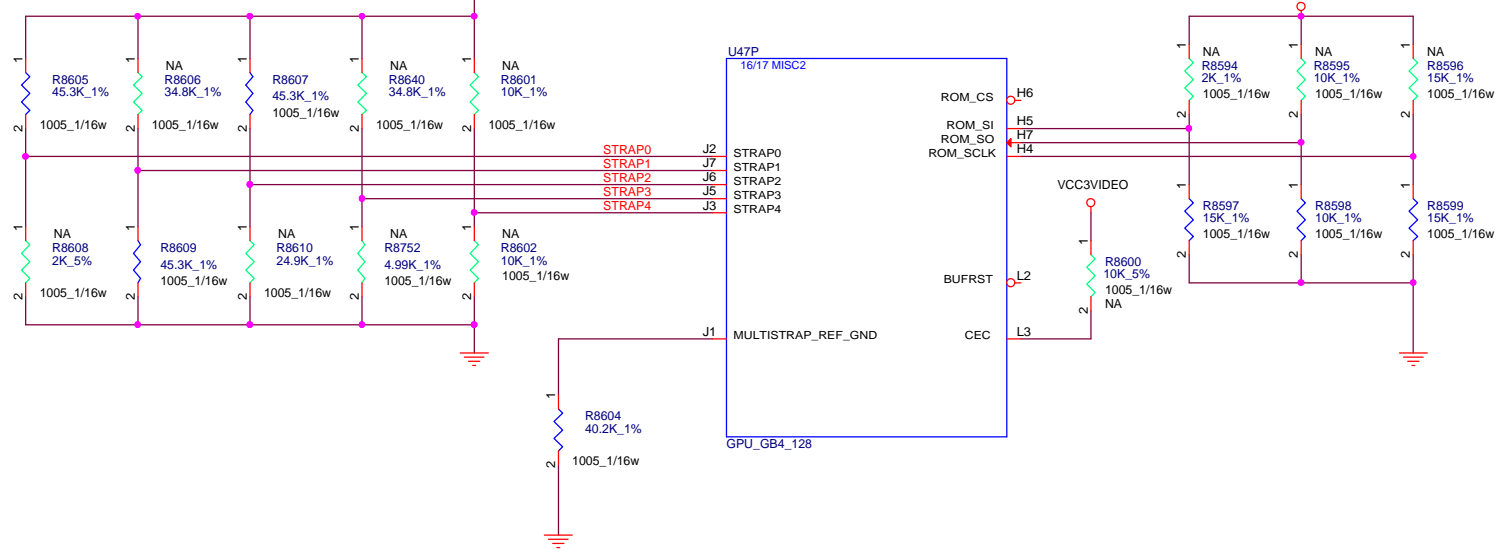


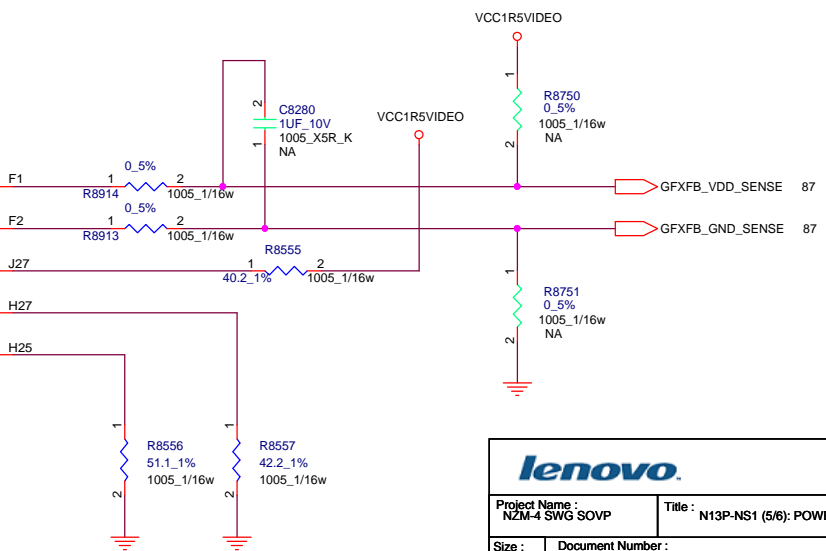
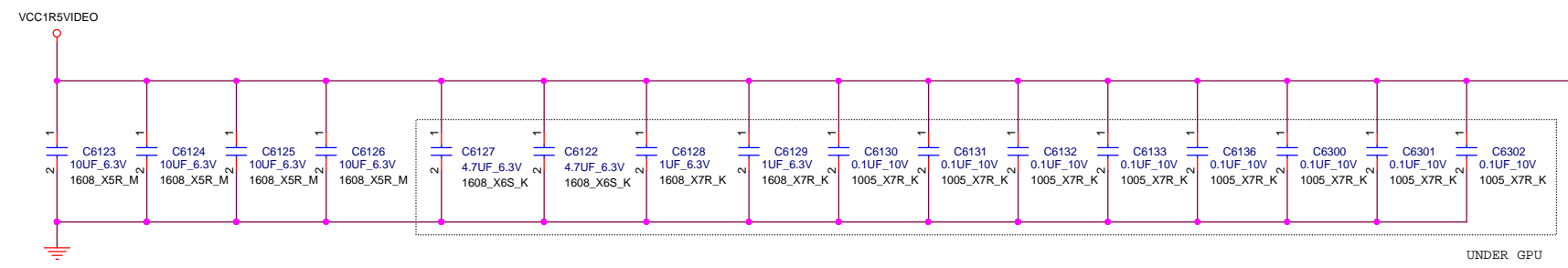
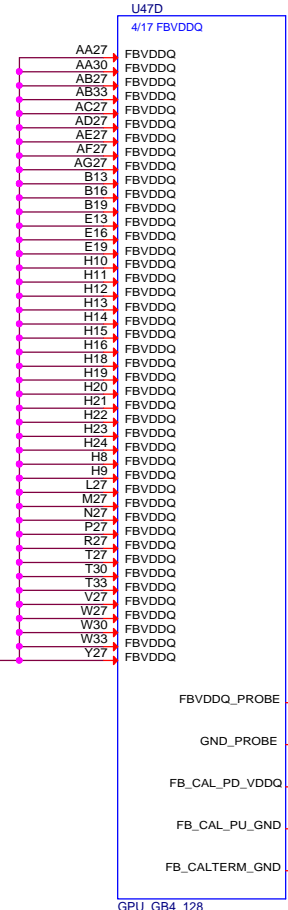
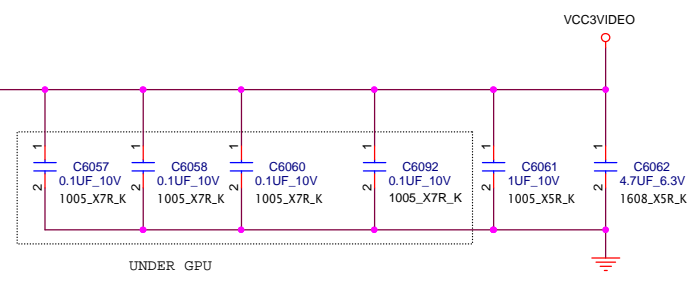
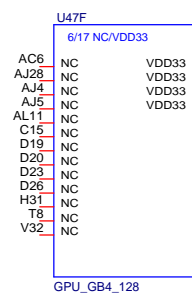
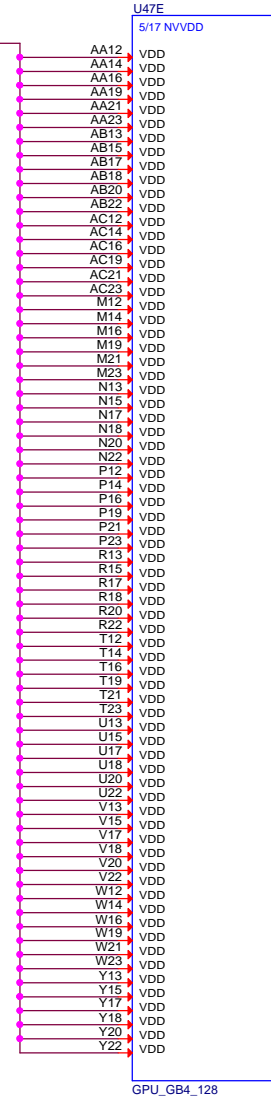
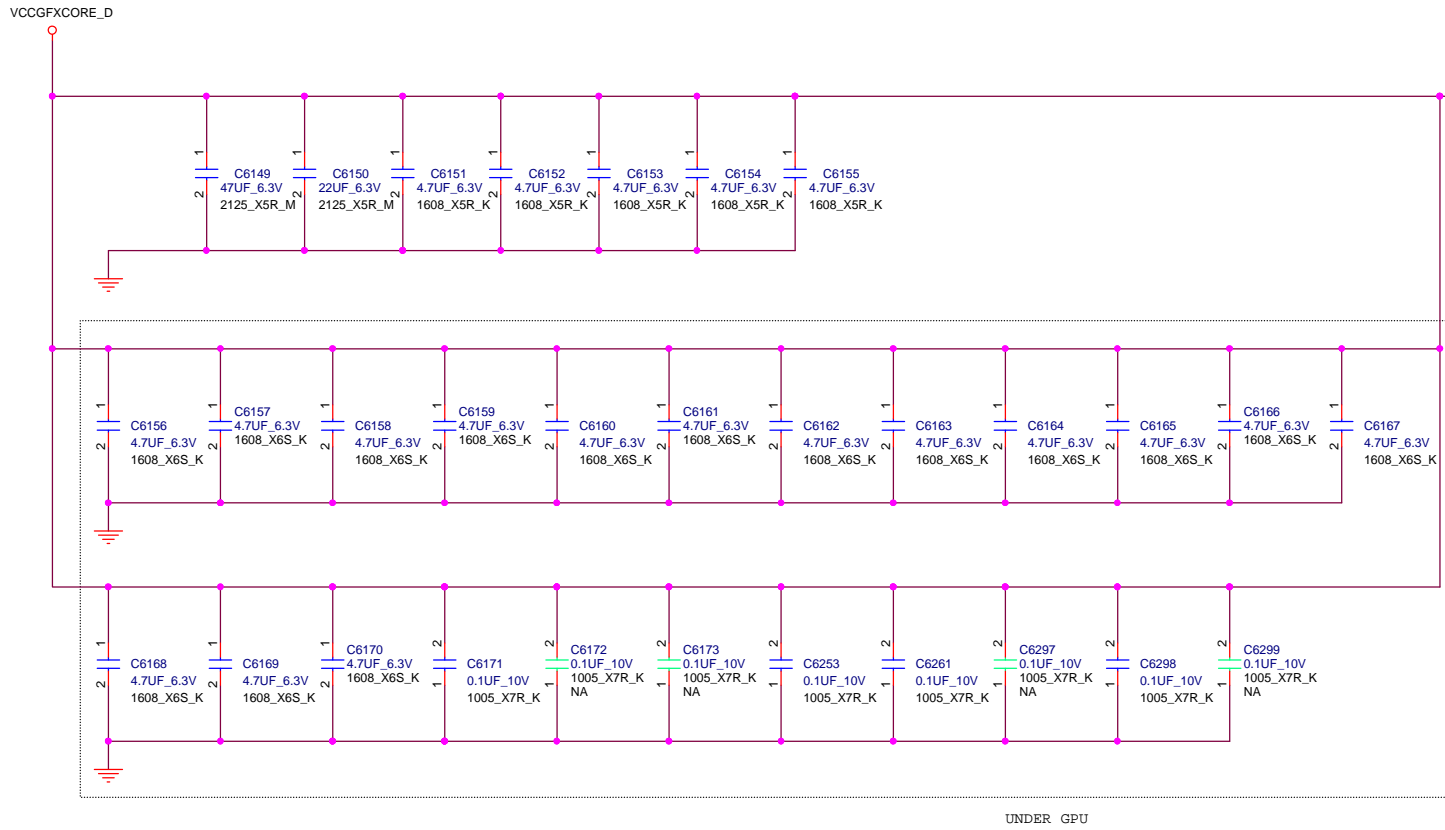
TXC 7M27000149
RIVER FCX-04-27.0000M-8.5PF-50PPM

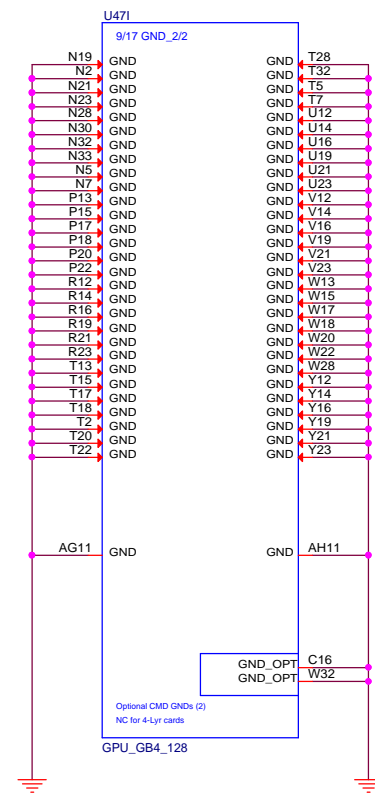
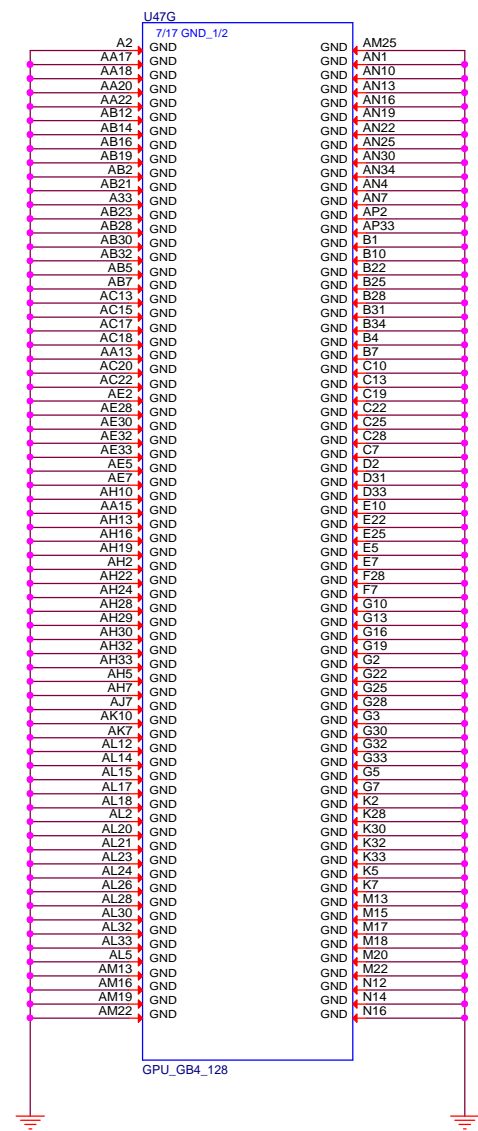
TABLE VIDEO MEMORY

	HYNIX 64Mx16 0010	SAMSUNG 64Mx16 0011	HYNIX 128Mx16 Rev.D 0101	HYNIX 128Mx16 Rev.B 0110	SAMSUNG 128Mx16 0111	HYNIX 256Mx16 TBD
ROM_SIPD R8597	15Kohm	20Kohm	30.1Kohm	34.8Kohm	45.3Kohm	TBD

LOGIC







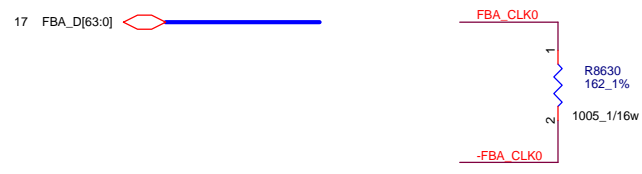
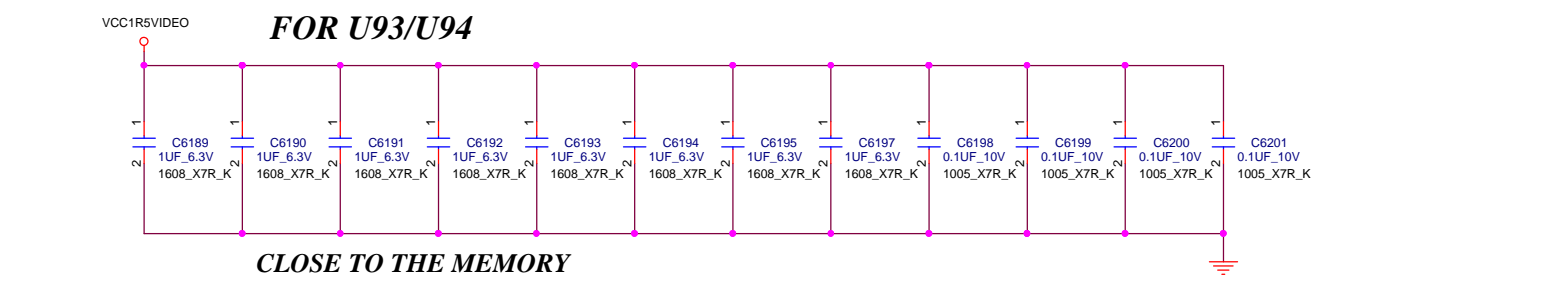
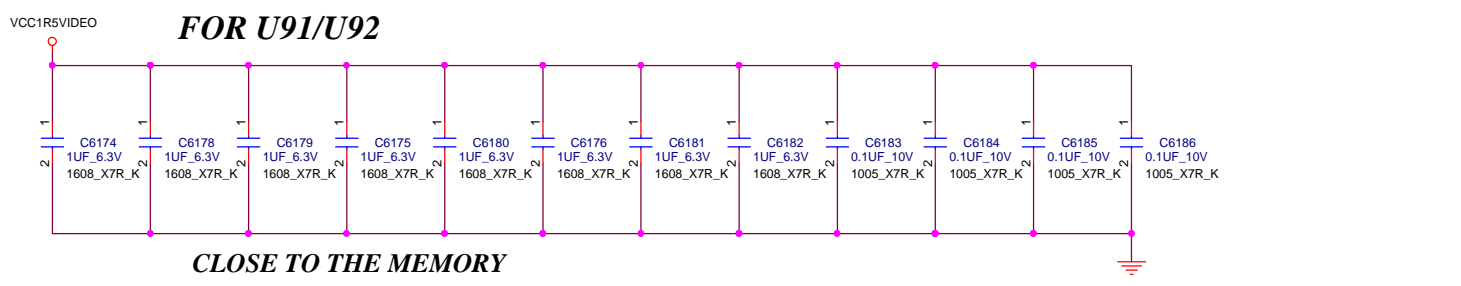
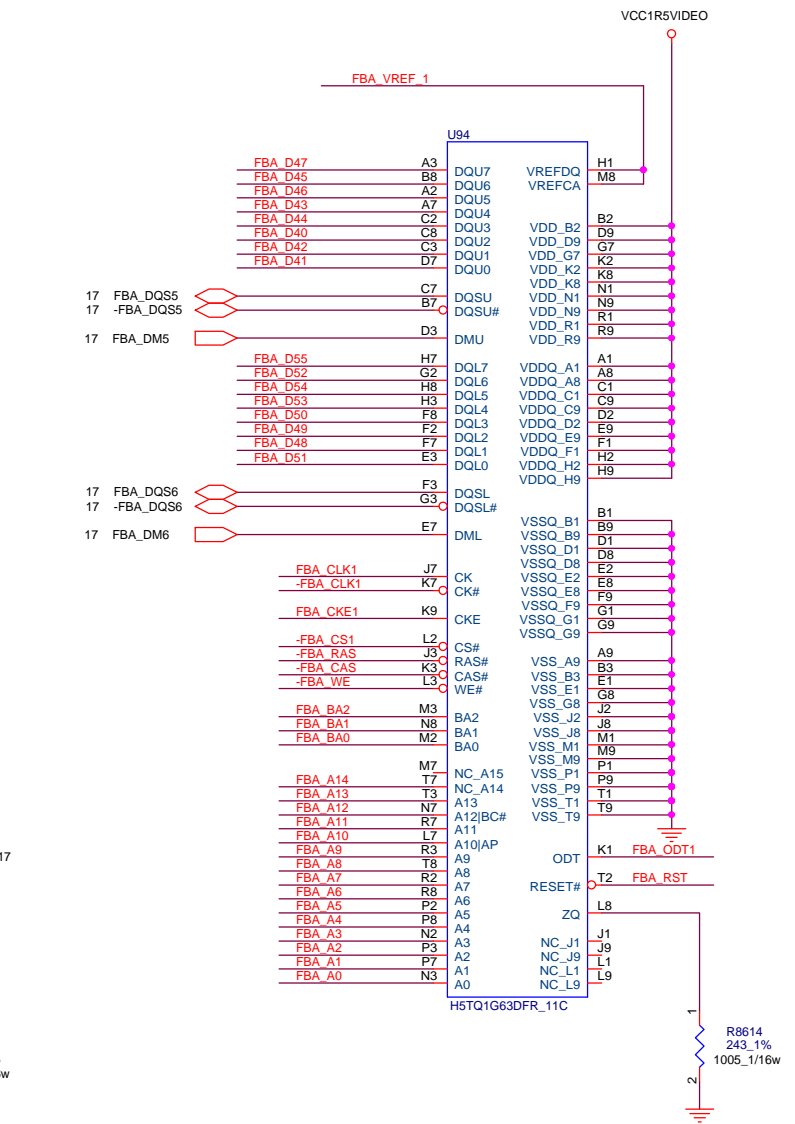
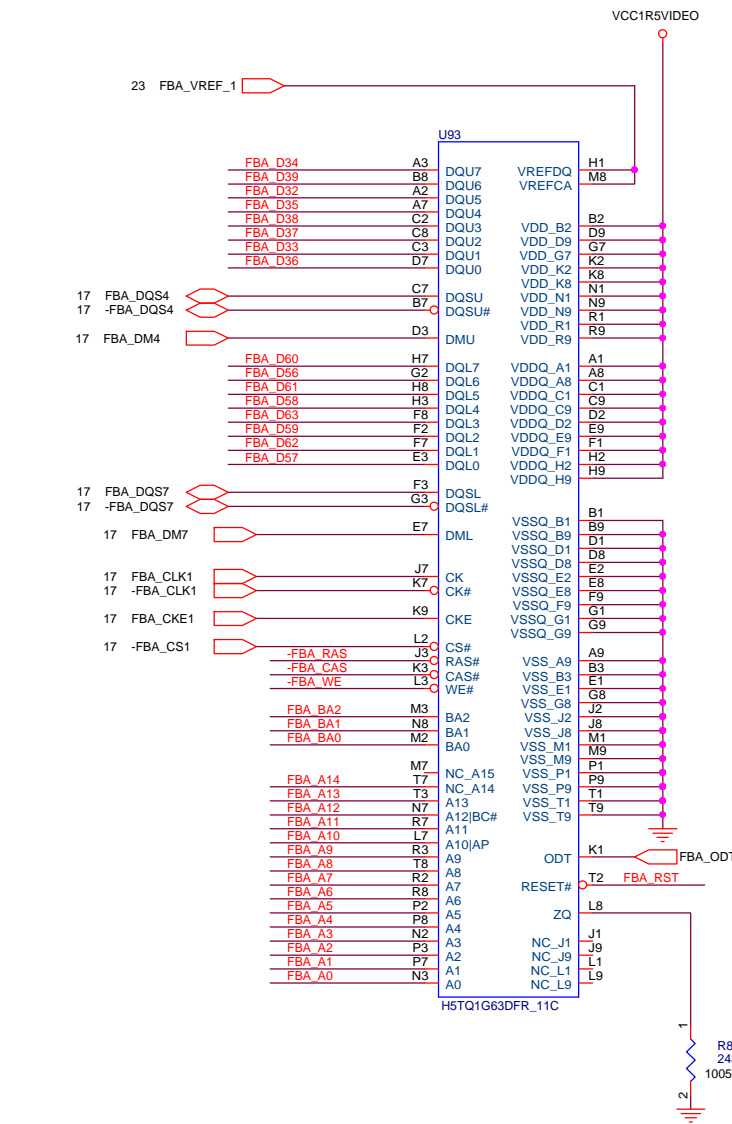
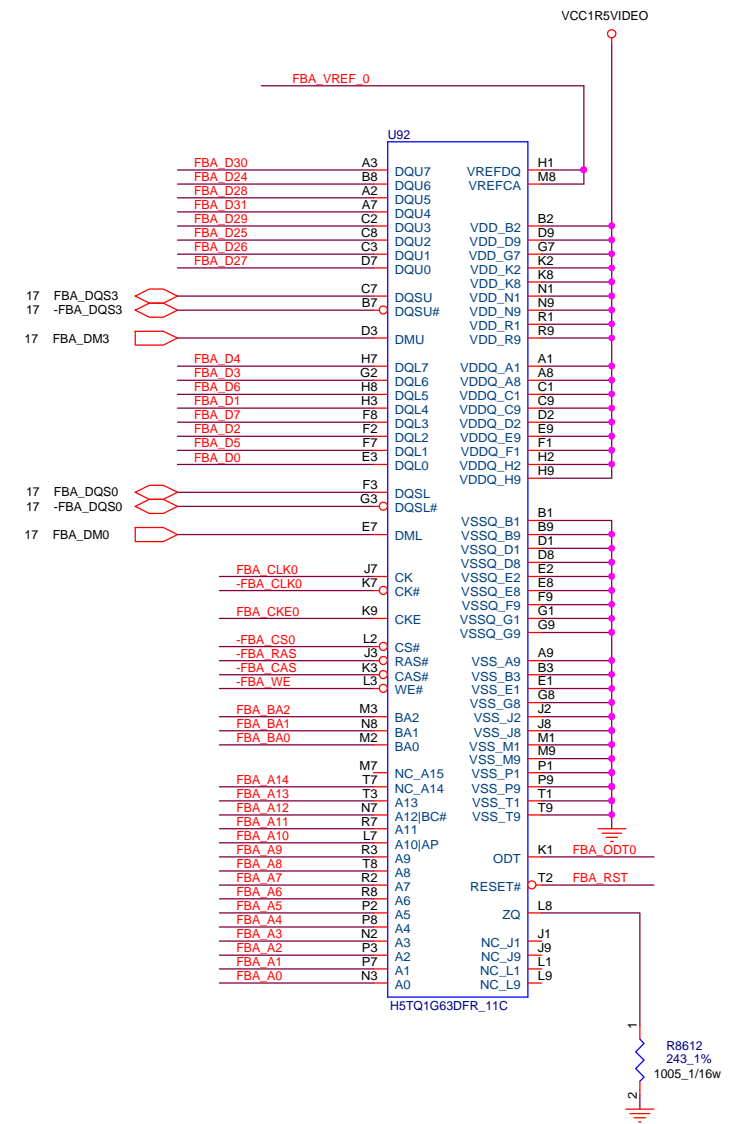
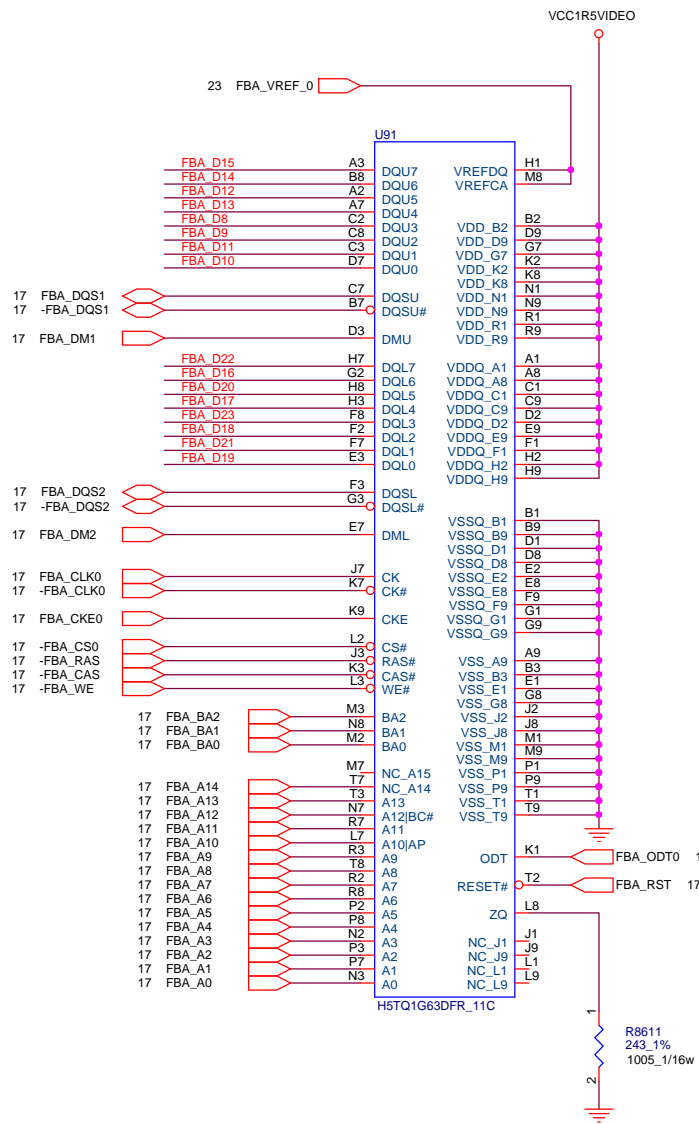


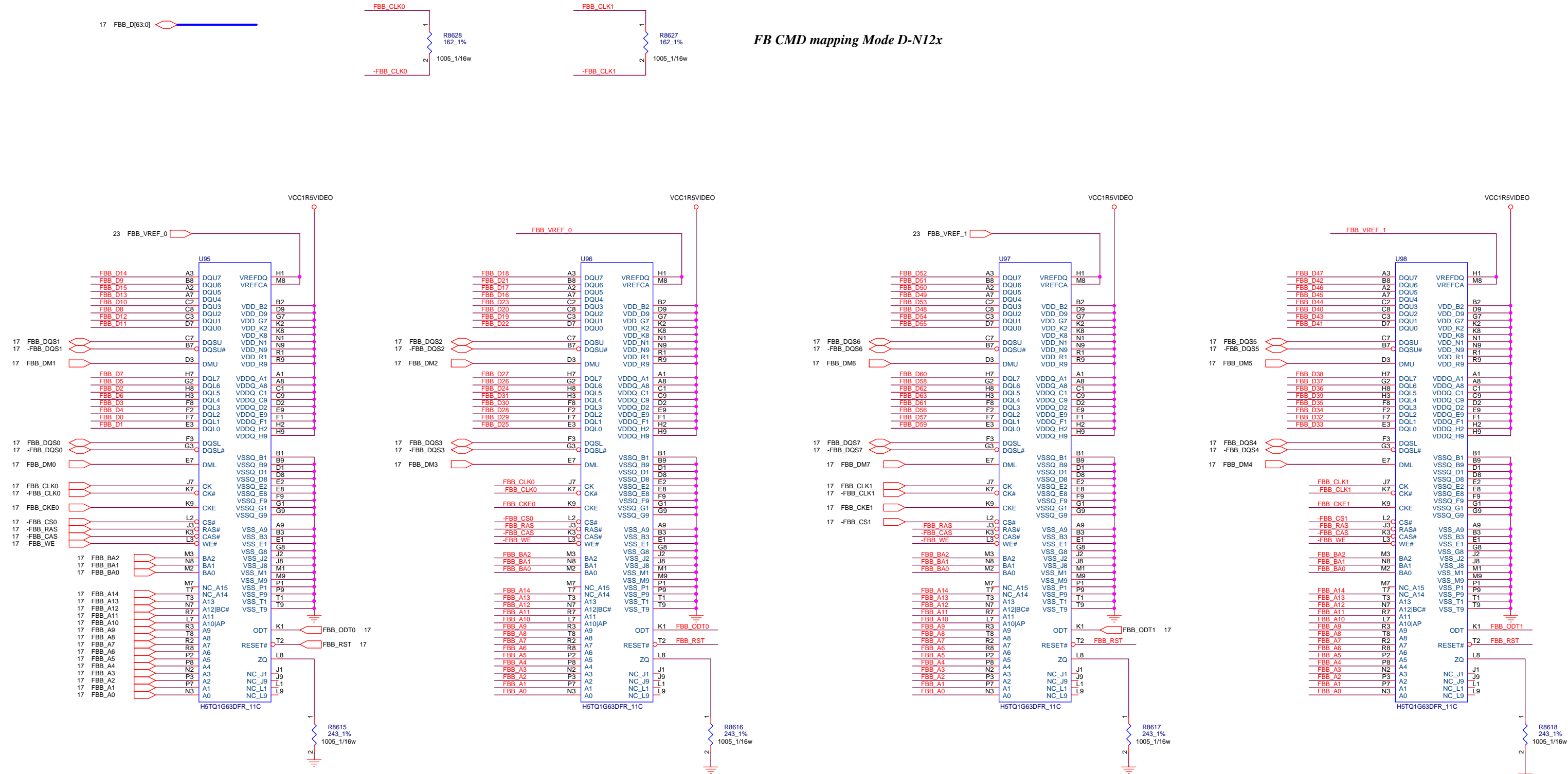
TABLE
DDR3 VIDEO MEMORY

	HYNIX 1GBITS (64Mx16)	SAMSUNG 1GBITS (64Mx16)	HYNIX 2GBITS (128Mx16)	HYNIX 2GBITS (128Mx16)	SAMSUNG 2GBITS (128Mx16)	HYNIX 4GBITS (256Mx16)
U91 U95 U92 U96 U93 U97 U94 U98	H5TQ1G63DFR-11C	K4W1G1646G-BC11	H5TQ2G63DFR-11C	H5TQ2G63BFR-11C	K4W2G1646C-HC11	TBD

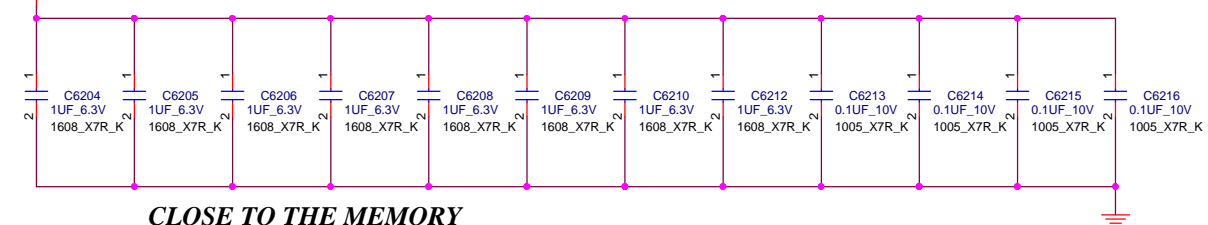
LOGIC



FB CMD mapping Mode D-N12x

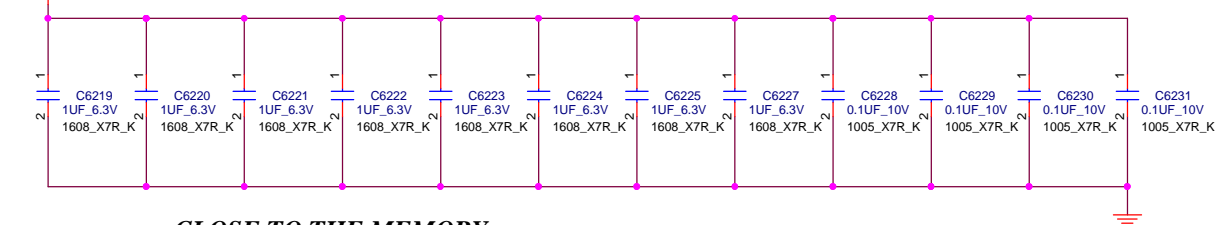


FOR U95/U96

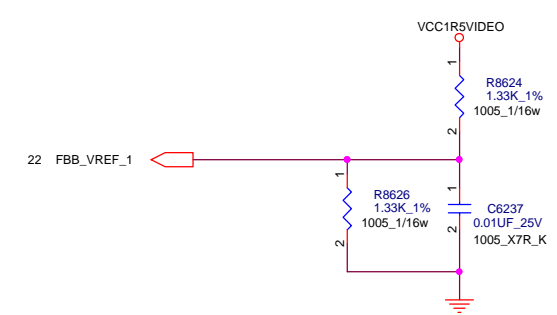
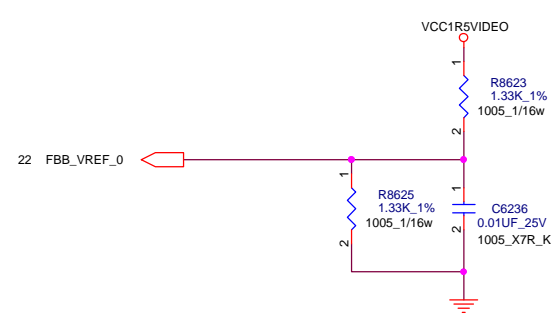
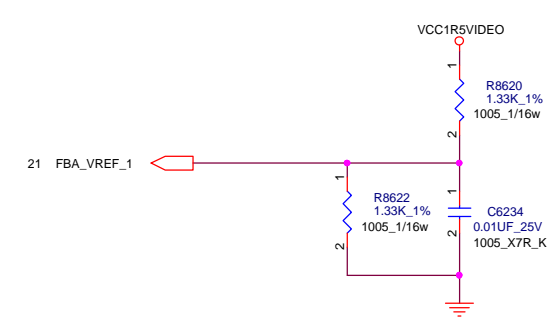
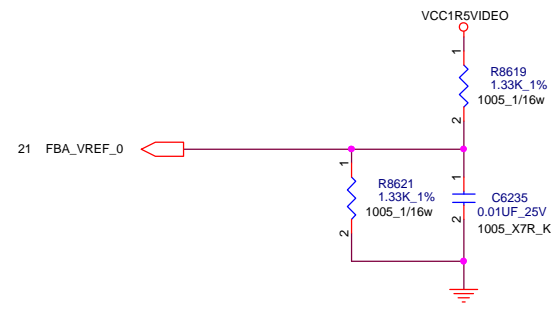


CLOSE TO THE MEMORY

FOR U97/U98



CLOSE TO THE MEMORY



P/N 41U6143
KDS DST310S-32.768KHz-20PPM-9PF
RIVER TFX-02-32.768KJ80862

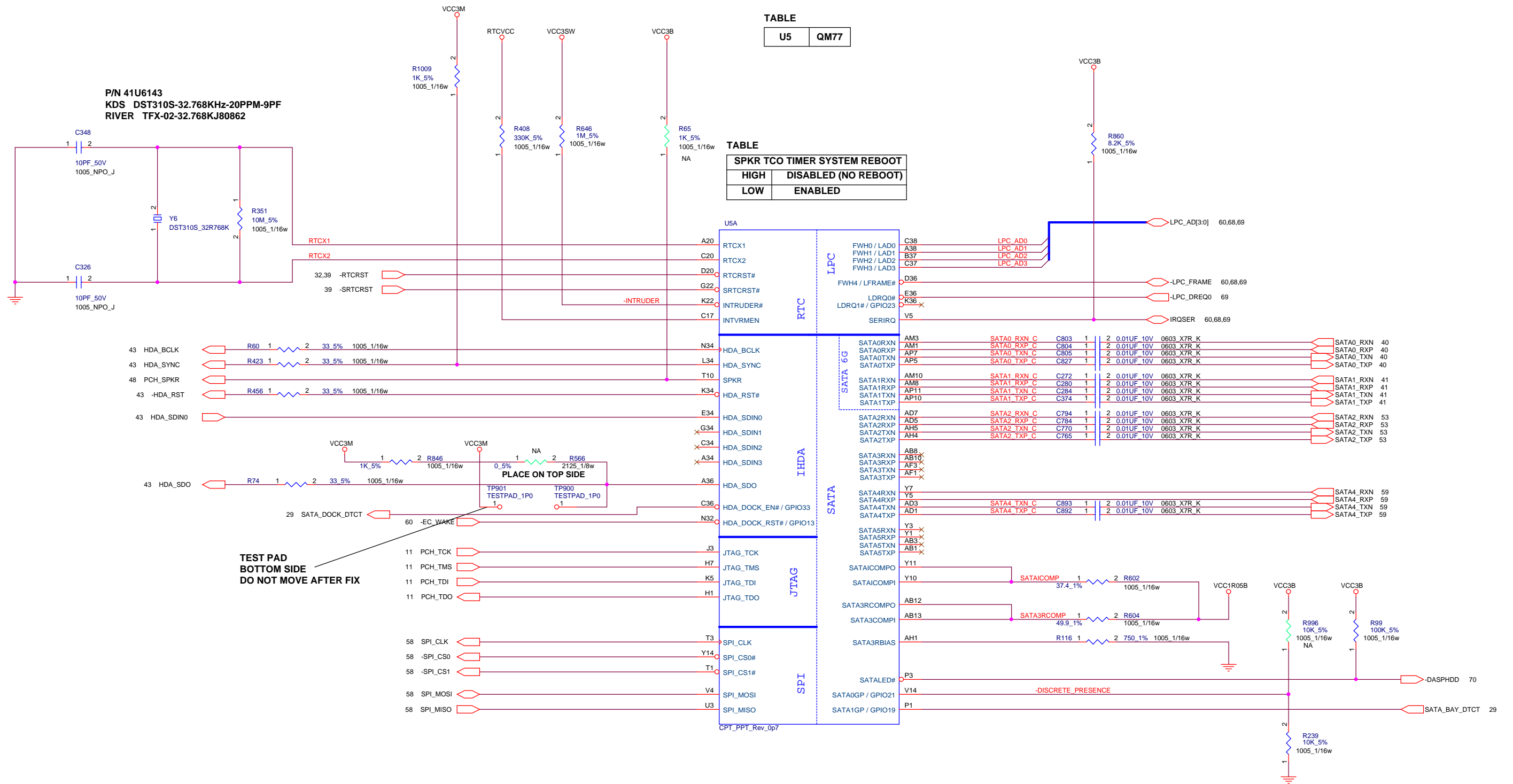
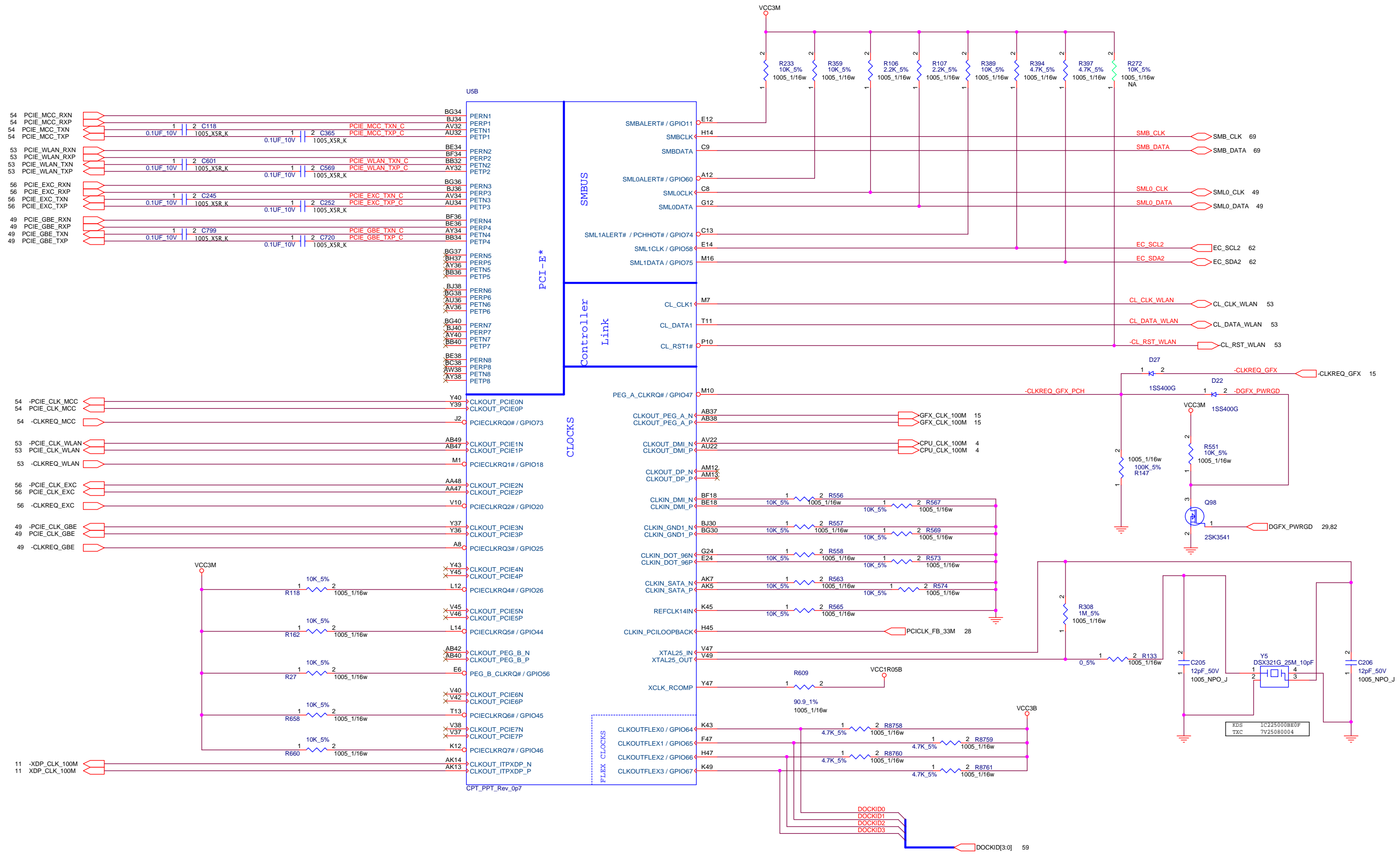
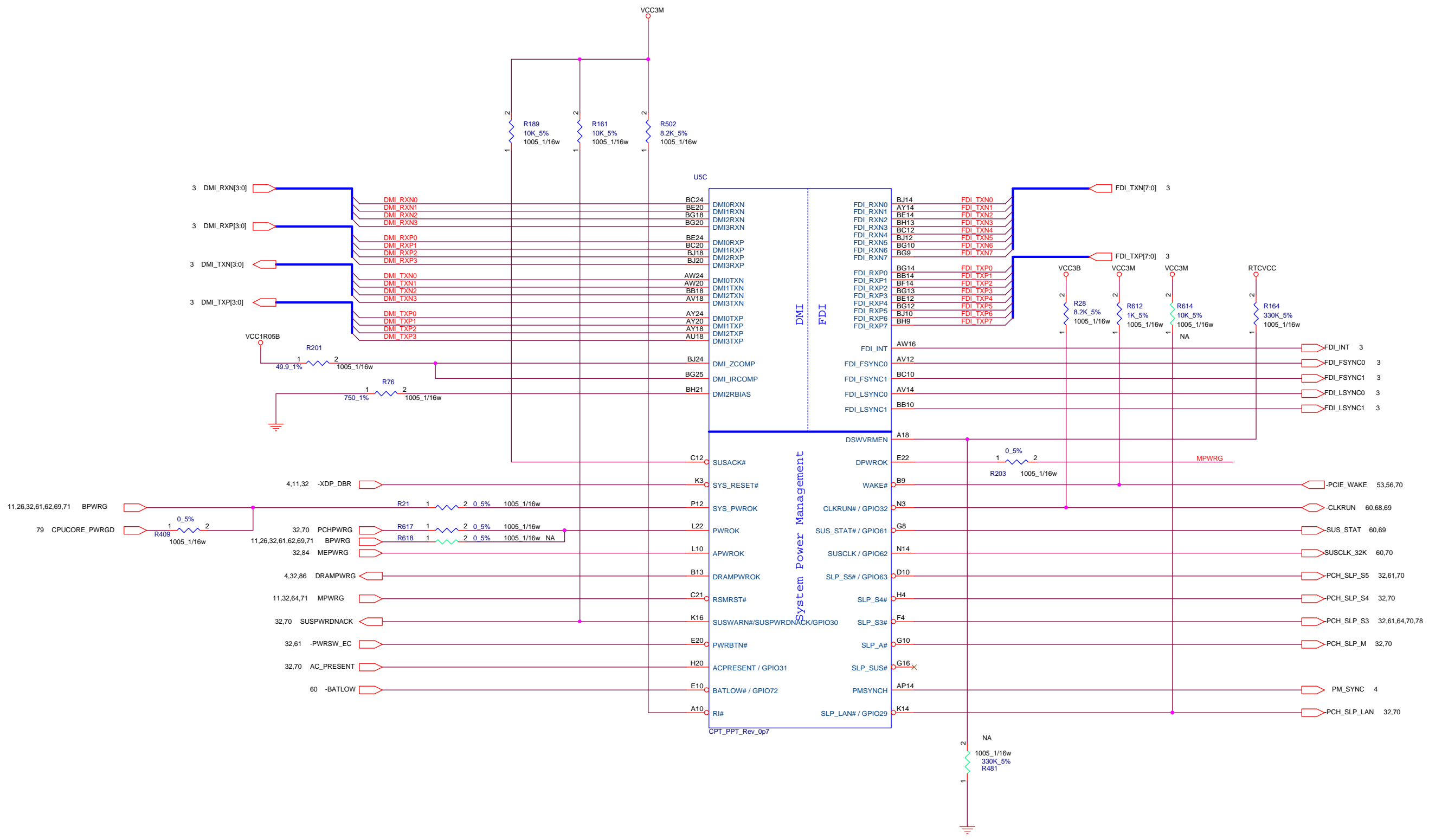


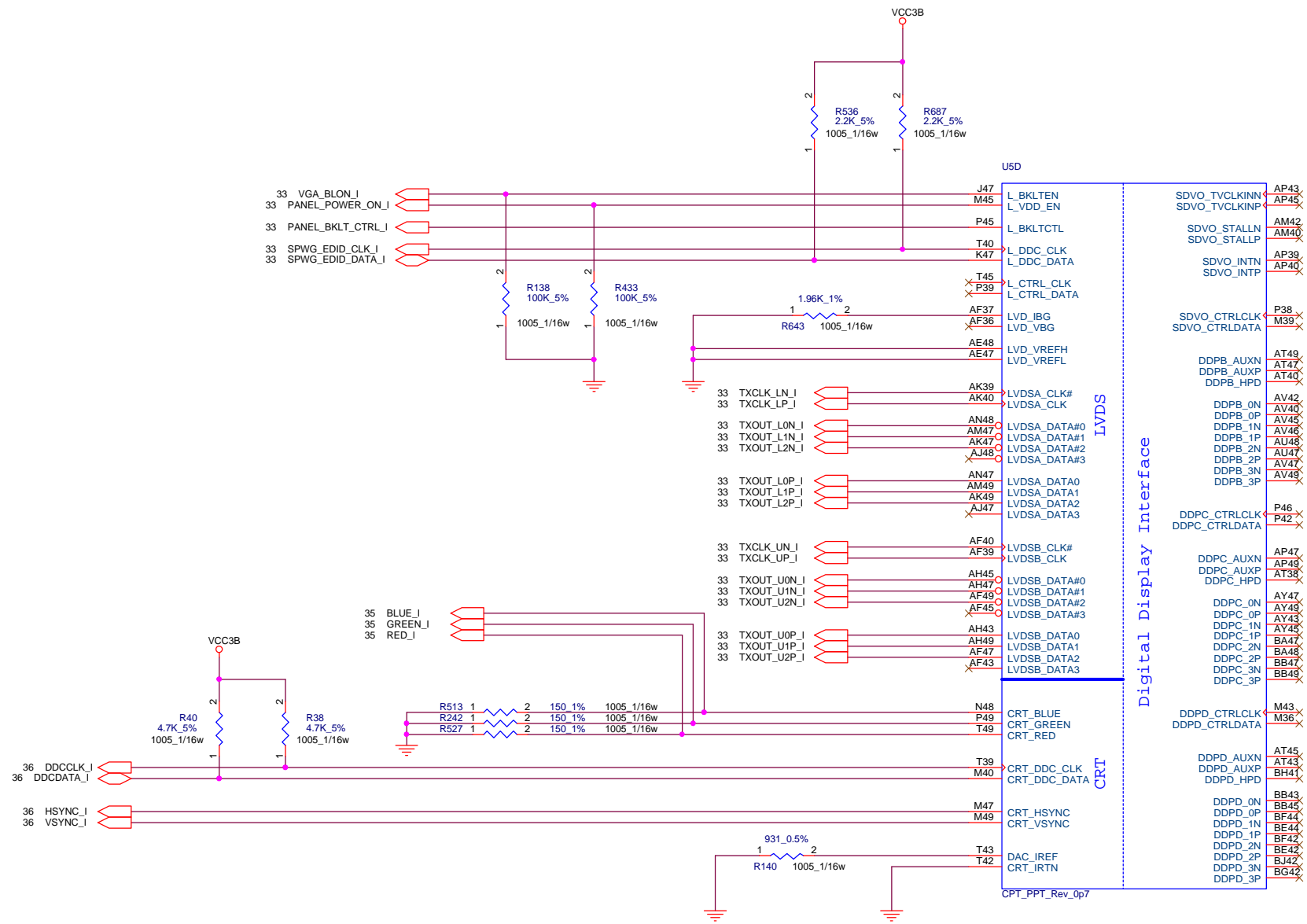
TABLE	
U5	QM77

TABLE	
SPKR TCO TIMER SYSTEM REBOOT	
HIGH	DISABLED (NO REBOOT)
LOW	ENABLED

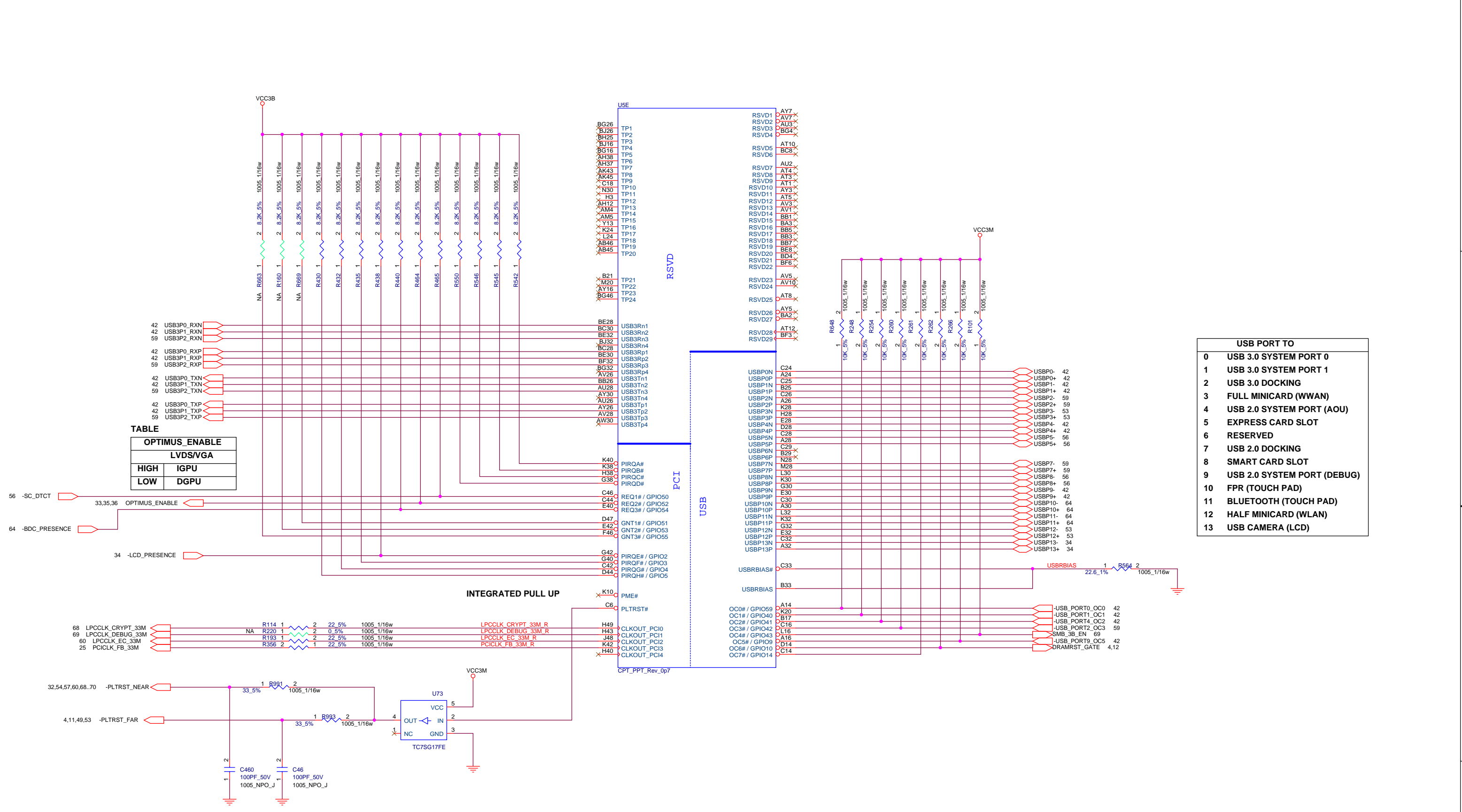
TEST PAD
BOTTOM SIDE
DO NOT MOVE AFTER FIX







Digital Display Interface



TABLE

OPTIMUS_ENABLE	
LVDS/VGA	
HIGH	IGPU
LOW	DGPU

USB PORT TO

0	USB 3.0 SYSTEM PORT 0
1	USB 3.0 SYSTEM PORT 1
2	USB 3.0 DOCKING
3	FULL MINICARD (WWAN)
4	USB 2.0 SYSTEM PORT (AOU)
5	EXPRESS CARD SLOT
6	RESERVED
7	USB 2.0 DOCKING
8	SMART CARD SLOT
9	USB 2.0 SYSTEM PORT (DEBUG)
10	FPR (TOUCH PAD)
11	BLUETOOTH (TOUCH PAD)
12	HALF MINICARD (WLAN)
13	USB CAMERA (LCD)

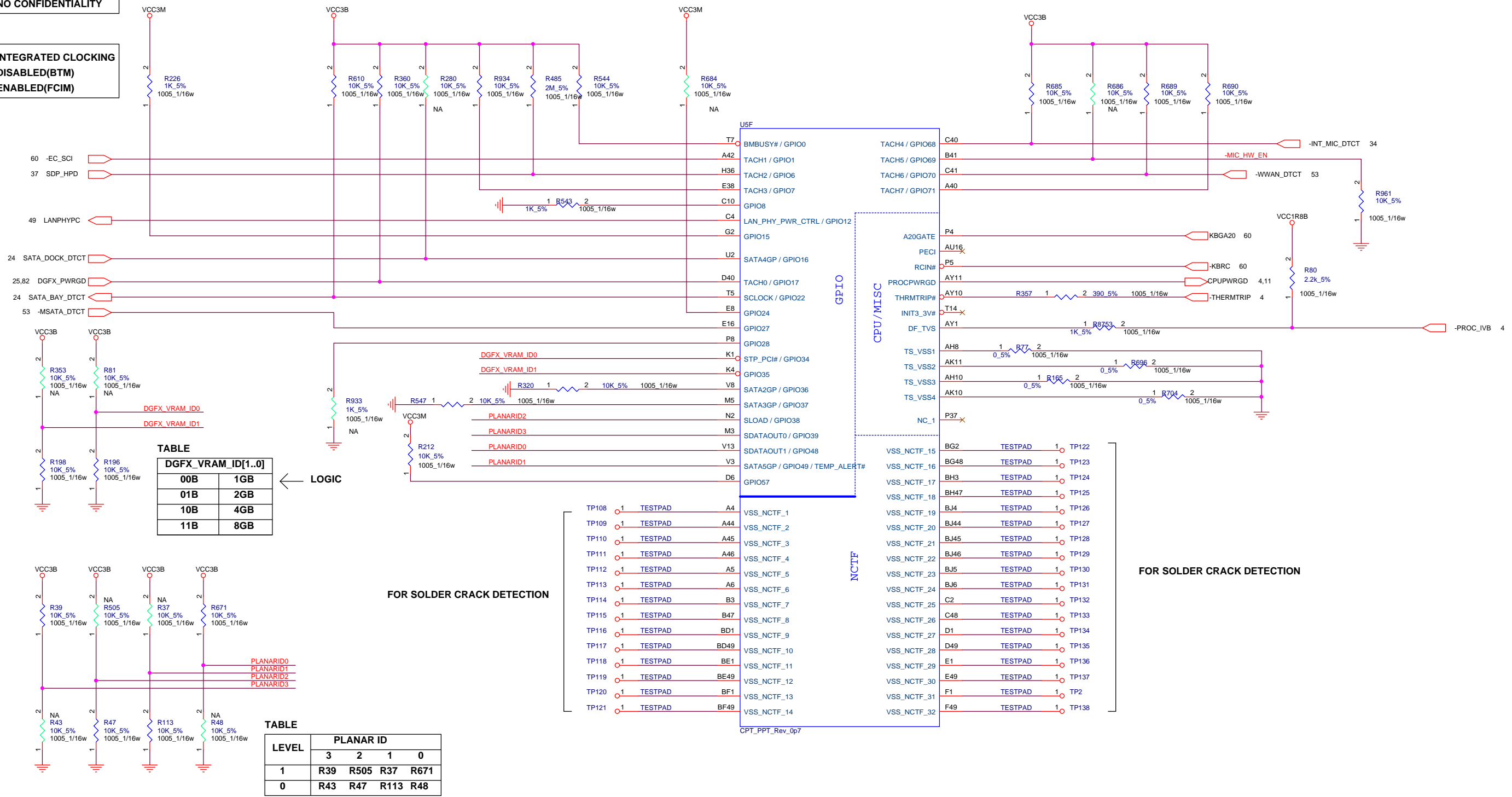


TABLE

GPIO15	ME CRYPTO STRAP
HIGH	WITH CONFIDENTIALITY
LOW	NO CONFIDENTIALITY

TABLE

GPIO8	INTEGRATED CLOCKING
HIGH	DISABLED(BTM)
LOW	ENABLED(FCIM)



TABLE

DGFV_VRAM_ID[1..0]	
00B	1GB
01B	2GB
10B	4GB
11B	8GB

← LOGIC

TABLE

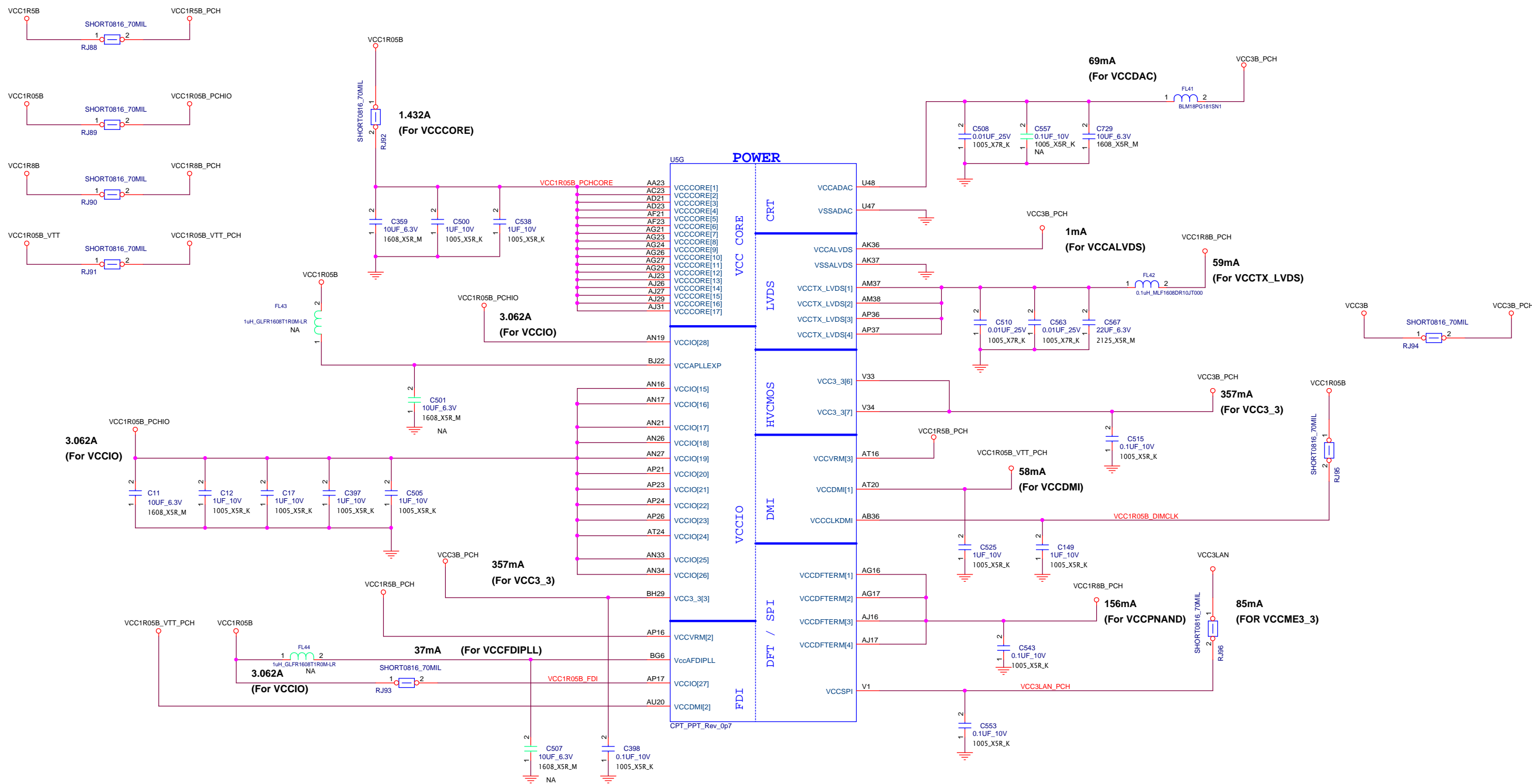
LEVEL	PLANAR ID			
	3	2	1	0
1	R39	R505	R37	R671
0	R43	R47	R113	R48

TABLE

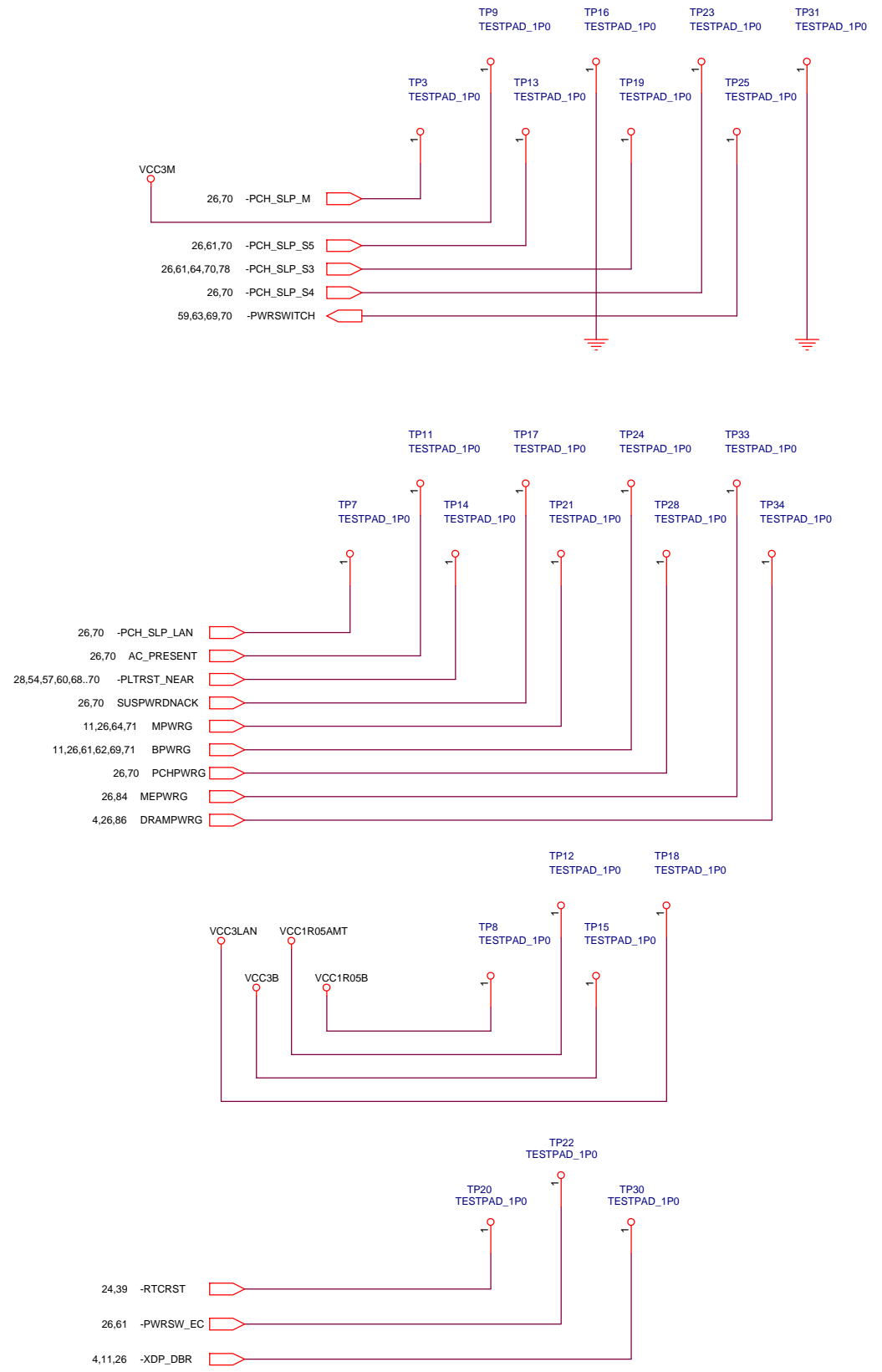
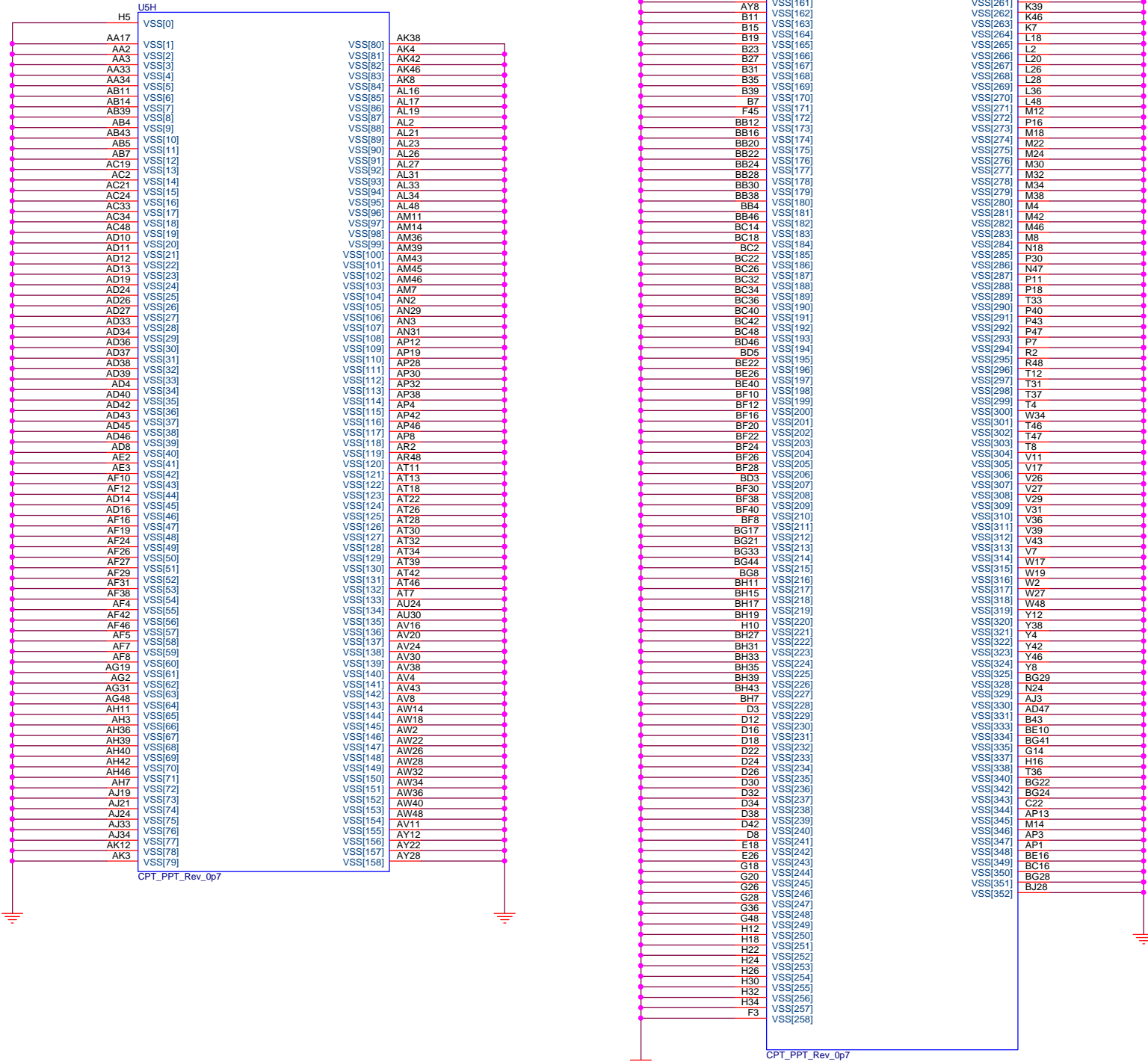
LEVEL	PLANARID[3..0]
PDV	0000B
SDV	0001B
MFVT	0010B
FVT	0011B
SW SIT-1	0100B
SIT	0101B
SIT-R1	0110B
SIT-R2	0111B
SVT	1000B
SOVP	1001B

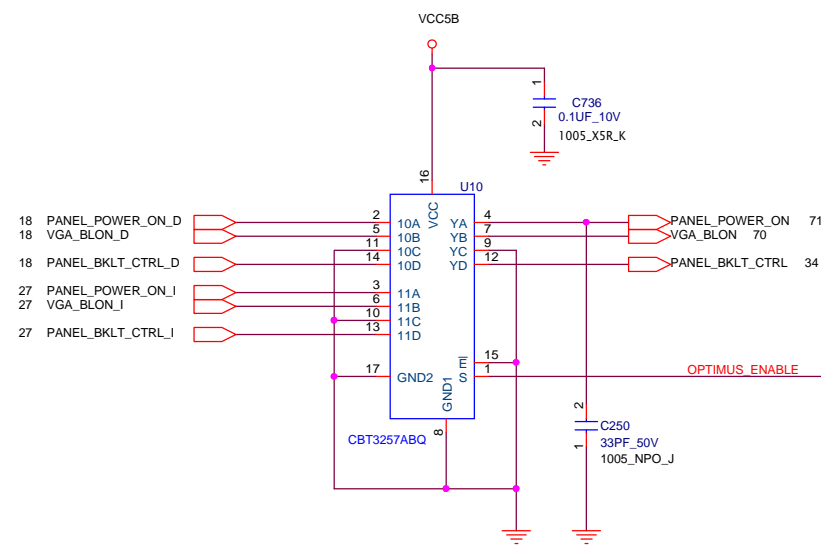
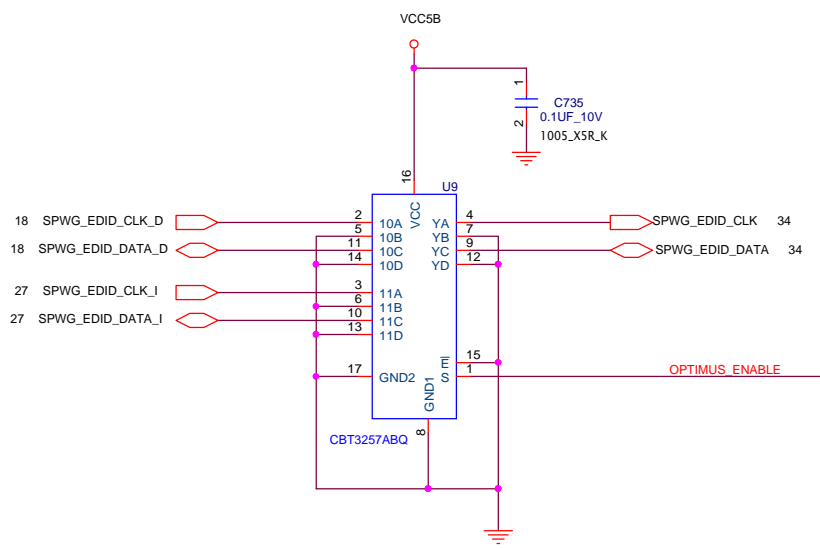
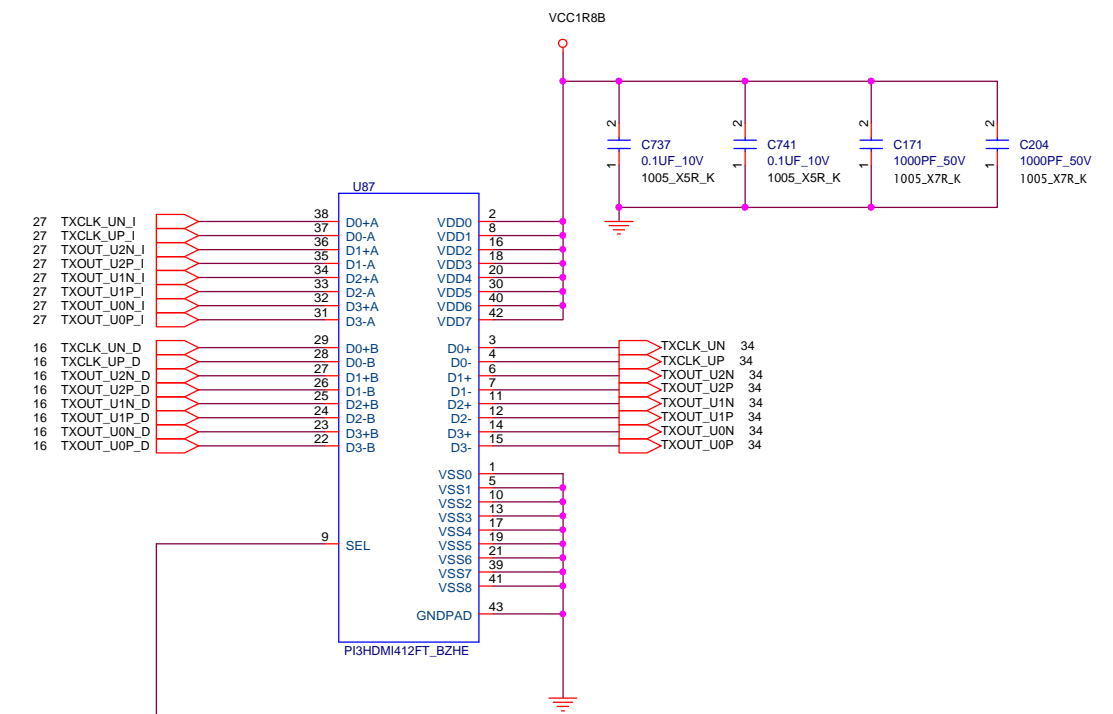
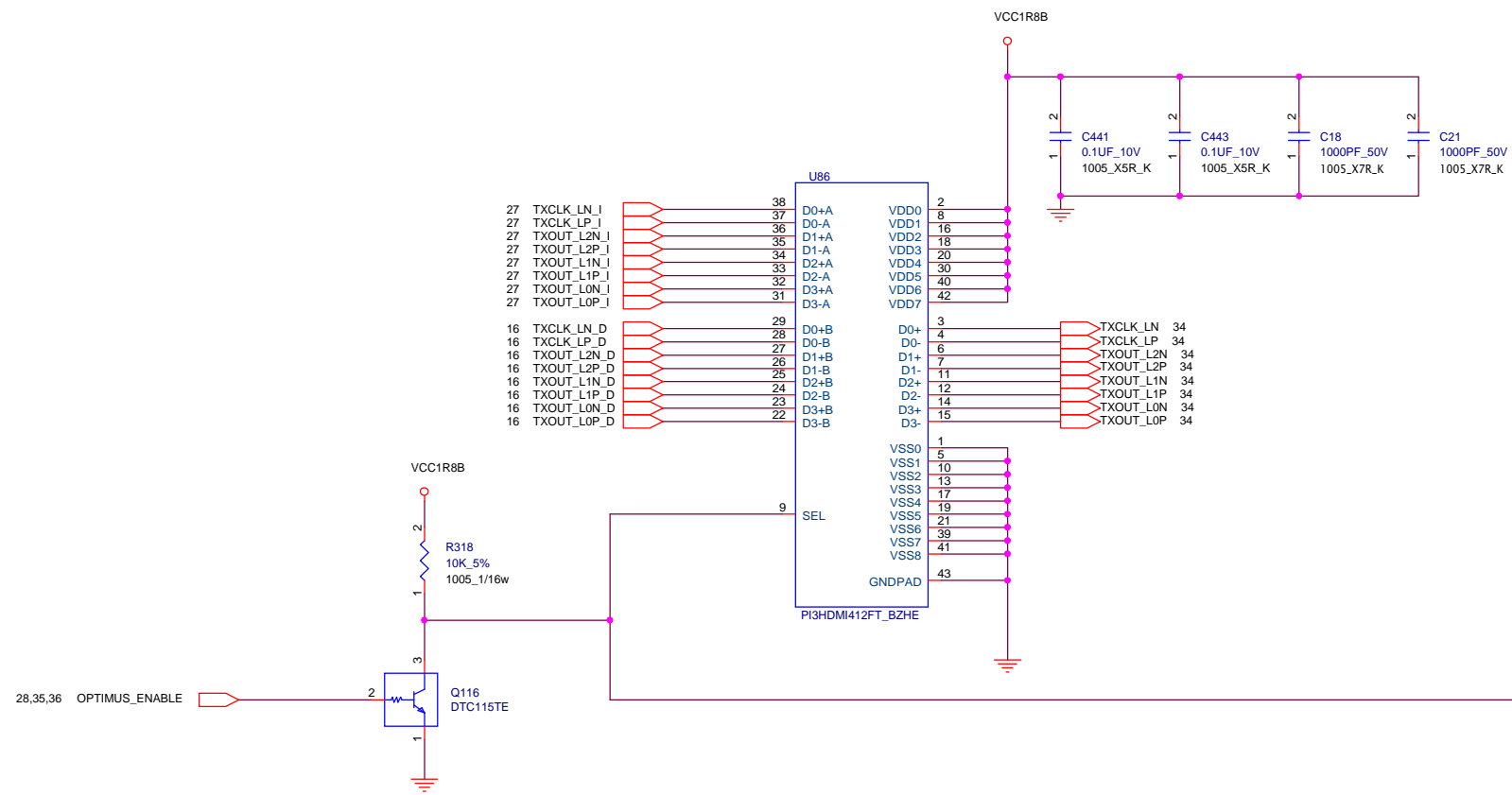
FOR SOLDER CRACK DETECTION

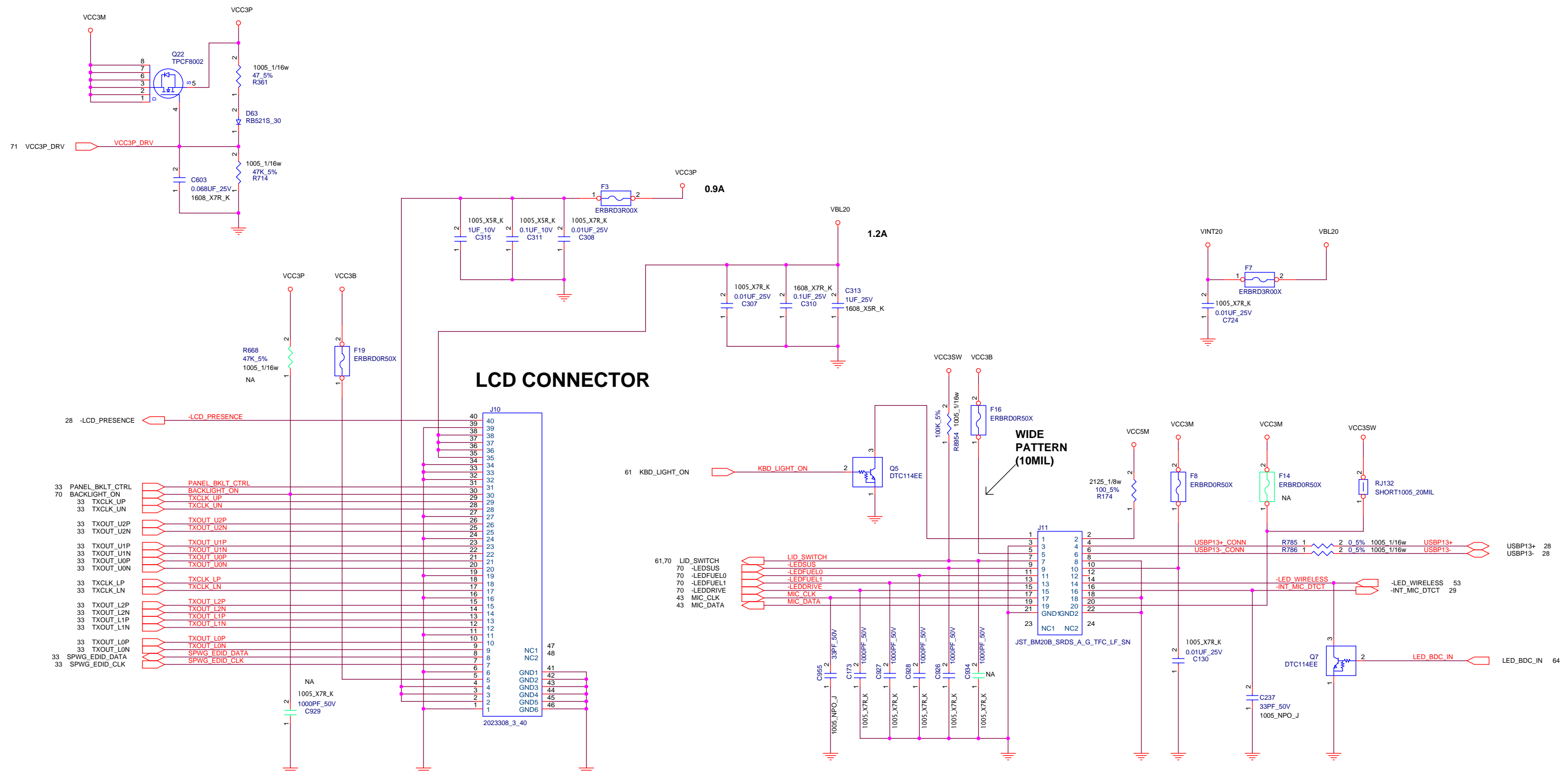
FOR SOLDER CRACK DETECTION



TEST PAD FOR METS/APS

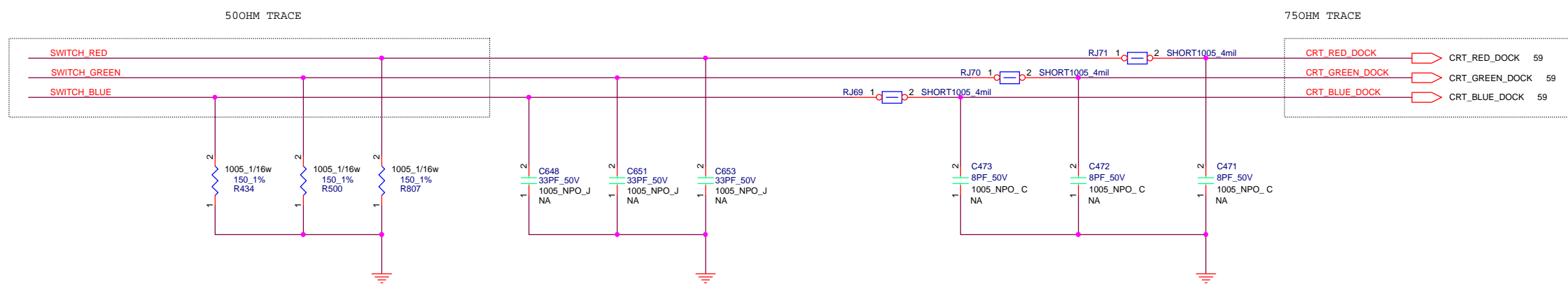
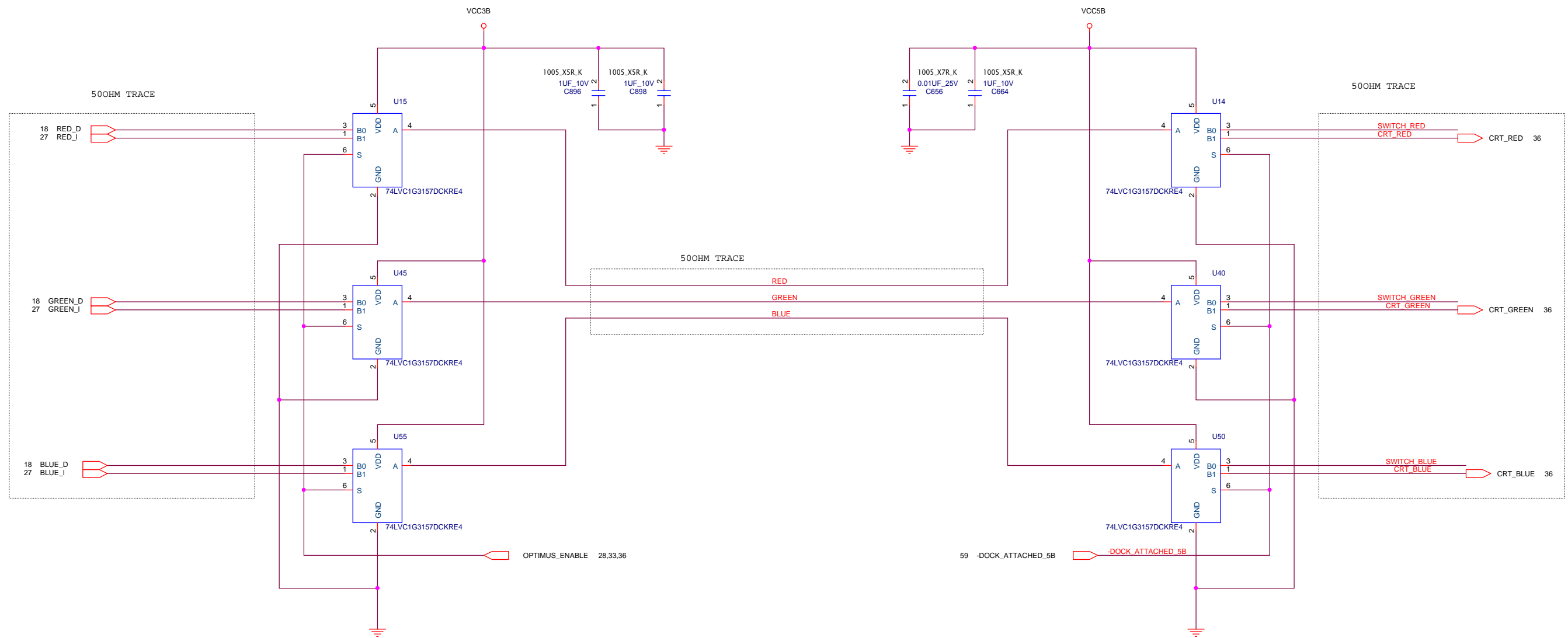


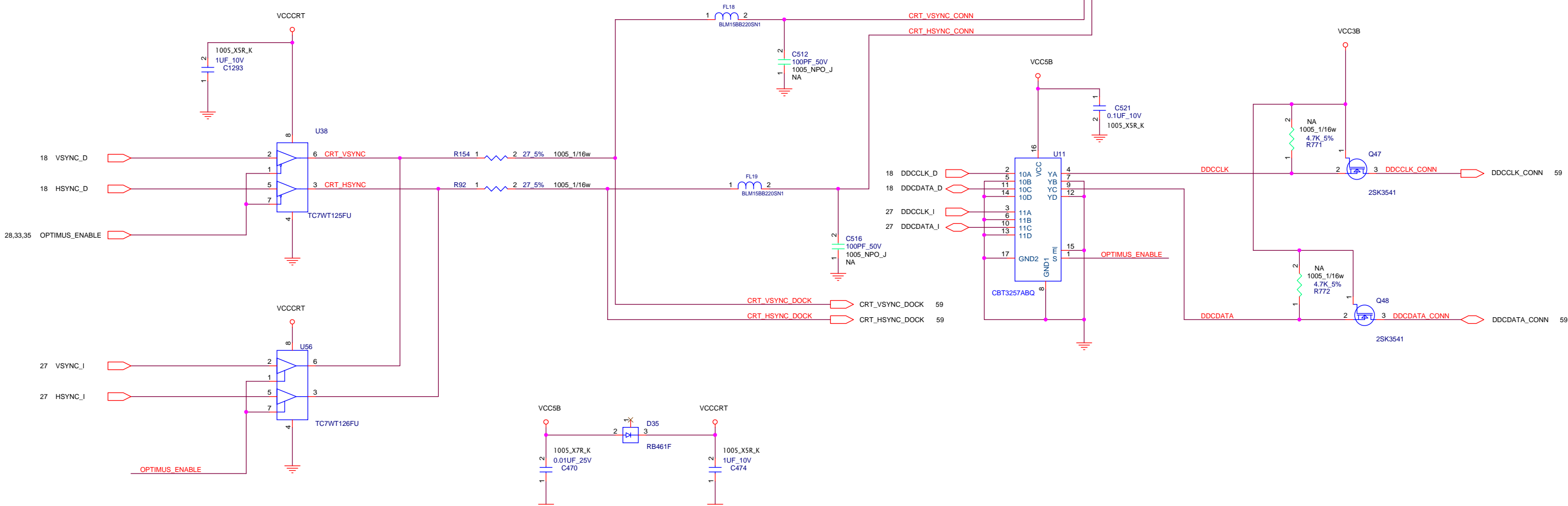
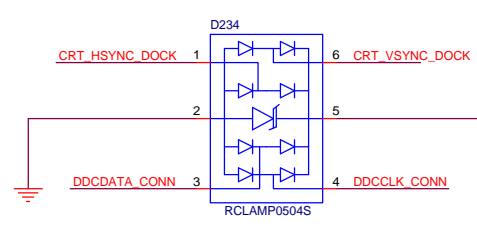
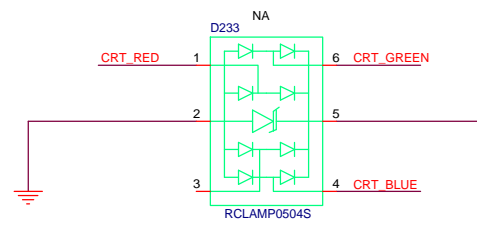
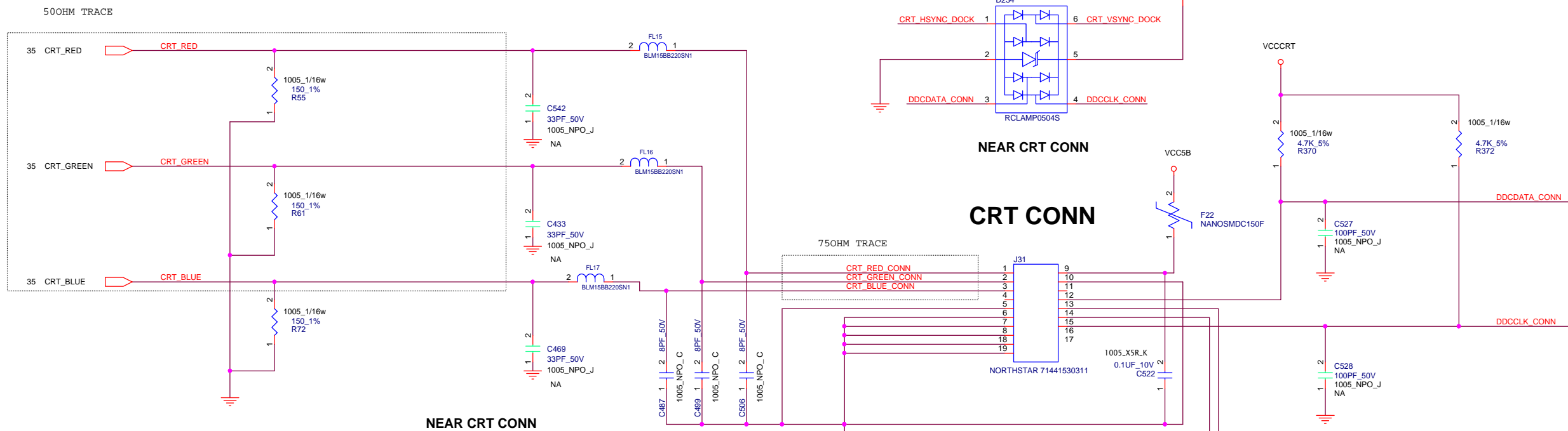




LCD CONNECTOR

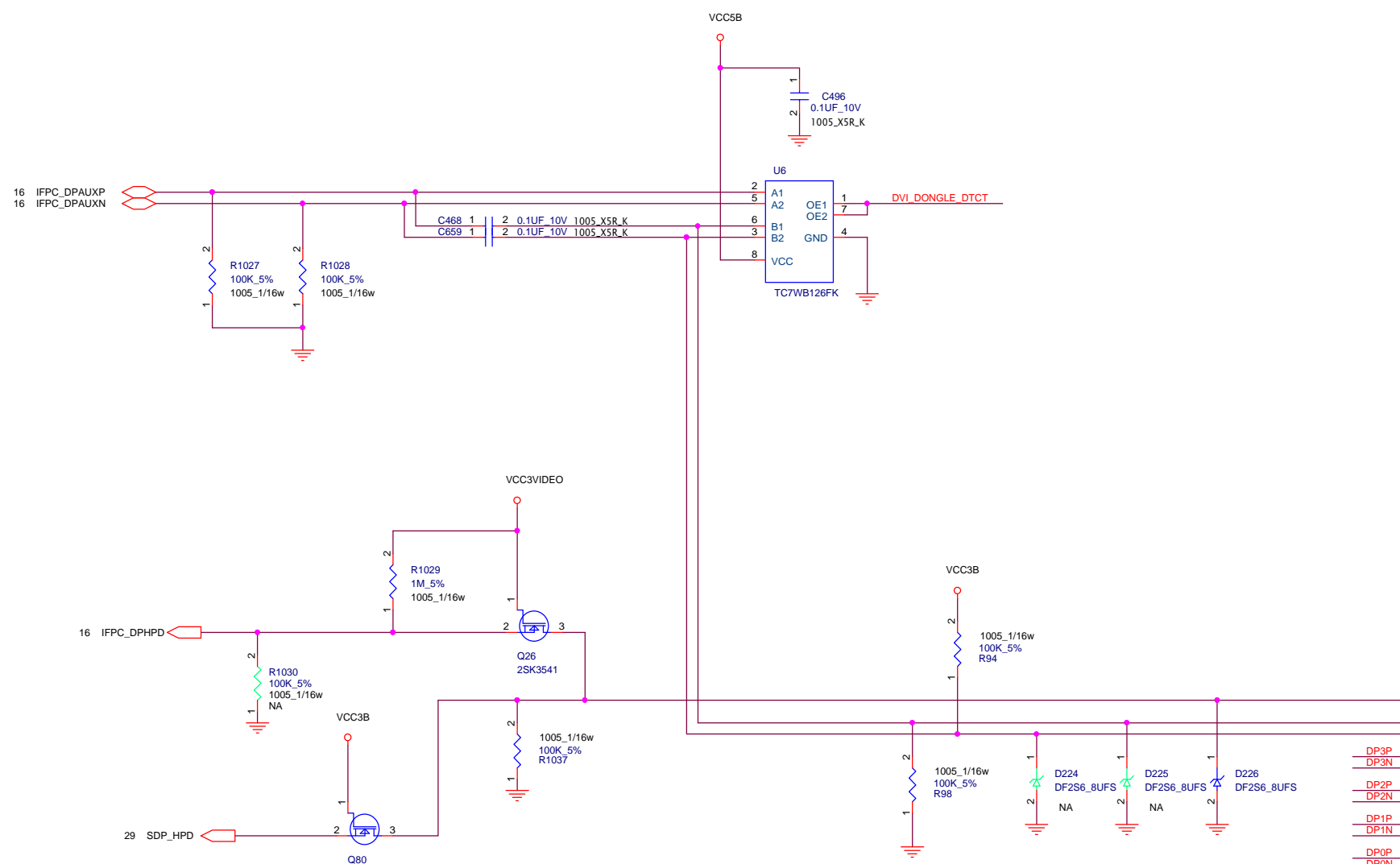
WIDE PATTERN (10MIL)



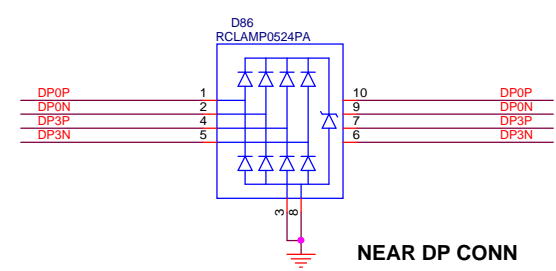
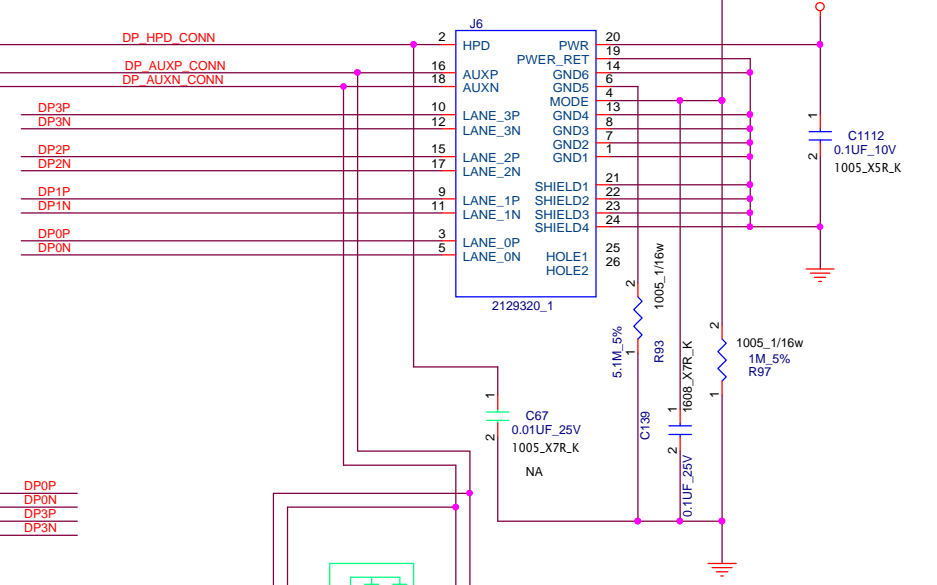


FOR SYSTEM DP NEAR DP CONN

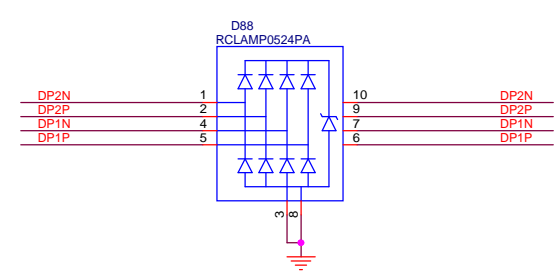
16 IFPC_DP3N	C339	2	1	0.1UF_10V	1005_X5R_K	DP3N
16 IFPC_DP3P	C323	2	1	0.1UF_10V	1005_X5R_K	DP3P
16 IFPC_DP2N	C312	2	1	0.1UF_10V	1005_X5R_K	DP2N
16 IFPC_DP2P	C317	2	1	0.1UF_10V	1005_X5R_K	DP2P
16 IFPC_DP1N	C277	2	1	0.1UF_10V	1005_X5R_K	DP1N
16 IFPC_DP1P	C276	2	1	0.1UF_10V	1005_X5R_K	DP1P
16 IFPC_DP0N	C218	2	1	0.1UF_10V	1005_X5R_K	DP0N
16 IFPC_DP0P	C226	2	1	0.1UF_10V	1005_X5R_K	DP0P



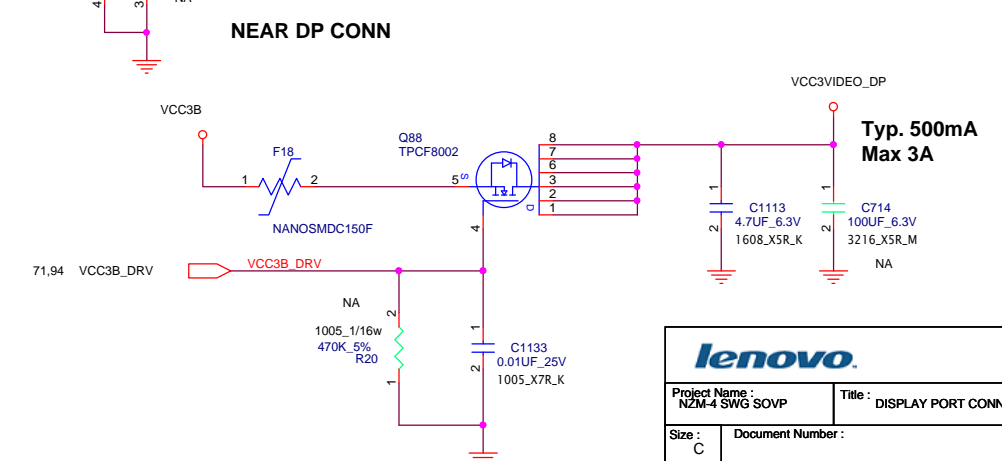
DP CONN



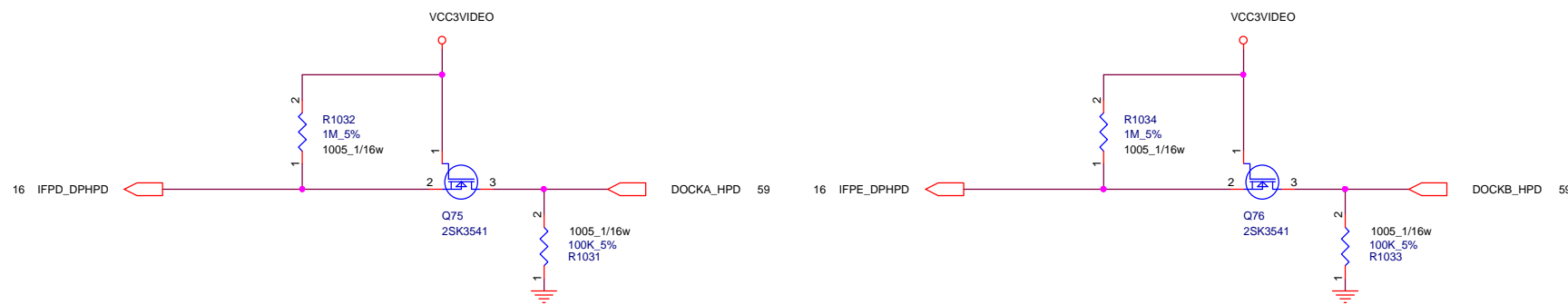
NEAR DP CONN



NEAR DP CONN



Typ. 500mA
Max 3A

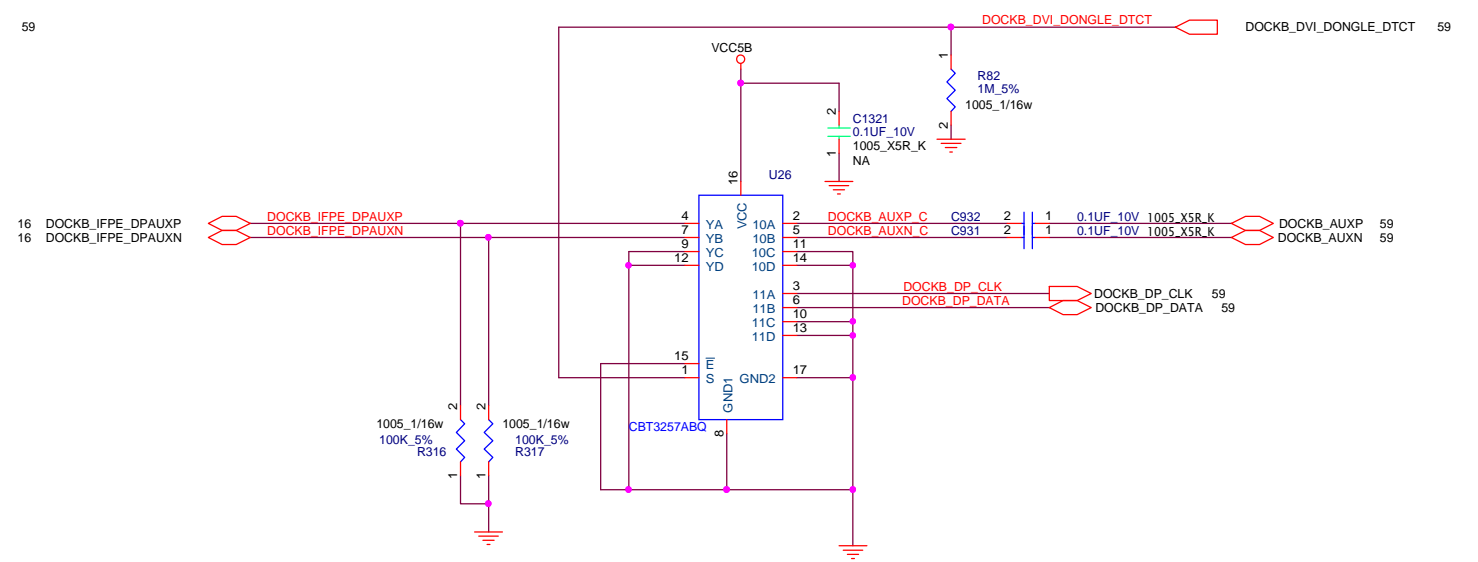
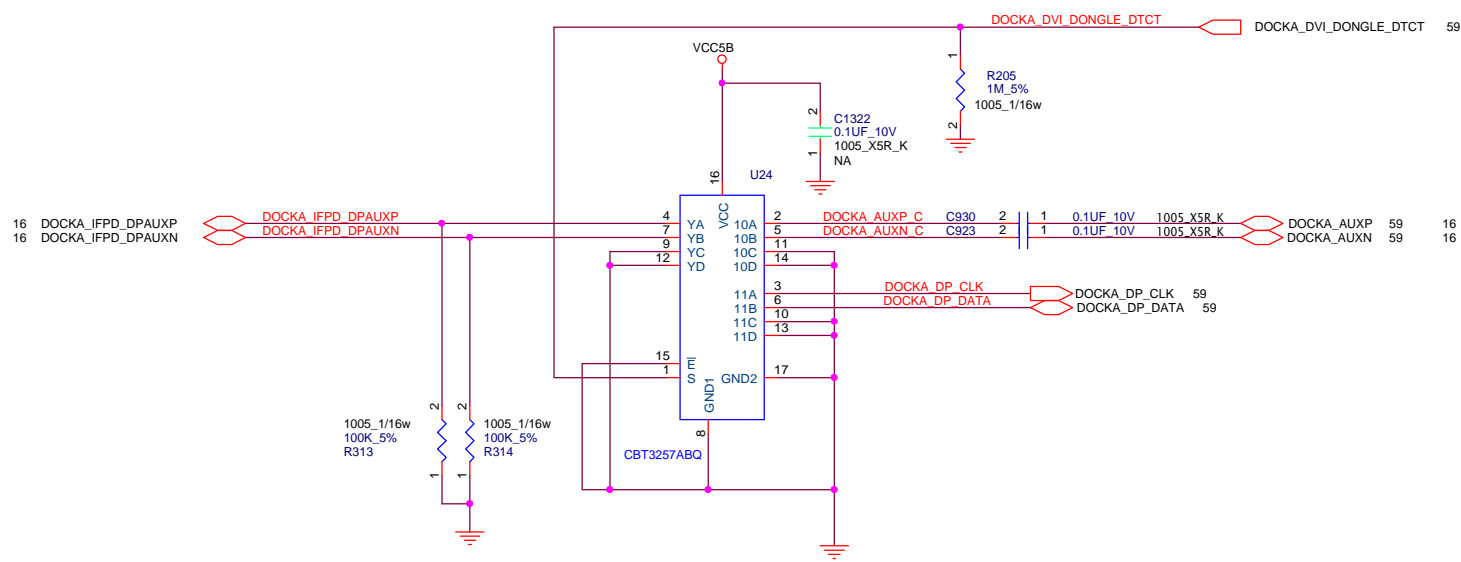


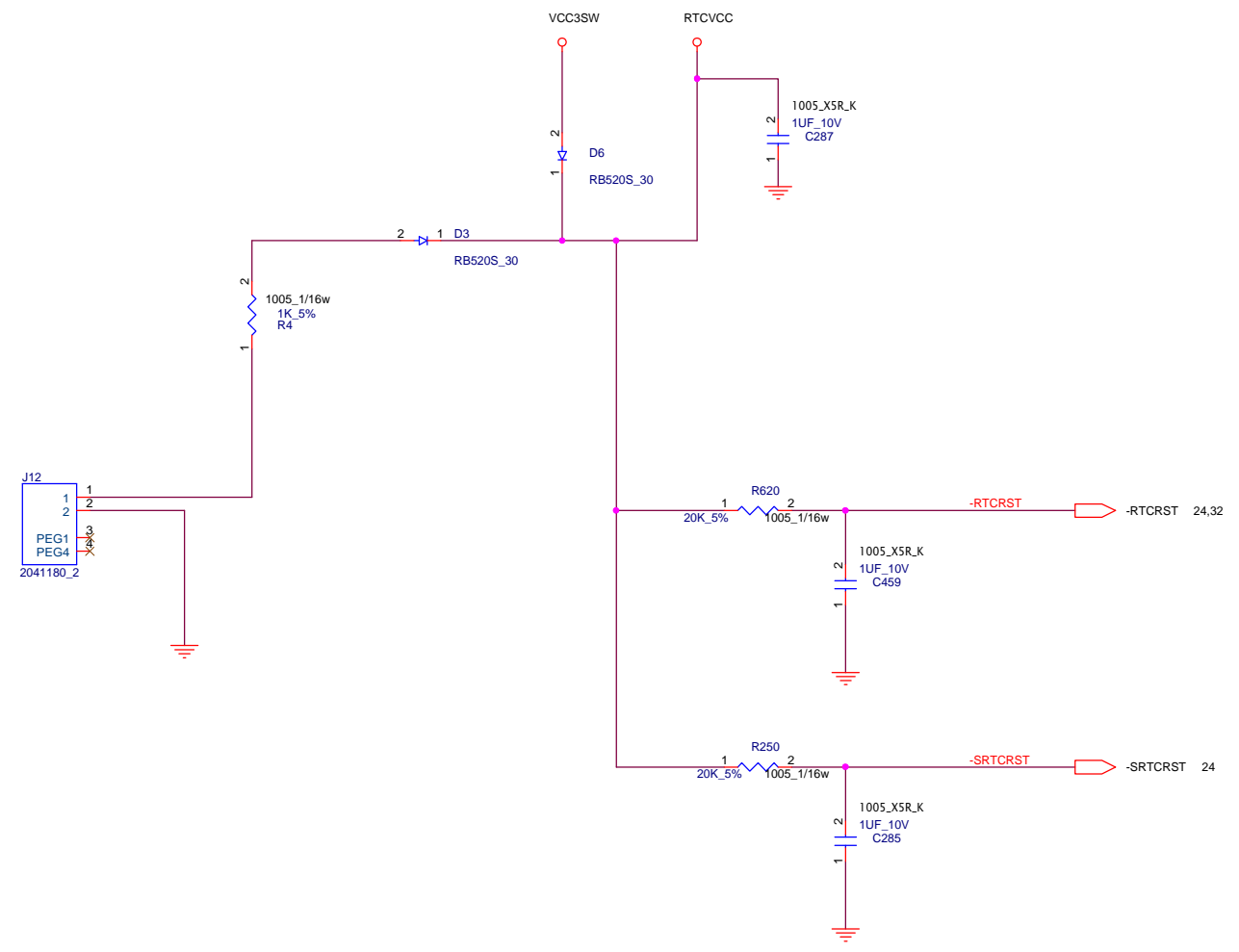
NEAR DOCK CONN

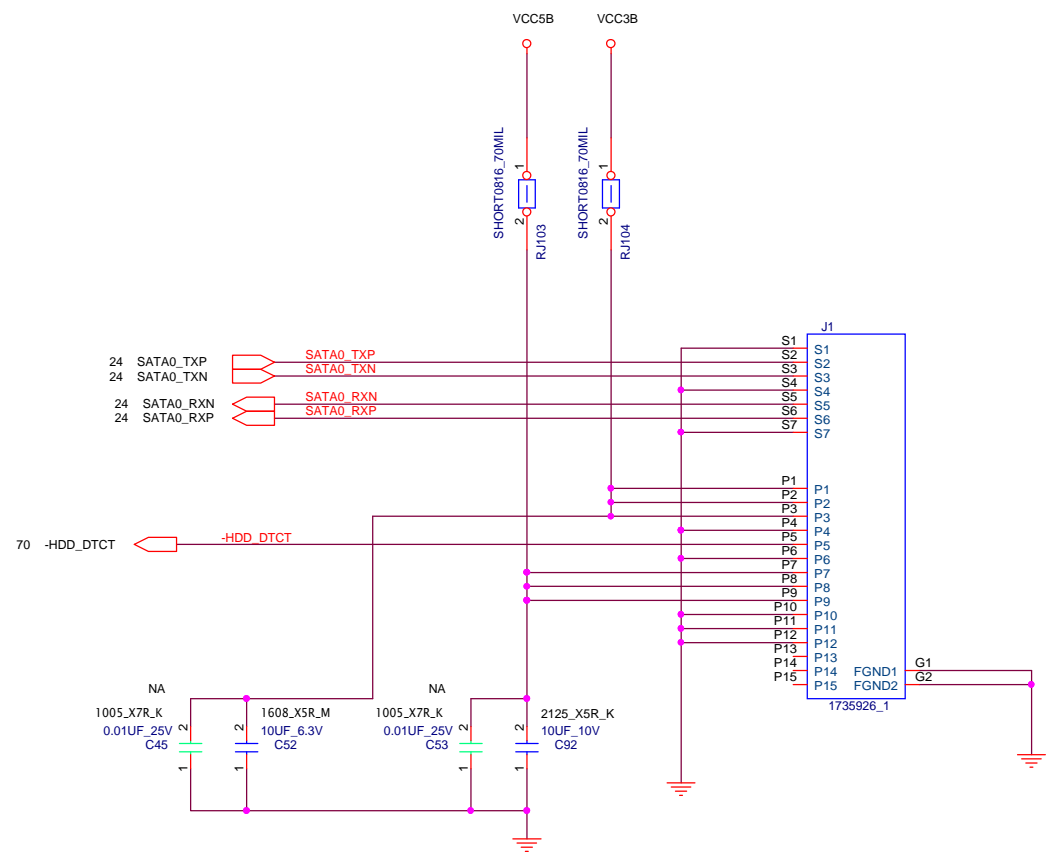
16	IFPD_DP3N	C841	2	1	0.1UF_10V	1005_X5R_K	DOCKA_DP3N	59
16	IFPD_DP3P	C843	2	1	0.1UF_10V	1005_X5R_K	DOCKA_DP3P	59
16	IFPD_DP2N	C883	2	1	0.1UF_10V	1005_X5R_K	DOCKA_DP2N	59
16	IFPD_DP2P	C887	2	1	0.1UF_10V	1005_X5R_K	DOCKA_DP2P	59
16	IFPD_DP1N	C901	2	1	0.1UF_10V	1005_X5R_K	DOCKA_DP1N	59
16	IFPD_DP1P	C900	2	1	0.1UF_10V	1005_X5R_K	DOCKA_DP1P	59
16	IFPD_DP0N	C903	2	1	0.1UF_10V	1005_X5R_K	DOCKA_DP0N	59
16	IFPD_DP0P	C902	2	1	0.1UF_10V	1005_X5R_K	DOCKA_DP0P	59

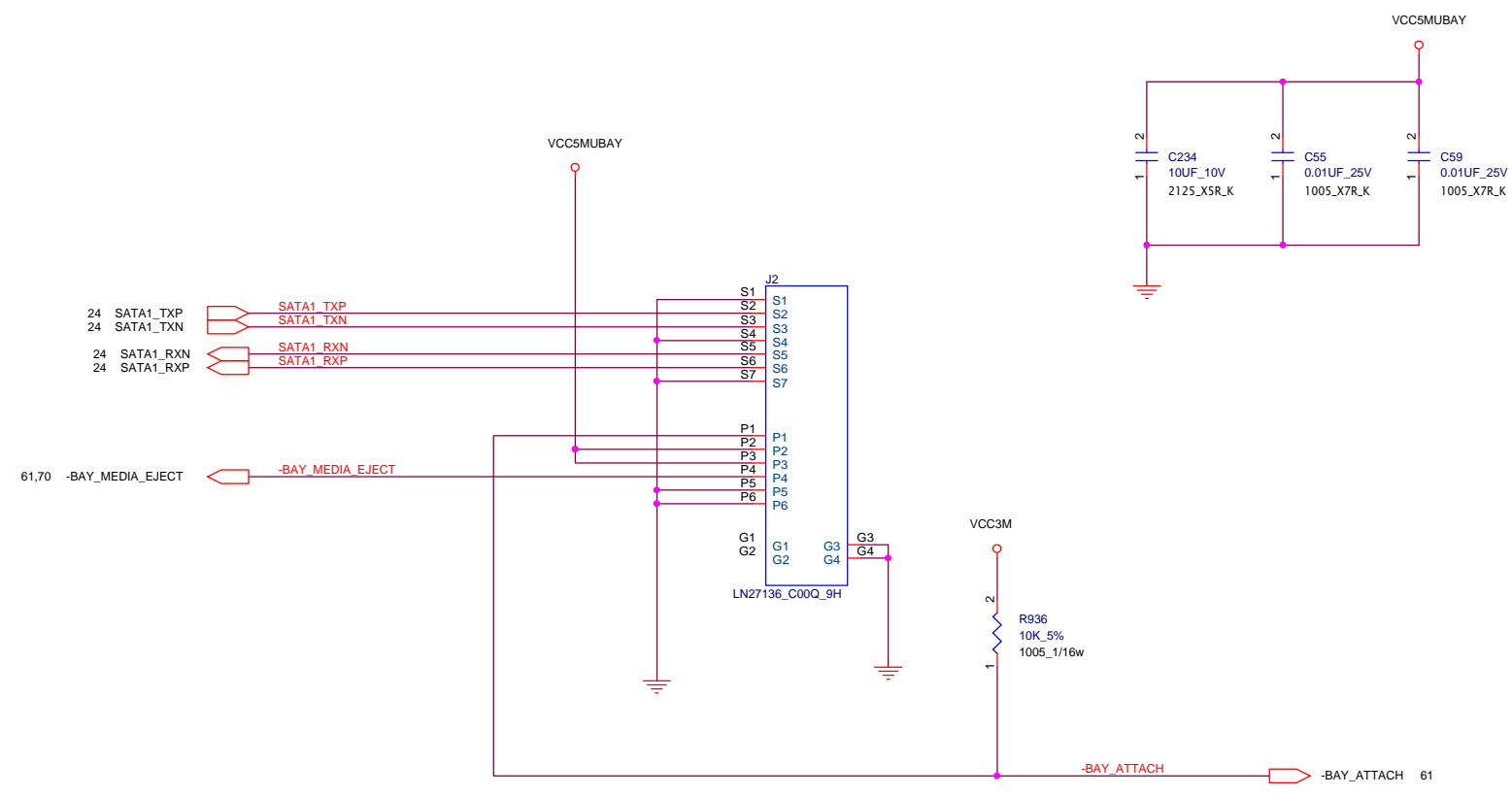
NEAR DOCK CONN

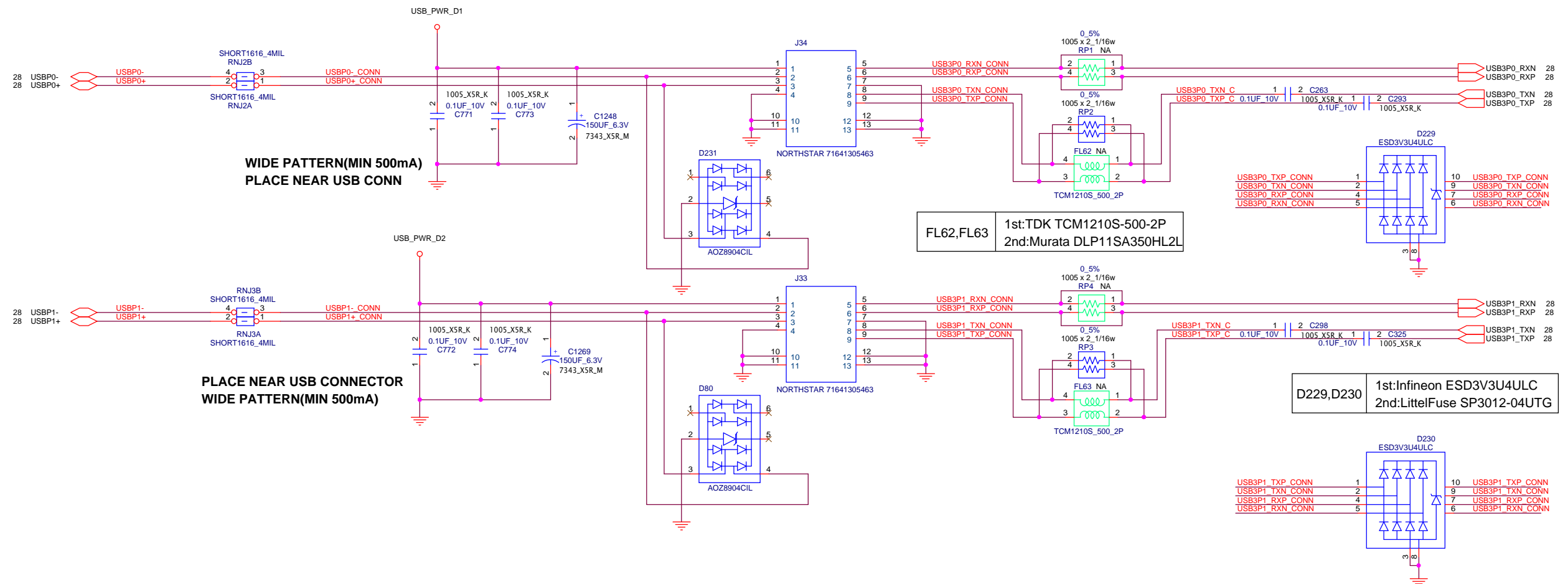
16	IFPE_DP3N	C904	2	1	0.1UF_10V	1005_X5R_K	DOCKB_DP3N	59
16	IFPE_DP3P	C907	2	1	0.1UF_10V	1005_X5R_K	DOCKB_DP3P	59
16	IFPE_DP2N	C909	2	1	0.1UF_10V	1005_X5R_K	DOCKB_DP2N	59
16	IFPE_DP2P	C906	2	1	0.1UF_10V	1005_X5R_K	DOCKB_DP2P	59
16	IFPE_DP1N	C922	2	1	0.1UF_10V	1005_X5R_K	DOCKB_DP1N	59
16	IFPE_DP1P	C920	2	1	0.1UF_10V	1005_X5R_K	DOCKB_DP1P	59
16	IFPE_DP0N	C919	2	1	0.1UF_10V	1005_X5R_K	DOCKB_DP0N	59
16	IFPE_DP0P	C918	2	1	0.1UF_10V	1005_X5R_K	DOCKB_DP0P	59









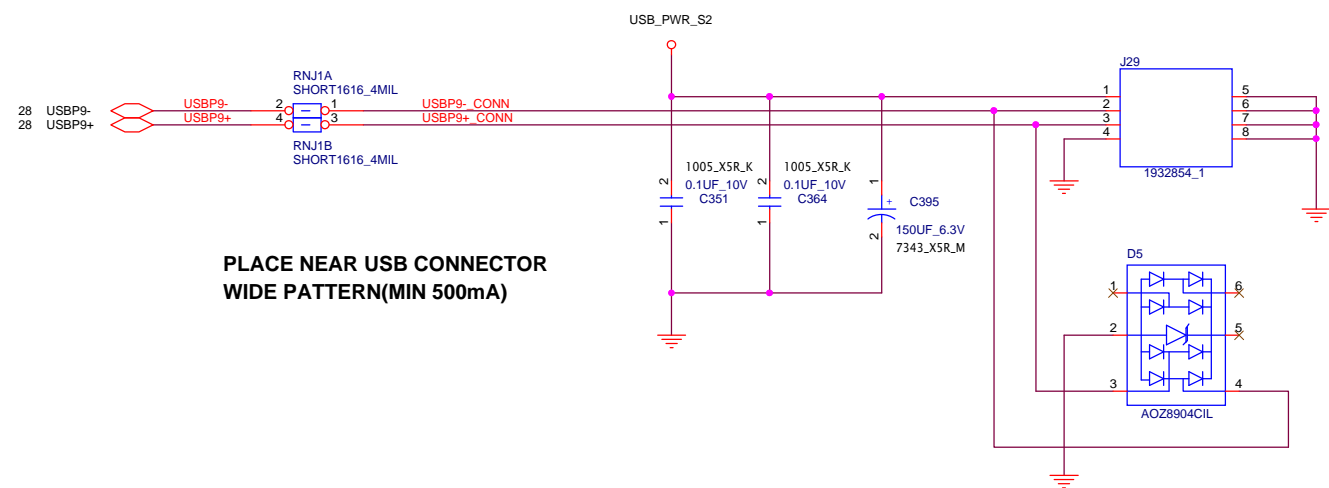


**WIDE PATTERN(MIN 500mA)
PLACE NEAR USB CONN**

**PLACE NEAR USB CONNECTOR
WIDE PATTERN(MIN 500mA)**

FL62,FL63
1st:TDK TCM1210S-500-2P
2nd:Murata DLP11SA350HL2L

D229,D230
1st:Infineon ESD3V3U4ULC
2nd:Littelfuse SP3012-04UTG



**PLACE NEAR USB CONNECTOR
WIDE PATTERN(MIN 500mA)**

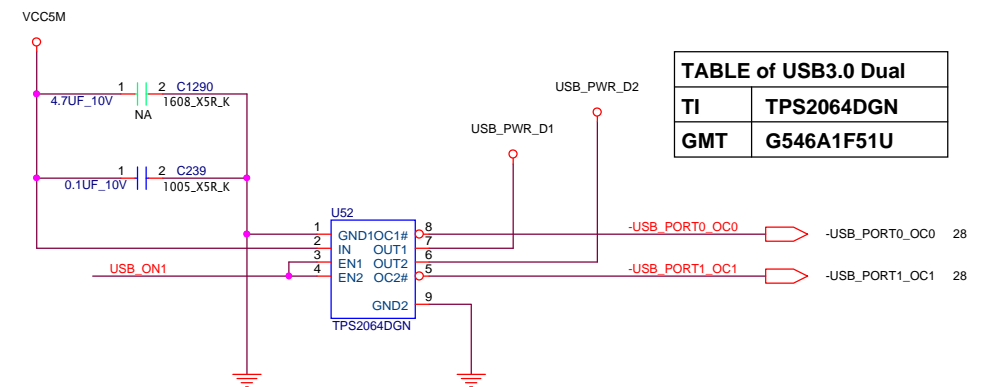
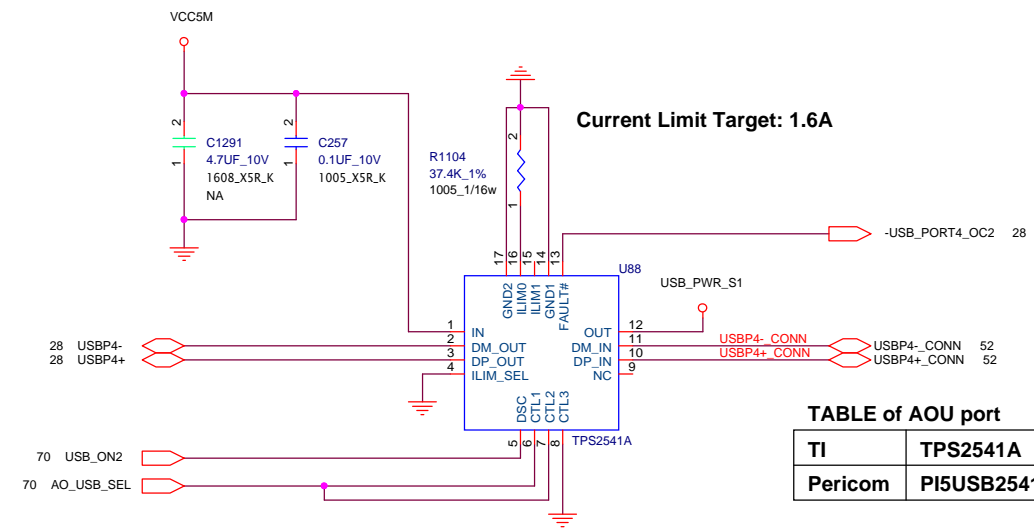


TABLE of USB3.0 Dual	
TI	TPS2064DGN
GMT	G546A1F51U

FOR ON BOARD DUAL USB 3.0 CONNECTOR



Current Limit Target: 1.6A

TABLE of AO port	
TI	TPS2541A
Pericom	PI5USB2541

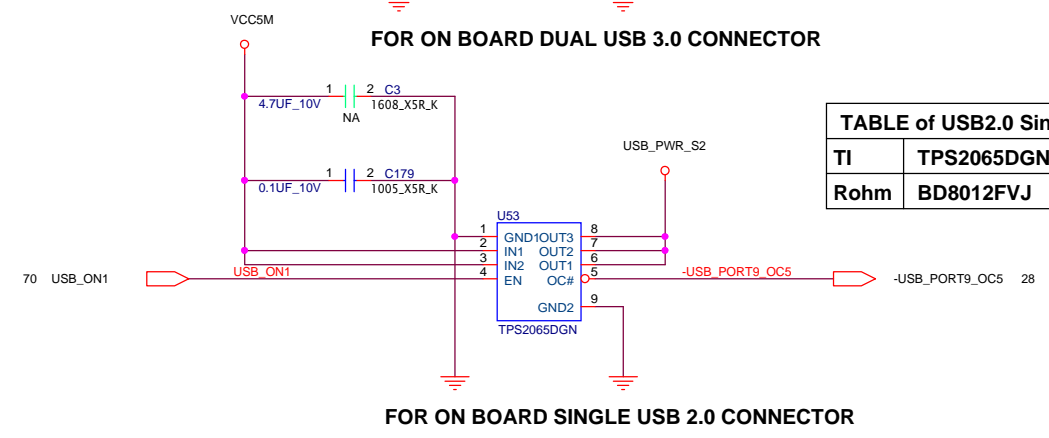


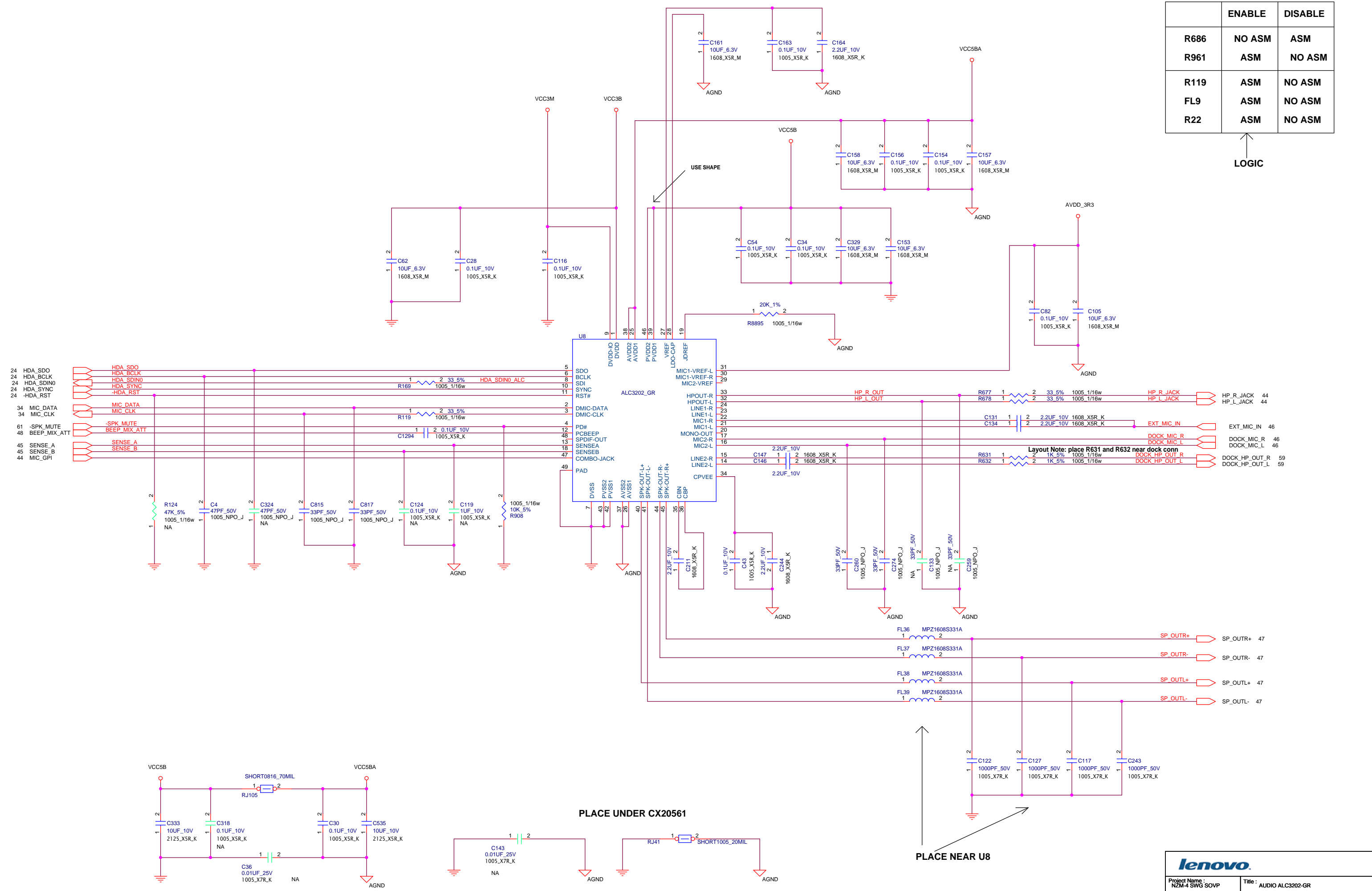
TABLE of USB2.0 Single	
TI	TPS2065DGN
Rohm	BD8012FVJ

FOR ON BOARD SINGLE USB 2.0 CONNECTOR

TABLE MIC HW ENABLE/DISABLE

	ENABLE	DISABLE
R686	NO ASM	ASM
R961	ASM	NO ASM
R119	ASM	NO ASM
FL9	ASM	NO ASM
R22	ASM	NO ASM

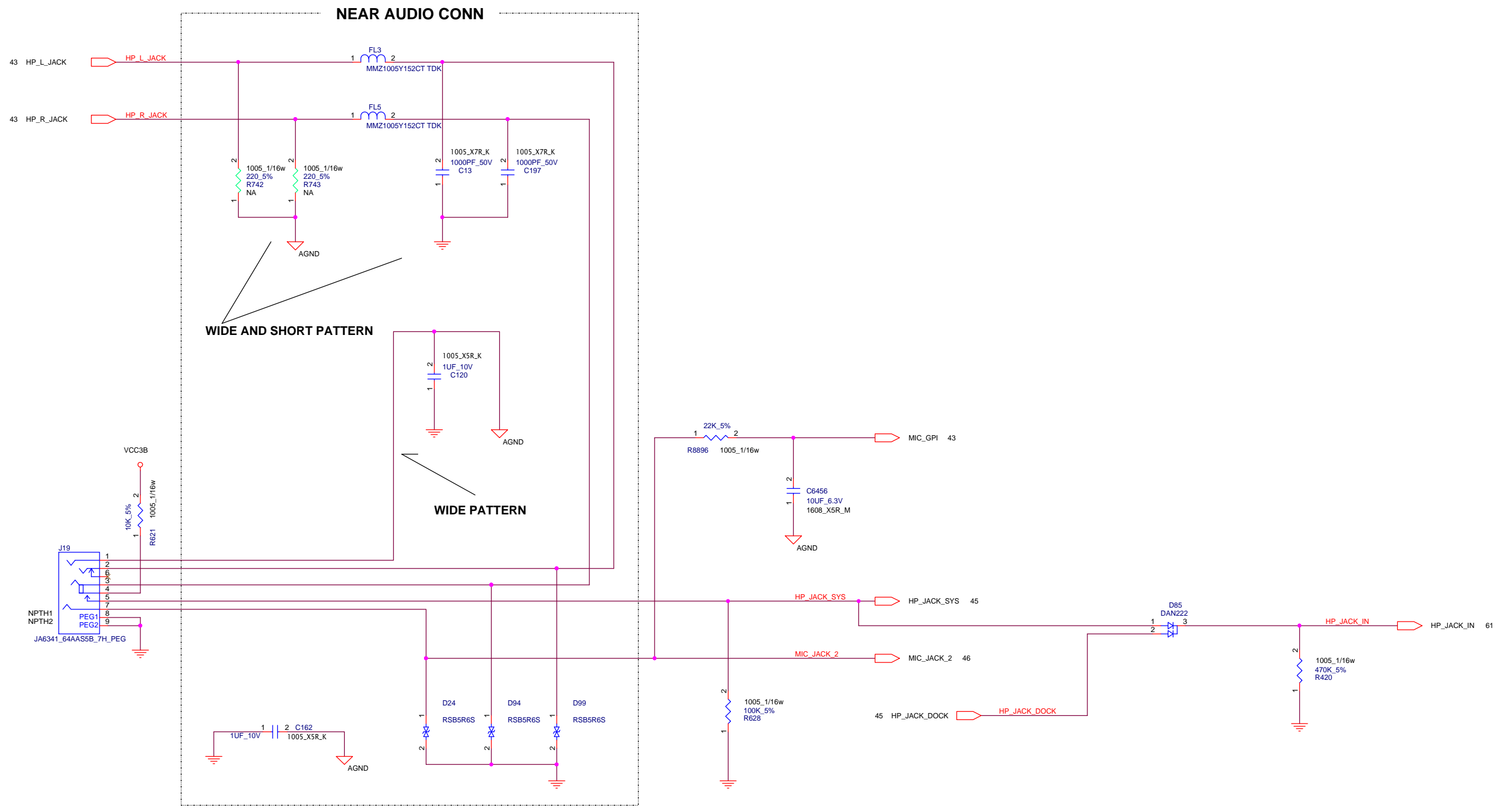
LOGIC

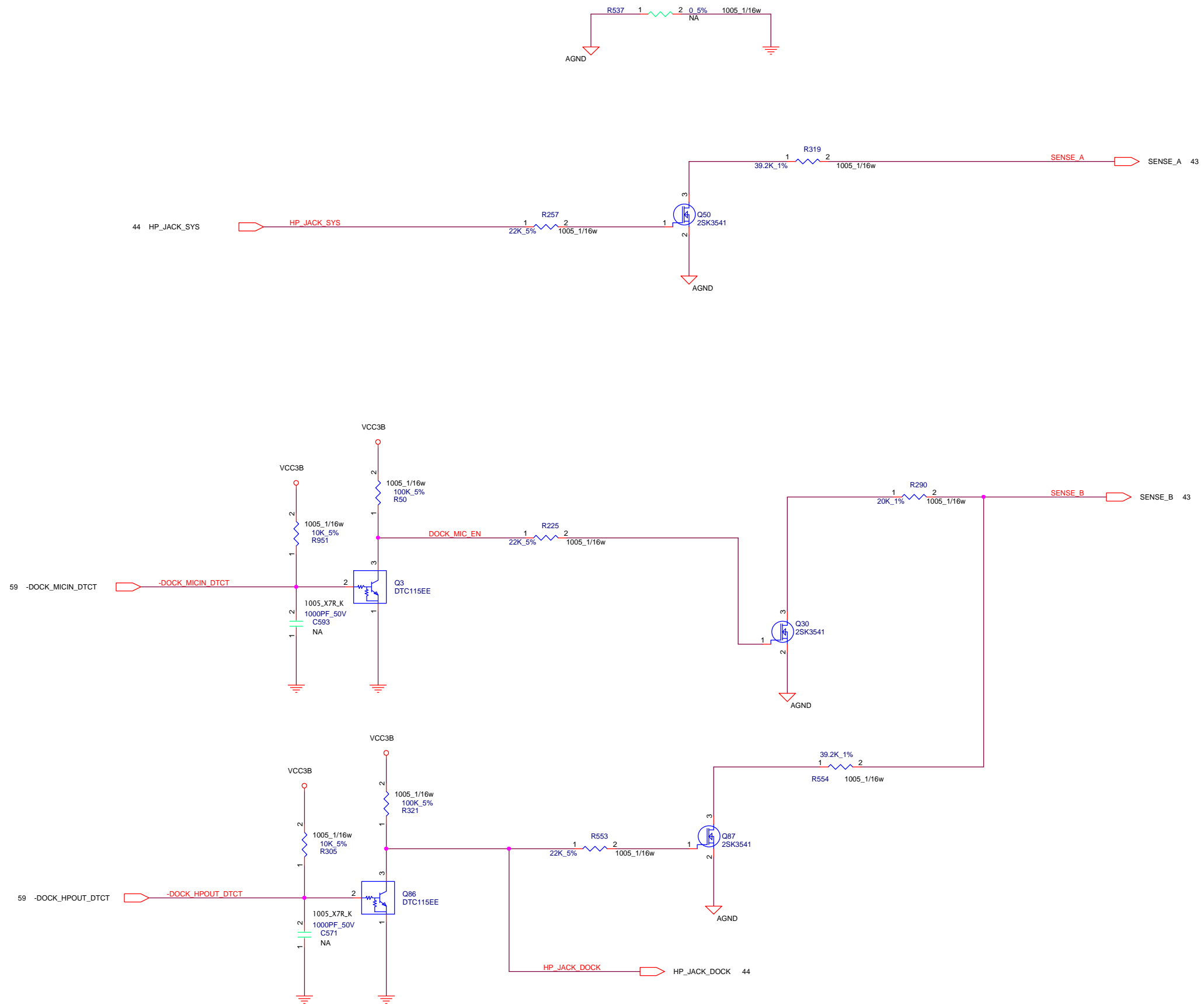


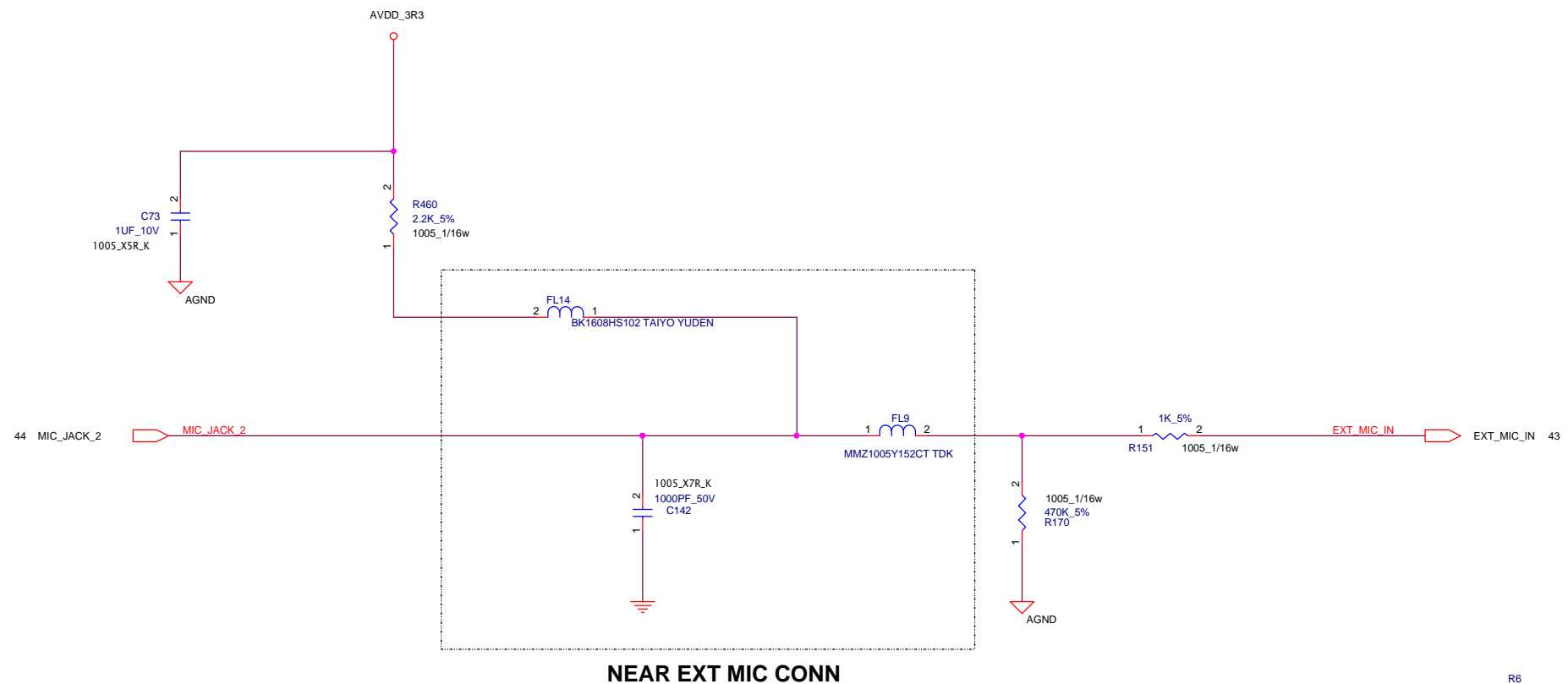
Layout Note: place R631 and R632 near dock conn

PLACE UNDER CX20561

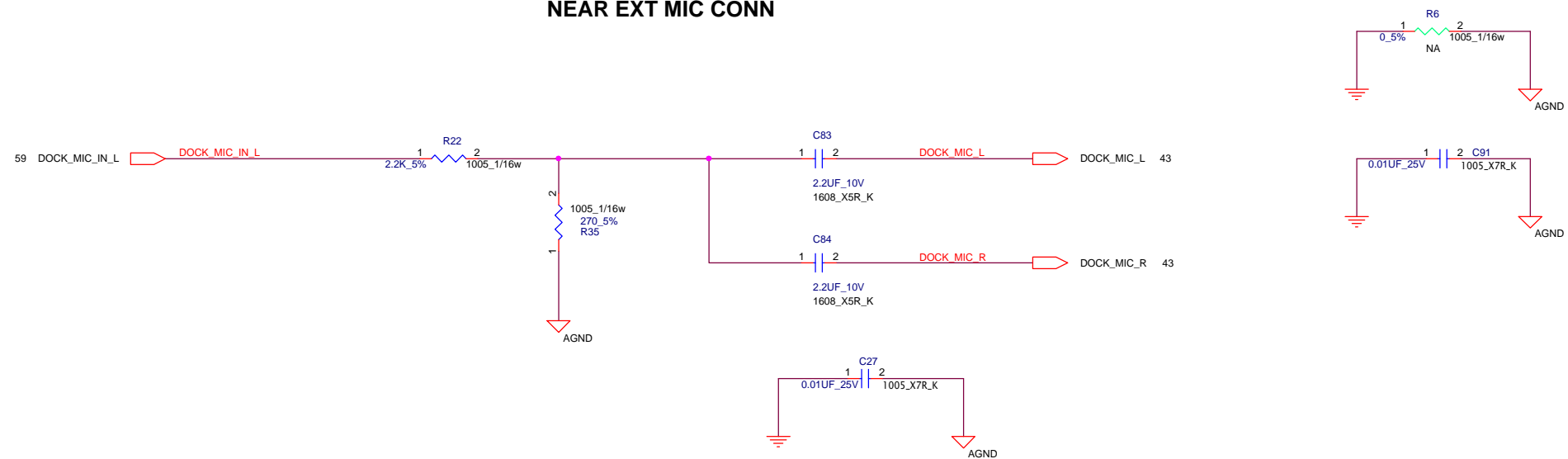
PLACE NEAR U8

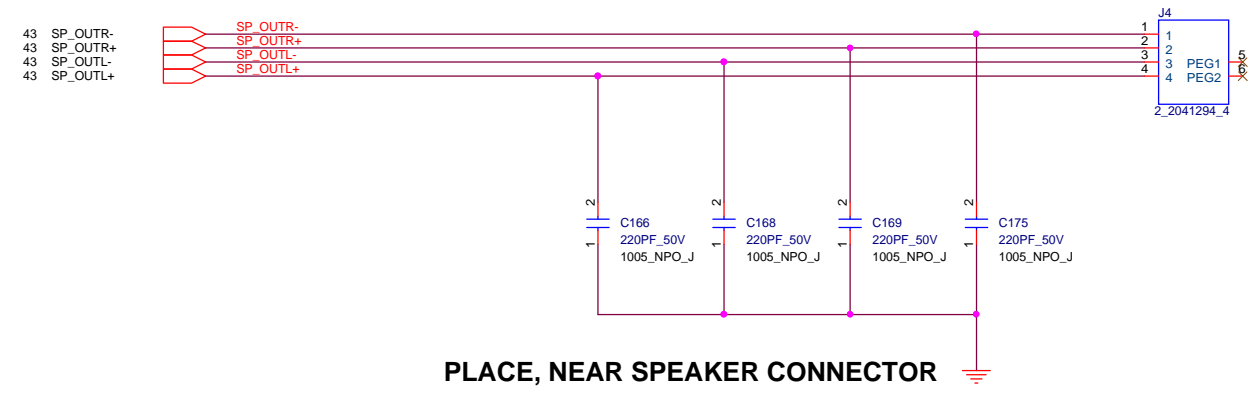


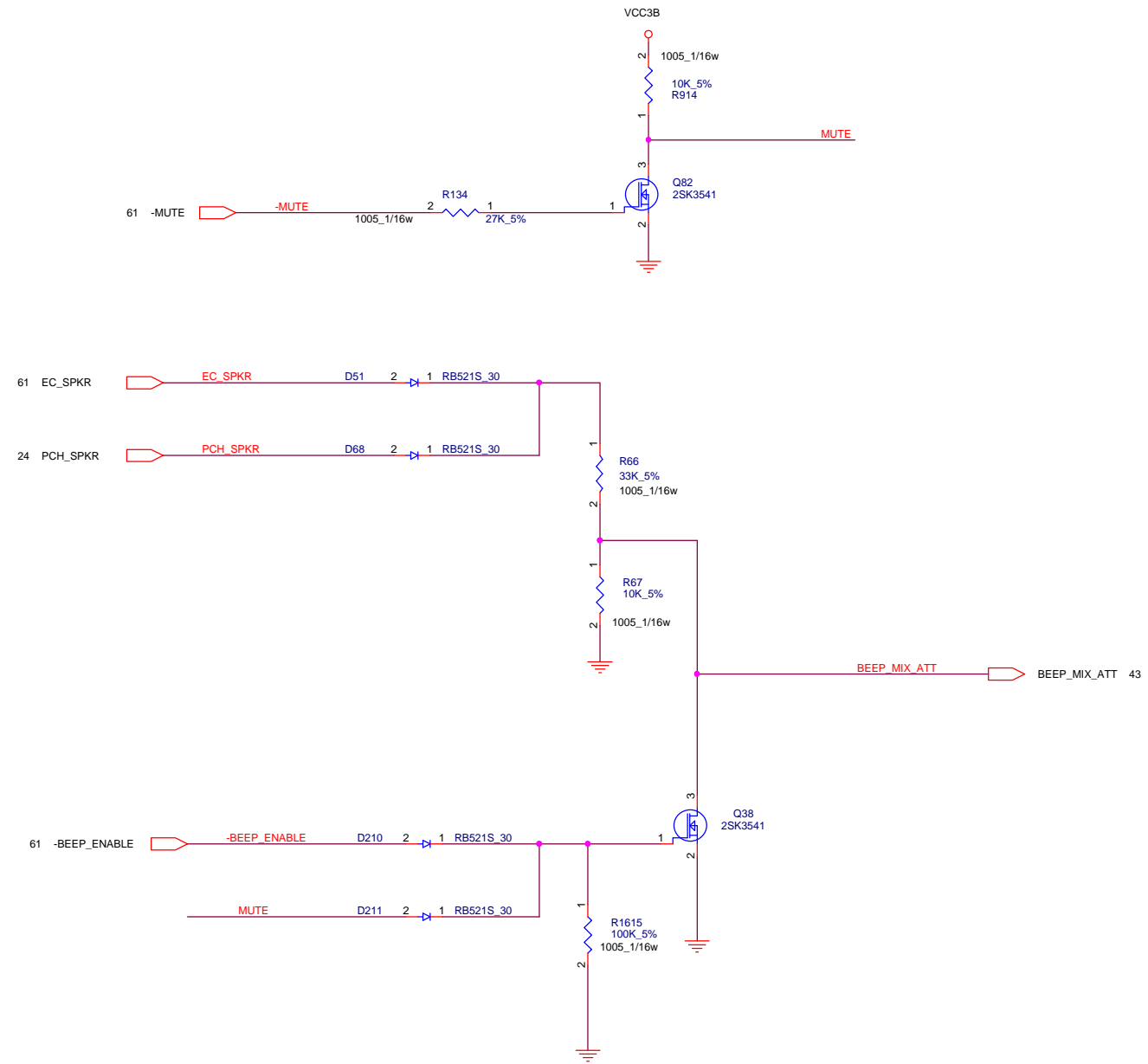


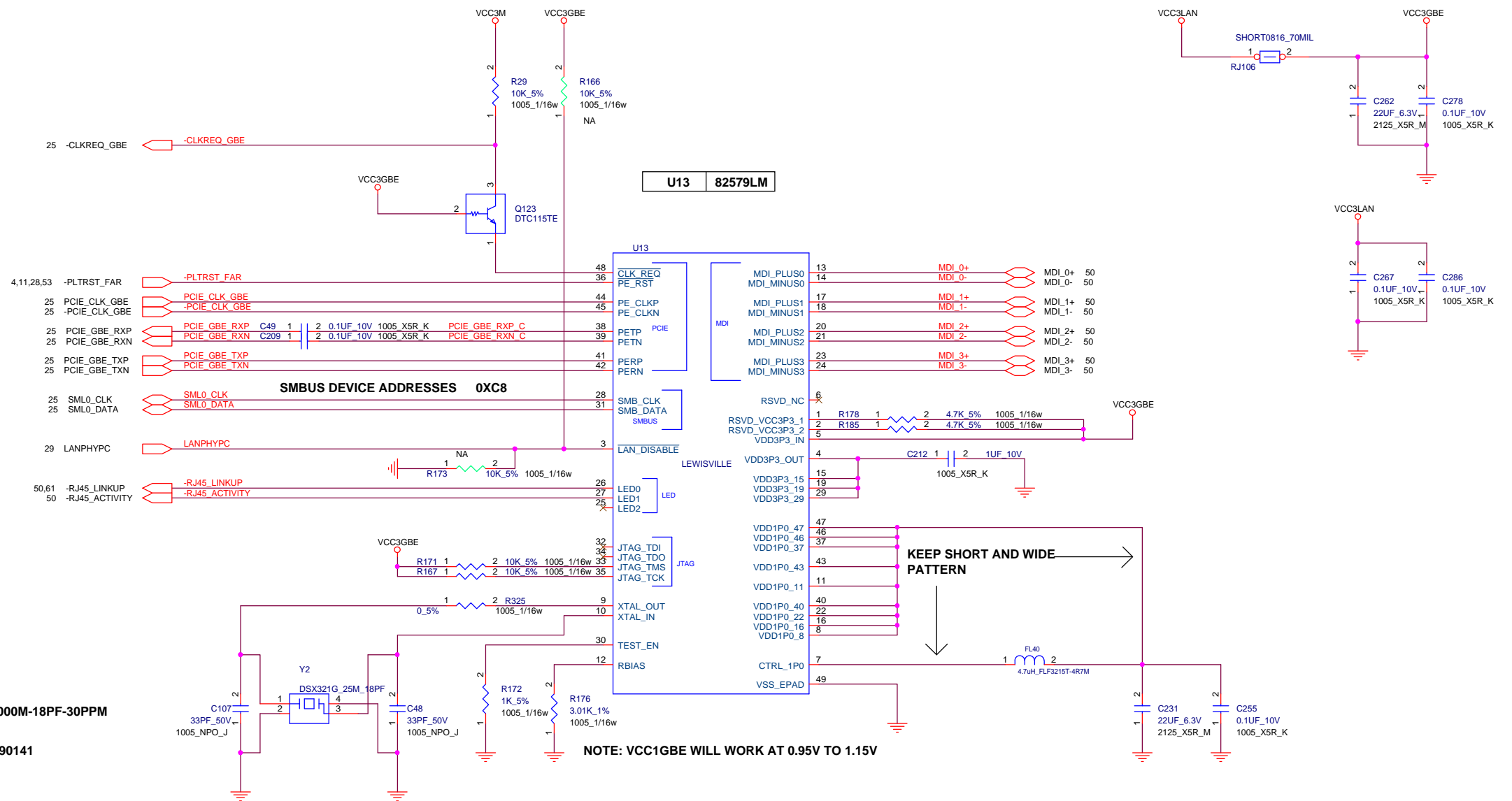


NEAR EXT MIC CONN





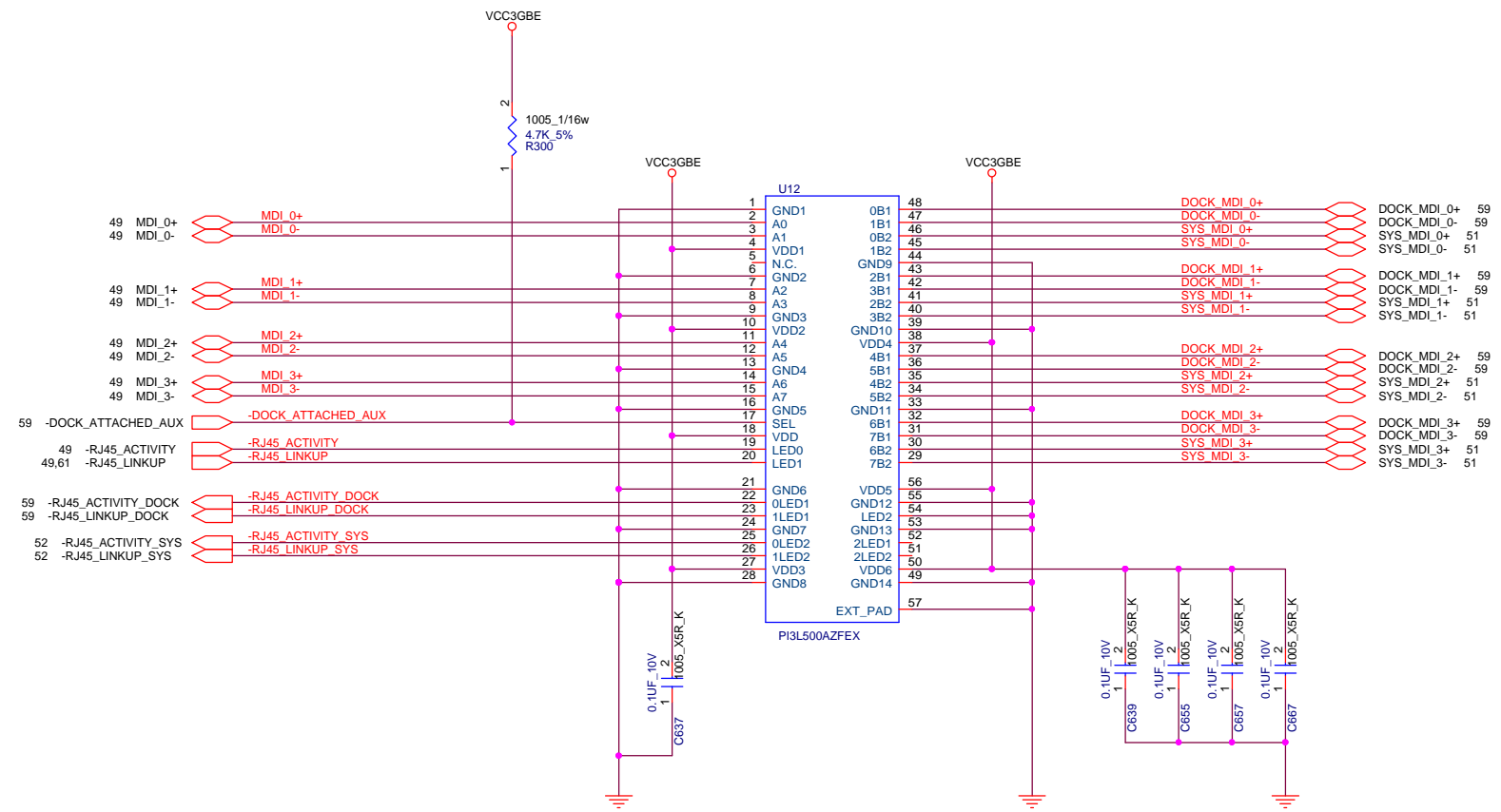




P/N 41U6141
 KDS DSX321G-25.000M-18PF-30PPM
 TXC 7V25020001
 RIVER FCX-04-25MJ90141

NOTE: VCC1GBE WILL WORK AT 0.95V TO 1.15V

KEEP SHORT AND WIDE PATTERN



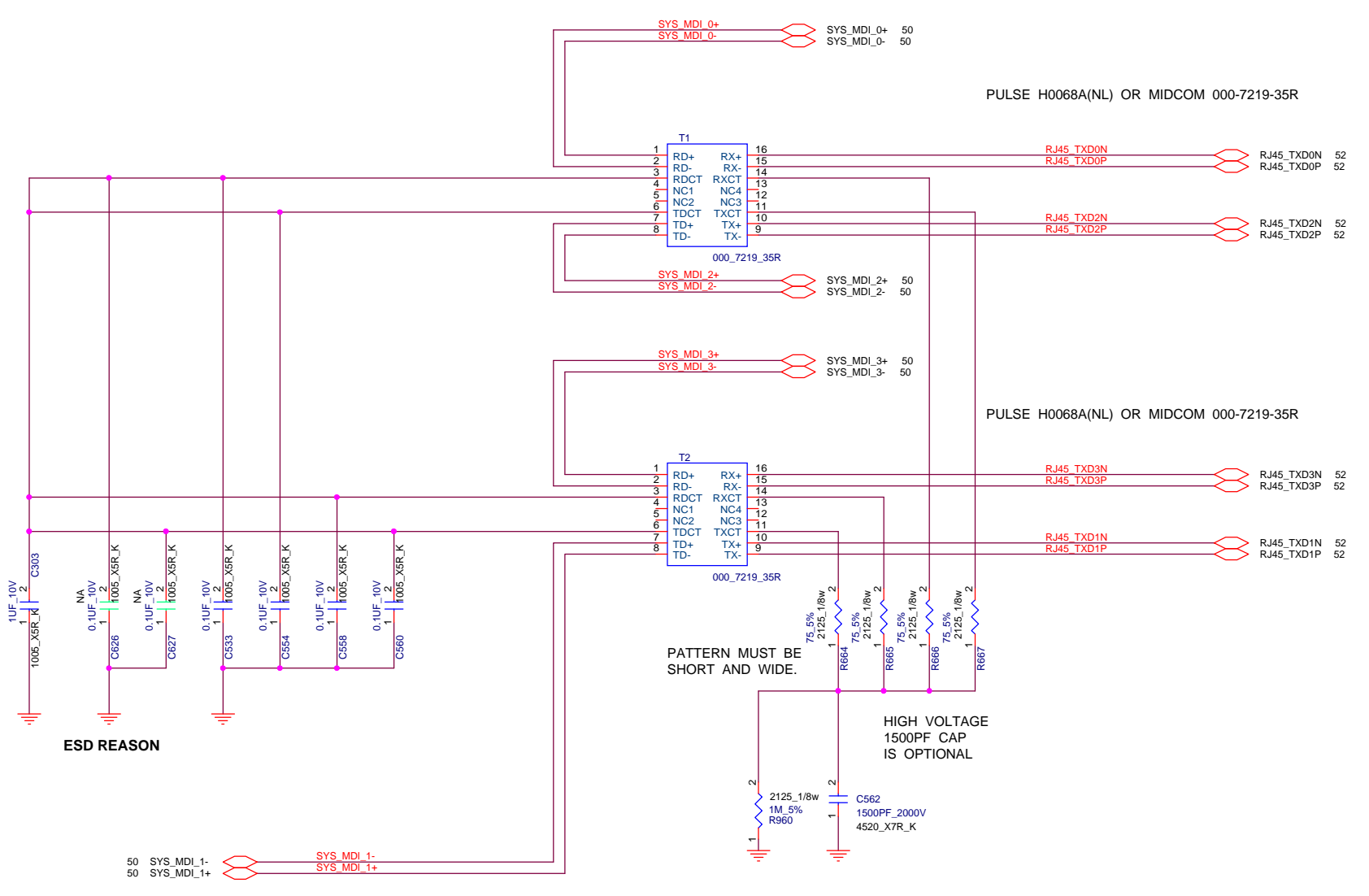
Project Name : NZM-4 SWG SOVP Title : GBE LAN SWITCH

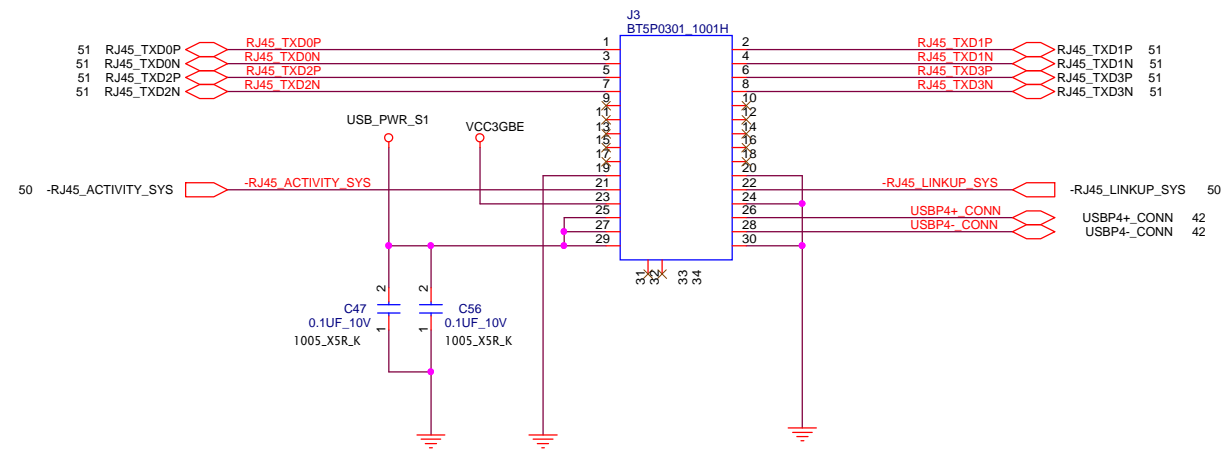
Size : C Document Number : Rev : 7.54

Date : Monday, March 19, 2012 Sheet : 50 of 97

THE WIDTH OF THESE TRACE SHOULD BE WIDER THAN 35MIL TO PREVENT VOLTAGE DROP.

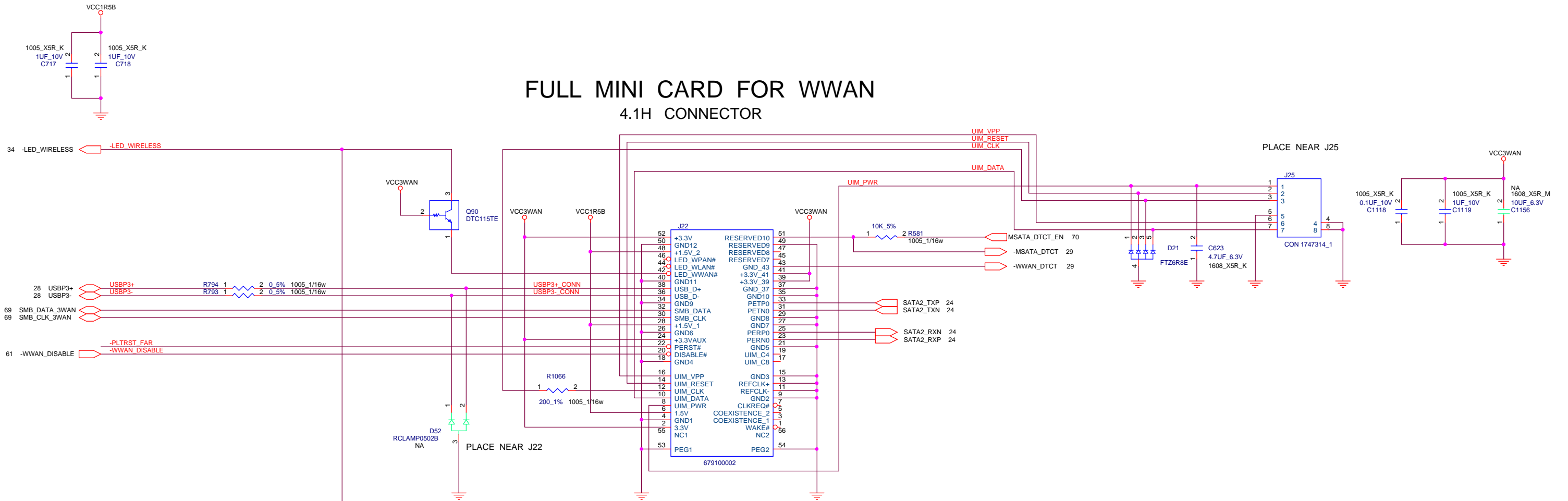
C303 SHOULD BE PLACED AS CLOSE TO MAGNETICS AS POSSIBLE.



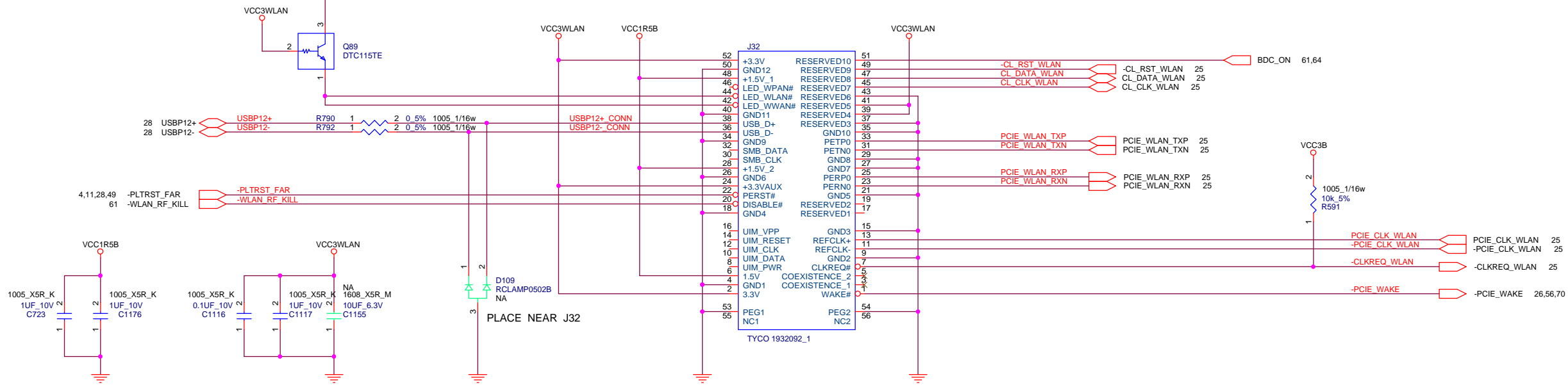


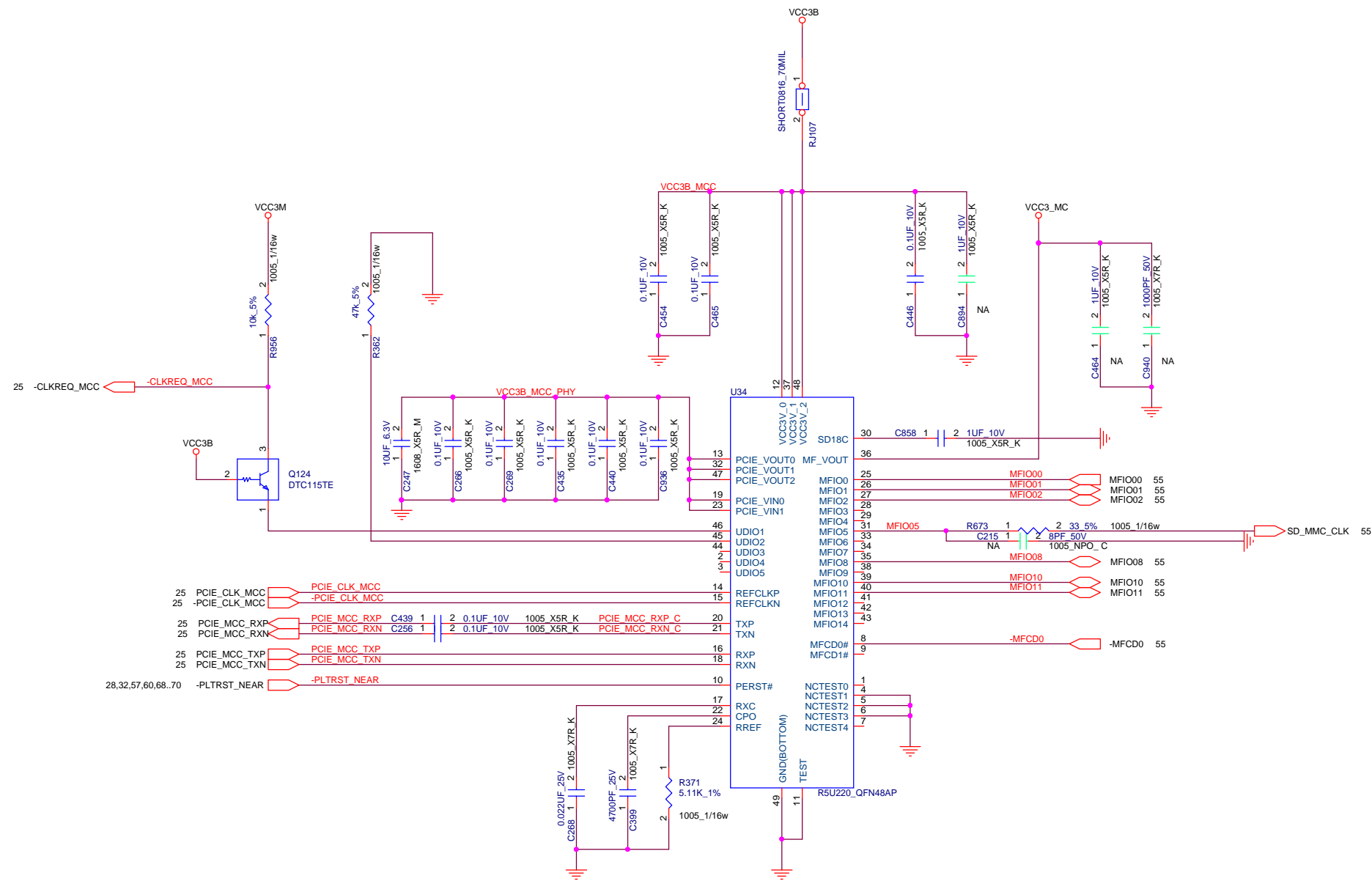
PLACE NEAR SUB CARD CONNECTOR
WIDE PATTERN(MIN 500mA)

FULL MINI CARD FOR WWAN 4.1H CONNECTOR



HALF MINI CARD FOR WLAN 7.0H CONNECTOR





TABLE

MEDIA I/F	SD/MMC	MEMORSTICK	XD
MFIO00	SDWP#	MSBS	XD_D7
MFIO01	SD_D1	XD_D6	
MFIO02	SD_D0	MS_D1	XD_D5
MFIO03	(SD_D7)	(XD_D4)	
MFIO04	(SD_D6)	(MS_D5)	XD_D3
MFIO05	SD_CLK	MS_D0	XD_D2
MFIO06		XD_D1	
MFIO07	(SD_D5)	(MS_D4)	XD_D0
MFIO08	SD_CMD	MS_D2	XD_WP#
MFIO09	(SD_D4)	(MS_D6)	XD_WE#
MFIO10	SD_D3	MS_D3	XD_ALE
MFIO11	SD_D2		XD_CLE
MFIO12			XD_CE#
MFIO13		(MS_D7)	XD_RE#
MFIO14		MS_CLK	XD_RB#
MFCD0#	SDCD#		XDCD0#
MFCD1#		MSINS#	XDCD1#

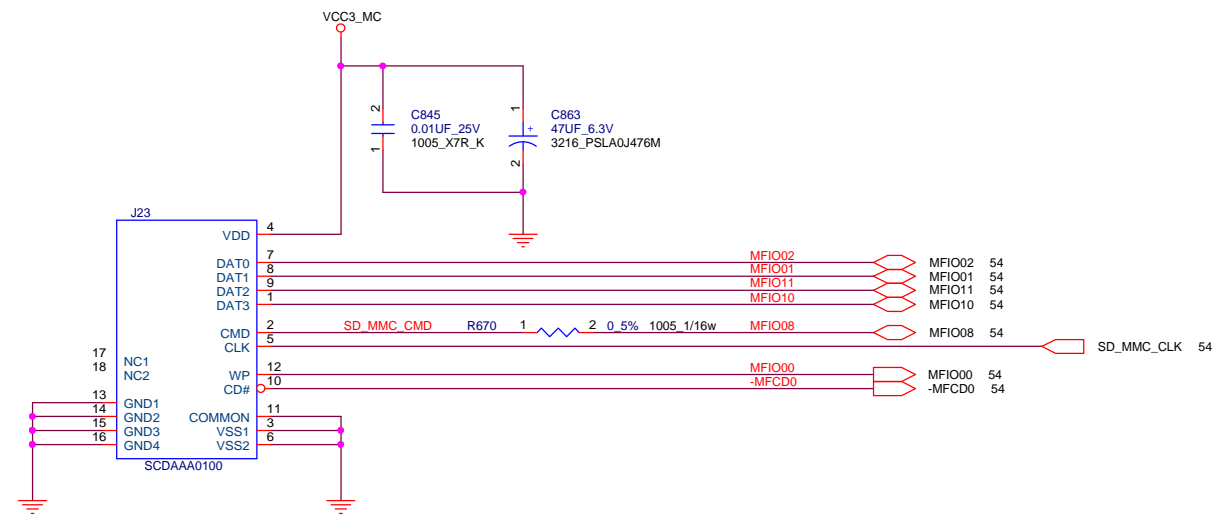
UDIO Pin Assignment Table

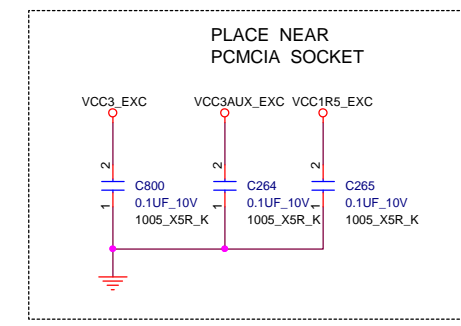
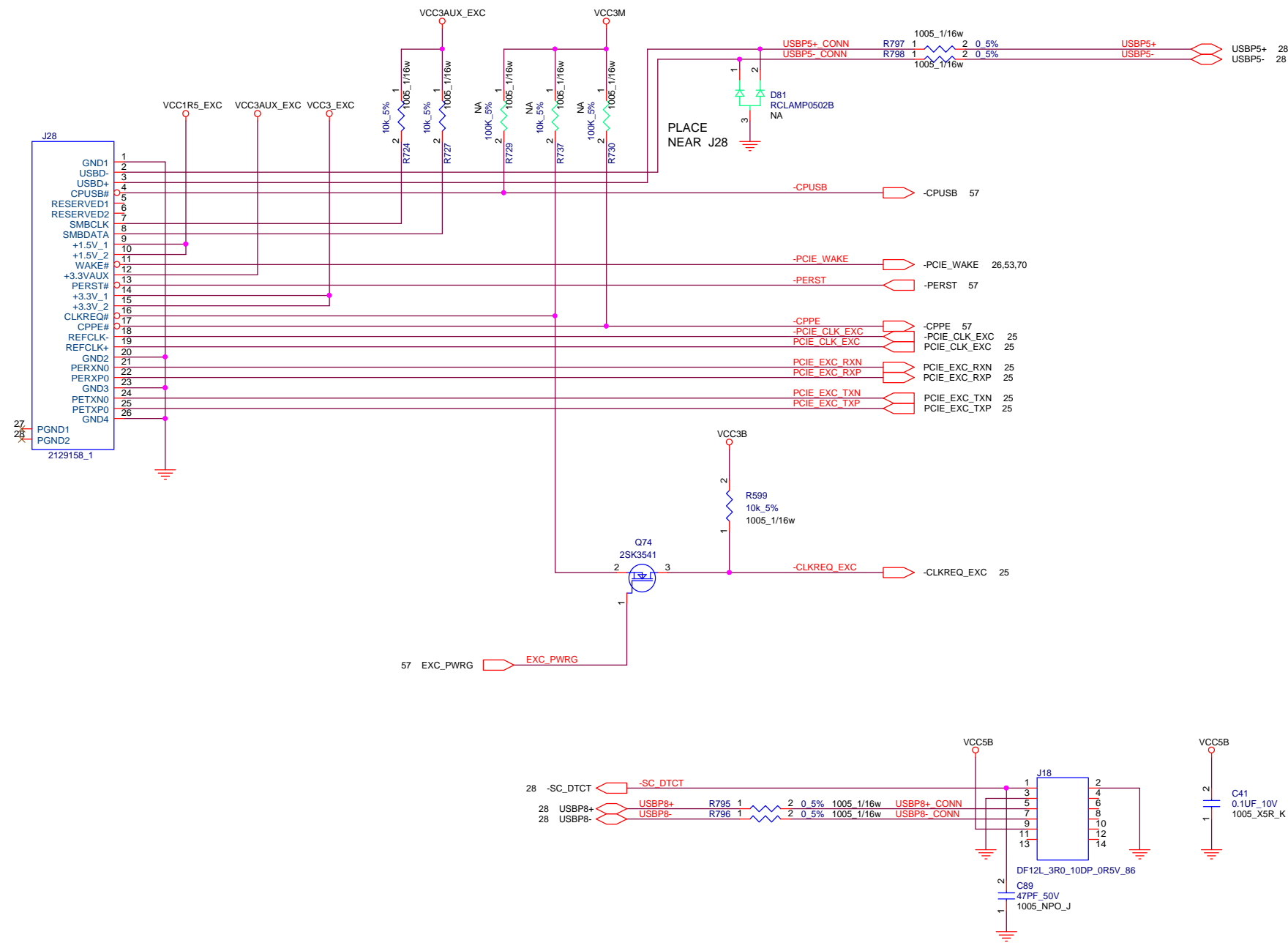
UDIO	Default
01	CLKREQ#
02	SCL/SROM_EN
03	SDA

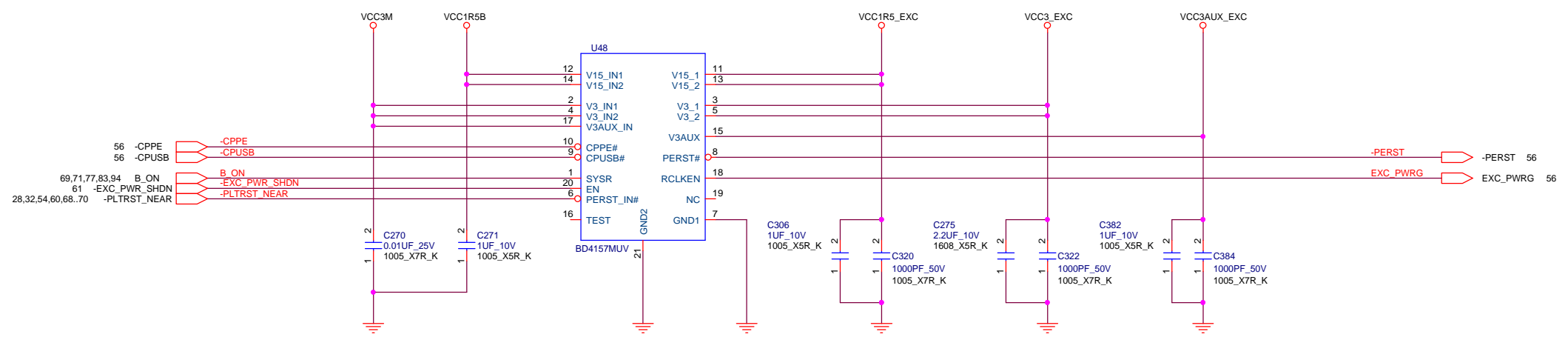
MFCDxN Detection Table

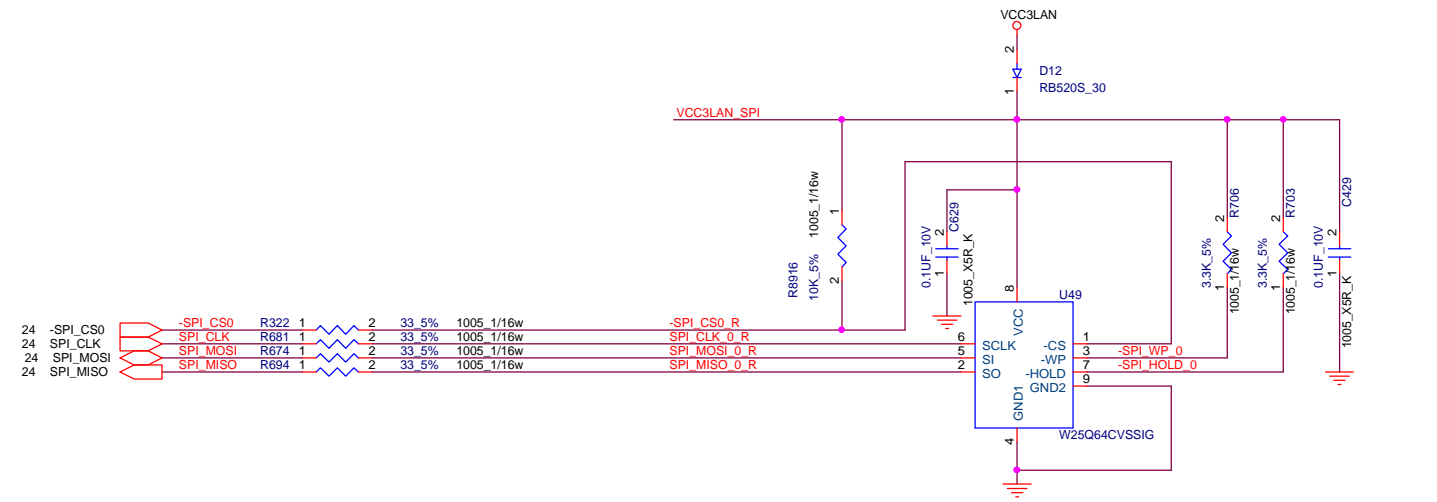
MFCDxN	Card Type
1 0	(No Card)
H H	SD Card/MMC
H L	MemoryStick
L L	XD Card





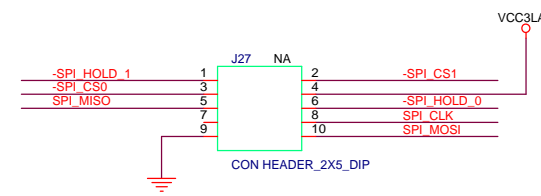






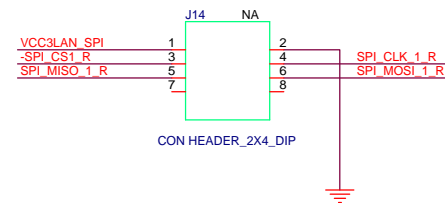
8MB SOIC8
 MACRONIX MX25L6406EM2I-12G
 WINBOND W25Q64CVSSIG

16MB WSON8
 MACRONIX MX25L12835EZNI-10G
 WINBOND W25Q128BVEIG



TABLE

EM100 PIN HEADER INTERFACE (TOP VIEW)			
1	(HOLD1#)	(CS1#)	2
3	CS0#	VCC	4
5	MISO	HOLD0#	6
7	WP0#	CLK	8
9	GND	MOSI	10



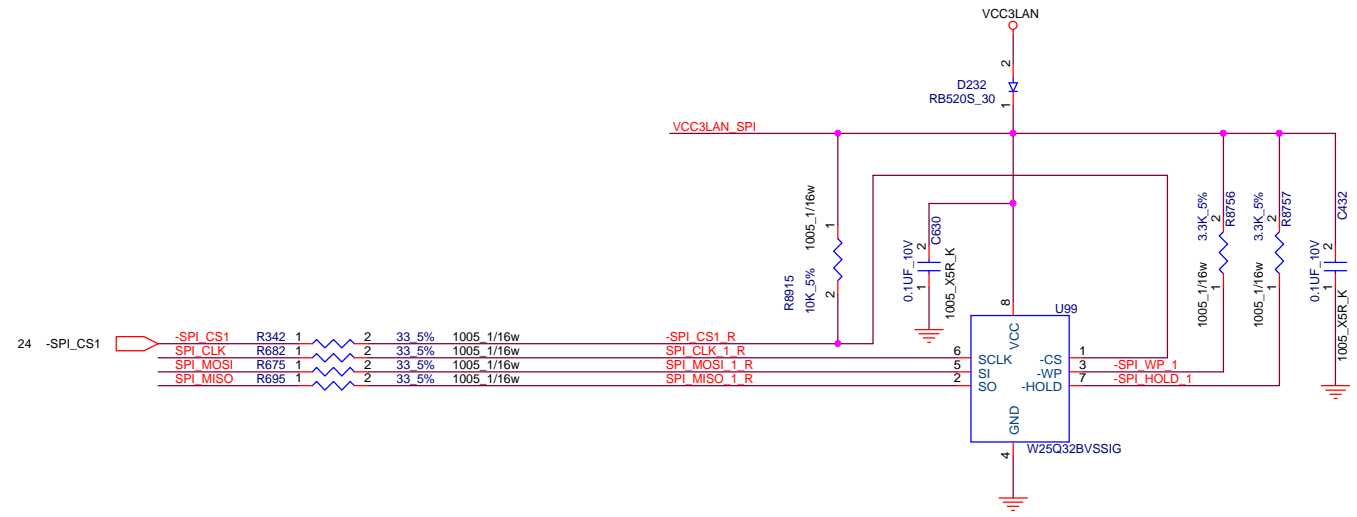
TABLE

SF100 PIN HEADER INTERFACE (TOP VIEW)			
1	VCC	D232.1	GND
3	CS#	R342.2	GND
5	MISO	R695.2	CLK
7	(KEY)	N/A	R675.2 MOSI
			(RESET) 8

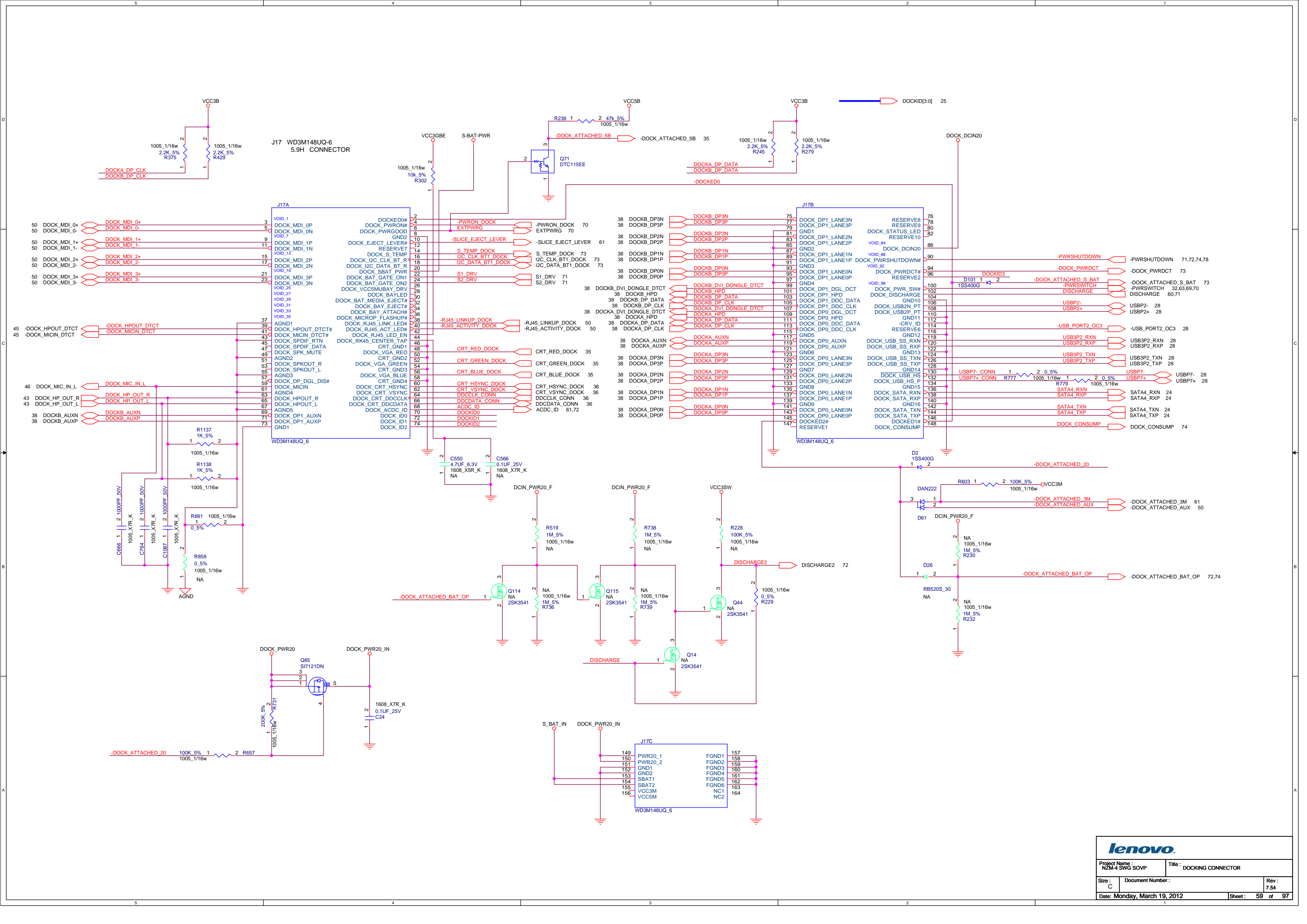
TABLE

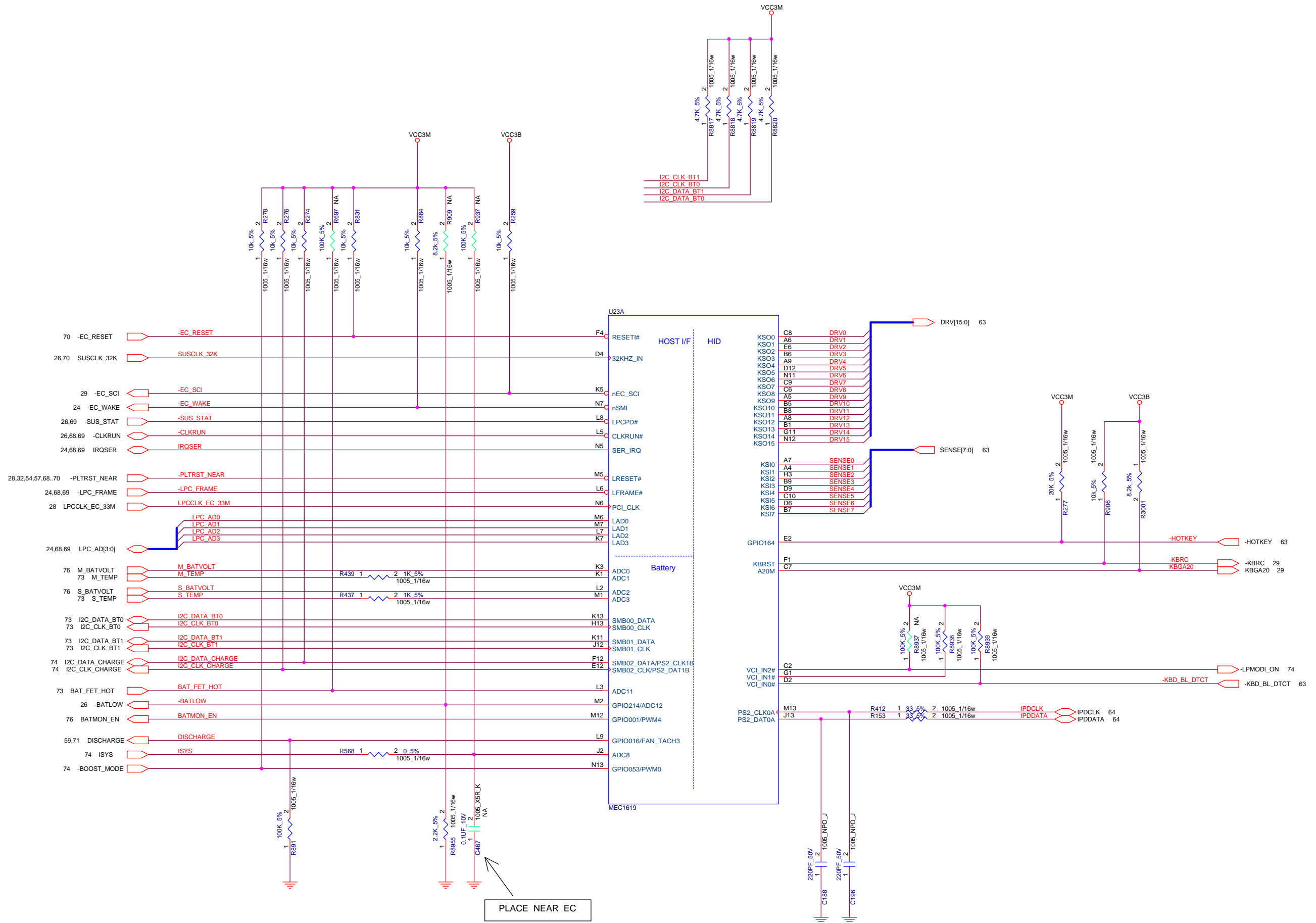
	CONFIG-1 16MB	CONFIG-2 12MB	CONFIG-3 8MB
U49 U99	16MB NO_ASM	8MB 4MB	8MB NO_ASM
D232 R8756 R8757 R8915 C432 C630	NO_ASM NO_ASM NO_ASM NO_ASM NO_ASM NO_ASM	ASM ASM ASM ASM ASM ASM	NO_ASM NO_ASM NO_ASM NO_ASM NO_ASM NO_ASM
R342 R682 R675 R695	NO_ASM NO_ASM NO_ASM NO_ASM	ASM ASM ASM ASM	NO_ASM NO_ASM NO_ASM NO_ASM

↑
LOGIC

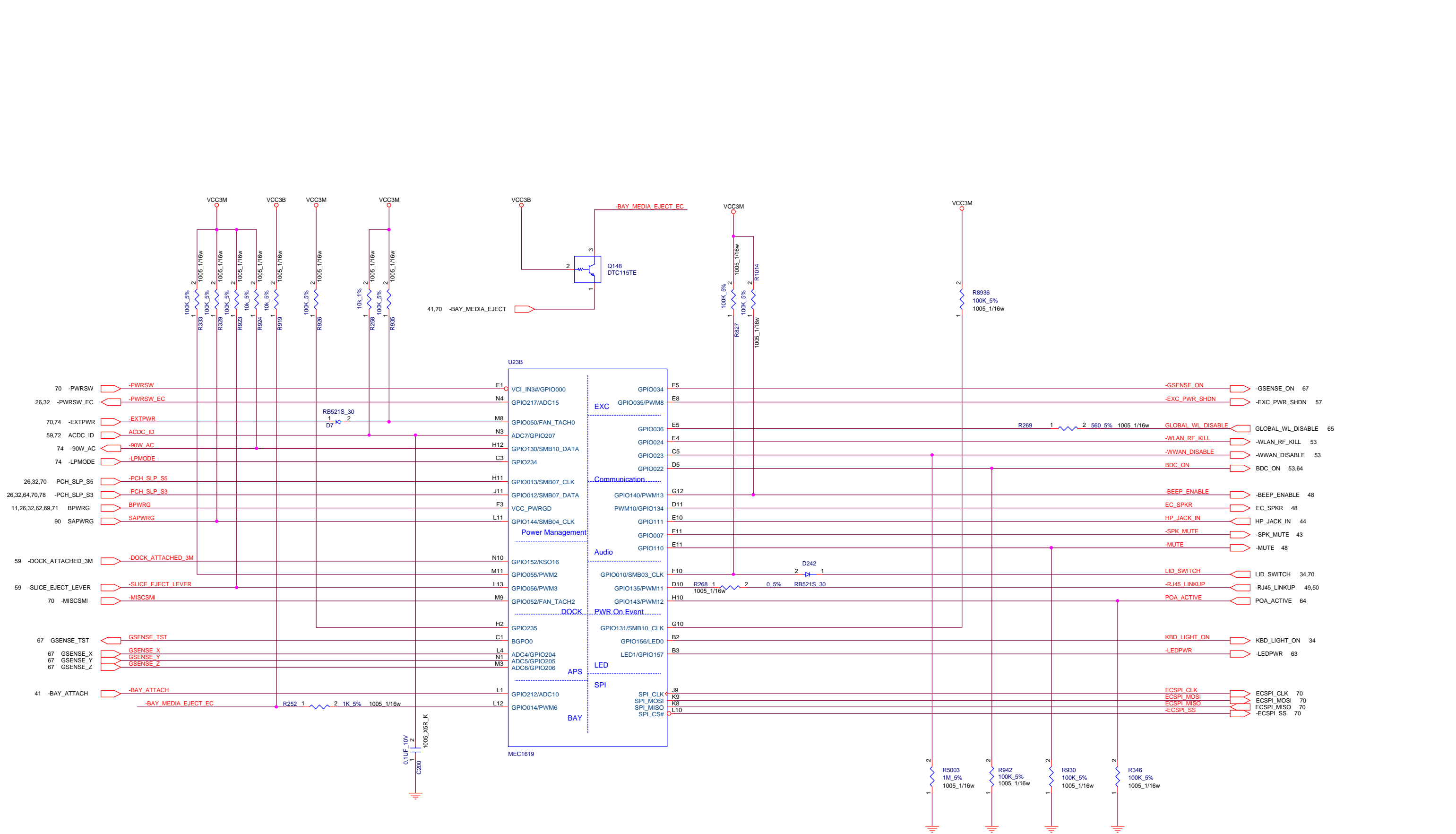


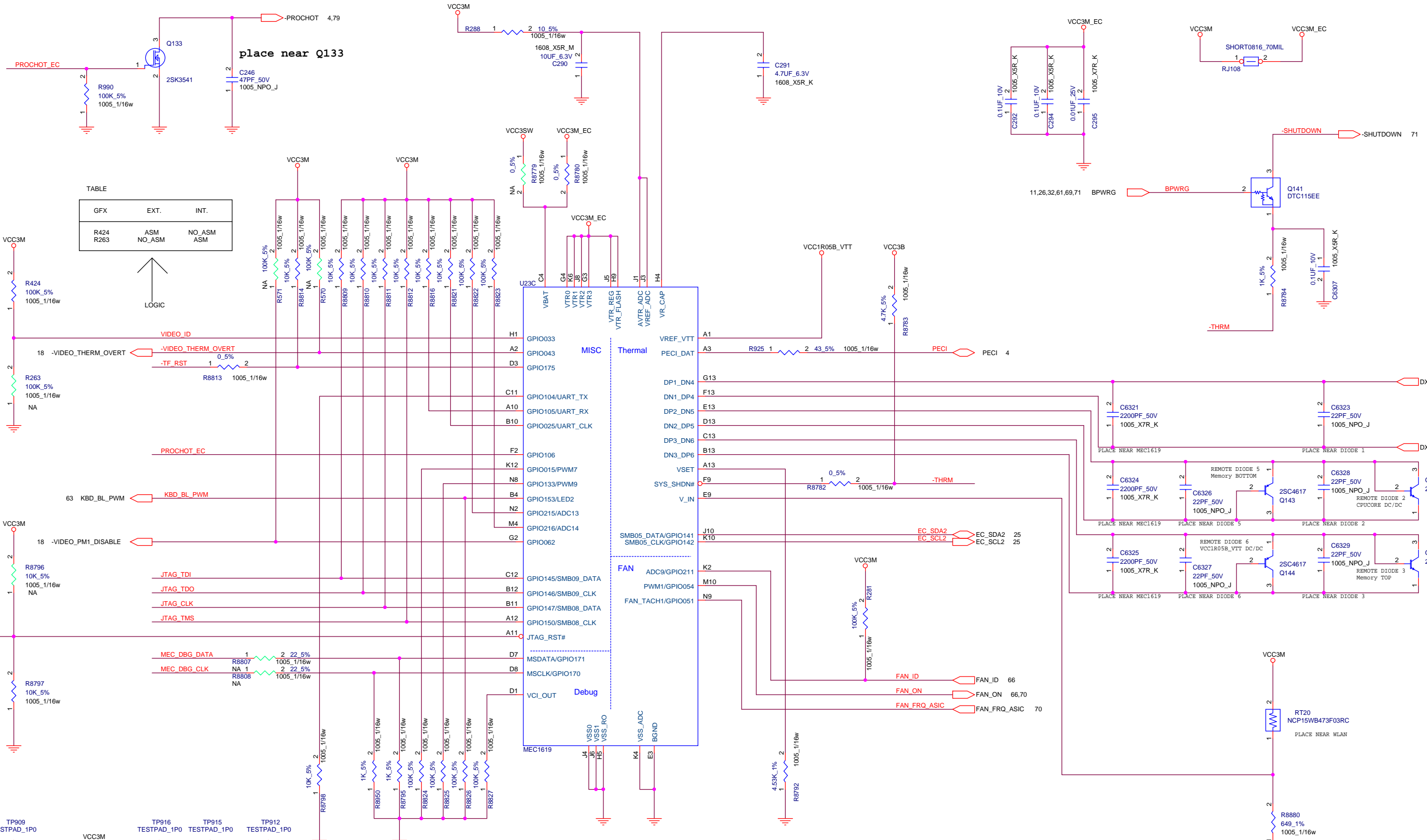
4MB SOIC8
 MACRONIX MX25L3206EM2I-12G
 WINBOND W25Q32BVSSIG





PLACE NEAR EC





TABLE

GFX	EXT.	INT.
R424	ASM	NO_ASM
R263	NO_ASM	ASM

↑
LOGIC

TABLE

JTAG debug port

	Enable	Disable
R8796	ASM	NO-ASM
R8797	NO-ASM	ASM
R8830	ASM	NO-ASM

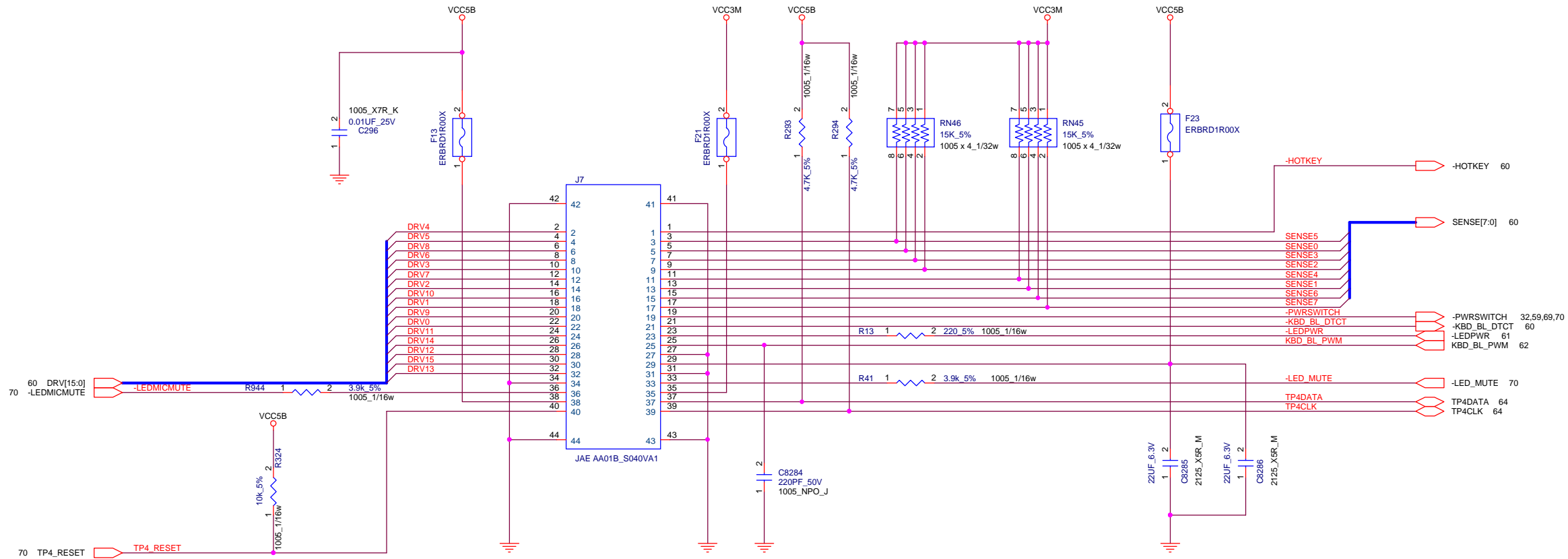
↑
LOGIC

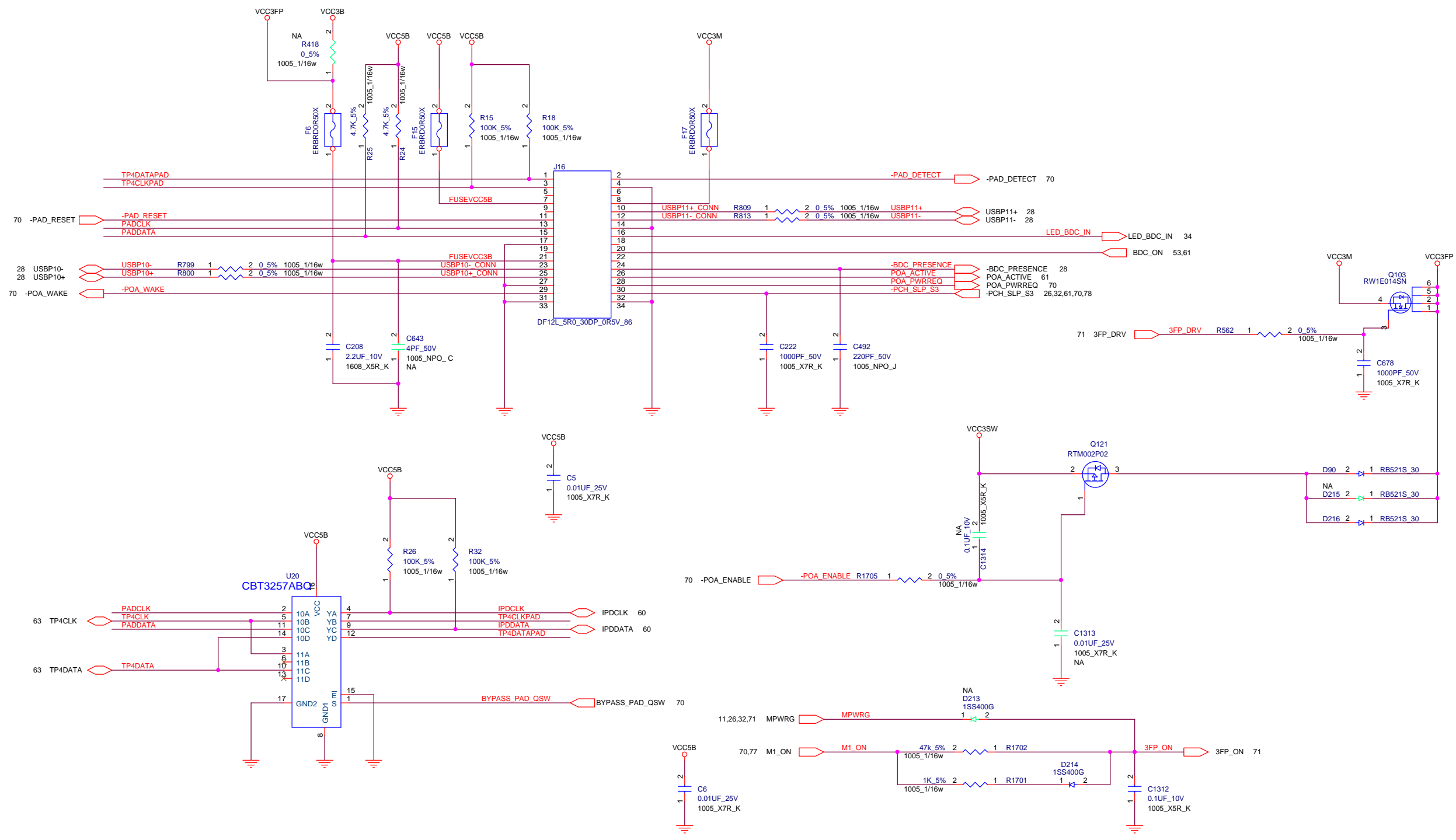
TABLE

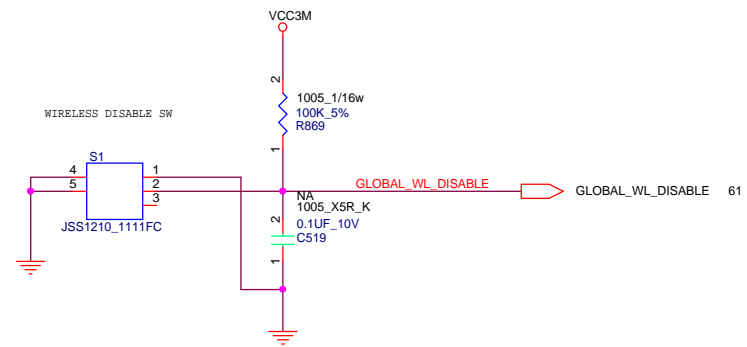
trace FIFO debug port

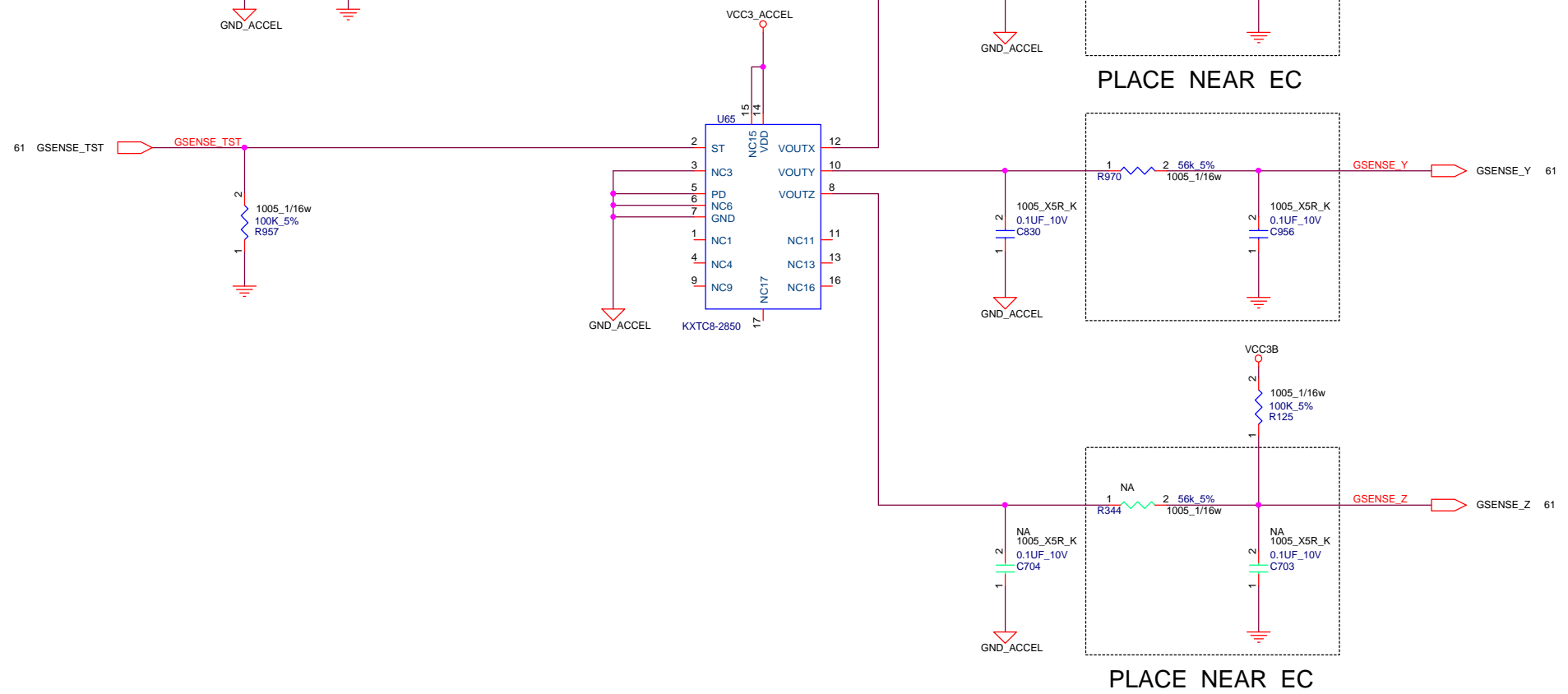
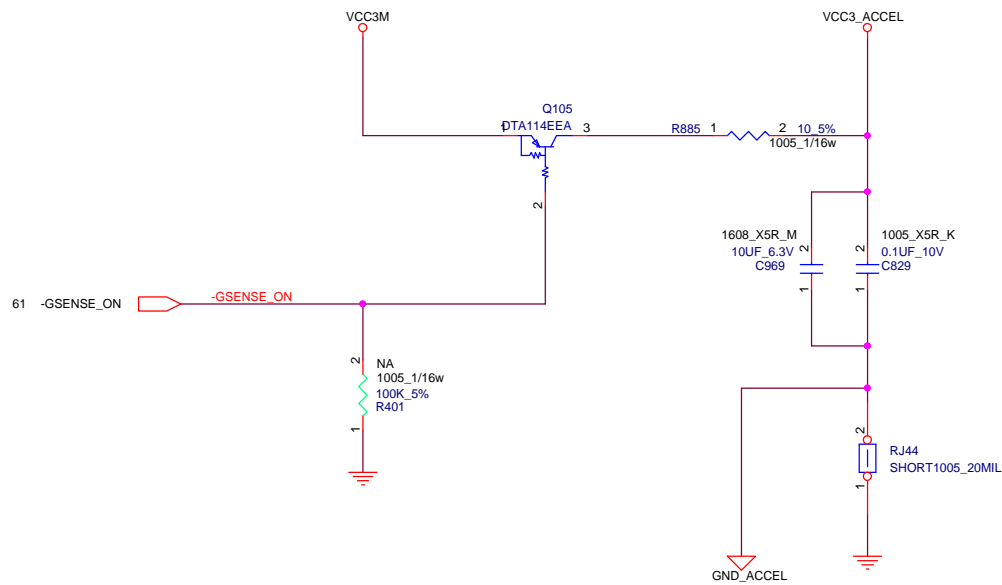
	Enable	Disable
R8807	ASM	NO-ASM
R8808	ASM	NO-ASM
R8950	NO-ASM	ASM

↑
LOGIC





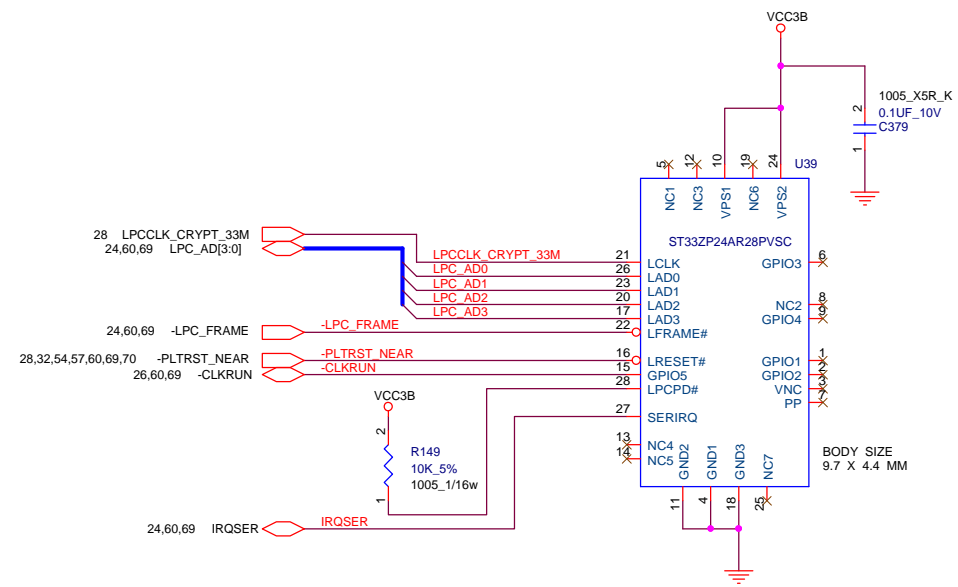




LOGIC
↓

TABLE

	LIS34AL KXTC8-2850	NO_ACC.
R401 R957	NO_ASM ASM	ASM ASM
U65 Q105	ASM ASM	NO_ASM NO_ASM
R885 C829 C969	10-OHM ASM ASM	NO_ASM NO_ASM NO_ASM
C830 C847	ASM ASM	NO_ASM NO_ASM
R969 C938 R970 C956	56K ASM 56K ASM	NO_ASM NO_ASM NO_ASM NO_ASM
C704 R344 C703	NO_ASM NO_ASM NO_ASM	NO_ASM NO_ASM NO_ASM
R125	ASM	ASM



TABLE

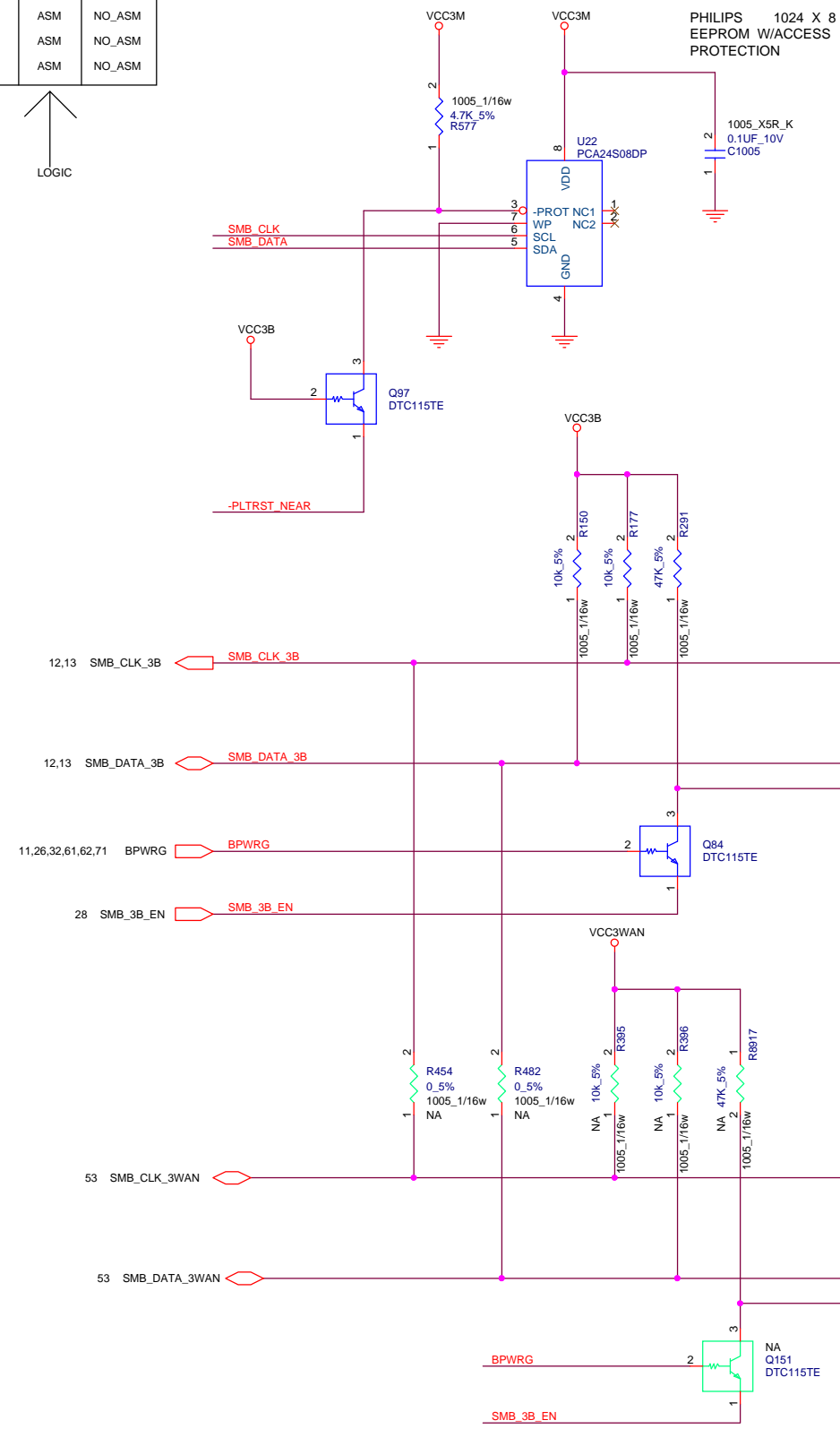
SDV and Mech FVT	ST19NP18ER28PVMO	1.2.8.20
FVT	ST33ZP24AR28PVOG	1.2.A.C
SW SIT-1, SIT, and SIT-R1	ST33ZP24AR28PVRC	1.2.C.0
SIT-R2 and SVT	ST33ZP24AR28PVSC Rev.I	1.2.D.0
SOVP	ST33ZP24AR28PVSC Rev.J	(1.2.D.0) 1.2.D.8 by FW Update
MP	ST33ZP24AR28PVSH	1.2.D.8

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TABLE

EEPROM	U22	U23
U22	ASM	NO_ASM
C1005	ASM	NO_ASM
R577	ASM	NO_ASM
Q97	ASM	NO_ASM

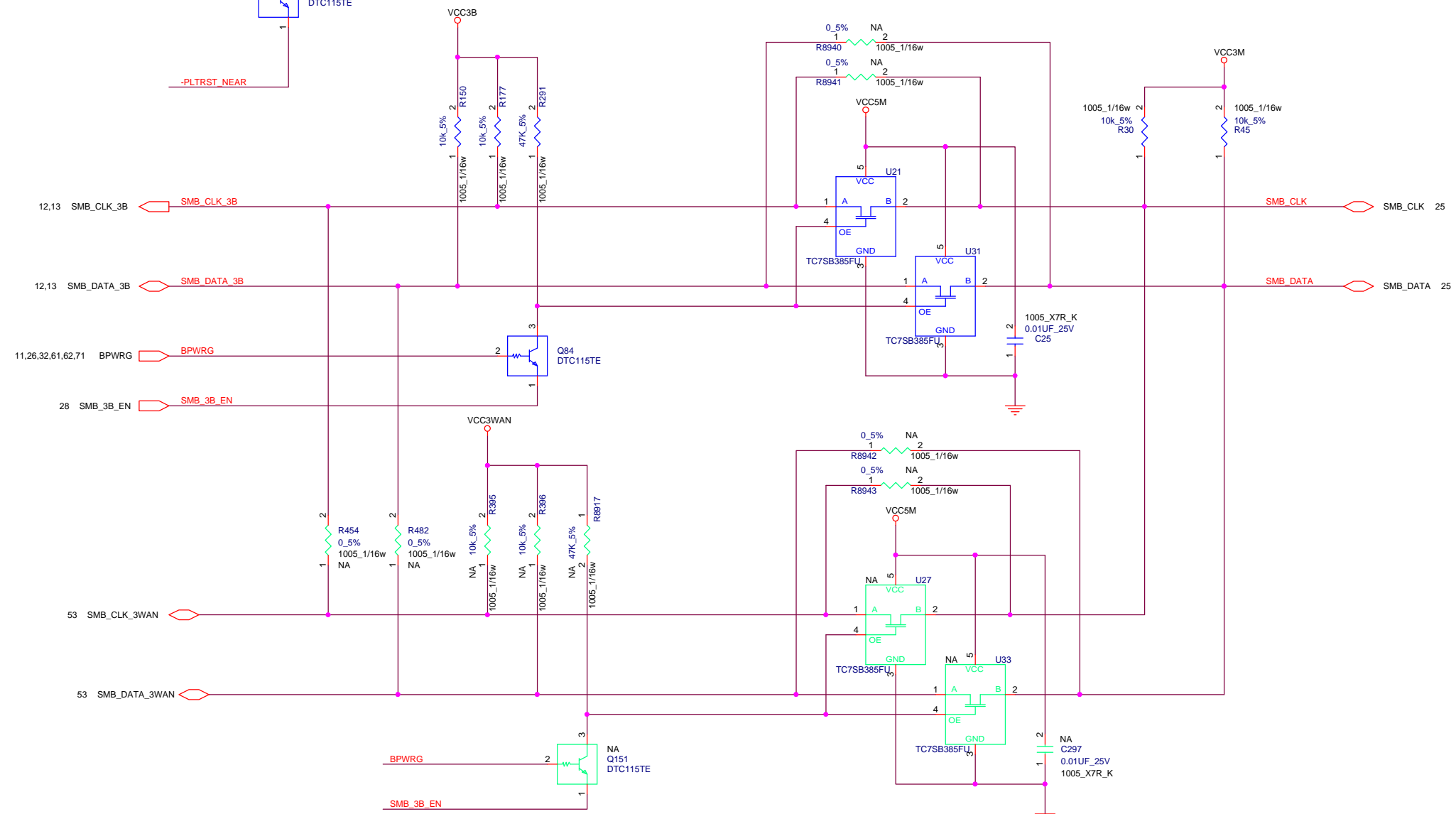
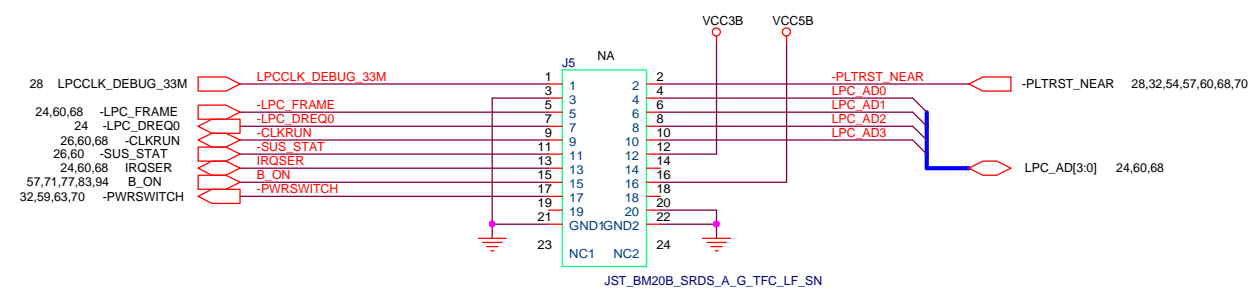
LOGIC



TABLE

REF DES	ENABLE	DISABLE
J5	ASM	NO_ASM
R220	ASM	NO_ASM

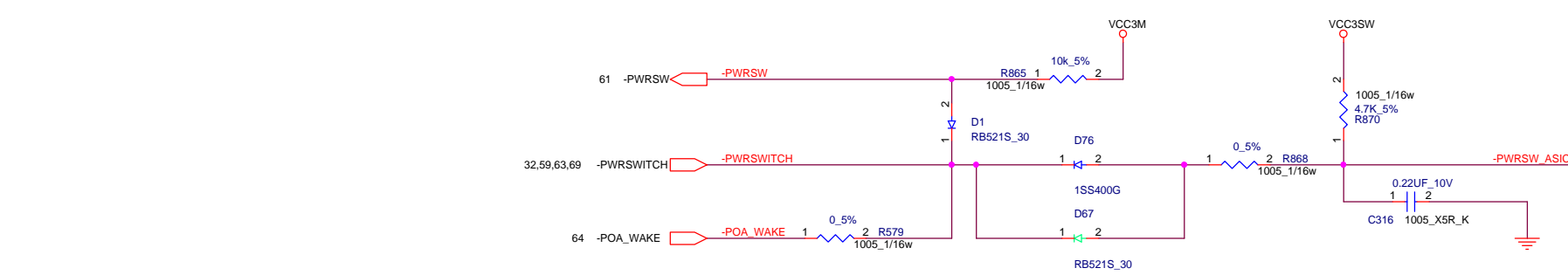
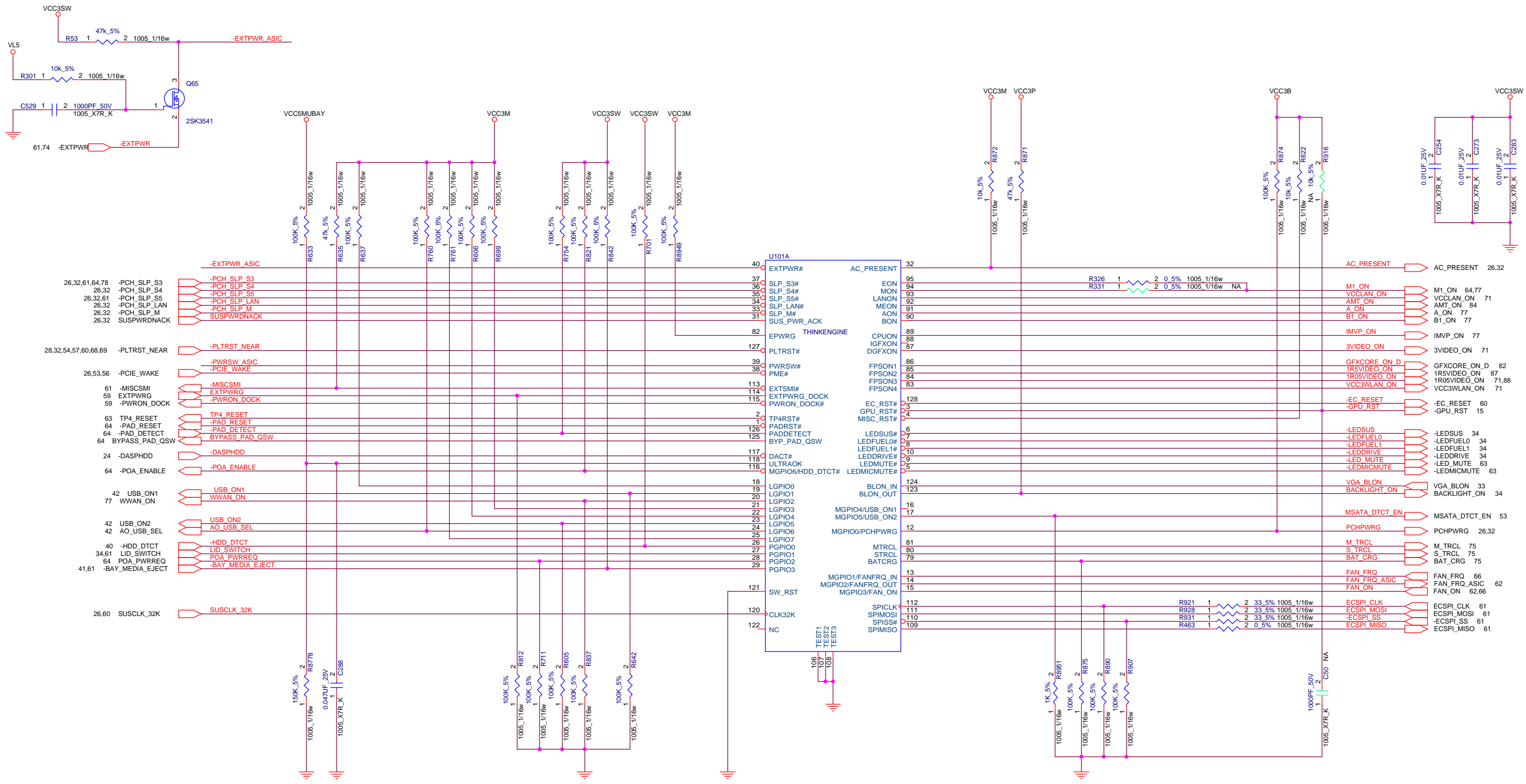
LOGIC

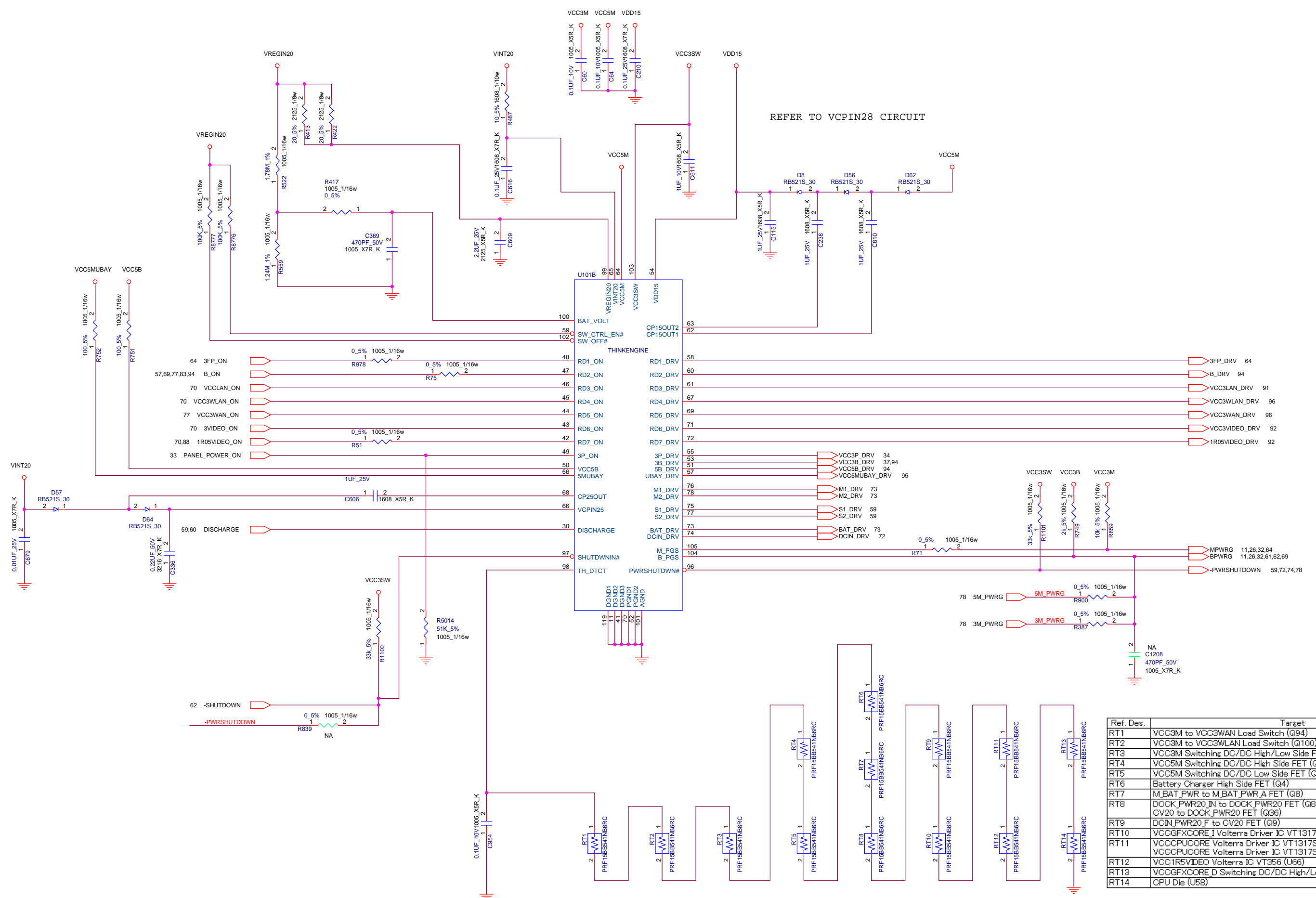


TABLE

Component	YES	YES	YES	YES	NO	NO	NO	NO
AOAC	YES	YES	YES	YES	NO	NO	NO	NO
Anti Theft	YES	YES	NO	NO	YES	YES	NO	NO
EEPROM	U22	U23	U22	U23	U22	U23	U22	U23
U27	ASM	NO_ASM	NO_ASM	NO_ASM	NO_ASM	NO_ASM	NO_ASM	NO_ASM
U33	ASM	NO_ASM	NO_ASM	NO_ASM	NO_ASM	NO_ASM	NO_ASM	NO_ASM
C297	ASM	NO_ASM	NO_ASM	NO_ASM	NO_ASM	NO_ASM	NO_ASM	NO_ASM
R395	ASM	ASM	NO_ASM	NO_ASM	NO_ASM	NO_ASM	NO_ASM	NO_ASM
R396	ASM	ASM	NO_ASM	NO_ASM	NO_ASM	NO_ASM	NO_ASM	NO_ASM
R8917	ASM	NO_ASM	NO_ASM	NO_ASM	NO_ASM	NO_ASM	NO_ASM	NO_ASM
Q151	ASM	NO_ASM	NO_ASM	NO_ASM	NO_ASM	NO_ASM	NO_ASM	NO_ASM
U21	ASM	ASM	NO_ASM	ASM	NO_ASM	ASM	NO_ASM	NO_ASM
U31	ASM	ASM	ASM	NO_ASM	ASM	NO_ASM	ASM	NO_ASM
C25	ASM	ASM	ASM	NO_ASM	ASM	NO_ASM	ASM	NO_ASM
R291	ASM	ASM	ASM	NO_ASM	ASM	NO_ASM	ASM	NO_ASM
Q84	ASM	NO_ASM	ASM	NO_ASM	ASM	NO_ASM	ASM	NO_ASM
R454	NO_ASM	NO_ASM	NO_ASM	NO_ASM	ASM	ASM	NO_ASM	NO_ASM
R482	NO_ASM	NO_ASM	NO_ASM	NO_ASM	ASM	ASM	NO_ASM	NO_ASM
R8943	NO_ASM	ASM	NO_ASM	NO_ASM	NO_ASM	NO_ASM	NO_ASM	NO_ASM
R8942	NO_ASM	ASM	NO_ASM	NO_ASM	NO_ASM	NO_ASM	NO_ASM	NO_ASM
R8941	NO_ASM	NO_ASM	NO_ASM	ASM	NO_ASM	ASM	NO_ASM	ASM
R8940	NO_ASM	NO_ASM	NO_ASM	ASM	NO_ASM	ASM	NO_ASM	ASM
R30	NO_ASM	NO_ASM	ASM	NO_ASM	ASM	ASM	NO_ASM	ASM
R45	ASM	NO_ASM	ASM	NO_ASM	ASM	NO_ASM	ASM	NO_ASM

LOGIC

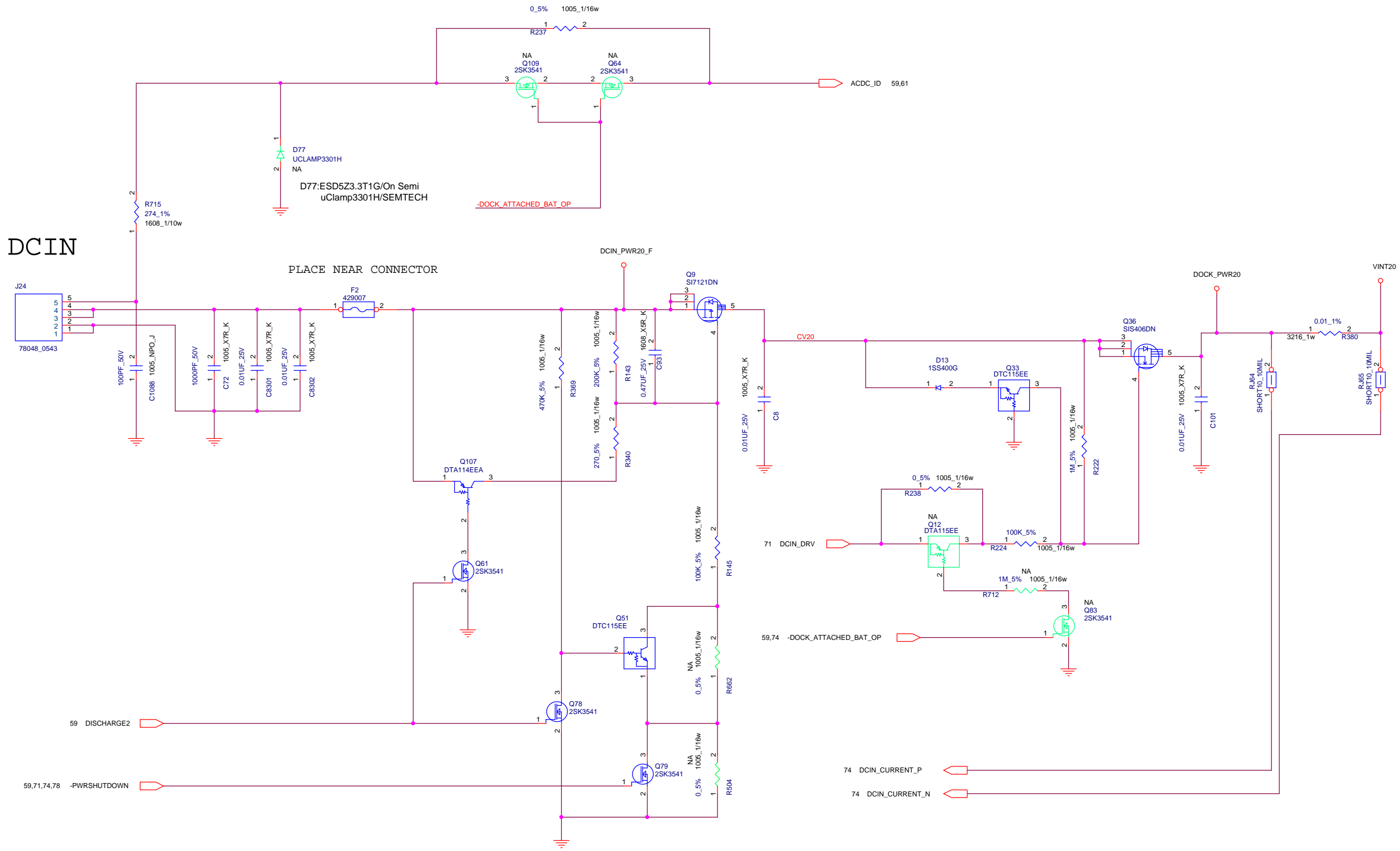




REFER TO VCPIN28 CIRCUIT

Ref. Des.	Target
RT1	VCC3M to VCC3WAN Load Switch (Q94)
RT2	VCC3M to VCC3WLAN Load Switch (Q100)
RT3	VCC3M Switching DC/DC High/Low Side FET (Q18/Q17)
RT4	VCC5M Switching DC/DC High Side FET (Q2/Q16)
RT5	VCC5M Switching DC/DC Low Side FET (Q6/Q46)
RT6	Battery Charger High Side FET (Q4)
RT7	M.BAT_PWR to M.BAT_PWR A FET (Q8)
RT8	DOCK_PWR20_IN to DOCK_PWR20 FET (Q85) CV20 to DOCK_PWR20 FET (Q36)
RT9	DCIN_PWR20_F to CV20 FET (Q9)
RT10	VCCGFXCORE_I Volterra Driver IC VT1317S (U25)
RT11	VCCCPUCORE Volterra Driver IC VT1317S Phase 1 (U16) VCCCPUCORE Volterra Driver IC VT1317S Phase 2 (U19)
RT12	VCC1R5VIDEO Volterra IC VT356 (U66)
RT13	VCCGFXCORE_D Switching DC/DC High/Low Side FET (Q40/Q95)
RT14	CPU Die (U58)

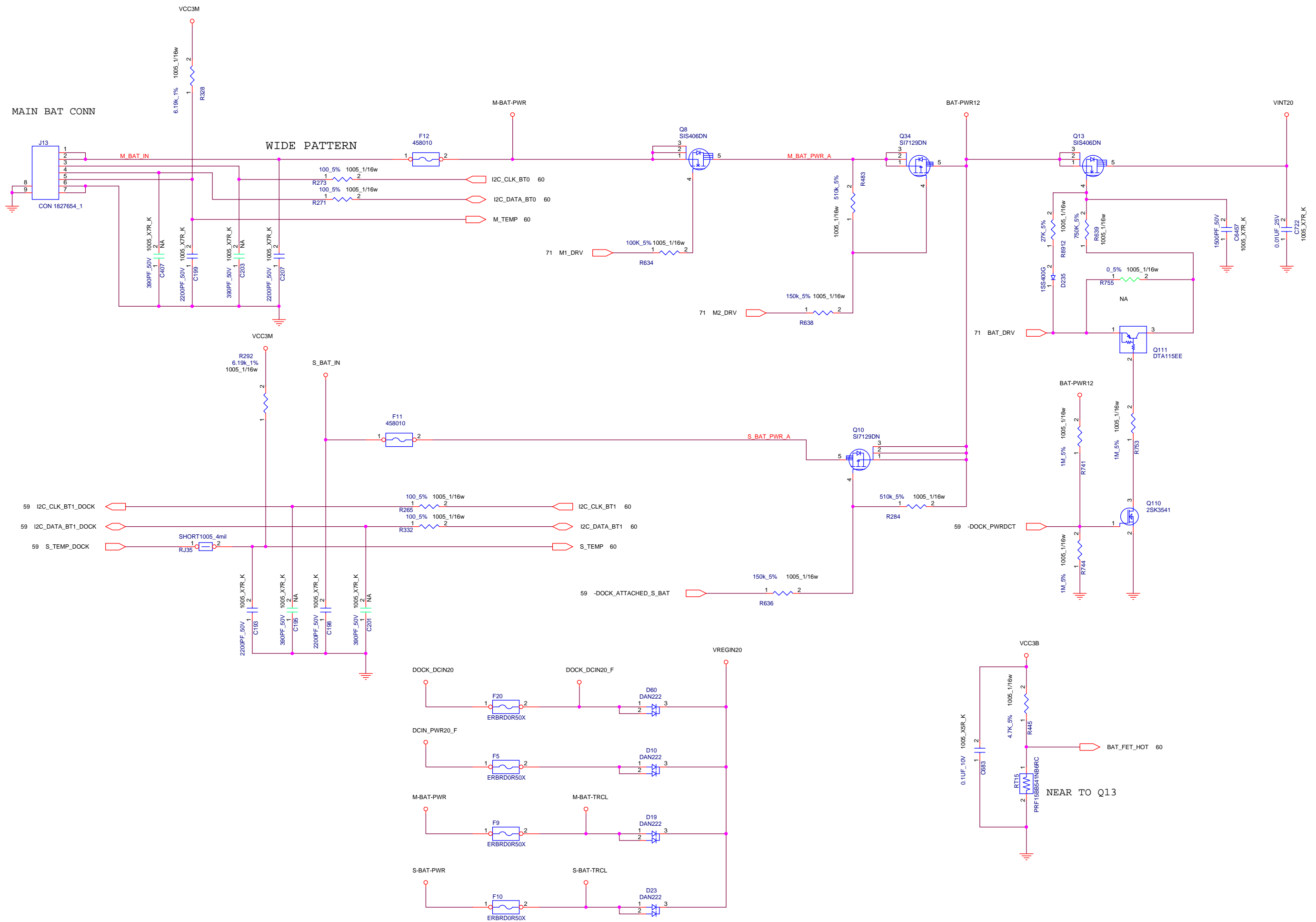


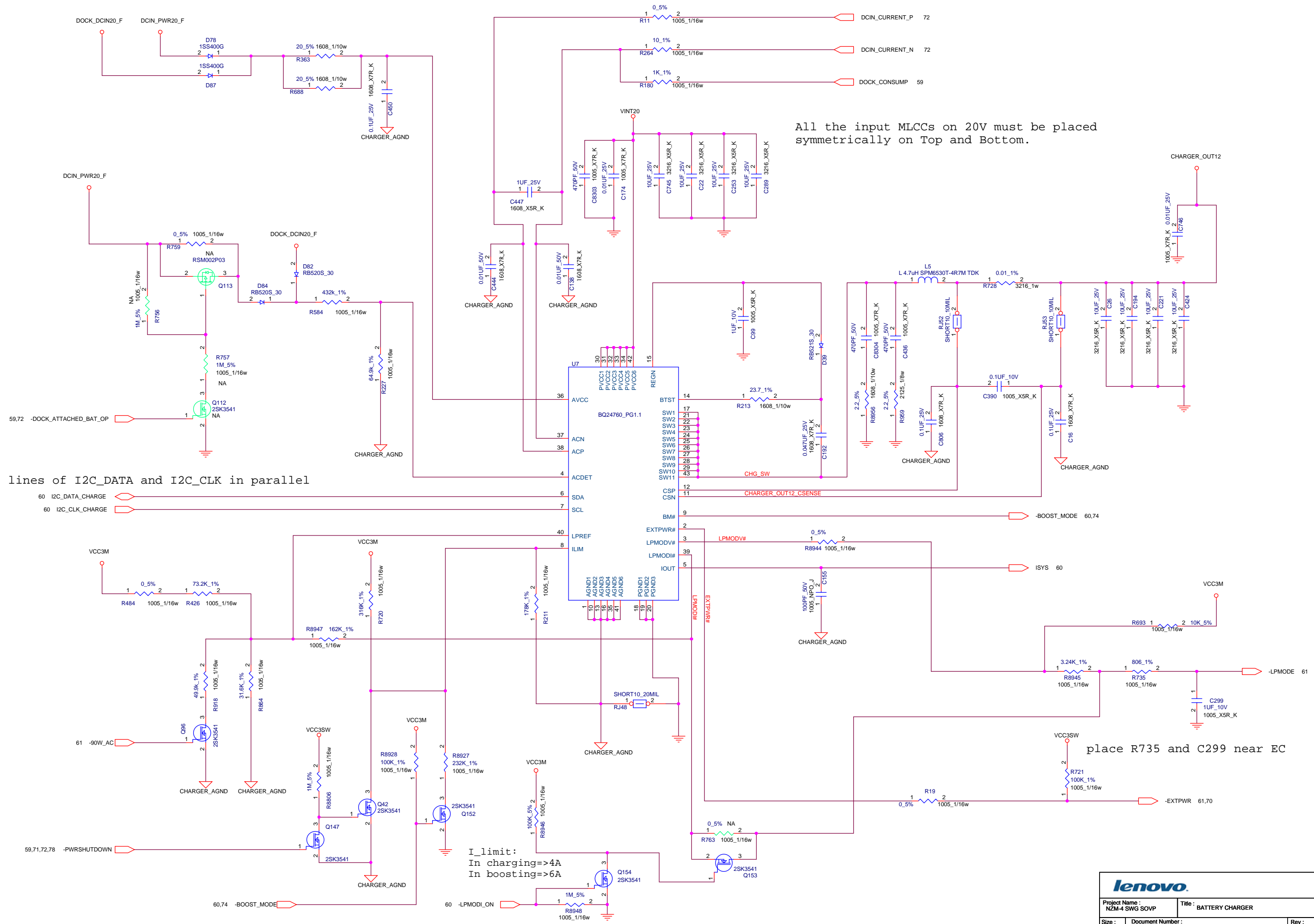


TABLE

PEAK SHIFT	YES	NO
R662	NO-ASM	ASM
R369	ASM	NO-ASM
Q78	ASM	NO-ASM
Q51	ASM	NO-ASM

↑
LOGIC



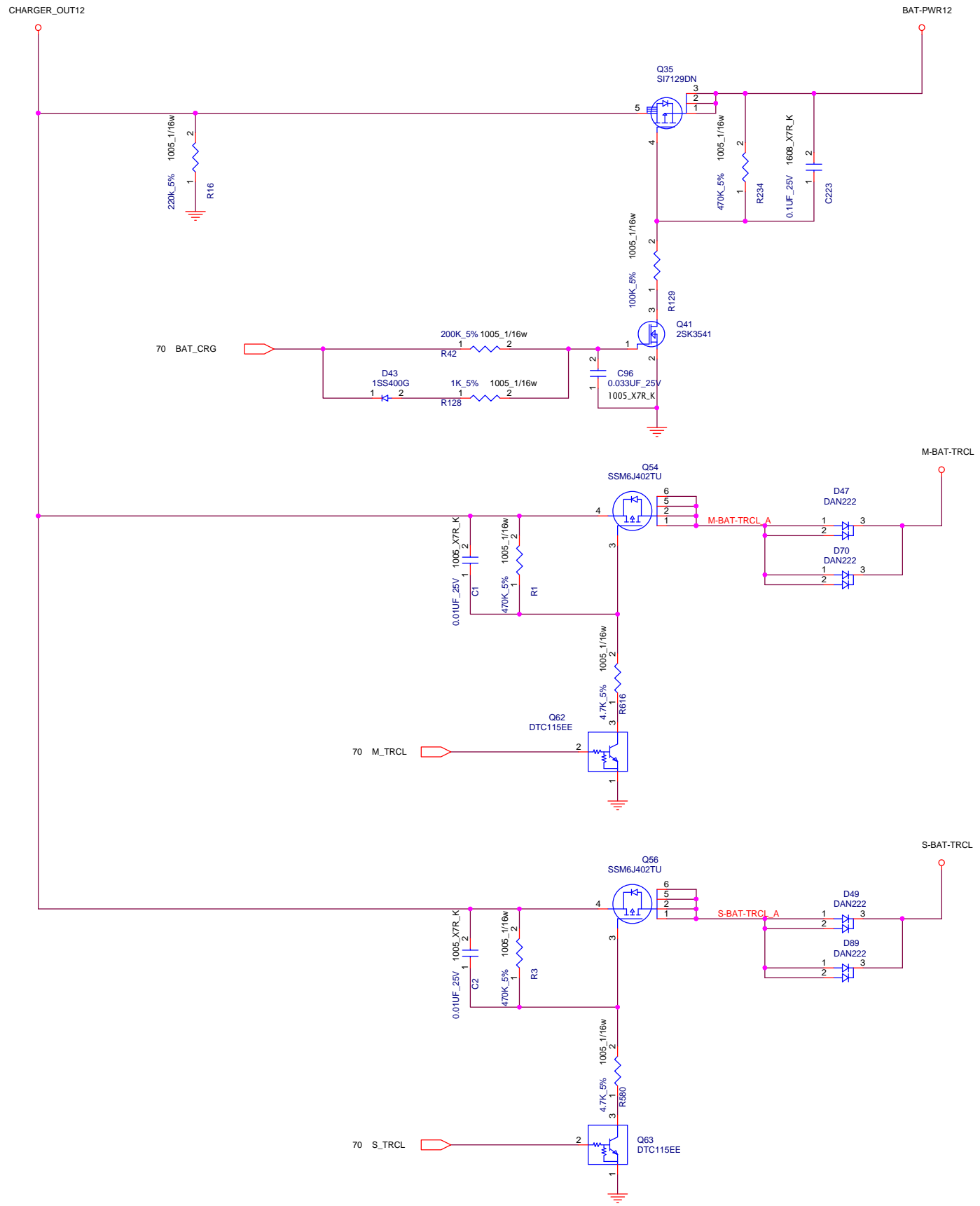


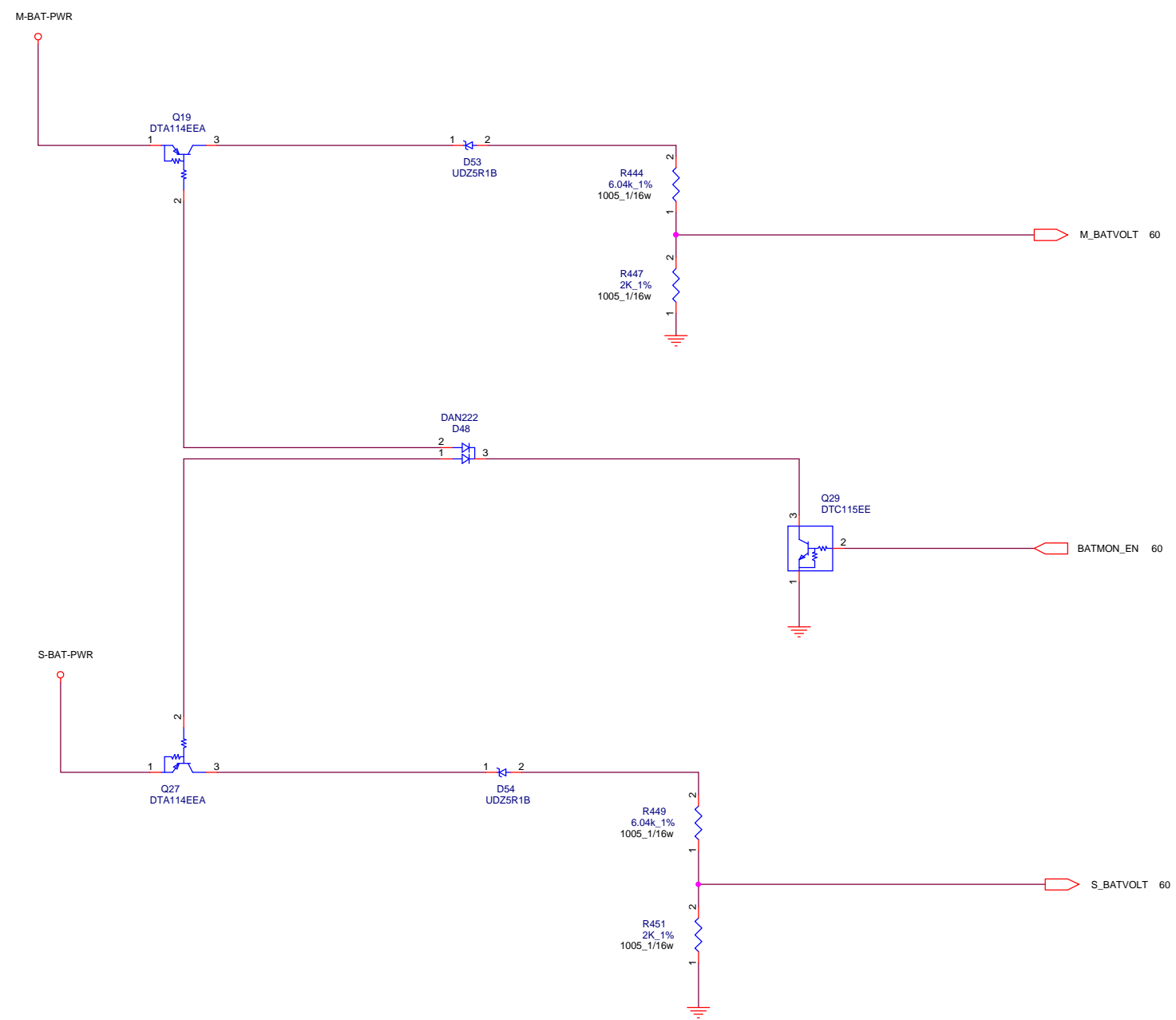
All the input MLCCs on 20V must be placed symmetrically on Top and Bottom.

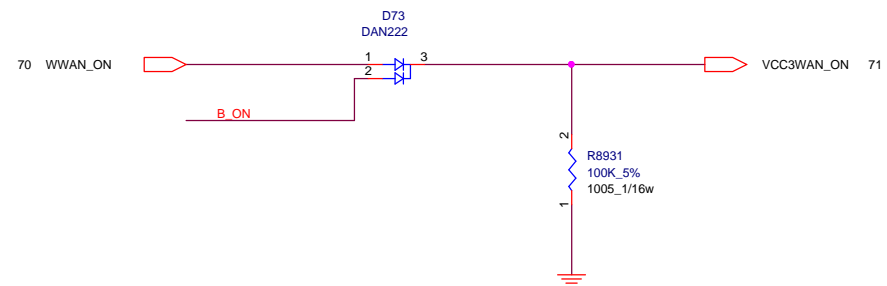
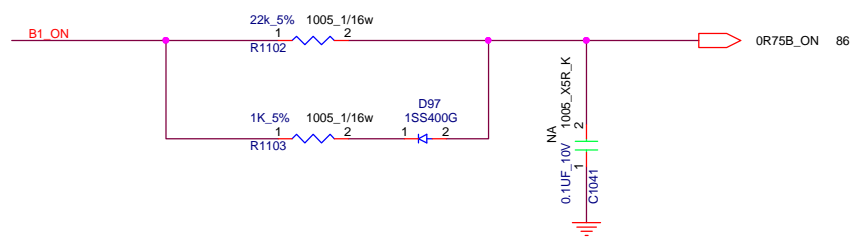
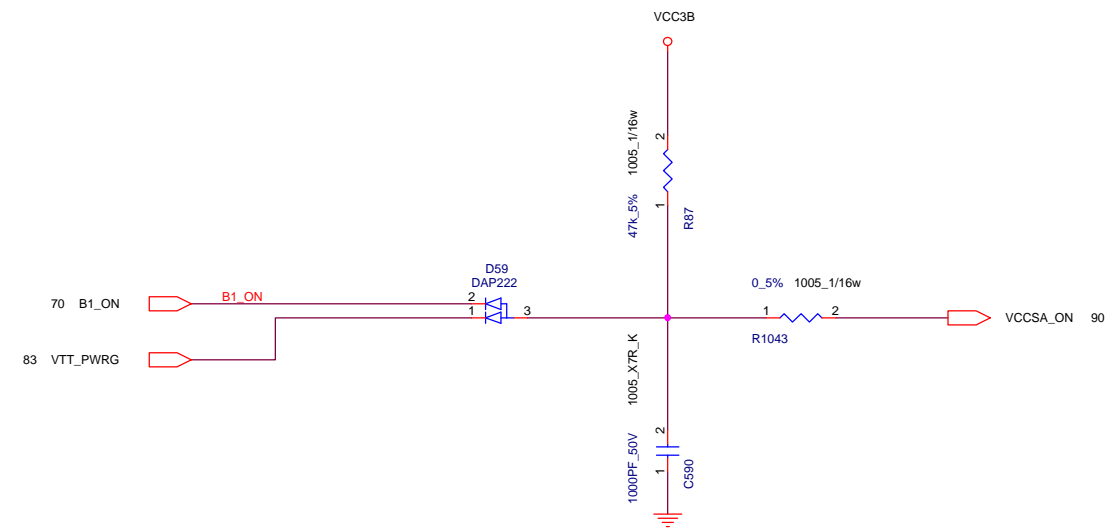
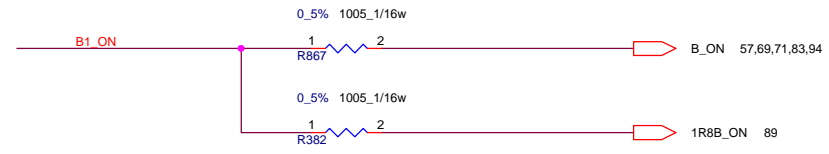
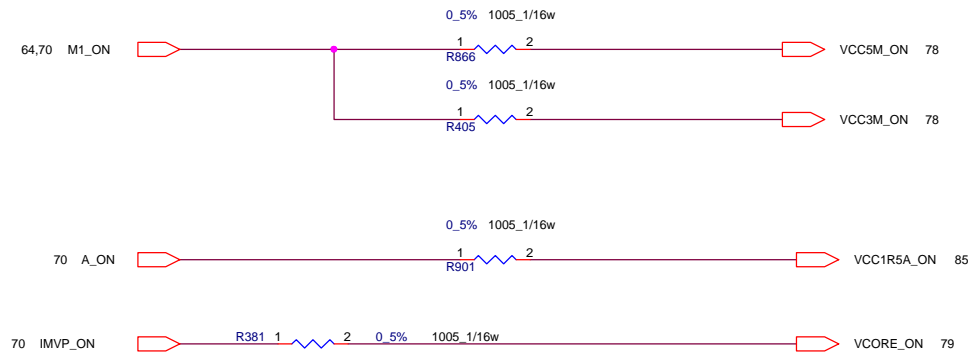
to connect lines of I2C_DATA and I2C_CLK in parallel

I_limit:
In charging=>4A
In boosting=>6A

place R735 and C299 near EC







TABLE

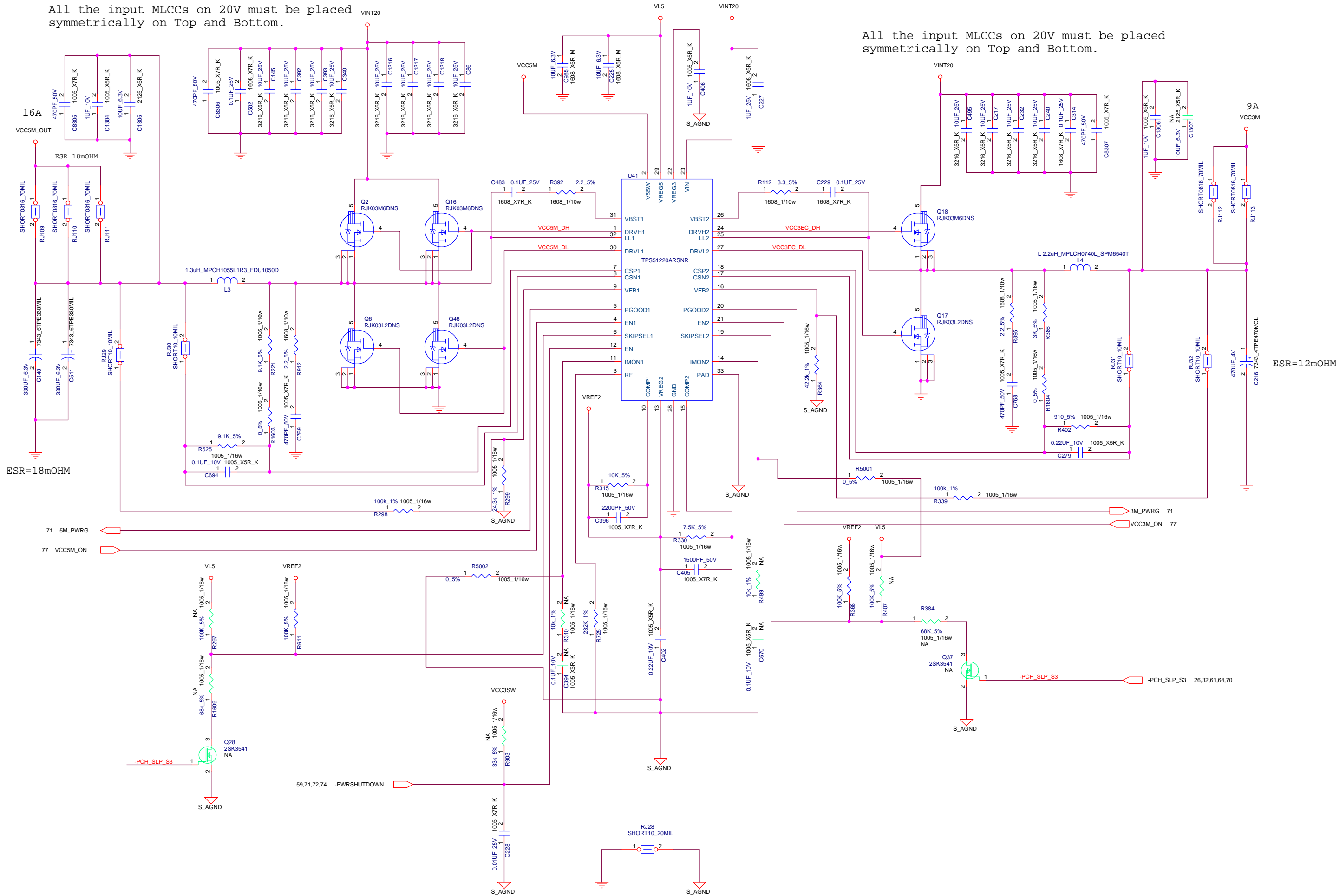
AOAC	YES	NO
D73	ASM	NO-ASM
R8931	ASM 100K-ohm	ASM 0-ohm

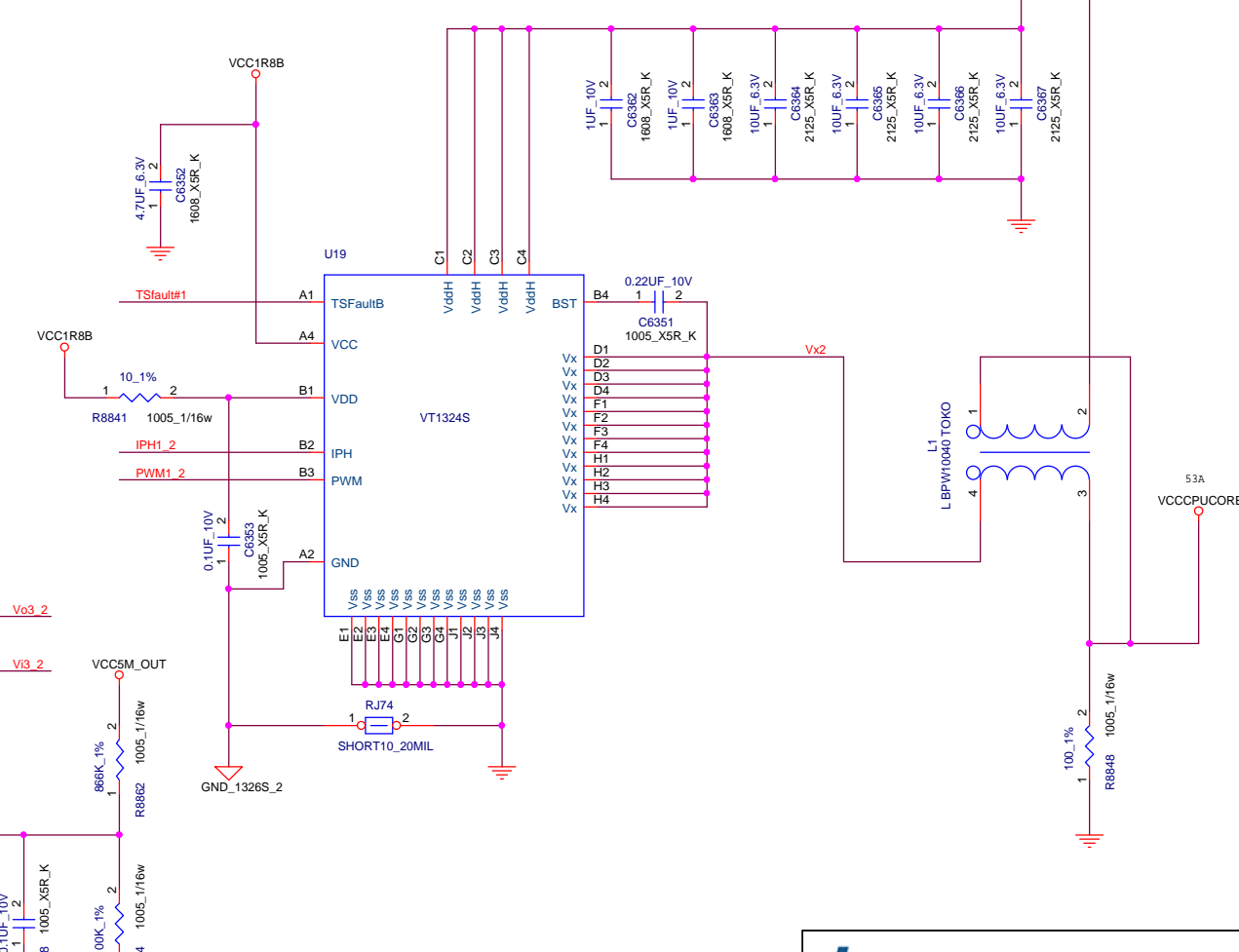
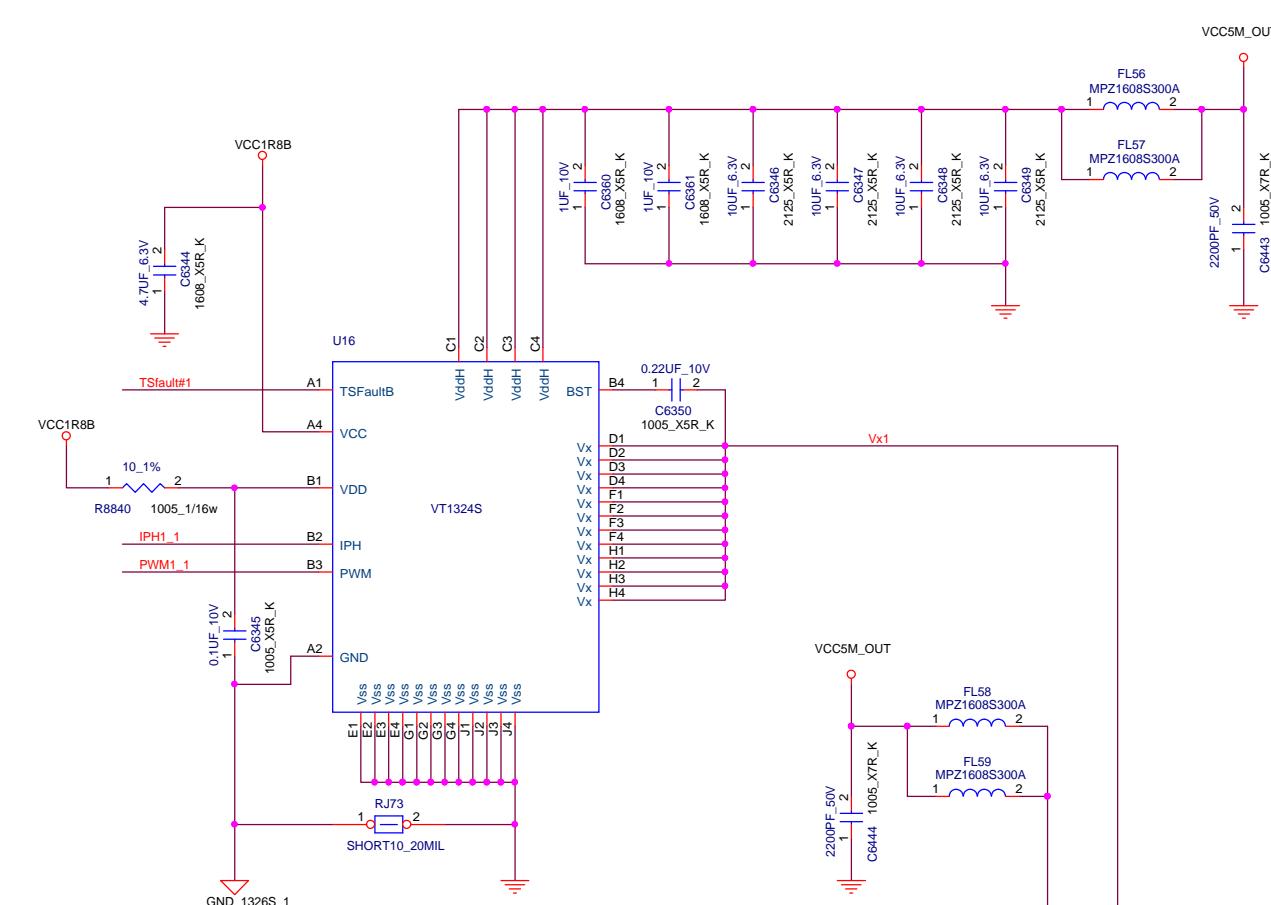
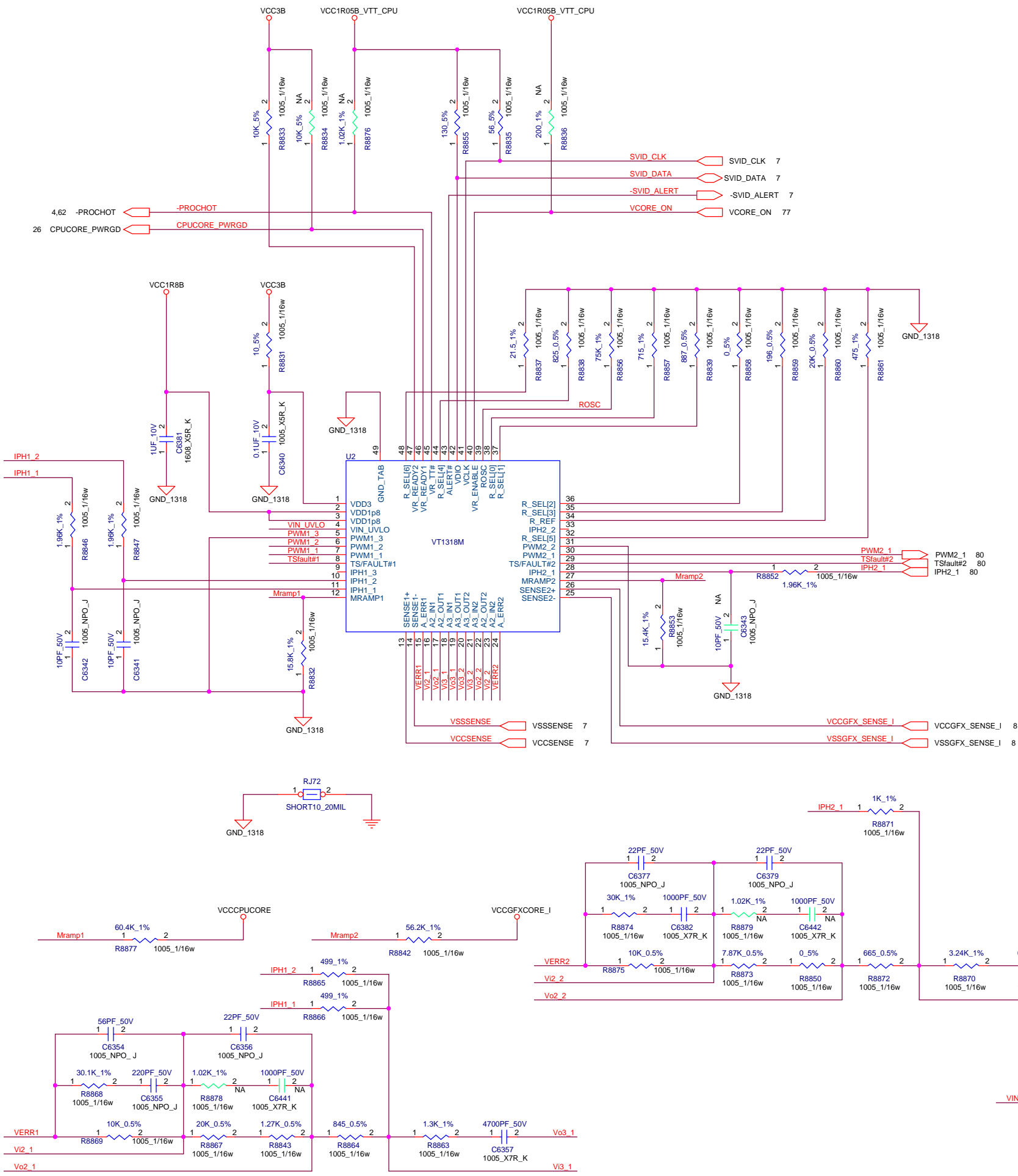
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LOGIC

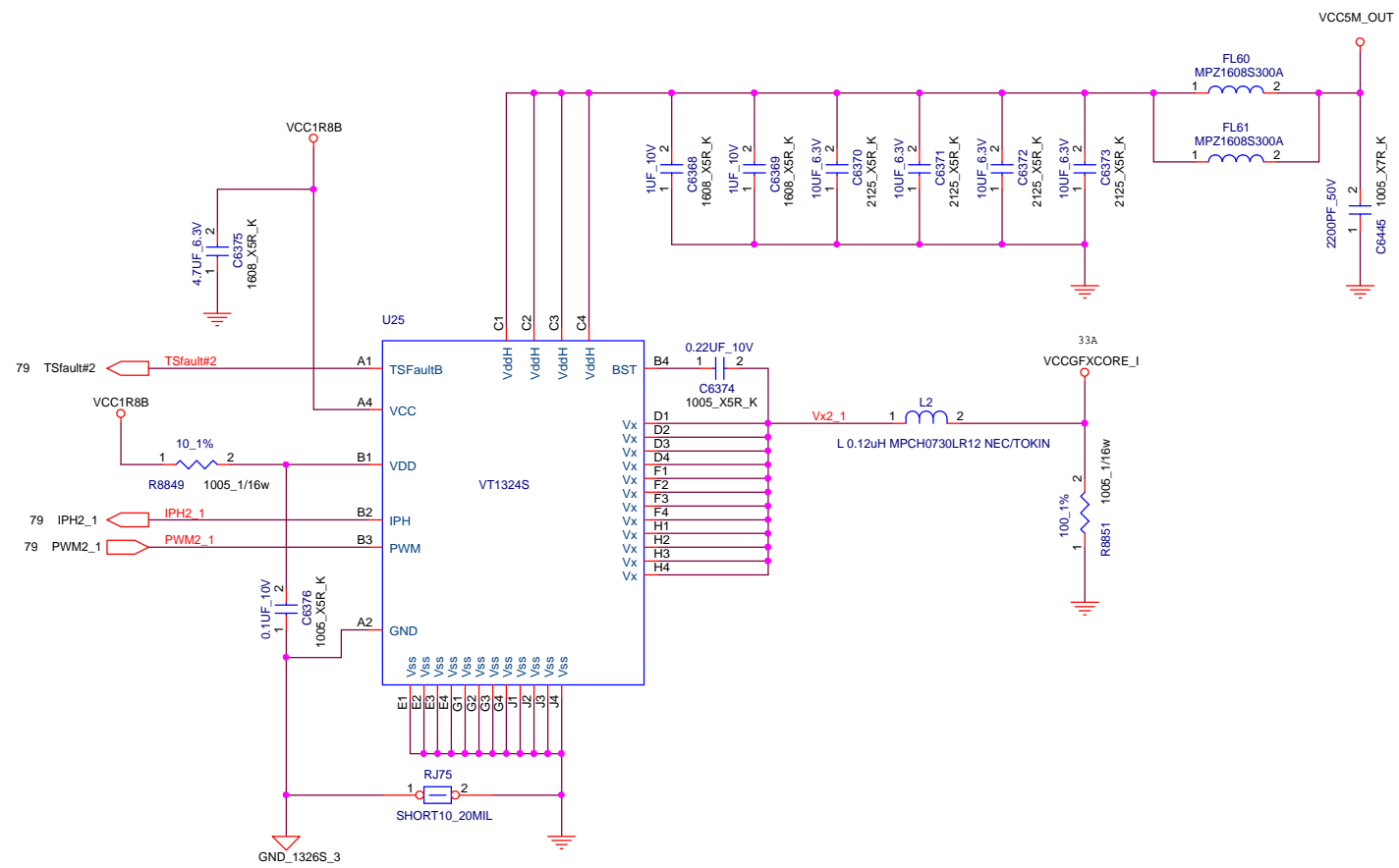


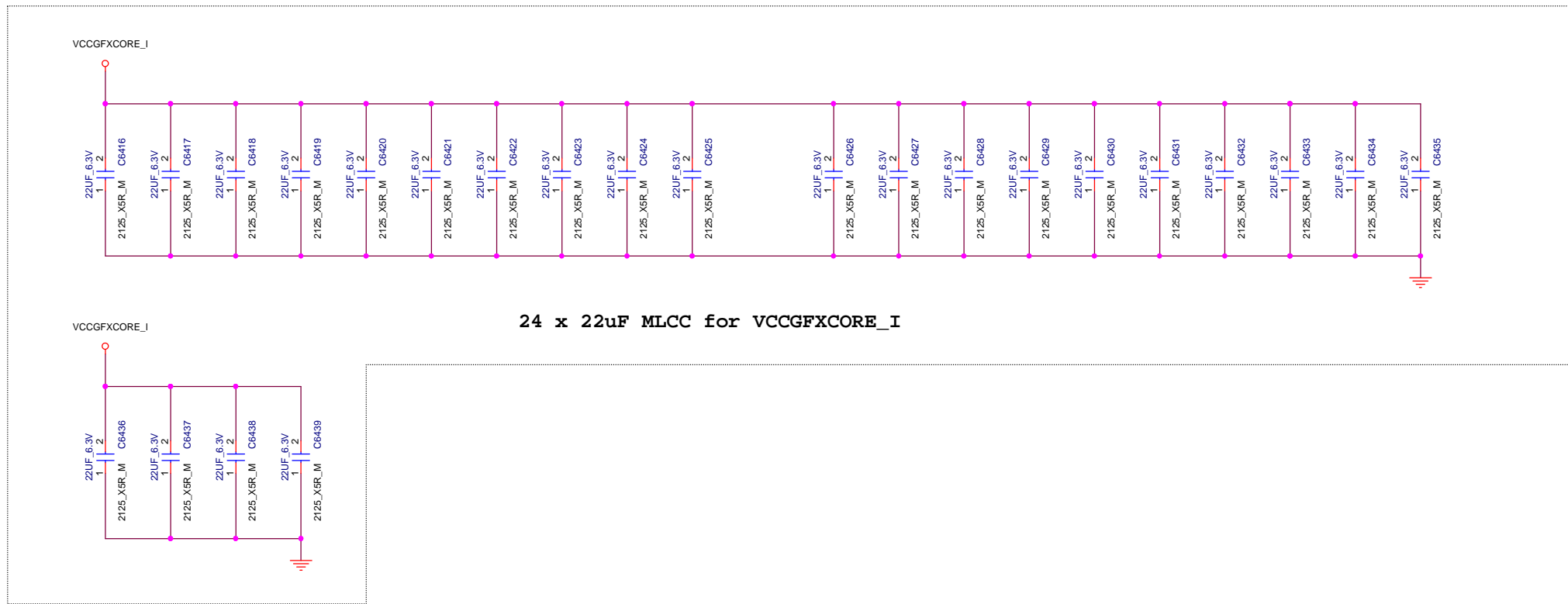
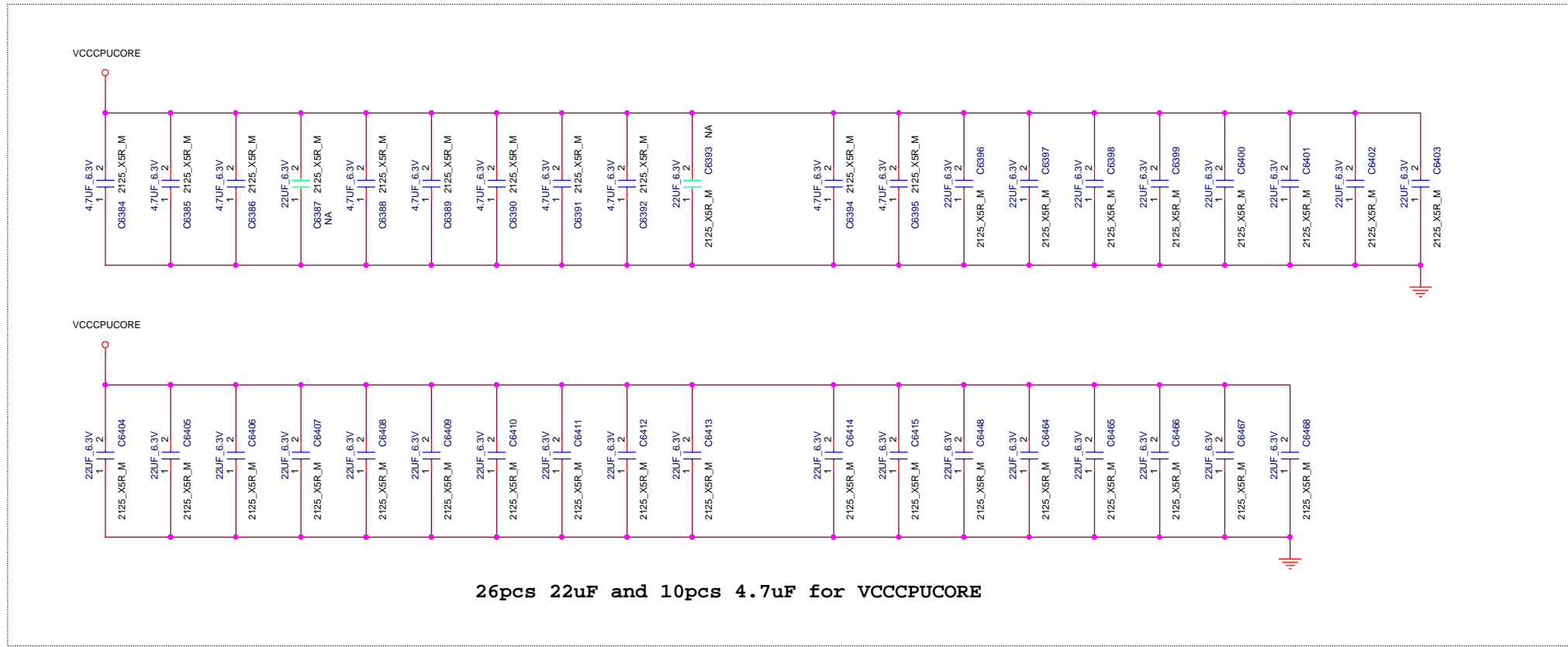
All the input MLCCs on 20V must be placed symmetrically on Top and Bottom.

All the input MLCCs on 20V must be placed symmetrically on Top and Bottom.

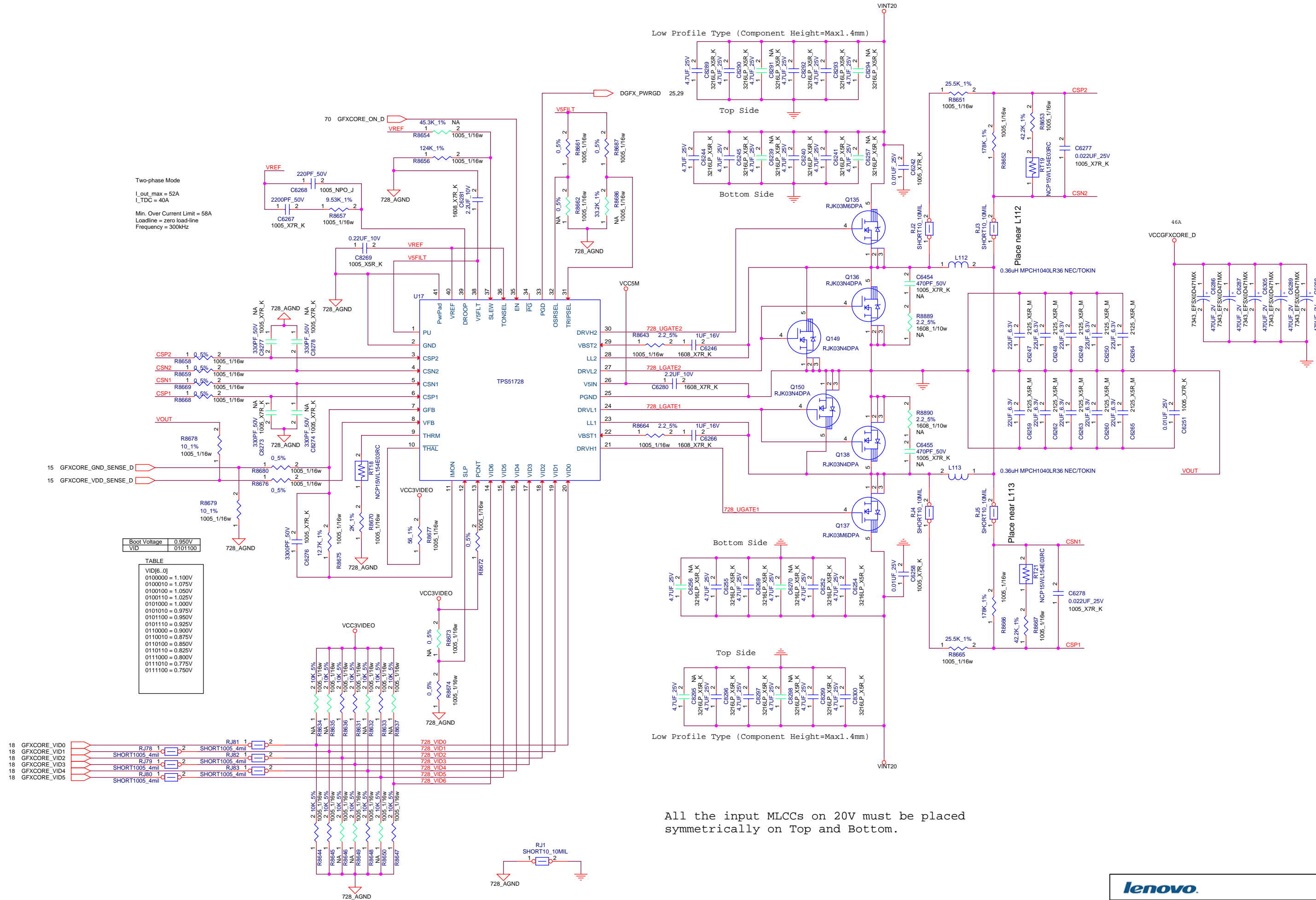








All the input MLCCs on 20V must be placed symmetrically on Top and Bottom.

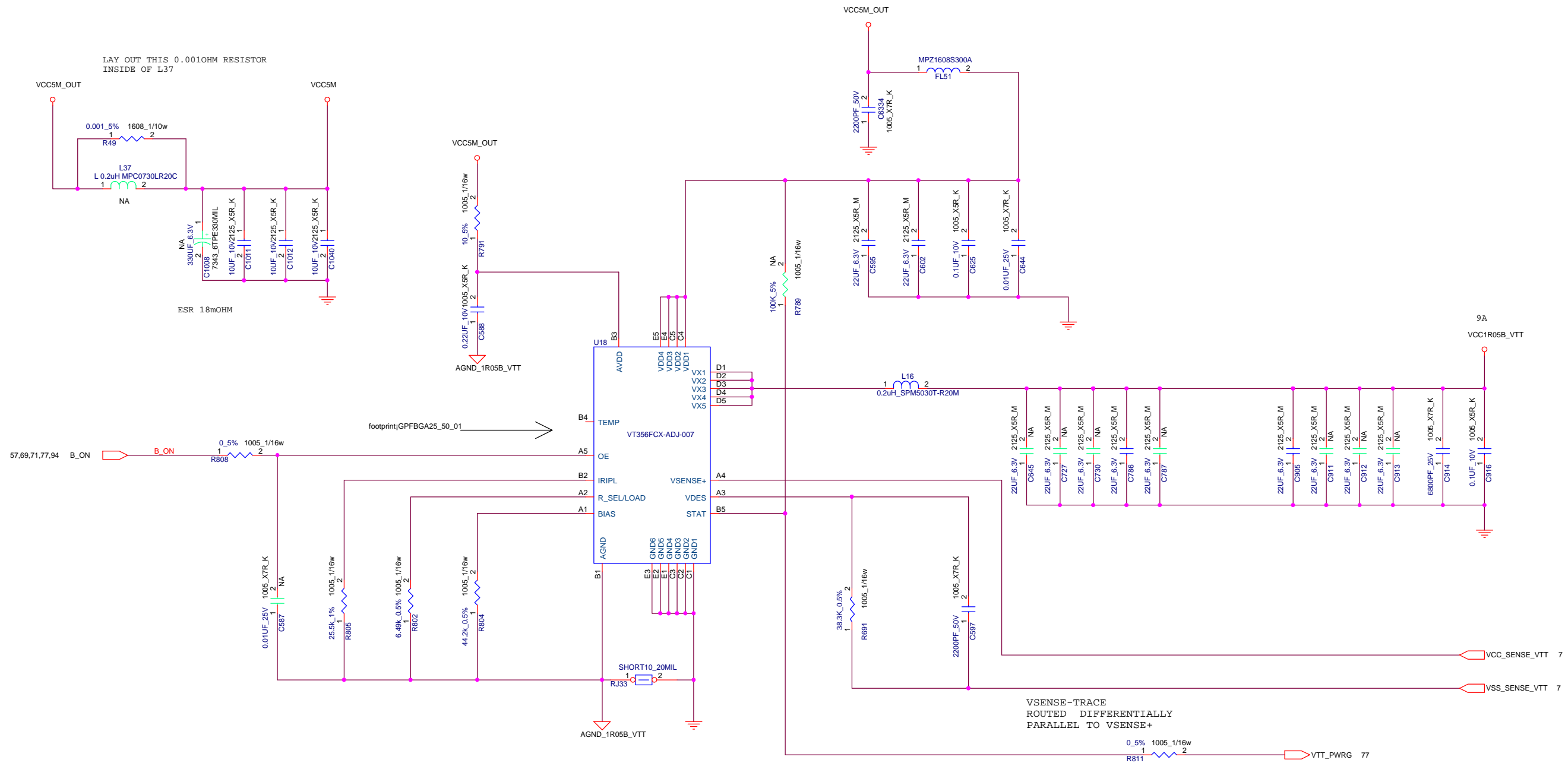


Two-phase Mode
 I_{out_max} = 52A
 I_{TDC} = 40A
 Min. Over Current Limit = 58A
 Loadline = zero load-line
 Frequency = 300kHz

Boat Voltage	VID
0.950V	0101100
0.850V	0101100

VID[6..0]	Voltage
0100000	1.100V
0100010	1.075V
0100100	1.050V
0100110	1.025V
0101000	1.000V
0101010	0.975V
0101100	0.950V
0101110	0.925V
0110000	0.900V
0110010	0.875V
0110100	0.850V
0110110	0.825V
0111000	0.800V
0111010	0.775V
0111100	0.750V

All the input MLCCs on 20V must be placed symmetrically on Top and Bottom.



TO ASSIGN VT356 IN BOM

1.10V R691 = 40.2K
 1.05V R691 = 38.3K
 1.00V R691 = 36.5K
 $V_{OUT} = 1.212 * R691 / 44.2K$

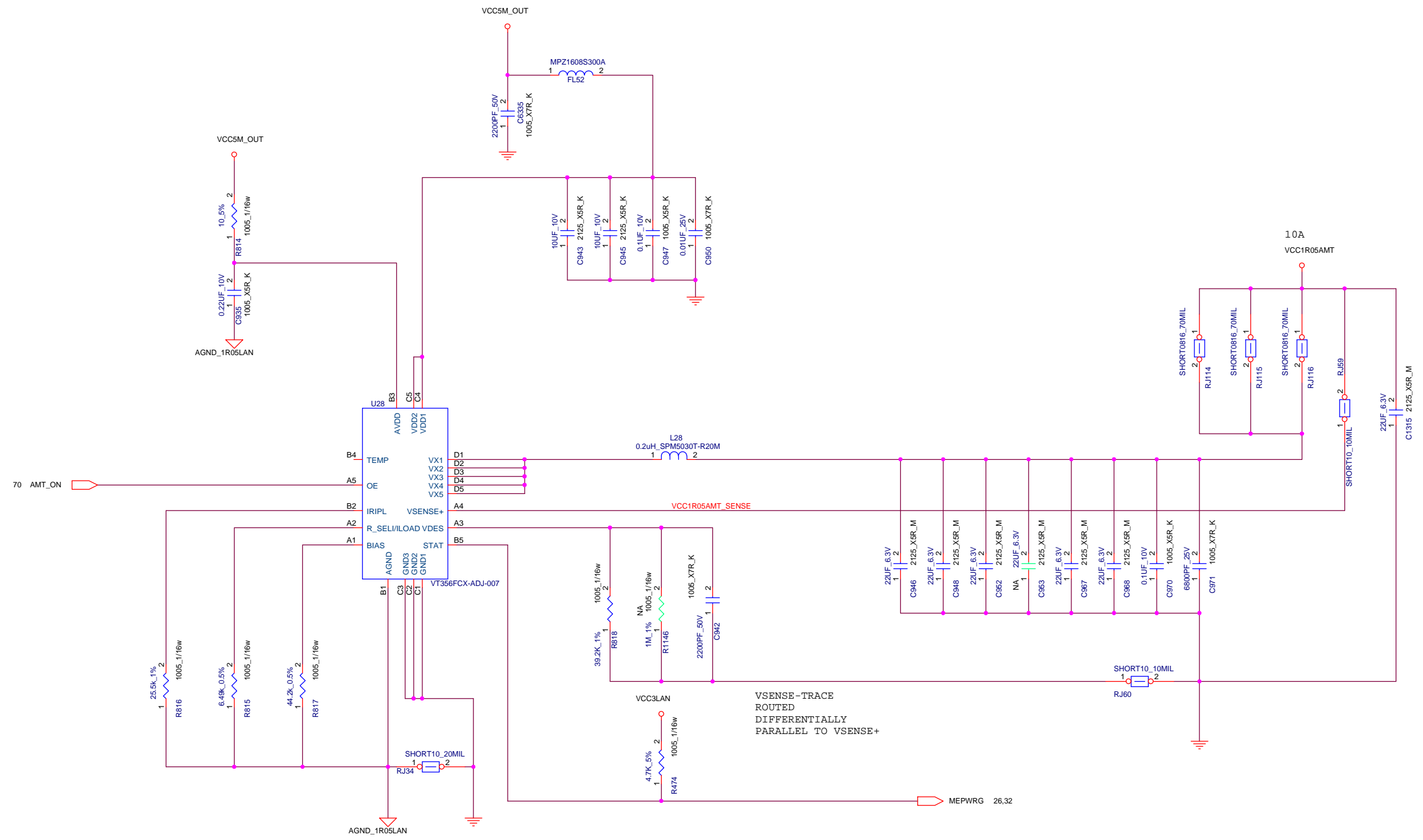
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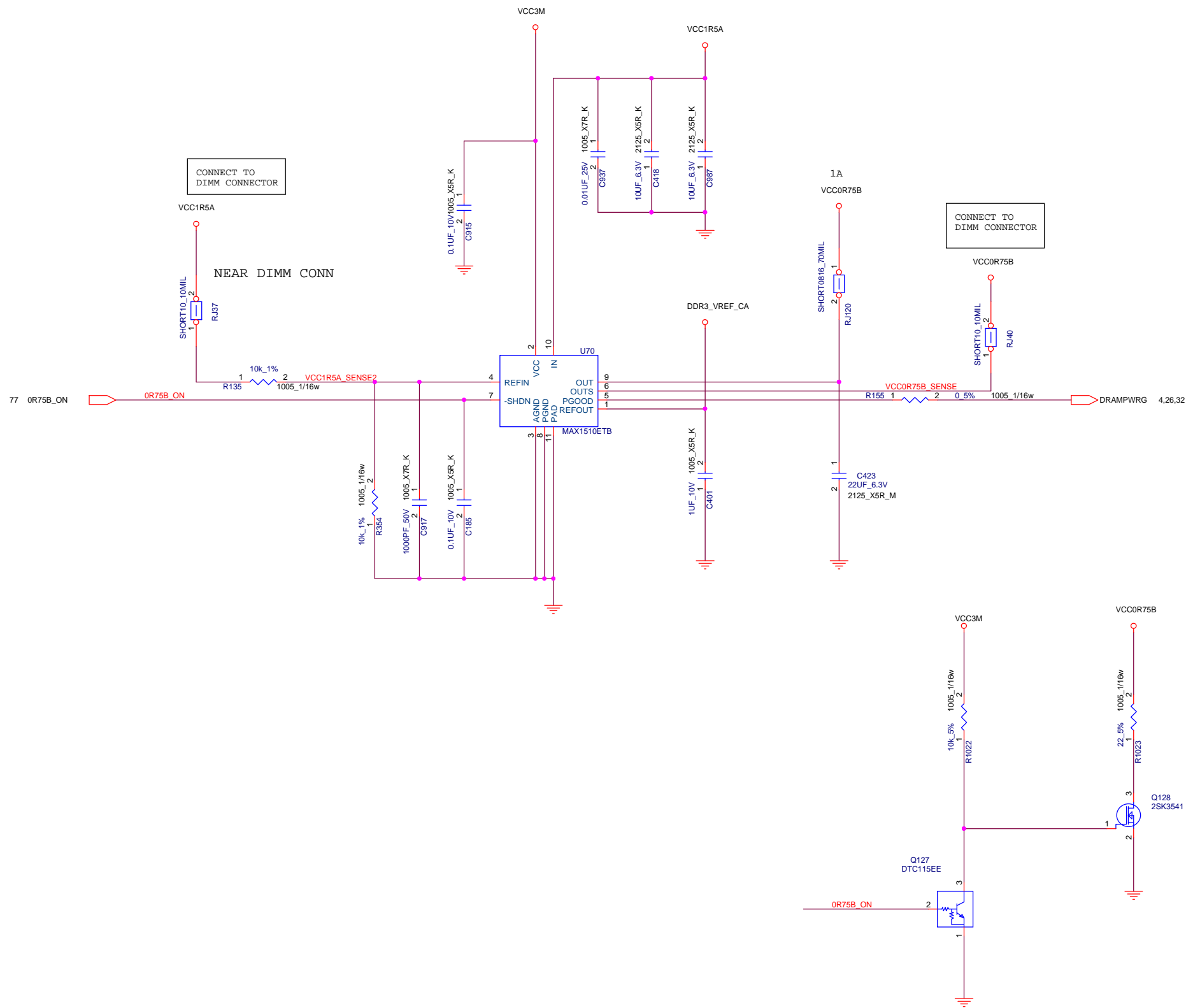
Processor	VCC1R05B_VTT
Sandy Bridge	1.05V
Ivy Bridge ES2	1.05V
Ivy Bridge ES1	1.00V

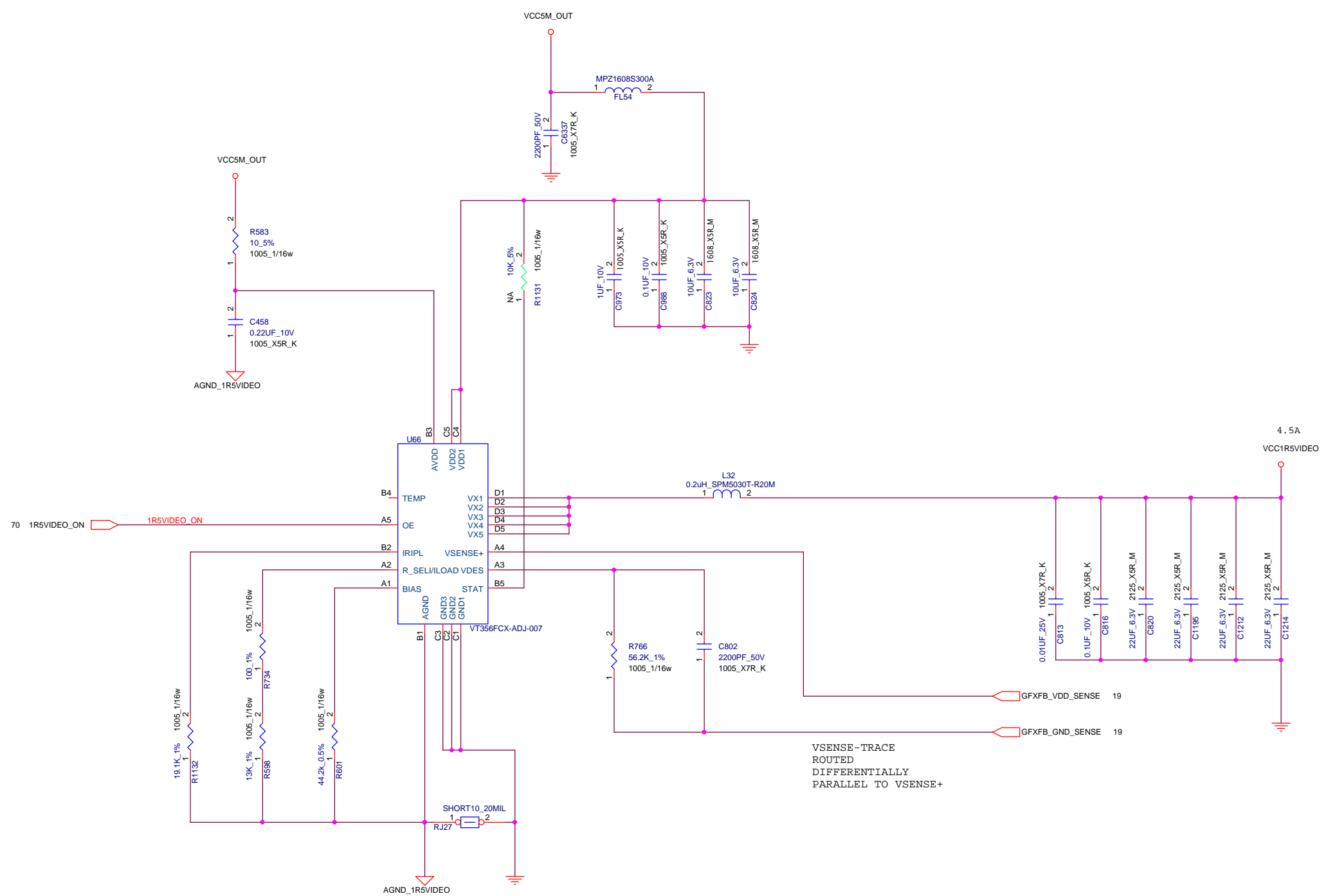


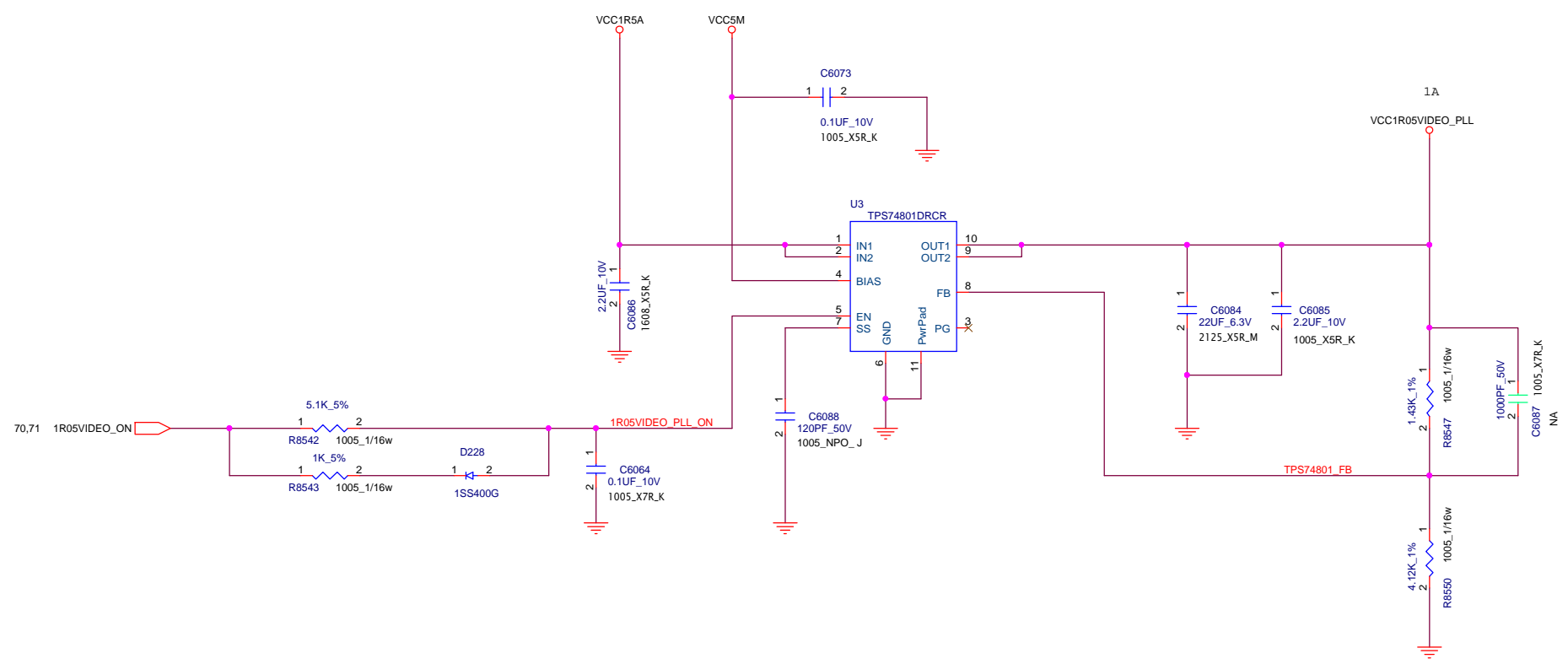
Project Name: NZM-4 SWG SOVP Title: DC/DC VCC1R05B_VTT

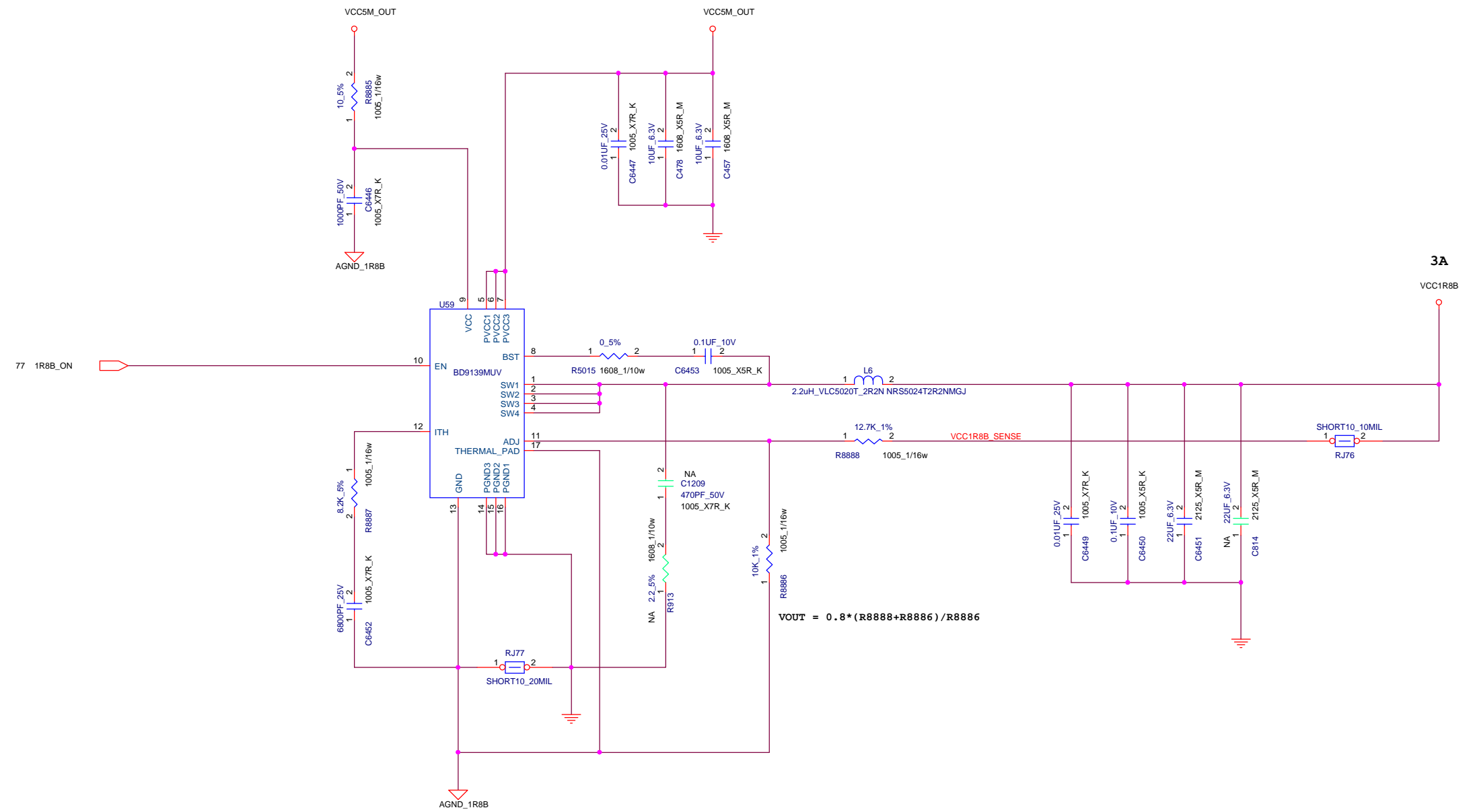
Size: C Document Number: Rev: 7.54
 Date: Monday, March 19, 2012 Sheet: 83 of 97



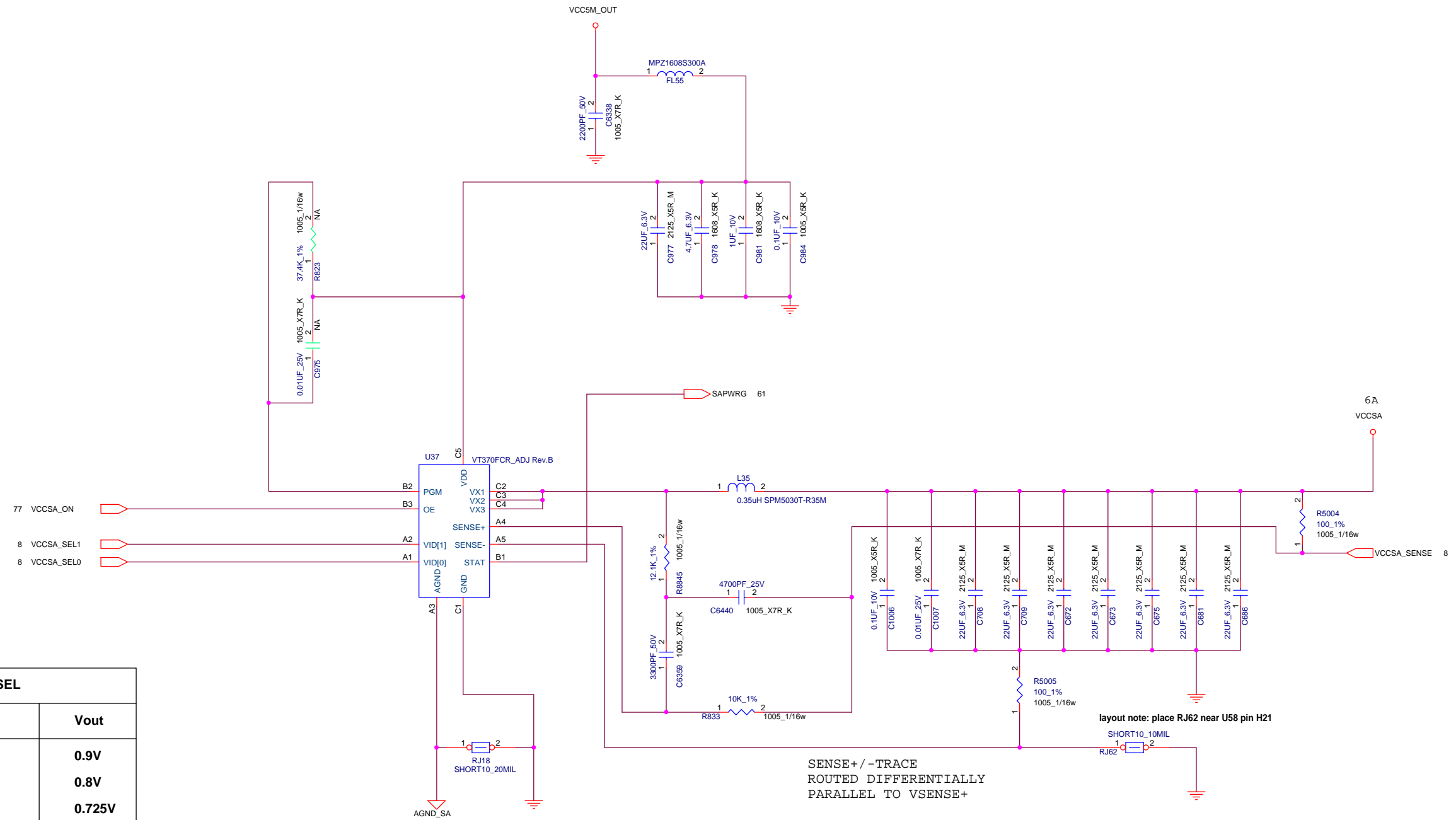


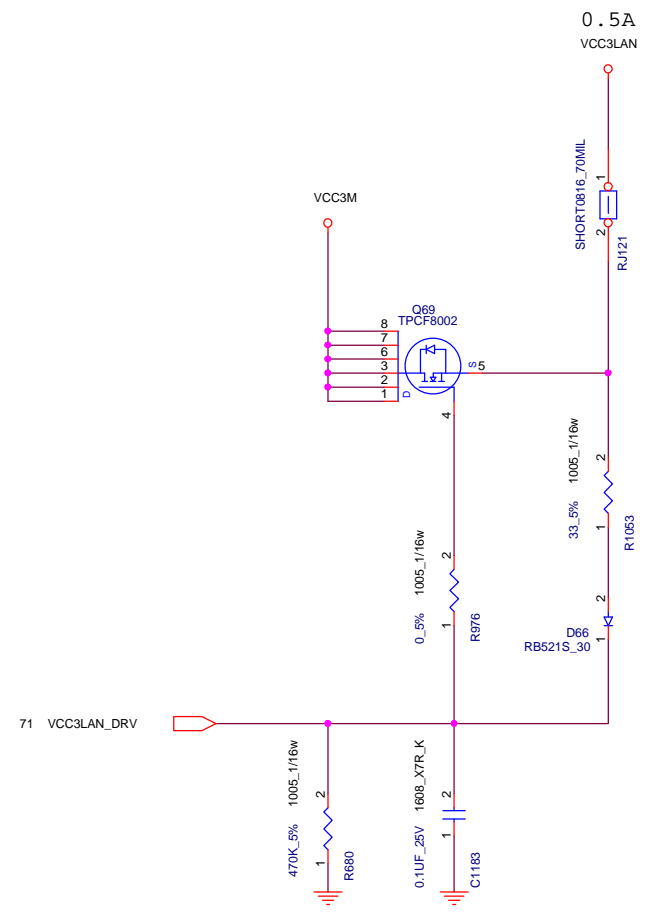


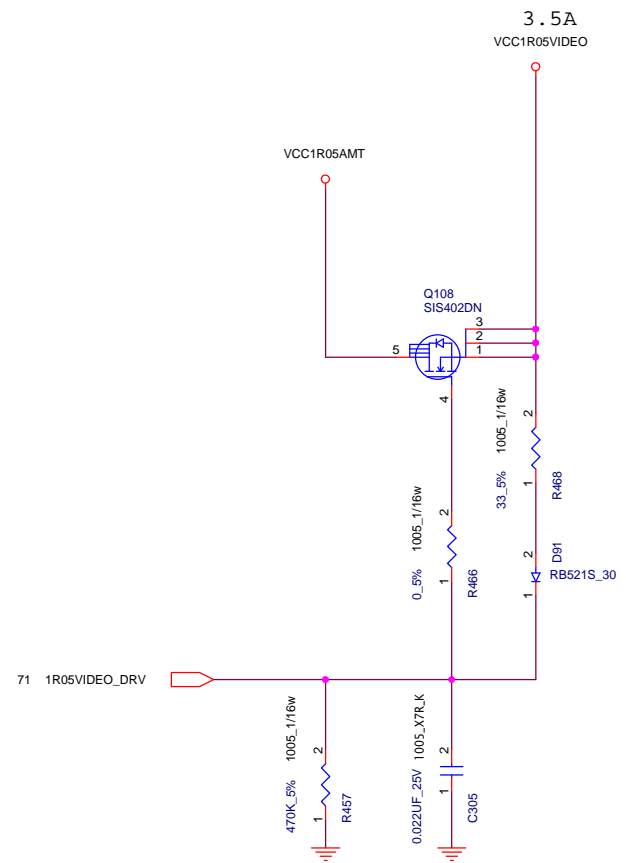
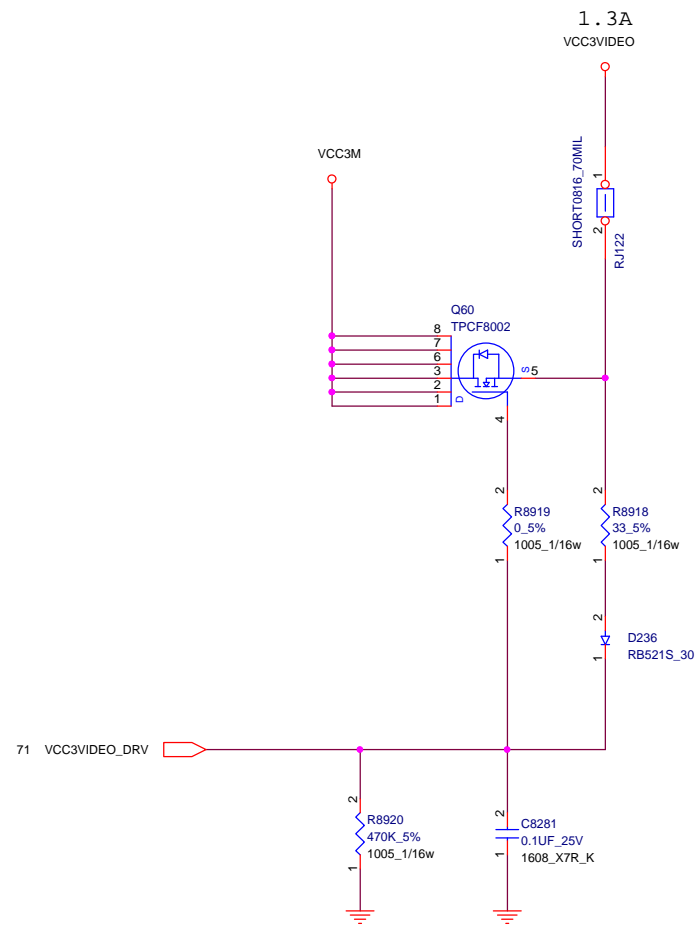





VCCSA_SEL		
VID0	VID1	Vout
L	L	0.9V
L	H	0.8V
H	L	0.725V
H	H	0.675V

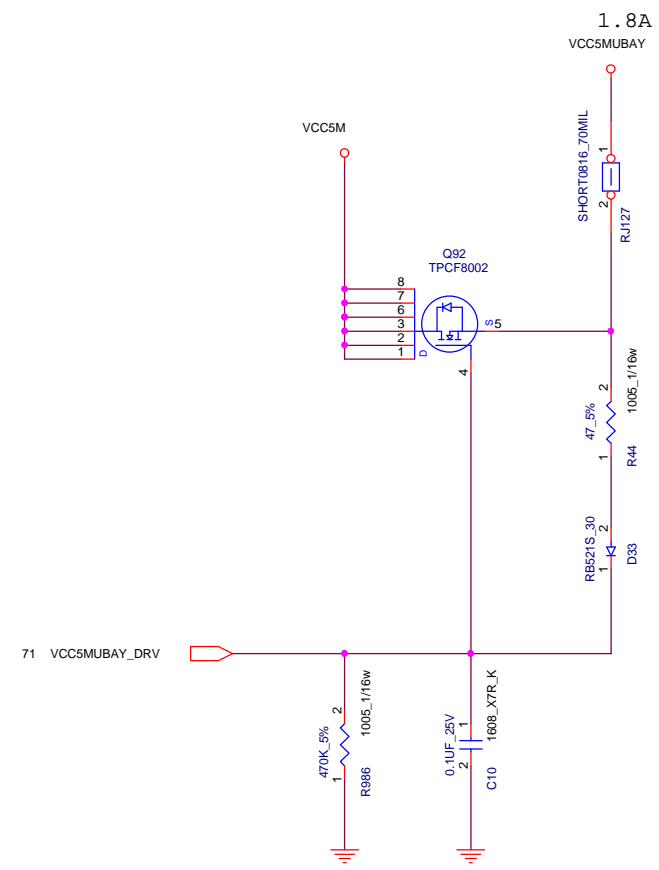


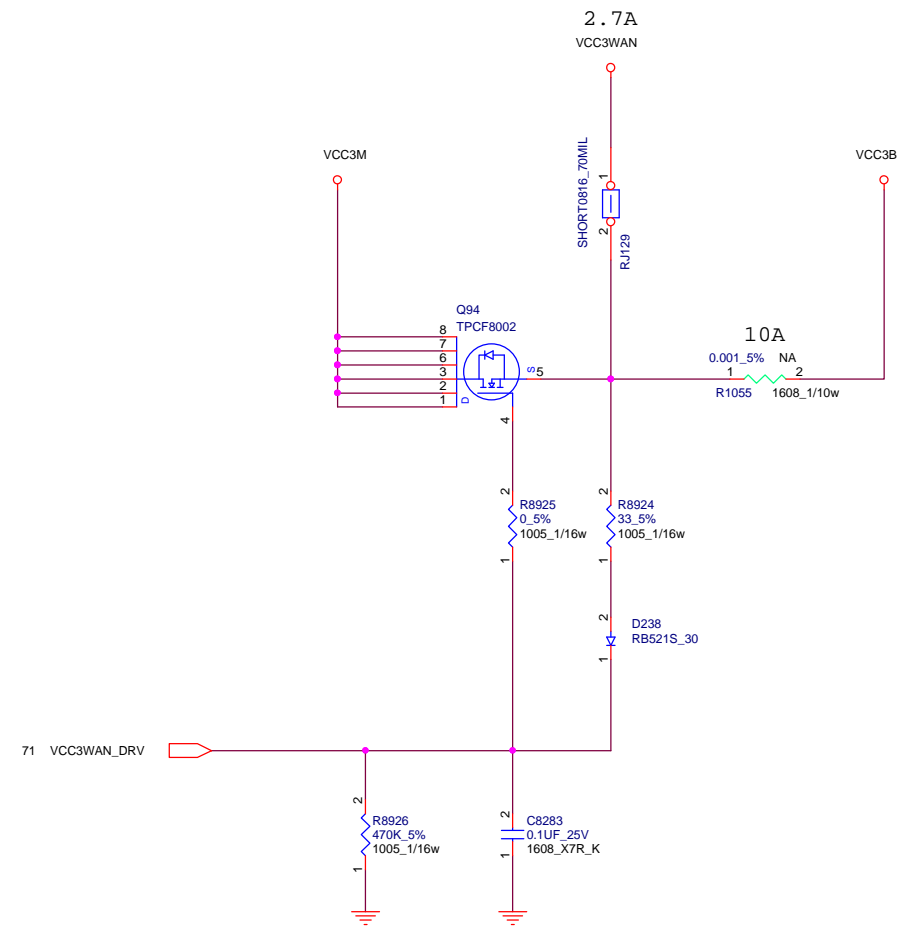
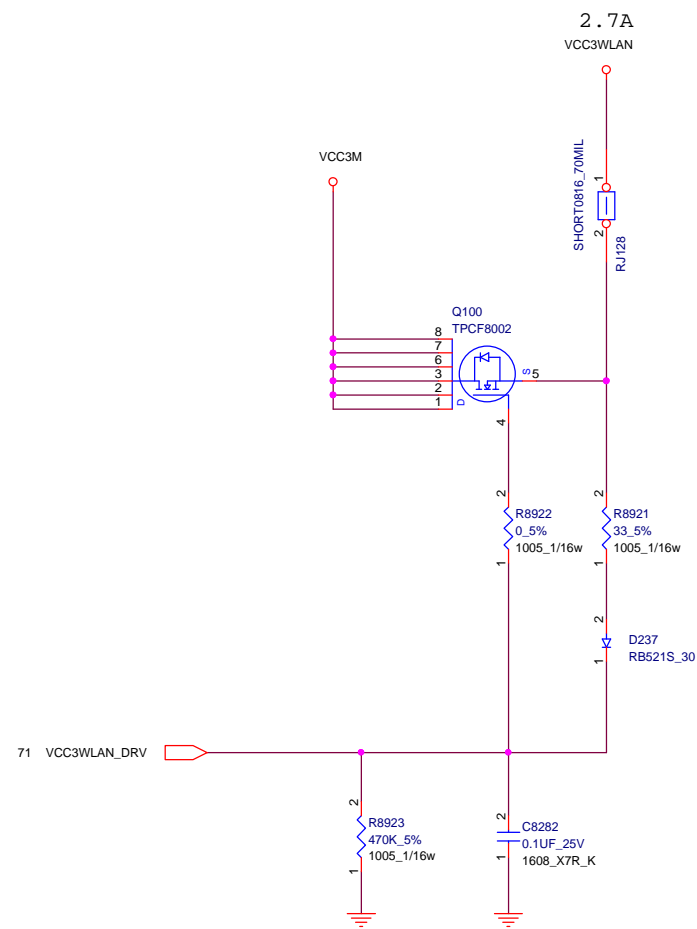




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Project Name : NZM-4 SWG SOVP		Title : BLANK
Size : C	Document Number :	Rev : 7.54
Date: Monday, March 19, 2012		Sheet: 93 of 97



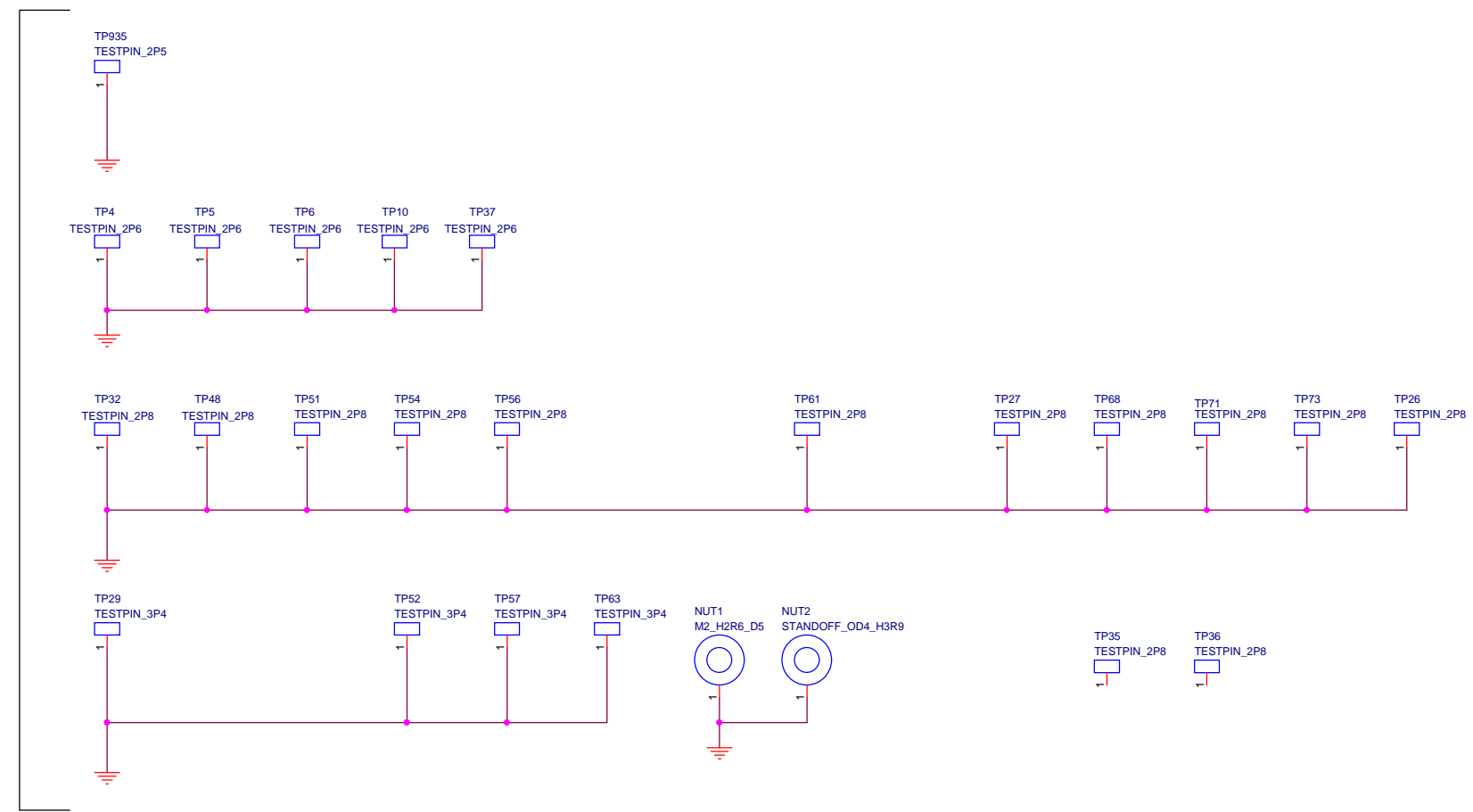


TABLE

AOAC	YES	NO
R1055	NO-ASM	ASM
Q94	ASM	NO-ASM
R8924	ASM	NO-ASM
R8925	ASM	NO-ASM
R8926	ASM	NO-ASM
C8283	ASM	NO-ASM
D238	ASM	NO-ASM

↑
LOGIC

PTH FOR SCREW HOLE



NPTH



FID Board Area

- FD1 NC, NO CONNECT TO ANY.
- FD2 NC, NO CONNECT TO ANY.
- FD3 NC, NO CONNECT TO ANY.

- FD4 NC, NO CONNECT TO ANY.
- FD5 NC, NO CONNECT TO ANY.
- FD6 NC, NO CONNECT TO ANY.

- CF25 NC, NO CONNECT TO ANY.
- CF26 NC, NO CONNECT TO ANY.

FID Component Area

- CF1 NC, NO CONNECT TO ANY.
- CF2 NC, NO CONNECT TO ANY.
- CF3 NC, NO CONNECT TO ANY.
- CF4 NC, NO CONNECT TO ANY.
- CF5 NC, NO CONNECT TO ANY.
- CF6 NC, NO CONNECT TO ANY.
- CF7 NC, NO CONNECT TO ANY.
- CF8 NC, NO CONNECT TO ANY.
- CF9 NC, NO CONNECT TO ANY.
- CF10 NC, NO CONNECT TO ANY.

- CF15 NC, NO CONNECT TO ANY.
- CF16 NC, NO CONNECT TO ANY.
- CF17 NC, NO CONNECT TO ANY.
- CF18 NC, NO CONNECT TO ANY.
- CF19 NC, NO CONNECT TO ANY.
- CF20 NC, NO CONNECT TO ANY.
- CF21 NC, NO CONNECT TO ANY.
- CF22 NC, NO CONNECT TO ANY.
- CF23 NC, NO CONNECT TO ANY.
- CF24 NC, NO CONNECT TO ANY.

lenovo	
Project Name: NZM-4 SWG SOVP	Title: PTH FOR SCREW HOLES
Size: C	Document Number:
Date: Monday, March 19, 2012	Rev: 7.54
Sheet:	97 of 97