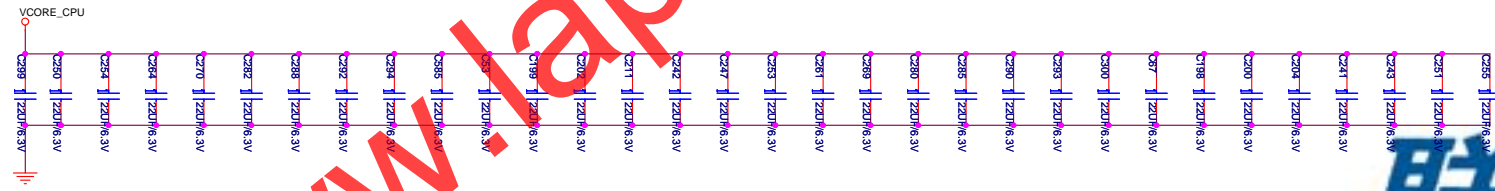
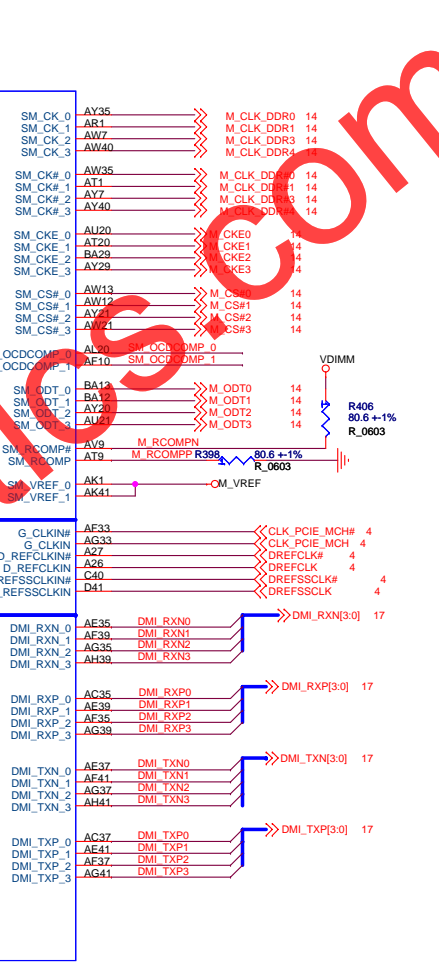
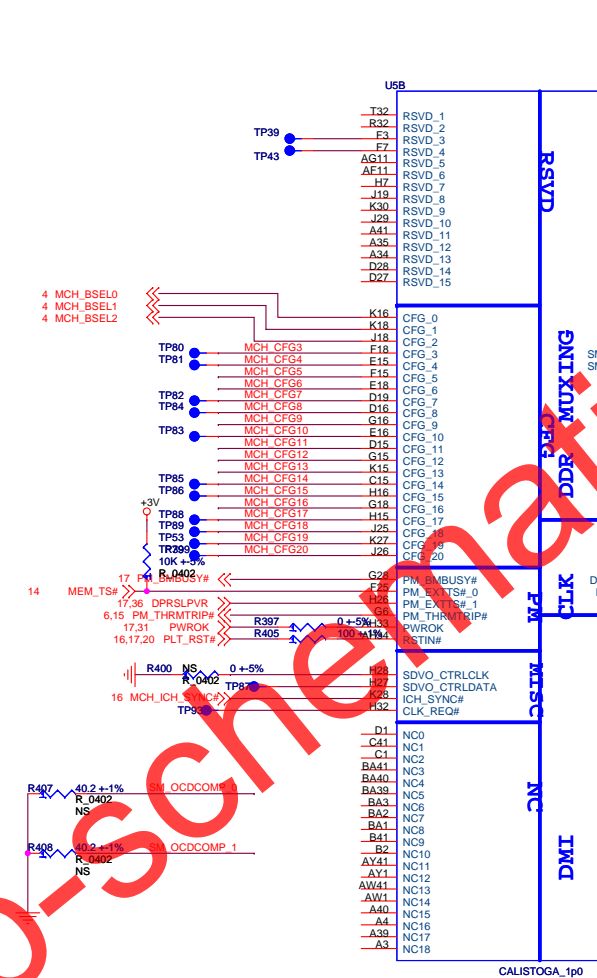
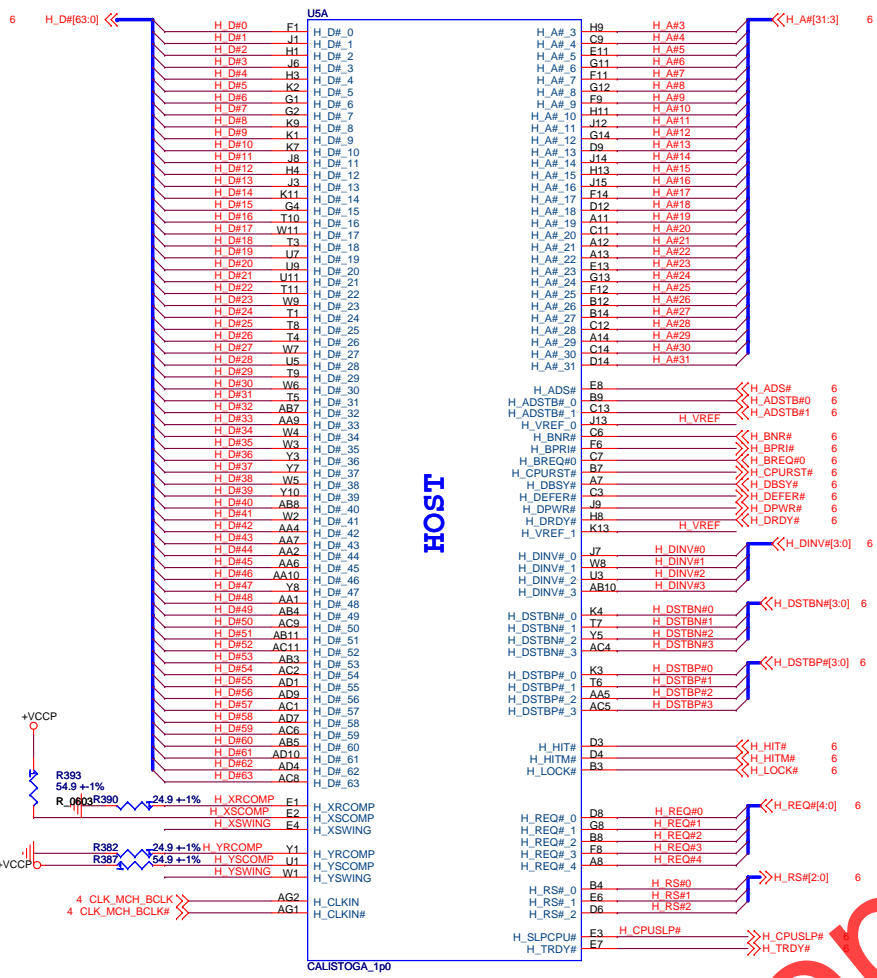


Layout note:
Place c233 c203 near pin B26

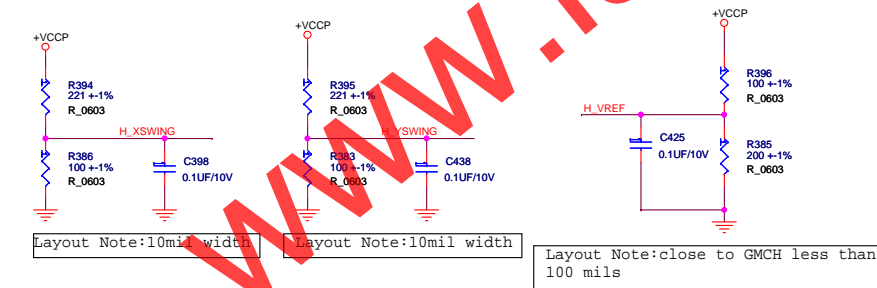
Layout note:
Route VCCSENSE and VSSSENSE traces at 27.4 OHM with 50 mil spacing place PU and PD within 1 inch of CPU





LEVEL	0	1
CFG5	DMI x2	DMI x4 (default)
CFG6	Moby Dick	Calistoga
CFG9	Reverse Lanes	Normal Operation (default)
CFG11	Calistoga	Moby Dick
CFG12	Reserved	Reserved
CFG13	Reserved	Reserved
CFG16	Dynamic ODT Disabled	Dynamic ODT Enabled (default)

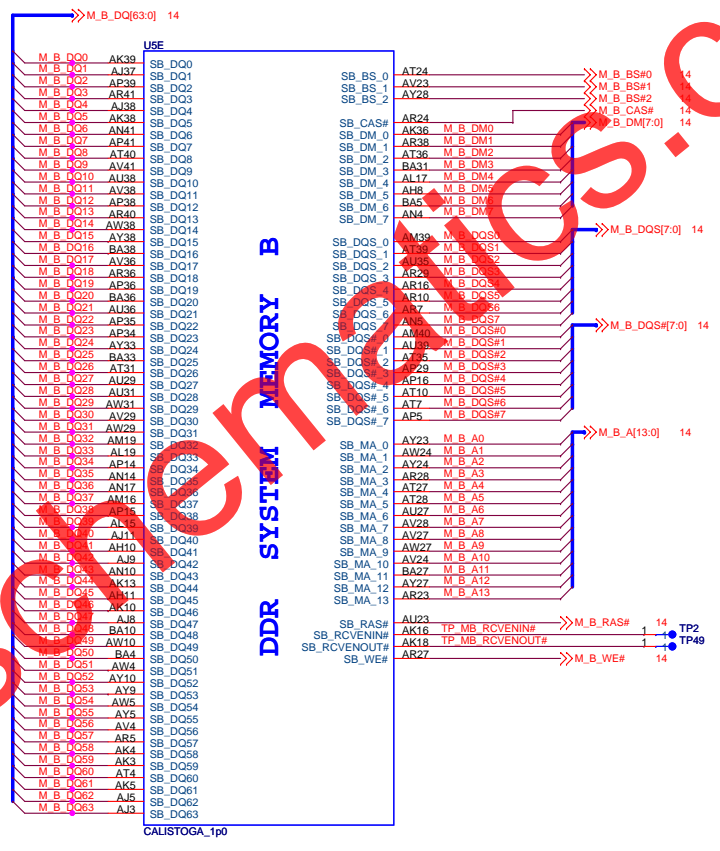
CFG[17:3] INTERNAL PULLUP
CFG[20:18] INTERNAL PULLDOWN



placed close to VREF pins of DDR2 SO-DIMM



Layout Note: 10mil width
Layout Note: 10mil width
Layout Note: close to GMCH less than 100 mils



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+VCCP

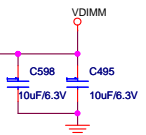
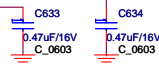
USG

AA33	VCC_0
W33	VCC_1
P33	VCC_2
N33	VCC_3
L33	VCC_4
J33	VCC_5
AA32	VCC_6
Y32	VCC_7
W32	VCC_8
P32	VCC_9
N32	VCC_10
L32	VCC_11
J32	VCC_12
AA31	VCC_13
W31	VCC_14
P31	VCC_15
N31	VCC_16
L31	VCC_17
J31	VCC_18
AA30	VCC_19
W30	VCC_20
P30	VCC_21
N30	VCC_22
L30	VCC_23
J30	VCC_24
AA29	VCC_25
W29	VCC_26
P29	VCC_27
N29	VCC_28
L29	VCC_29
J29	VCC_30
AA28	VCC_31
W28	VCC_32
P28	VCC_33
N28	VCC_34
L28	VCC_35
J28	VCC_36
AA27	VCC_37
W27	VCC_38
P27	VCC_39
N27	VCC_40
L27	VCC_41
J27	VCC_42
AA26	VCC_43
W26	VCC_44
P26	VCC_45
N26	VCC_46
L26	VCC_47
J26	VCC_48
AA25	VCC_49
W25	VCC_50
P25	VCC_51
N25	VCC_52
L25	VCC_53
J25	VCC_54
AA24	VCC_55
W24	VCC_56
P24	VCC_57
N24	VCC_58
L24	VCC_59
J24	VCC_60
AA23	VCC_61
W23	VCC_62
P23	VCC_63
N23	VCC_64
L23	VCC_65
J23	VCC_66
AA22	VCC_67
W22	VCC_68
P22	VCC_69
N22	VCC_70
L22	VCC_71
J22	VCC_72
AA21	VCC_73
W21	VCC_74
P21	VCC_75
N21	VCC_76
L21	VCC_77
J21	VCC_78
AA20	VCC_79
W20	VCC_80
P20	VCC_81
N20	VCC_82
L20	VCC_83
J20	VCC_84
AA19	VCC_85
W19	VCC_86
P19	VCC_87
N19	VCC_88
L19	VCC_89
J19	VCC_90
AA18	VCC_91
W18	VCC_92
P18	VCC_93
N18	VCC_94
L18	VCC_95
J18	VCC_96
AA17	VCC_97
W17	VCC_98
P17	VCC_99
N17	VCC_100
L17	VCC_101
J17	VCC_102
AA16	VCC_103
W16	VCC_104
P16	VCC_105
N16	VCC_106
L16	VCC_107
J16	VCC_108
AA15	VCC_109
W15	VCC_110
P15	VCC_111
N15	VCC_112
L15	VCC_113
J15	VCC_114
AA14	VCC_115
W14	VCC_116
P14	VCC_117
N14	VCC_118
L14	VCC_119
J14	VCC_120
AA13	VCC_121
W13	VCC_122
P13	VCC_123
N13	VCC_124
L13	VCC_125
J13	VCC_126
AA12	VCC_127
W12	VCC_128
P12	VCC_129
N12	VCC_130
L12	VCC_131
J12	VCC_132
AA11	VCC_133
W11	VCC_134
P11	VCC_135
N11	VCC_136
L11	VCC_137
J11	VCC_138
AA10	VCC_139
W10	VCC_140
P10	VCC_141
N10	VCC_142
L10	VCC_143
J10	VCC_144
AA9	VCC_145
W9	VCC_146
P9	VCC_147
N9	VCC_148
L9	VCC_149
J9	VCC_150
AA8	VCC_151
W8	VCC_152
P8	VCC_153
N8	VCC_154
L8	VCC_155
J8	VCC_156
AA7	VCC_157
W7	VCC_158
P7	VCC_159
N7	VCC_160
L7	VCC_161
J7	VCC_162
AA6	VCC_163
W6	VCC_164
P6	VCC_165
N6	VCC_166
L6	VCC_167
J6	VCC_168
AA5	VCC_169
W5	VCC_170
P5	VCC_171
N5	VCC_172
L5	VCC_173
J5	VCC_174
AA4	VCC_175
W4	VCC_176
P4	VCC_177
N4	VCC_178
L4	VCC_179
J4	VCC_180
AA3	VCC_181
W3	VCC_182
P3	VCC_183
N3	VCC_184
L3	VCC_185
J3	VCC_186
AA2	VCC_187
W2	VCC_188
P2	VCC_189
N2	VCC_190
L2	VCC_191
J2	VCC_192
AA1	VCC_193
W1	VCC_194
P1	VCC_195
N1	VCC_196
L1	VCC_197
J1	VCC_198
AA0	VCC_199
W0	VCC_200
P0	VCC_201
N0	VCC_202
L0	VCC_203
J0	VCC_204

VCC

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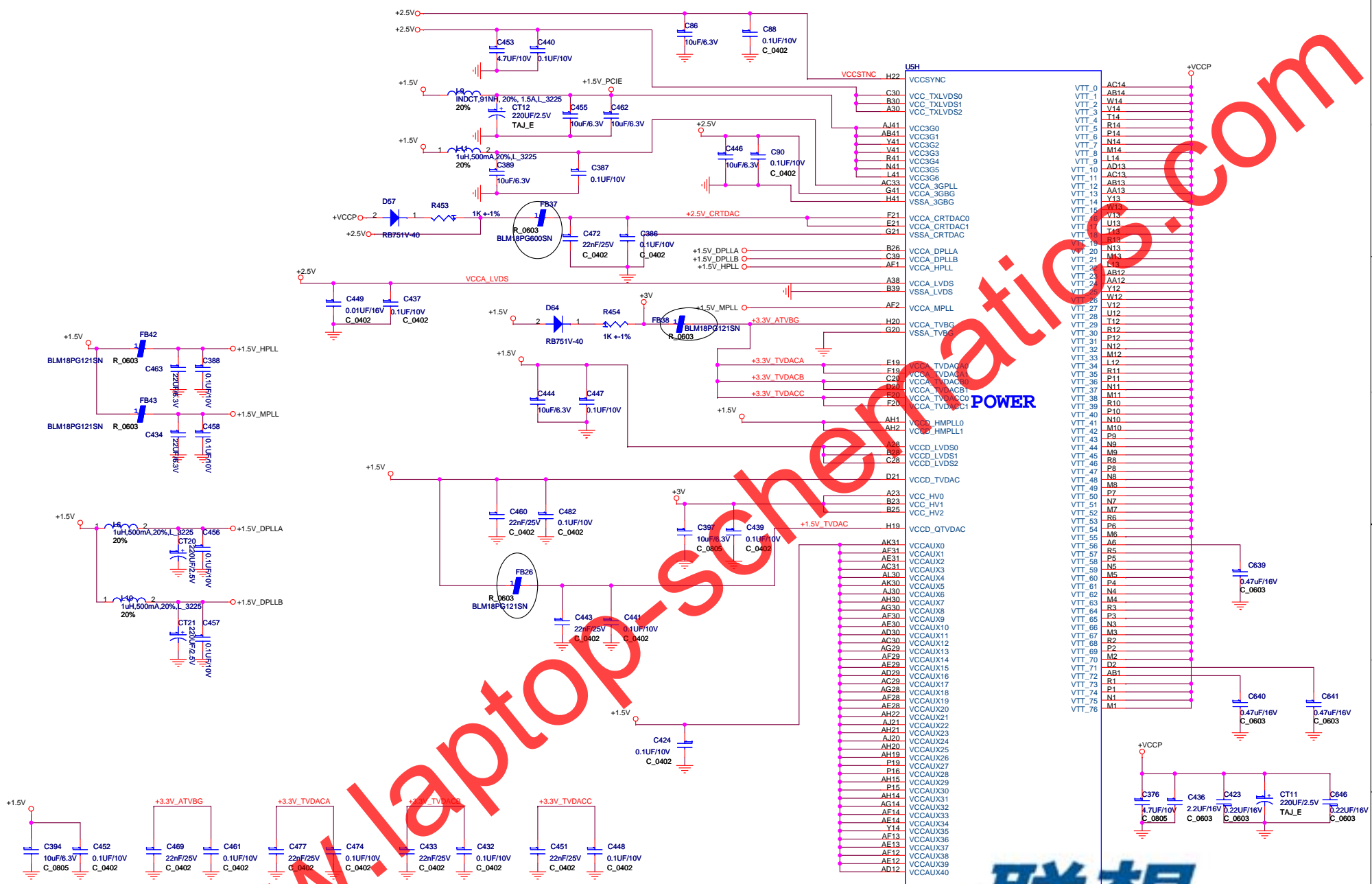
VCC_SM_0	AU41
VCC_SM_1	AM41
VCC_SM_2	AU40
VCC_SM_3	BA34
VCC_SM_4	AW34
VCC_SM_5	AV34
VCC_SM_6	AU34
VCC_SM_7	AT34
VCC_SM_8	AR34
VCC_SM_9	BA30
VCC_SM_10	AY30
VCC_SM_11	AW30
VCC_SM_12	AV30
VCC_SM_13	AU30
VCC_SM_14	AT30
VCC_SM_15	AR30
VCC_SM_16	AN30
VCC_SM_17	AM30
VCC_SM_18	AV29
VCC_SM_19	AU29
VCC_SM_20	AT29
VCC_SM_21	AR29
VCC_SM_22	AL29
VCC_SM_23	AK29
VCC_SM_24	AJ29
VCC_SM_25	AH29
VCC_SM_26	AG29
VCC_SM_27	AF29
VCC_SM_28	AE29
VCC_SM_29	AD29
VCC_SM_30	AC29
VCC_SM_31	AB29
VCC_SM_32	AA29
VCC_SM_33	AV26
VCC_SM_34	AU26
VCC_SM_35	AT26
VCC_SM_36	AR26
VCC_SM_37	AQ26
VCC_SM_38	AJ25
VCC_SM_39	AH25
VCC_SM_40	AG25
VCC_SM_41	AF25
VCC_SM_42	AE25
VCC_SM_43	AD25
VCC_SM_44	AC25
VCC_SM_45	AB25
VCC_SM_46	AA25
VCC_SM_47	AX22
VCC_SM_48	AW22
VCC_SM_49	AV22
VCC_SM_50	AU22
VCC_SM_51	AT22
VCC_SM_52	AR22
VCC_SM_53	AP22
VCC_SM_54	AQ22
VCC_SM_55	AJ21
VCC_SM_56	AH21
VCC_SM_57	AG21
VCC_SM_58	AF21
VCC_SM_59	AE21
VCC_SM_60	AD21
VCC_SM_61	AC21
VCC_SM_62	AB21
VCC_SM_63	AA21
VCC_SM_64	AV19
VCC_SM_65	AU19
VCC_SM_66	AT19
VCC_SM_67	AR19
VCC_SM_68	AQ19
VCC_SM_69	AJ18
VCC_SM_70	AH18
VCC_SM_71	AG18
VCC_SM_72	AF18
VCC_SM_73	AE18
VCC_SM_74	AD18
VCC_SM_75	AC18
VCC_SM_76	AB18
VCC_SM_77	AA18
VCC_SM_78	AR15
VCC_SM_79	AQ15
VCC_SM_80	AJ14
VCC_SM_81	AH14
VCC_SM_82	AG14
VCC_SM_83	AF14
VCC_SM_84	AE14
VCC_SM_85	AD14
VCC_SM_86	AC14
VCC_SM_87	AB14
VCC_SM_88	AA14
VCC_SM_89	AV8
VCC_SM_90	AU8
VCC_SM_91	AT8
VCC_SM_92	AR8
VCC_SM_93	AQ8
VCC_SM_94	AJ6
VCC_SM_95	AH6
VCC_SM_96	AG6
VCC_SM_97	AF6
VCC_SM_98	AE6
VCC_SM_99	AD6
VCC_SM_100	AC6
VCC_SM_101	AB6
VCC_SM_102	AA6
VCC_SM_103	AV1
VCC_SM_104	AU1
VCC_SM_105	AT1
VCC_SM_106	AR1
VCC_SM_107	AQ1
VCC_SM_108	AJ0
VCC_SM_109	AH0
VCC_SM_110	AG0
VCC_SM_111	AF0
VCC_SM_112	AE0
VCC_SM_113	AD0
VCC_SM_114	AC0
VCC_SM_115	AB0
VCC_SM_116	AA0



+VCCP

USF

AD27	VCC_NCTF0
AC27	VCC_NCTF1
AB27	VCC_NCTF2
AA27	VCC_NCTF3
W27	VCC_NCTF4
V27	VCC_NCTF5
U27	VCC_NCTF6
T27	VCC_NCTF7
R27	VCC_NCTF8
Q27	VCC_NCTF9
AD26	VCC_NCTF10
AC26	VCC_NCTF11
AB26	VCC_NCTF12
AA26	VCC_NCTF13
W26	VCC_NCTF14
V26	VCC_NCTF15
U26	VCC_NCTF16
T26	VCC_NCTF17
R26	VCC_NCTF18
Q26	VCC_NCTF19
AD25	VCC_NCTF20
AC25	VCC_NCTF21
AB25	VCC_NCTF22
AA25	VCC_NCTF23
W25	VCC_NCTF24
V25	VCC_NCTF25
U25	VCC_NCTF26
T25	VCC_NCTF27
R25	VCC_NCTF28
Q25	VCC_NCTF29
AD24	VCC_NCTF30
AC24	VCC_NCTF31
AB24	VCC_NCTF32
AA24	VCC_NCTF33
W24	VCC_NCTF34
V24	VCC_NCTF35
U24	VCC_NCTF36
T24	VCC_NCTF37
R24	VCC_NCTF38
Q24	VCC_NCTF39
AD23	VCC_NCTF40
AC23	VCC_NCTF41
AB23	VCC_NCTF42
AA23	VCC_NCTF43
W23	VCC_NCTF44
V23	VCC_NCTF45
U23	VCC_NCTF46
T23	VCC_NCTF47
R23	VCC_NCTF48
Q23	VCC_NCTF49
AD22	VCC_NCTF50
AC22	VCC_NCTF51
AB22	VCC_NCTF52
AA22	VCC_NCTF53
W22	VCC_NCTF54
V22	VCC_NCTF55
U22	VCC_NCTF56
T22	VCC_NCTF57
R22	VCC_NCTF58
Q22	VCC_NCTF59
AD21	VCC_NCTF60
AC21	VCC_NCTF61
AB21	VCC_NCTF62
AA21	VCC_NCTF63
W21	VCC_NCTF64
V21	VCC_NCTF65
U21	VCC_NCTF66
T21	VCC_NCTF67
R21	VCC_NCTF68
Q21	VCC_NCTF69
AD20	VCC_NCTF70
AC20	VCC_NCTF71
AB20	VCC_NCTF72
AA20	VCC_NCTF73
W20	VCC_NCTF74
V20	VCC_NCTF75
U20	VCC_NCTF76
T20	VCC_NCTF77
R20	VCC_NCTF78
Q20	VCC_NCTF79
AD19	VCC_NCTF80
AC19	VCC_NCTF81
AB19	VCC_NCTF82
AA19	VCC_NCTF83
W19	VCC_NCTF84
V19	VCC_NCTF85
U19	VCC_NCTF86
T19	VCC_NCTF87
R19	VCC_NCTF88
Q19	VCC_NCTF89
AD18	VCC_NCTF90
AC18	VCC_NCTF91
AB18	VCC_NCTF92
AA18	VCC_NCTF93
W18	VCC_NCTF94
V18	VCC_NCTF95
U18	VCC_NCTF96
T18	VCC_NCTF97
R18	VCC_NCTF98
Q18	VCC_NCTF99
AD17	VCC_NCTF100
AC17	VCC_NCTF101
AB17	VCC_NCTF102
AA17	VCC_NCTF103
W17	VCC_NCTF104
V17	VCC_NCTF105
U17	VCC_NCTF106
T17	VCC_NCTF107
R17	VCC_NCTF108
Q17	VCC_NCTF109
AD16	VCC_NCTF110
AC16	VCC_NCTF111
AB16	VCC_NCTF112
AA16	VCC_NCTF113
W16	VCC_NCTF114
V16	VCC_NCTF115
U16	VCC_NCTF116
T16	VCC_NCTF117
R16	VCC_NCTF118
Q16	VCC_NCTF119
AD15	VCC_NCTF120
AC15	VCC_NCTF121
AB15	VCC_NCTF122
AA15	VCC_NCTF123
W15	VCC_NCTF124
V15	VCC_NCTF125
U15	VCC_NCTF126
T15	VCC_NCTF127
R15	VCC_NCTF128
Q15	VCC_NCTF129
AD14	VCC_NCTF130
AC14	VCC_NCTF131
AB14	VCC_NCTF132
AA14	VCC_NCTF133
W14	VCC_NCTF134
V14	VCC_NCTF135
U14	VCC_NCTF136
T14	VCC_NCTF137
R14	VCC_NCTF138
Q14	VCC_NCTF139
AD13	VCC_NCTF140
AC13	VCC_NCTF141
AB13	VCC_NCTF142
AA13	VCC_NCTF143
W13	VCC_NCTF144
V13	VCC_NCTF145
U13	VCC_NCTF146
T13	VCC_NCTF147
R13	VCC_NCTF148
Q13	VCC_NCTF149
AD12	VCC_NCTF150
AC12	VCC_NCTF151
AB12	VCC_NCTF152
AA12	VCC_NCTF153
W12	VCC_NCTF154
V12	VCC_NCTF155
U12	VCC_NCTF156
T12	VCC_NCTF157
R12	VCC_NCTF158
Q12	VCC_NCTF159
AD11	VCC_NCTF160
AC11	VCC_NCTF161
AB11	VCC_NCTF162
AA11	VCC_NCTF163
W11	VCC_NCTF164
V11	VCC_NCTF165
U11	VCC_NCTF166
T11	VCC_NCTF167
R11	VCC_NCTF168
Q11	VCC_NCTF169
AD10	VCC_NCTF170
AC10	VCC_NCTF171
AB10	VCC_NCTF172
AA10	VCC_NCTF173
W10	VCC_NCTF174
V10	VCC_NCTF175
U10	VCC_NCTF176
T10	VCC_NCTF177
R10	VCC_NCTF178
Q10	VCC_NCTF179
AD9	VCC_NCTF180
AC9	VCC_NCTF181
AB9	VCC_NCTF182
AA9	VCC_NCTF183
W9	VCC_NCTF184
V9	VCC_NCTF185
U9	VCC_NCTF186
T9	VCC_NCTF187
R9	VCC_NCTF188
Q9	VCC_NCTF189
AD8	VCC_NCTF190
AC8	VCC_NCTF191
AB8	VCC_NCTF192
AA8	VCC_NCTF193
W8	VCC_NCTF194
V8	VCC_NCTF195
U8	VCC_NCTF196
T8	VCC_NCTF197
R8	VCC_NCTF198
Q8	VCC_NCTF199</

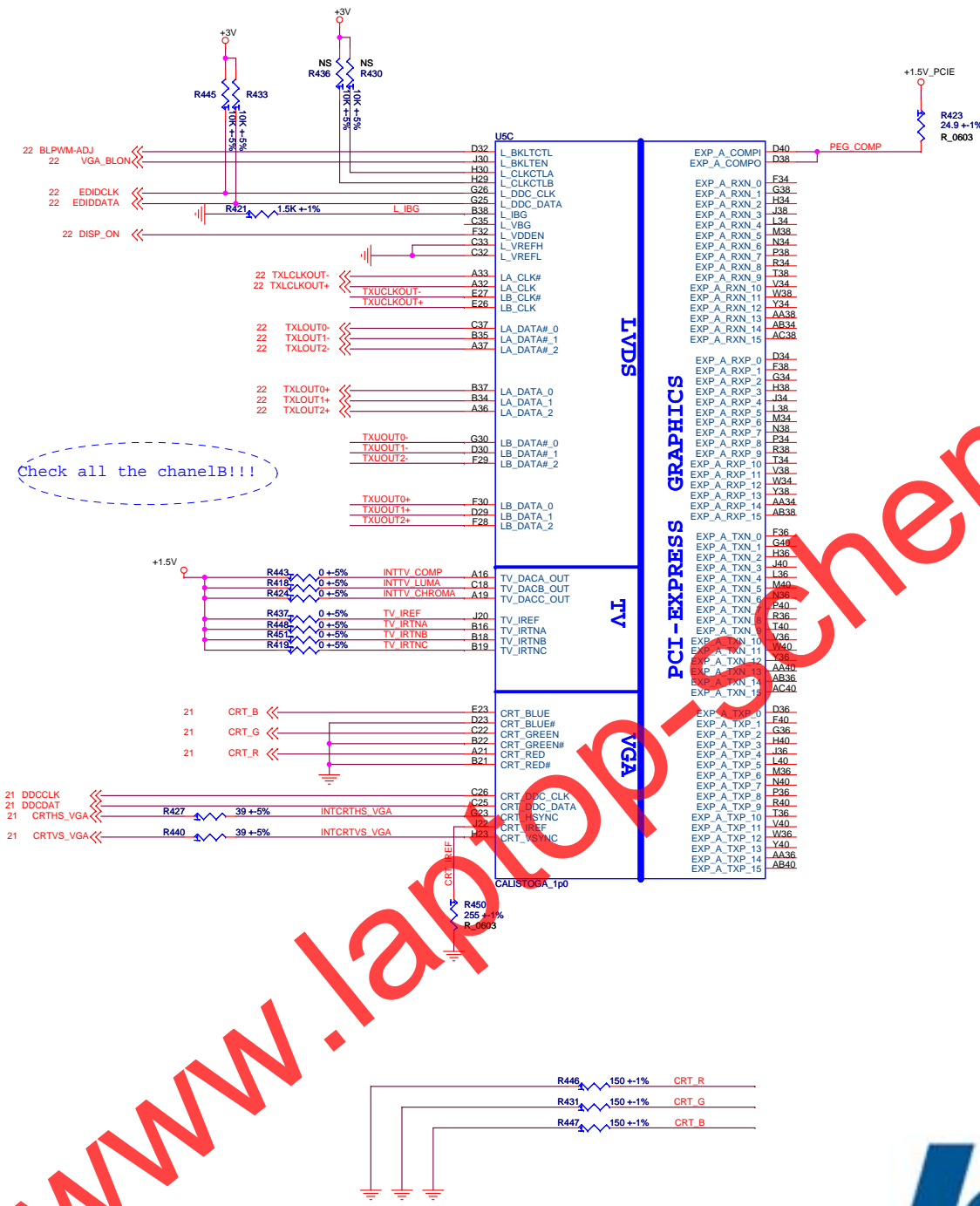


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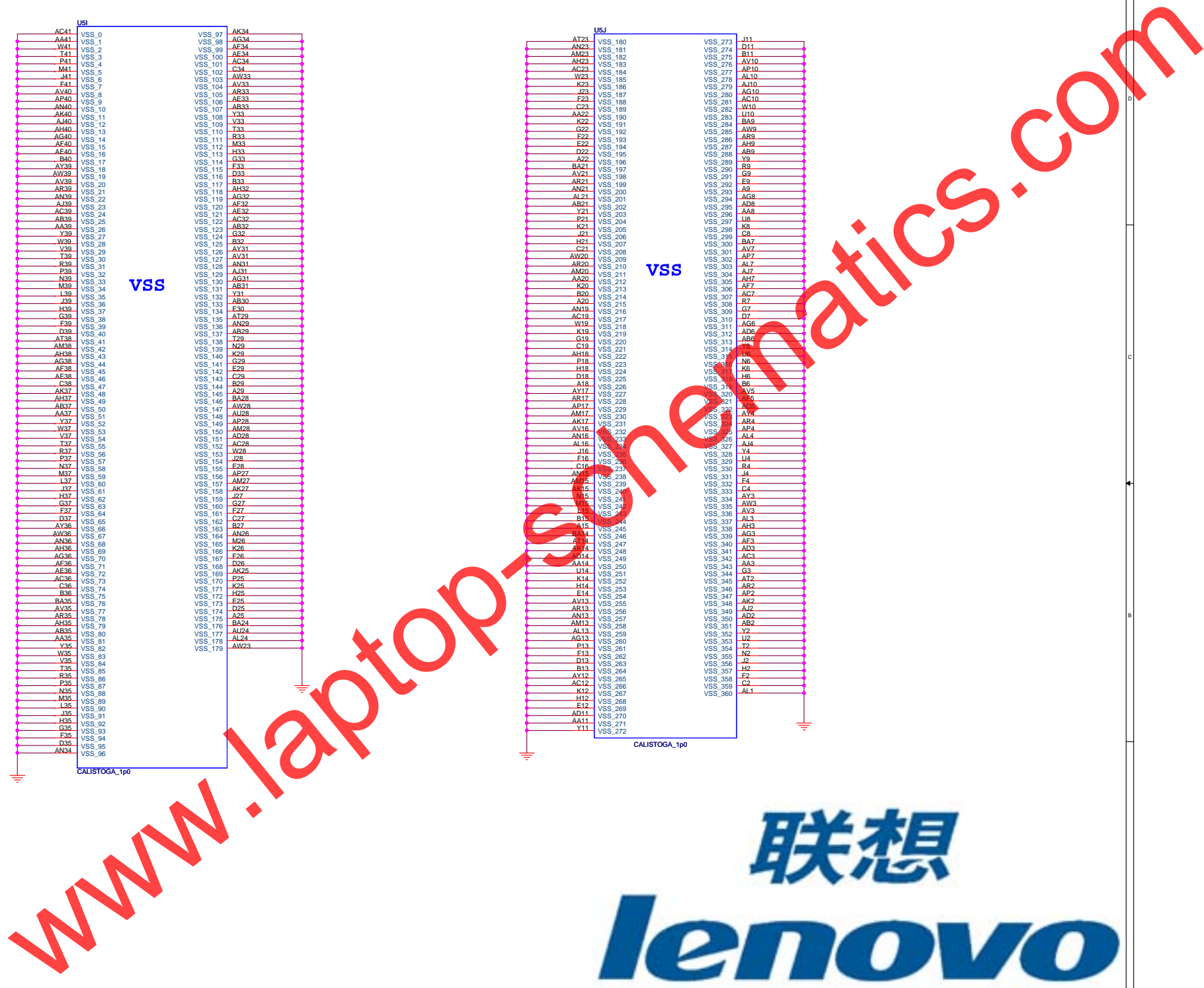


USI	VSS	AK34
AC41	VSS_0	VSS_97
AA41	VSS_1	AG24
W41	VSS_2	AF24
L41	VSS_3	AE34
F41	VSS_4	AH24
M41	VSS_5	C34
J41	VSS_6	AW33
E41	VSS_7	K23
AV40	VSS_8	AR33
AP40	VSS_9	AE33
AH40	VSS_10	AB33
AK40	VSS_11	CB33
AJ40	VSS_12	Y33
AH40	VSS_13	V33
AG40	VSS_14	T33
AF40	VSS_15	R33
AE40	VSS_16	M33
B40	VSS_17	H33
AV39	VSS_18	G33
AW39	VSS_19	D33
AV39	VSS_20	B32
AR39	VSS_21	AH32
AN39	VSS_22	AG32
AJ39	VSS_23	AF32
AC39	VSS_24	Y21
AR39	VSS_25	AE32
AA39	VSS_26	V21
V39	VSS_27	AC32
W39	VSS_28	AB32
V39	VSS_29	G32
T39	VSS_30	H32
R39	VSS_31	Y31
P39	VSS_32	AY31
N39	VSS_33	AV31
M39	VSS_34	AW20
L39	VSS_35	AN20
J39	VSS_36	AM20
H39	VSS_37	AJ20
G39	VSS_38	AG20
F39	VSS_39	AF20
D39	VSS_40	Y20
AT38	VSS_41	P21
AM38	VSS_42	K21
AH38	VSS_43	J21
AG38	VSS_44	H21
AF38	VSS_45	C21
AE38	VSS_46	AW20
C38	VSS_47	AM20
AK37	VSS_48	AJ20
AH37	VSS_49	AG20
AB37	VSS_50	AF20
AA37	VSS_51	Y20
V37	VSS_52	V20
W37	VSS_53	AC20
V37	VSS_54	AB20
T37	VSS_55	T20
R37	VSS_56	N20
N37	VSS_57	K20
M37	VSS_58	AH18
L37	VSS_59	G20
J37	VSS_60	VSS_142
H37	VSS_61	E20
G37	VSS_62	C20
F37	VSS_63	A18
E37	VSS_64	AY17
D37	VSS_65	AR17
AV36	VSS_66	AW28
AW36	VSS_67	AU28
AN36	VSS_68	AK17
AH36	VSS_69	AM28
AG36	VSS_70	AD28
AF36	VSS_71	AL16
AE36	VSS_72	W28
AC36	VSS_73	J28
C36	VSS_74	F28
B36	VSS_75	E28
BA35	VSS_76	VSS_156
AV35	VSS_77	VSS_157
AR35	VSS_78	VSS_158
AJ35	VSS_79	VSS_159
AC35	VSS_80	VSS_160
AR35	VSS_81	VSS_161
AA35	VSS_82	VSS_162
V35	VSS_83	VSS_163
W35	VSS_84	VSS_164
V35	VSS_85	VSS_165
T35	VSS_86	VSS_166
R35	VSS_87	VSS_167
P35	VSS_88	VSS_168
N35	VSS_89	VSS_169
M35	VSS_90	VSS_170
L35	VSS_91	VSS_171
J35	VSS_92	VSS_172
H35	VSS_93	VSS_173
G35	VSS_94	VSS_174
F35	VSS_95	VSS_175
D35	VSS_96	VSS_176
AN34	VSS_96	VSS_177
		VSS_178
		VSS_179

CALISTOGA_1p0

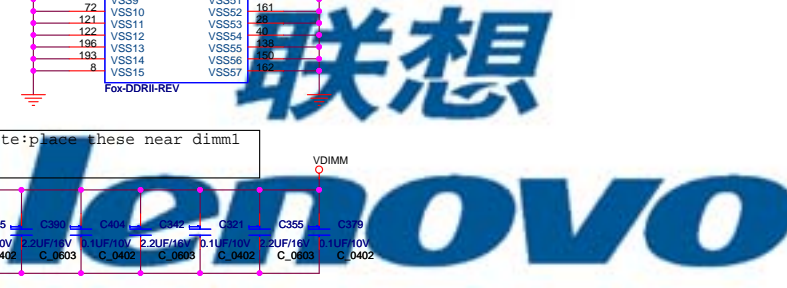
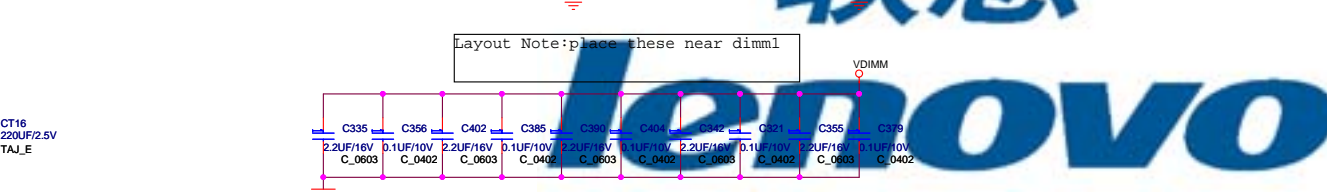
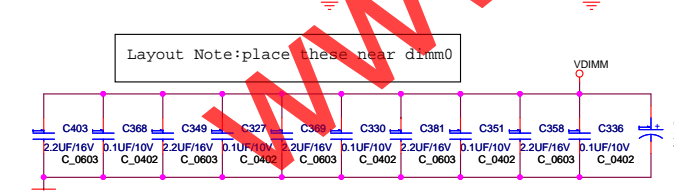
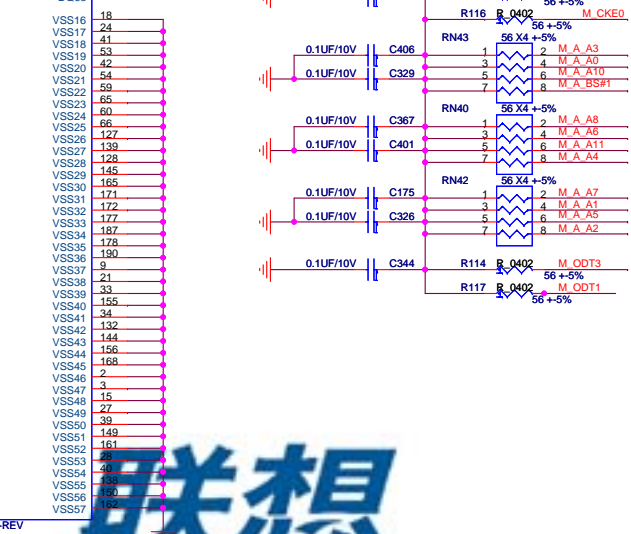
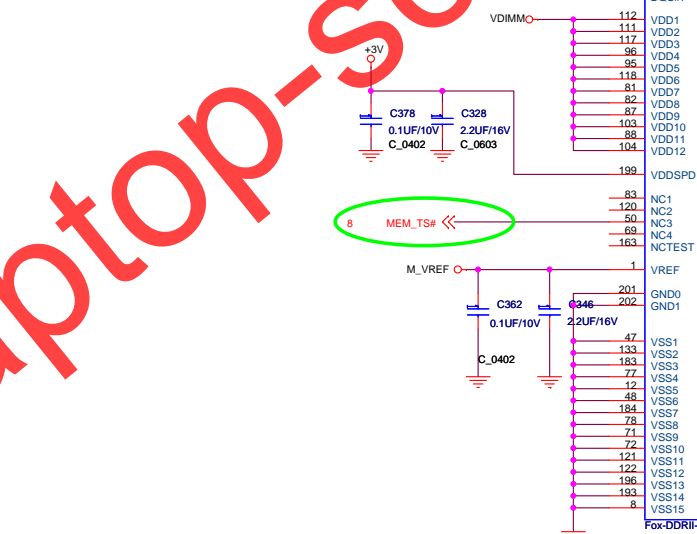
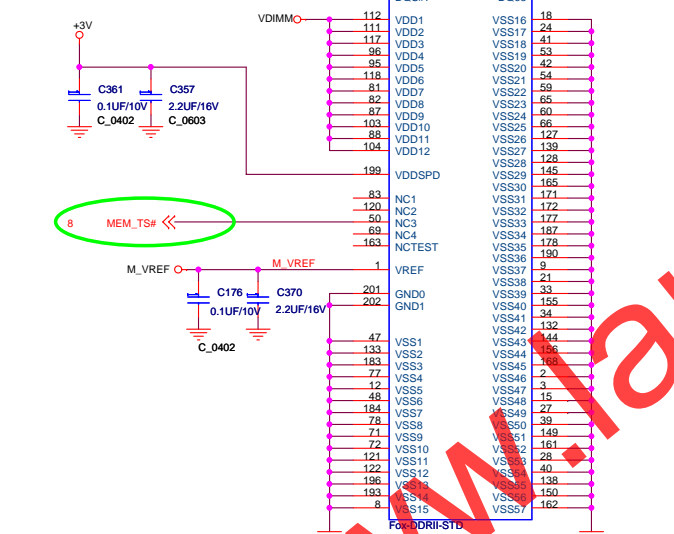
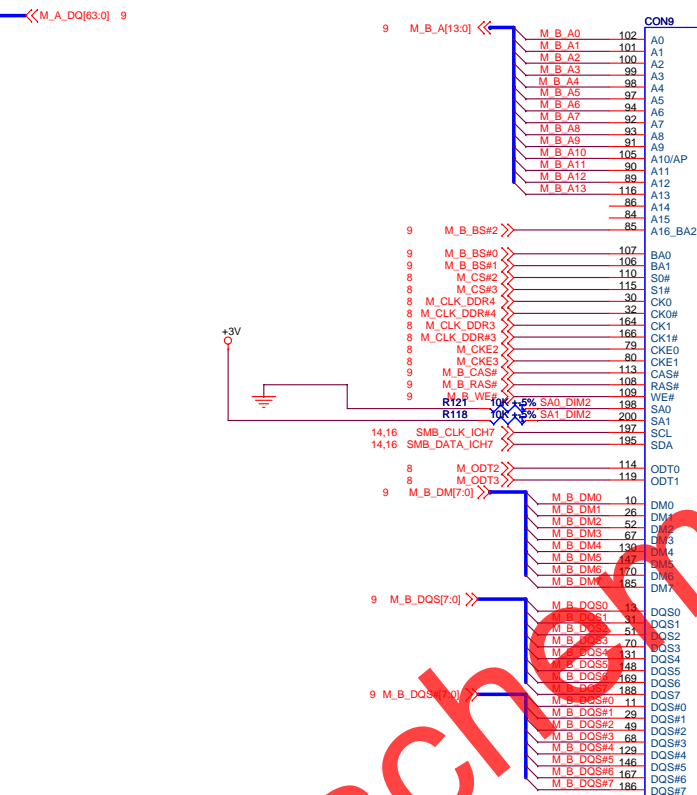
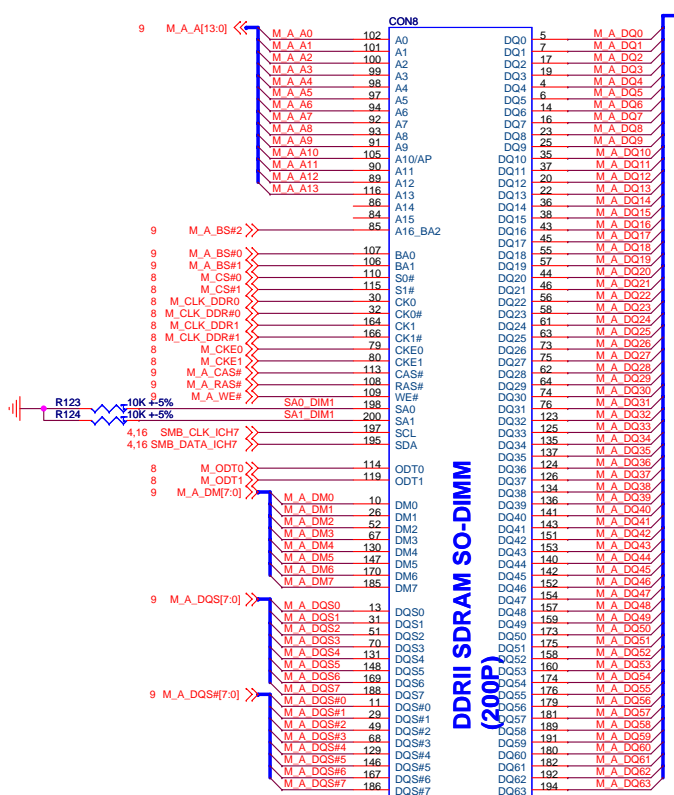
USJ	VSS	J11
AT23	VSS_180	D11
AN23	VSS_181	B11
AM23	VSS_182	AV10
AH23	VSS_183	AP10
AC23	VSS_184	AL10
W23	VSS_185	AJ10
K23	VSS_186	AG10
J23	VSS_187	AW9
F23	VSS_188	AR9
CB23	VSS_189	AH9
Y23	VSS_190	AB9
V23	VSS_191	Y9
T23	VSS_192	R9
R23	VSS_193	G9
M23	VSS_194	E9
H23	VSS_195	AG8
G23	VSS_196	AD8
D23	VSS_197	AA8
AB21	VSS_198	U8
AV21	VSS_199	K8
AR21	VSS_200	CA8
AH21	VSS_201	BA7
AG21	VSS_202	AV7
AF21	VSS_203	AP7
Y21	VSS_204	AL7
P21	VSS_205	AJ7
K21	VSS_206	AH7
J21	VSS_207	AF7
H21	VSS_208	AC7
C21	VSS_209	R7
AW20	VSS_210	G7
AM20	VSS_211	D7
AJ20	VSS_212	AG6
AG20	VSS_213	AD6
AF20	VSS_214	AA6
Y20	VSS_215	U6
V20	VSS_216	K6
T20	VSS_217	CA6
R20	VSS_218	BA5
M20	VSS_219	AV5
H20	VSS_220	AP5
G20	VSS_221	AL5
D20	VSS_222	AJ5
AB20	VSS_223	AH5
AV20	VSS_224	AF5
AR20	VSS_225	AC5
AH20	VSS_226	R5
AG20	VSS_227	G5
AF20	VSS_228	D5
Y20	VSS_229	AG4
V20	VSS_230	AD4
T20	VSS_231	AA4
R20	VSS_232	G4
M20	VSS_233	AL4
H20	VSS_234	AJ4
G20	VSS_235	AH4
D20	VSS_236	AG4
AB20	VSS_237	AF4
AV20	VSS_238	AD4
AR20	VSS_239	AA4
AH20	VSS_240	G4
AG20	VSS_241	AL4
AF20	VSS_242	Y4
Y20	VSS_243	U4
V20	VSS_244	R4
T20	VSS_245	F4
R20	VSS_246	C4
M20	VSS_247	AV3
H20	VSS_248	AW3
G20	VSS_249	AV3
D20	VSS_250	AL3
AB20	VSS_251	AJ3
AV20	VSS_252	AH3
AR20	VSS_253	AG3
AH20	VSS_254	AF3
AG20	VSS_255	AD3
AF20	VSS_256	AA3
Y20	VSS_257	G3
V20	VSS_258	AL3
T20	VSS_259	Y3
R20	VSS_260	U3
M20	VSS_261	T3
H20	VSS_262	N3
G20	VSS_263	J3
D20	VSS_264	H3
AB20	VSS_265	F3
AV20	VSS_266	VSS_358
AR20	VSS_267	VSS_359
AH20	VSS_268	VSS_360
AG20	VSS_269	
AF20	VSS_270	
Y20	VSS_271	
V20	VSS_272	

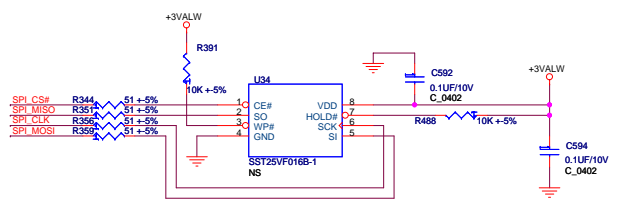
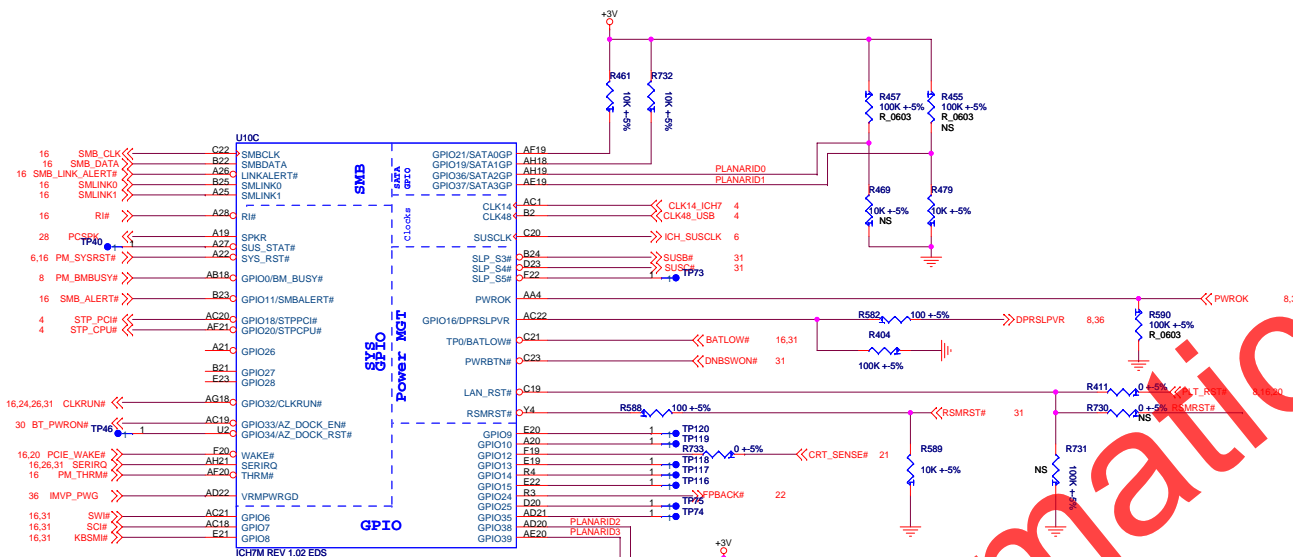
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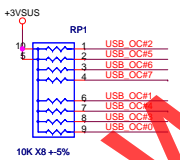
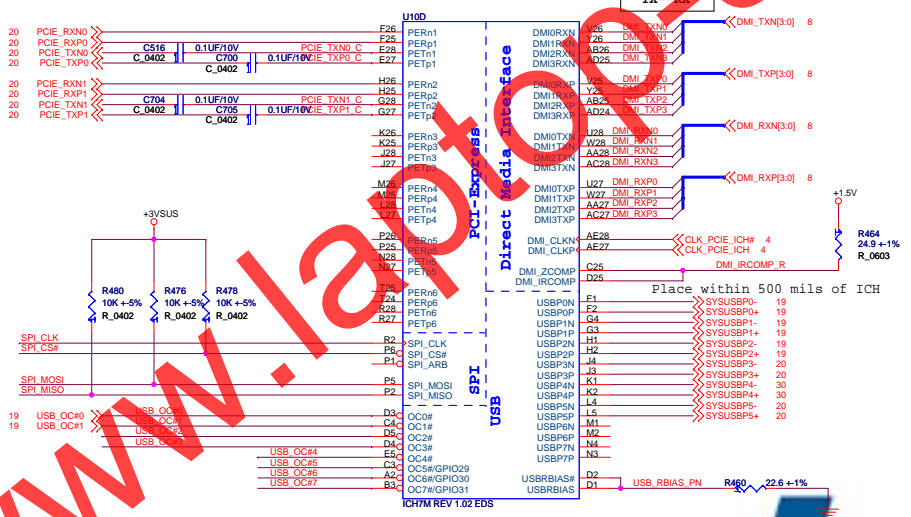




PLACE LESS THAN 2 INCH FROM THE ICH IF USING SHARED ARCHITECTURE.

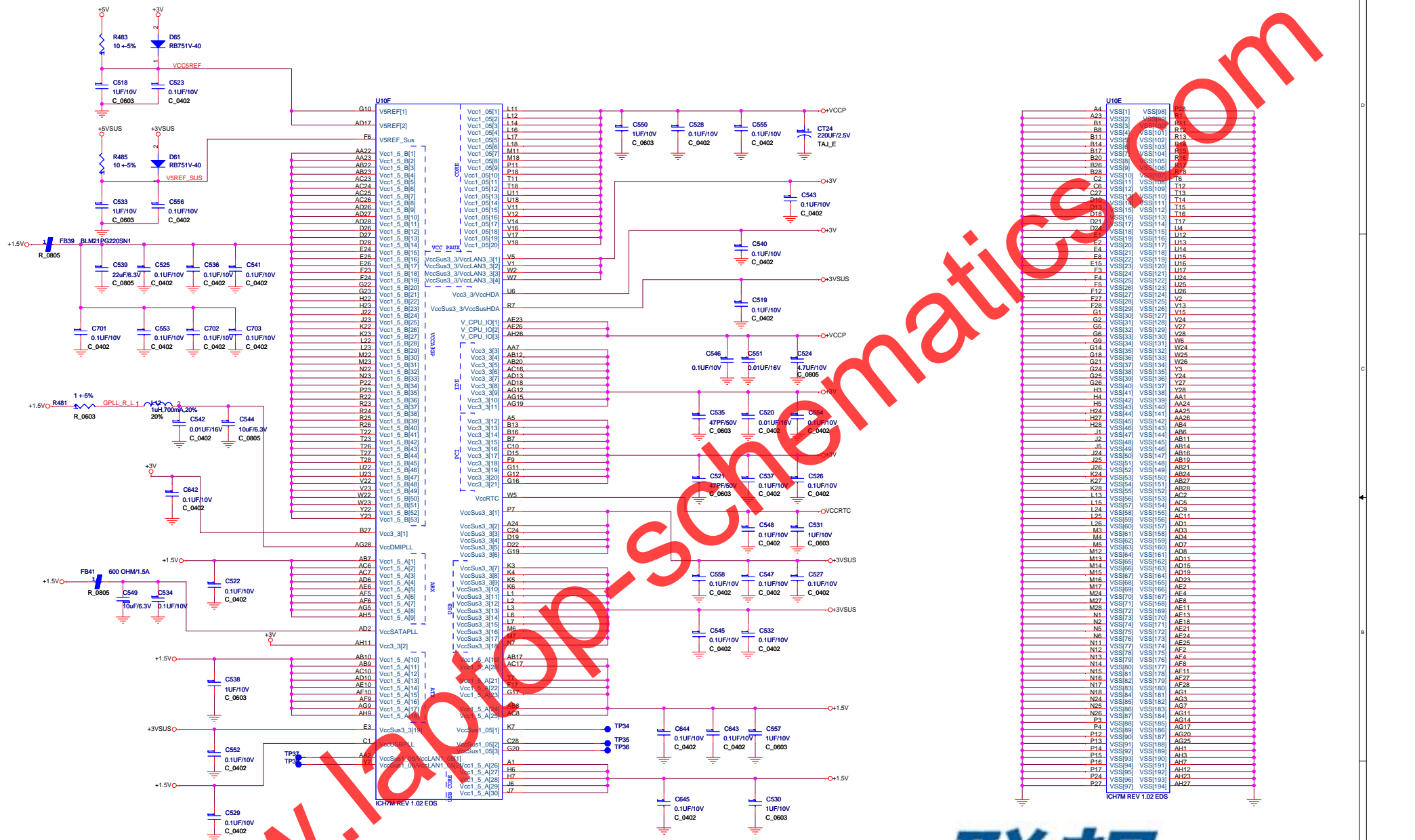
LEVEL	3	2	1	0
	R458	R412	R455	R457
	R459	R475	R479	R469

NOTE:
RX--TX
TX--RX



Place within 500 mils of ICH



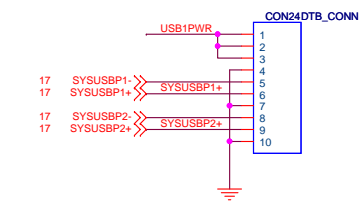
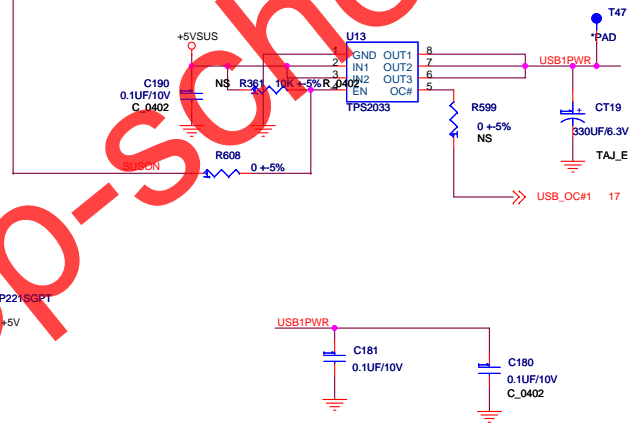
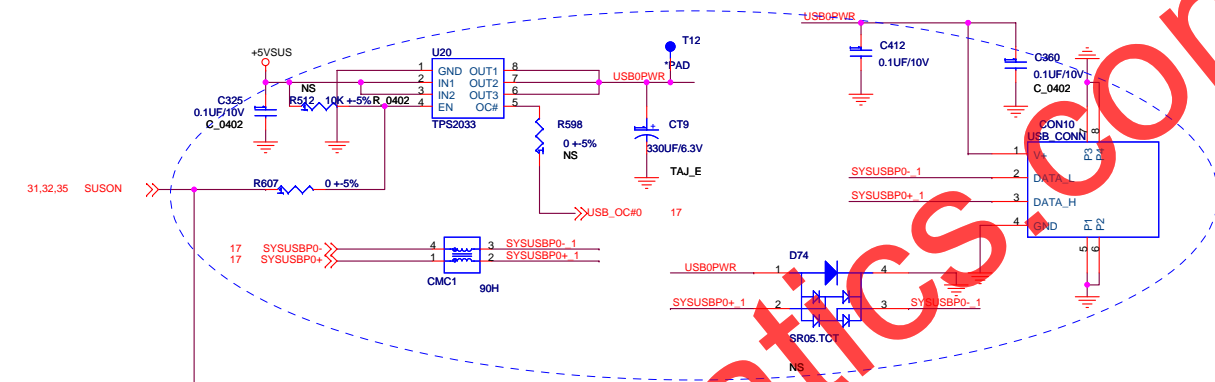
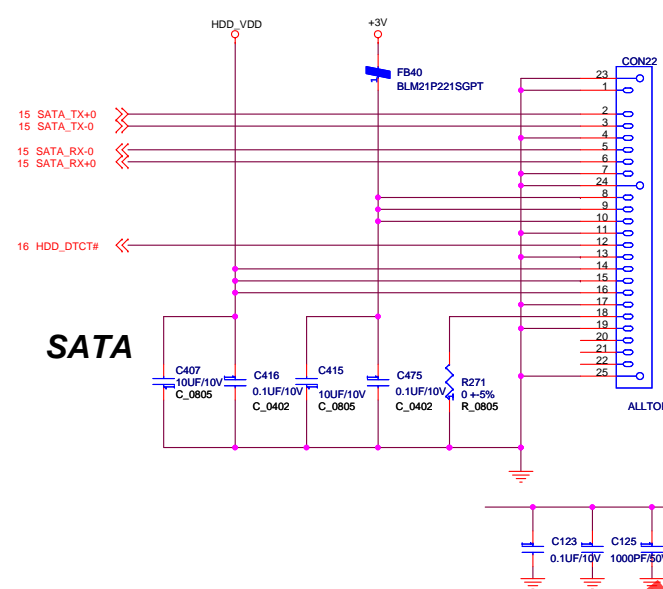


U10E	
A4	VSS[98]
A23	VSS[99]
B1	VSS[100]
B8	VSS[101]
B11	VSS[102]
B14	VSS[103]
B17	VSS[104]
B20	VSS[105]
B23	VSS[106]
B26	VSS[107]
C6	VSS[108]
C27	VSS[109]
D13	VSS[110]
D18	VSS[111]
D21	VSS[112]
D24	VSS[113]
D27	VSS[114]
E2	VSS[115]
E4	VSS[116]
E8	VSS[117]
F3	VSS[118]
F5	VSS[119]
F8	VSS[120]
F9	VSS[121]
F12	VSS[122]
F27	VSS[123]
G1	VSS[124]
G2	VSS[125]
G6	VSS[126]
G9	VSS[127]
G14	VSS[128]
G18	VSS[129]
G21	VSS[130]
G24	VSS[131]
G25	VSS[132]
G28	VSS[133]
H3	VSS[134]
H4	VSS[135]
H5	VSS[136]
H24	VSS[137]
H27	VSS[138]
J1	VSS[139]
J2	VSS[140]
J5	VSS[141]
J24	VSS[142]
J25	VSS[143]
G11	VSS[144]
K24	VSS[145]
K27	VSS[146]
K28	VSS[147]
L13	VSS[148]
L24	VSS[149]
L25	VSS[150]
L26	VSS[151]
M3	VSS[152]
M4	VSS[153]
M5	VSS[154]
M6	VSS[155]
M12	VSS[156]
M13	VSS[157]
M14	VSS[158]
M15	VSS[159]
M16	VSS[160]
M17	VSS[161]
M24	VSS[162]
M27	VSS[163]
M28	VSS[164]
N1	VSS[165]
N2	VSS[166]
N5	VSS[167]
N6	VSS[168]
N11	VSS[169]
N12	VSS[170]
N13	VSS[171]
N14	VSS[172]
N15	VSS[173]
N16	VSS[174]
N17	VSS[175]
N18	VSS[176]
N19	VSS[177]
N24	VSS[178]
N25	VSS[179]
N26	VSS[180]
P3	VSS[181]
P4	VSS[182]
P12	VSS[183]
P13	VSS[184]
P14	VSS[185]
P15	VSS[186]
P16	VSS[187]
P17	VSS[188]
P24	VSS[189]
P27	VSS[190]

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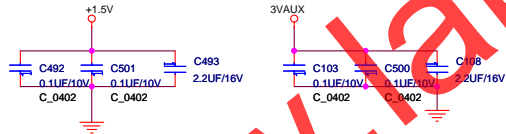
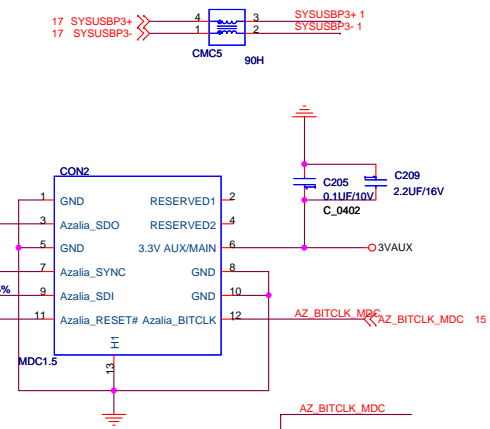
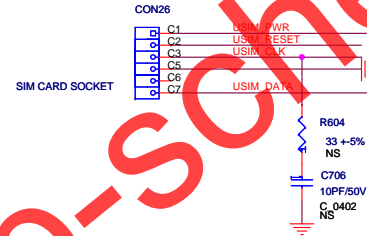
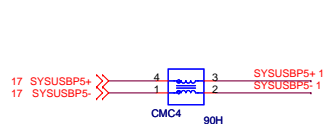
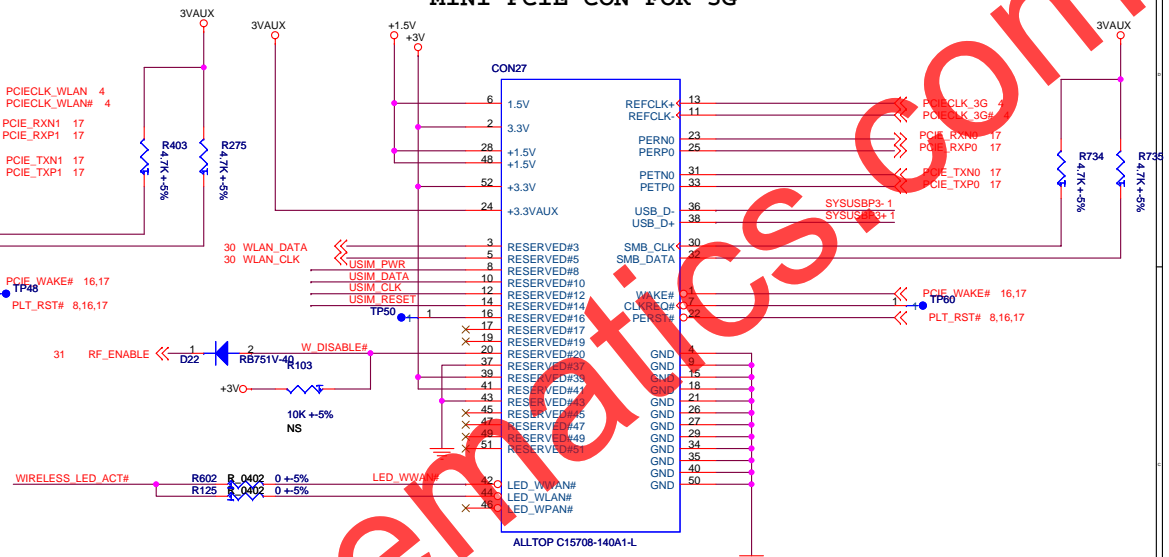
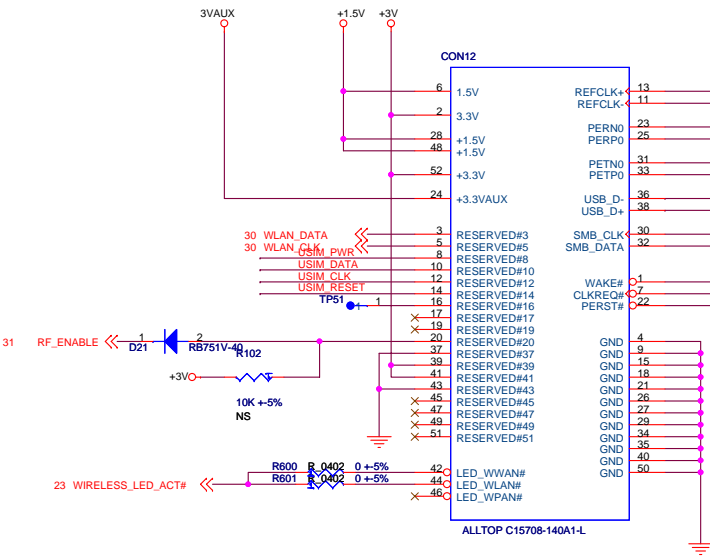
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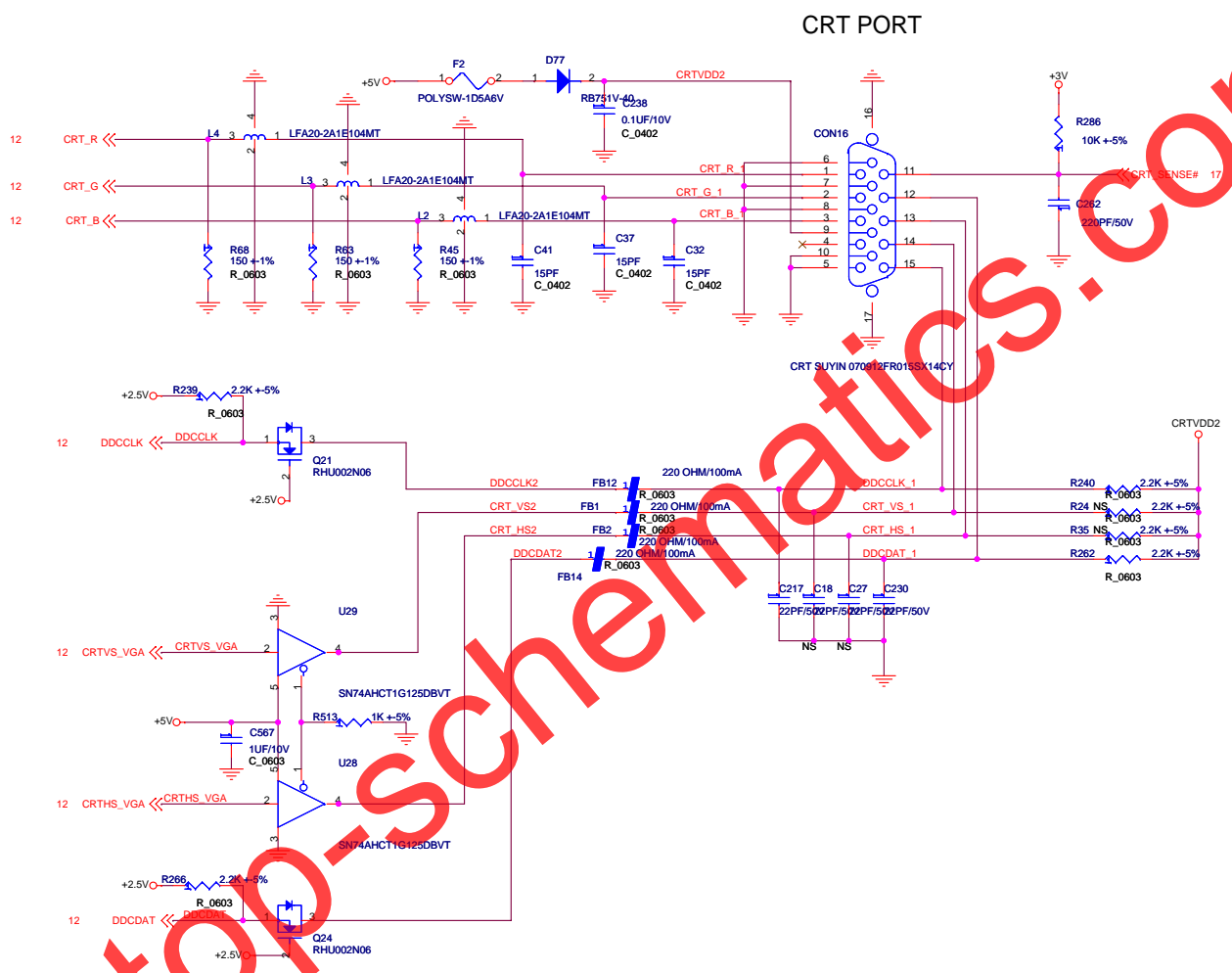
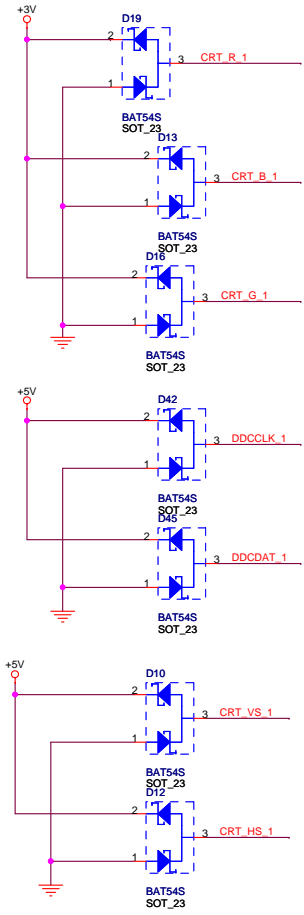
MINI PCIE CON FOR WLAN

MINI PCIE CON FOR 3G



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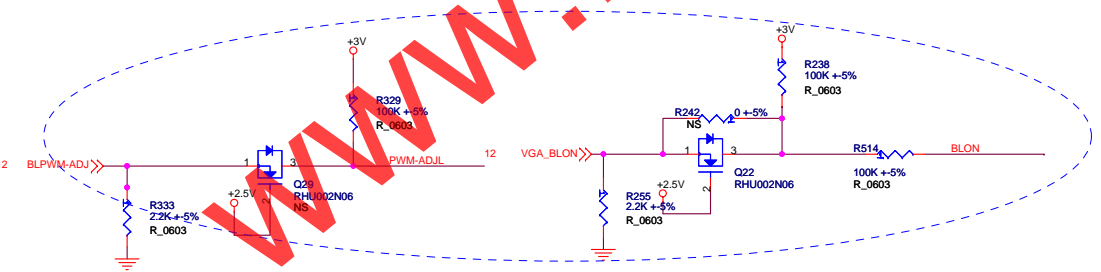
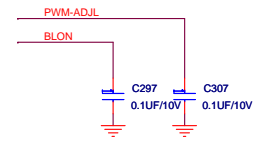
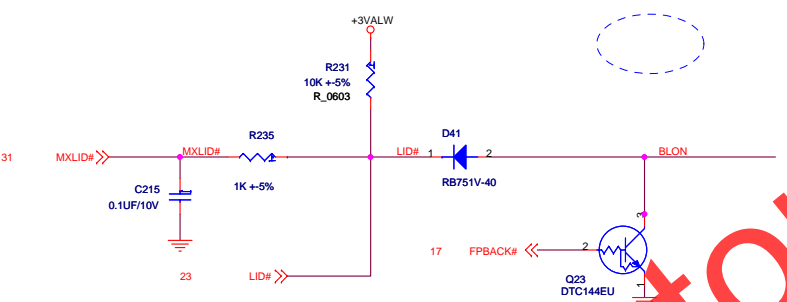
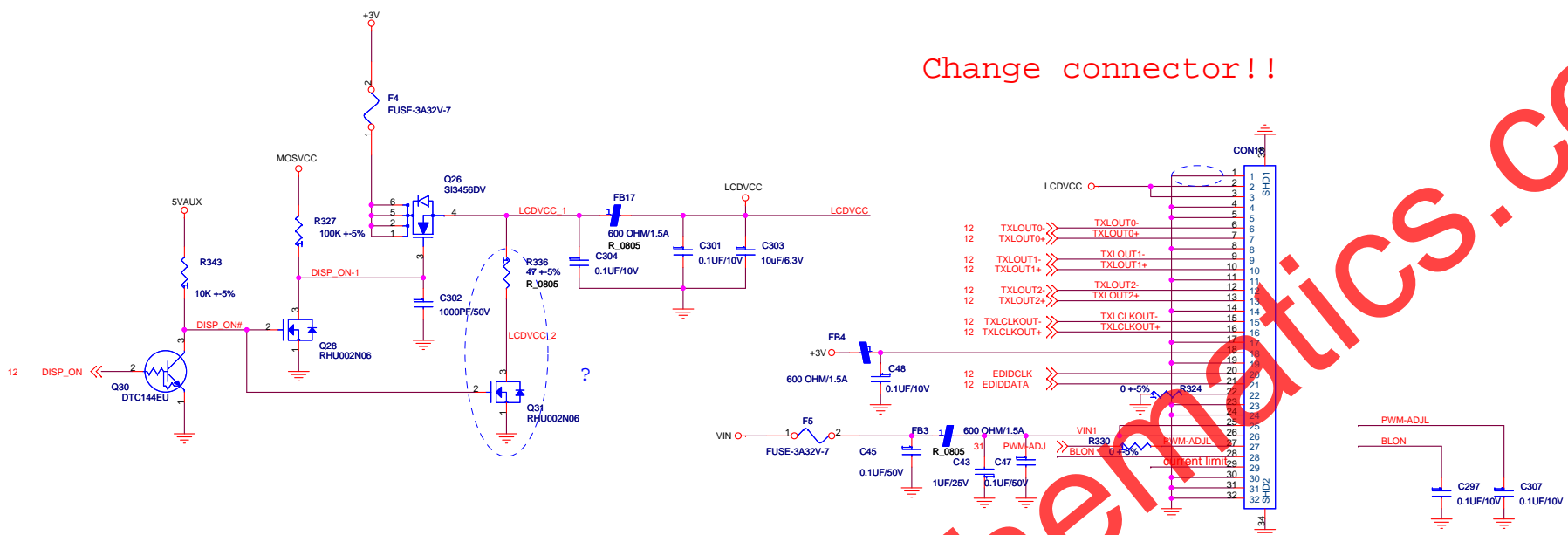




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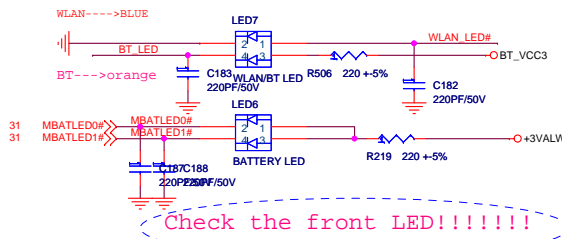
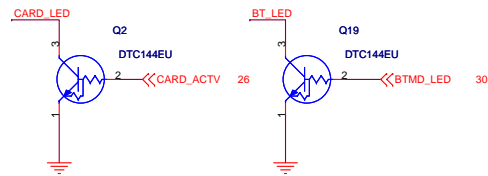


Change connector!!

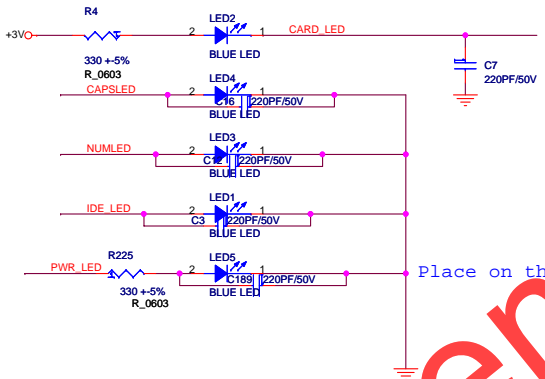
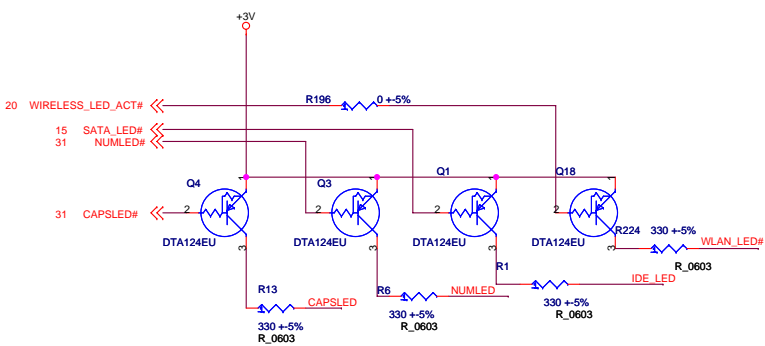


Tentative LVDS Voltage Translation Circuitry

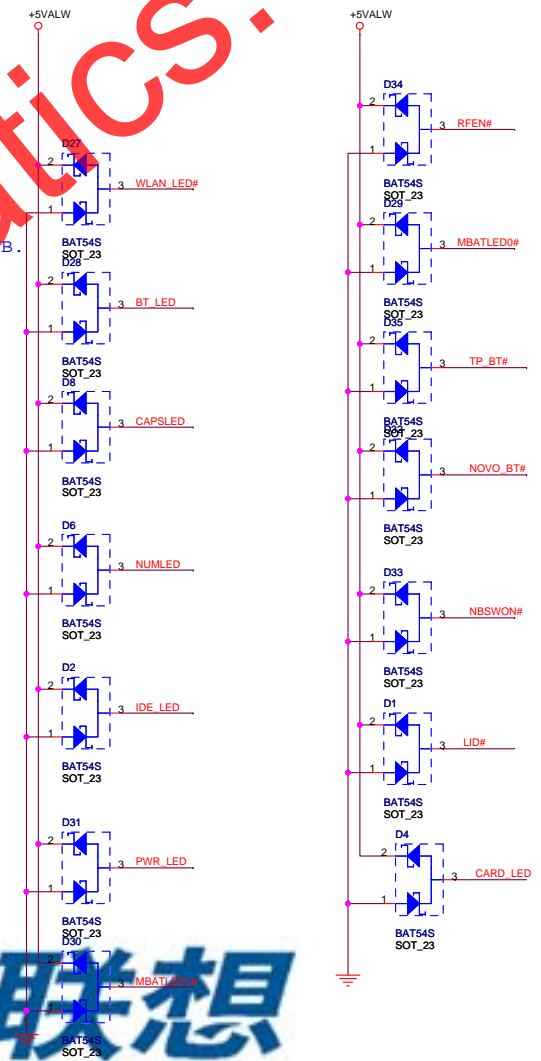
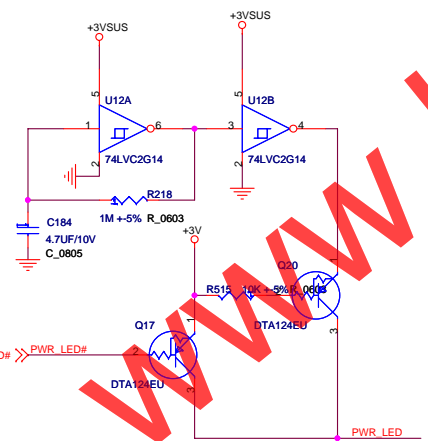
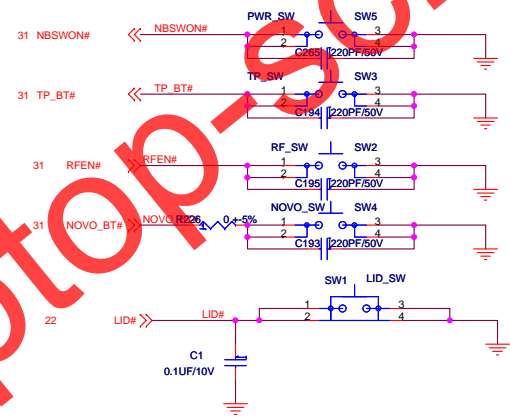




Check the front LED!!!!!!



Place on the front of NB.

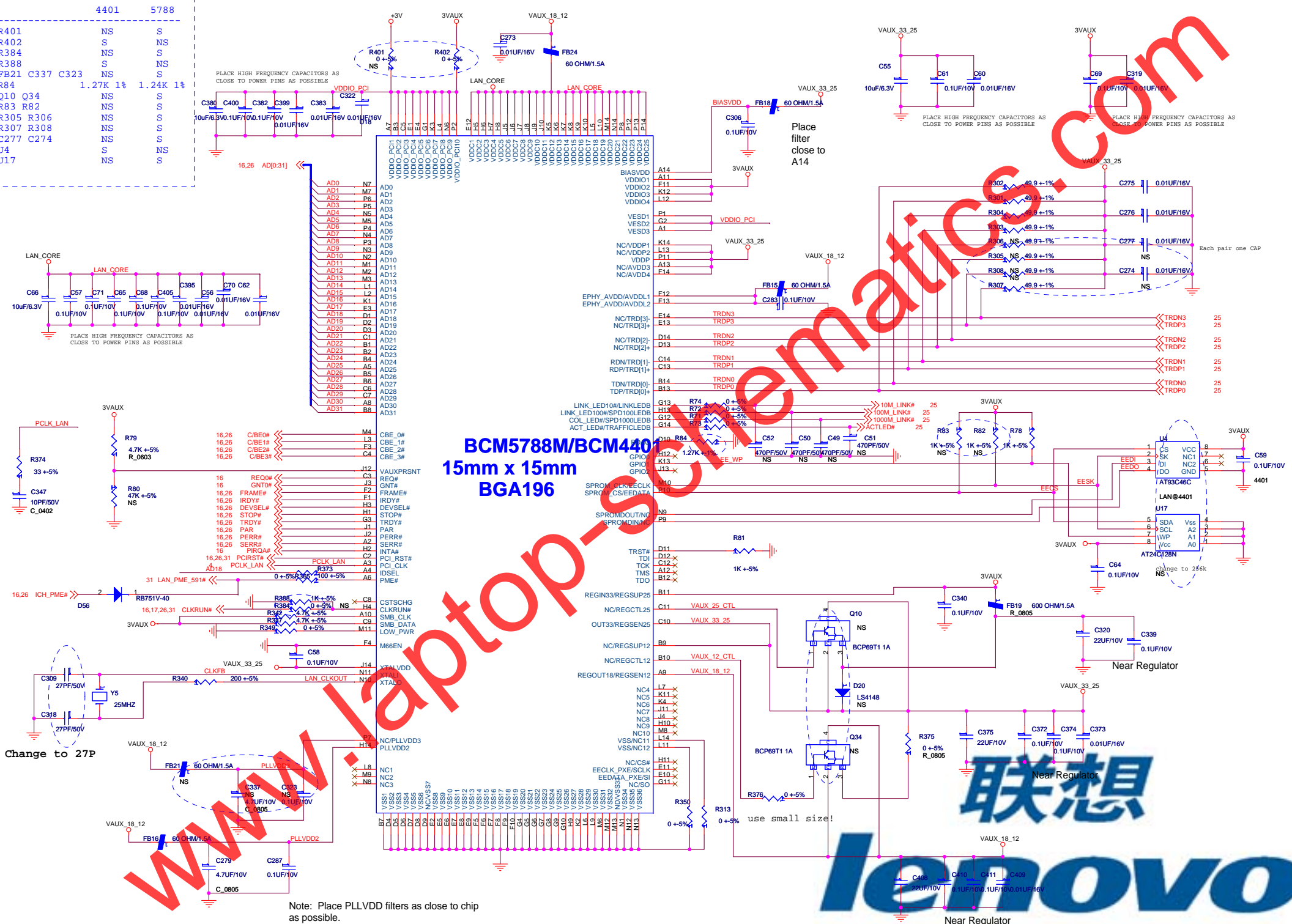


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Component Staffing Requirements

Part	4401	5788
R401	NS	S
R402	S	NS
R384	NS	S
R388	S	NS
FB21 C337 C323	NS	S
R84	1.27K 1%	1.24K 1%
Q10 Q34	NS	S
R83 R82	NS	S
R305 R306	NS	S
R307 R308	NS	S
C277 C274	NS	S
U4	S	NS
U17	NS	S



BCM5788M/BCM4401
15mm x 15mm
BGA196

Note: Place PLLVDD filters as close to chip as possible.



Each pair one CAP

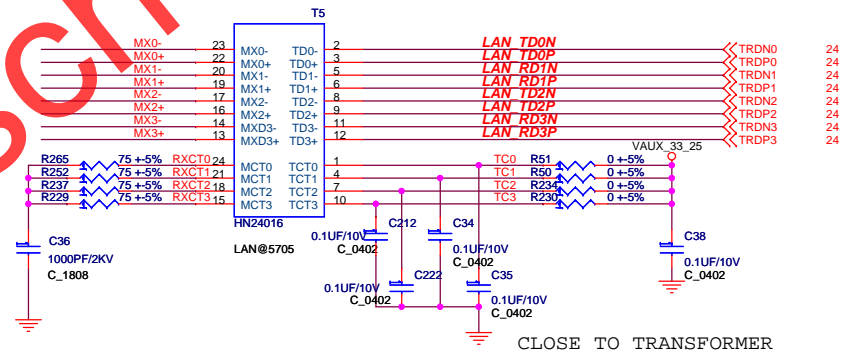
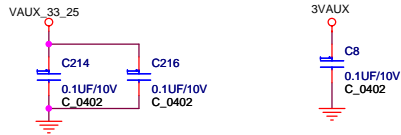
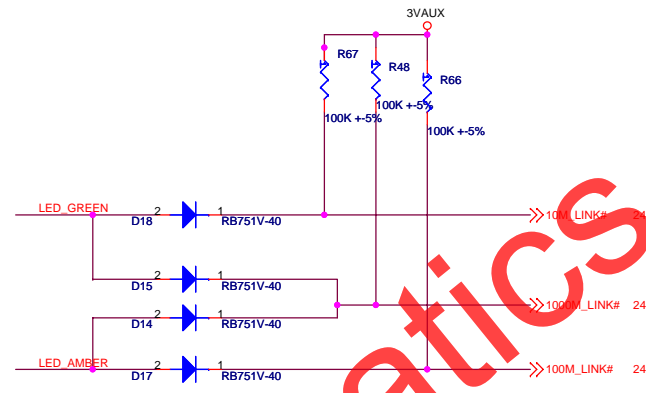
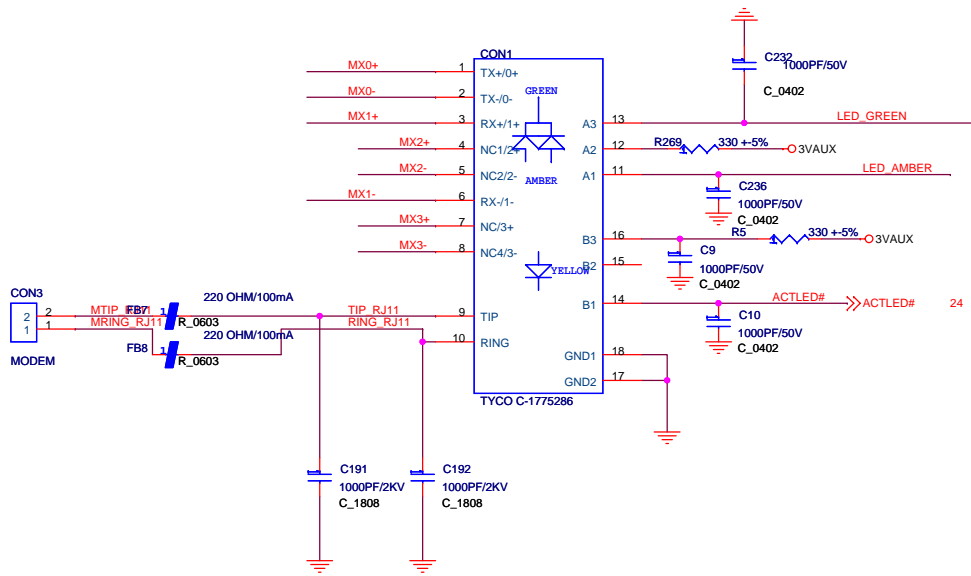
Place filter close to A14

use small size!

Near Regulator

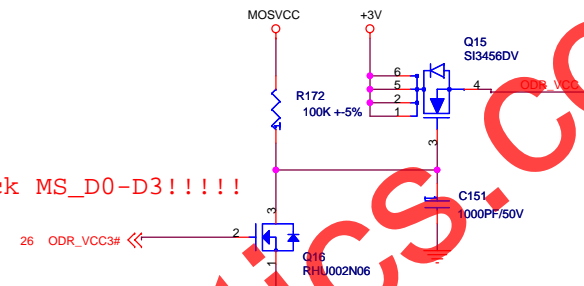
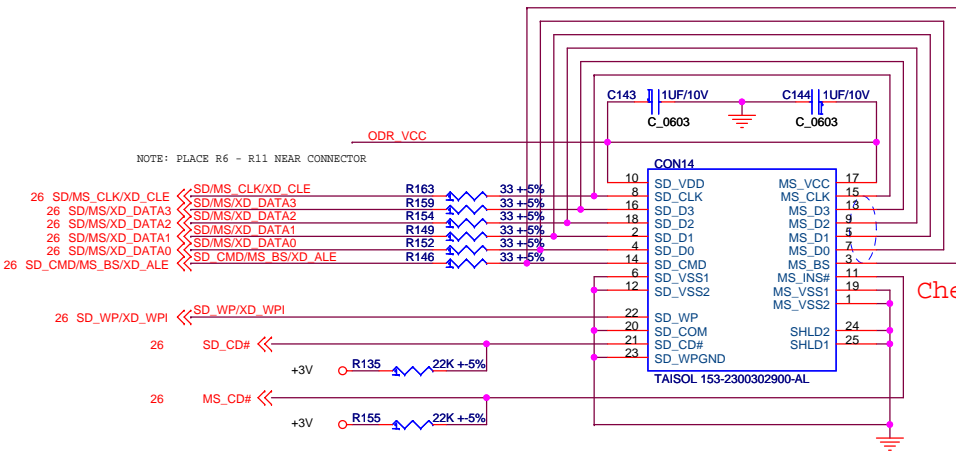
Near Regulator

Near Regulator

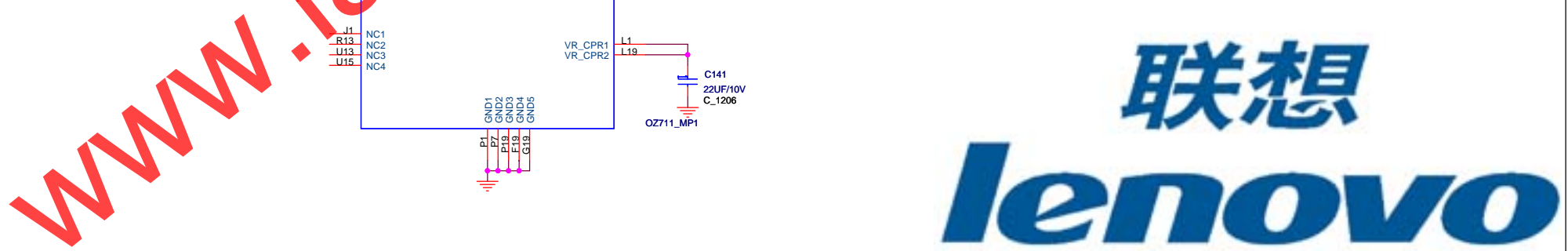
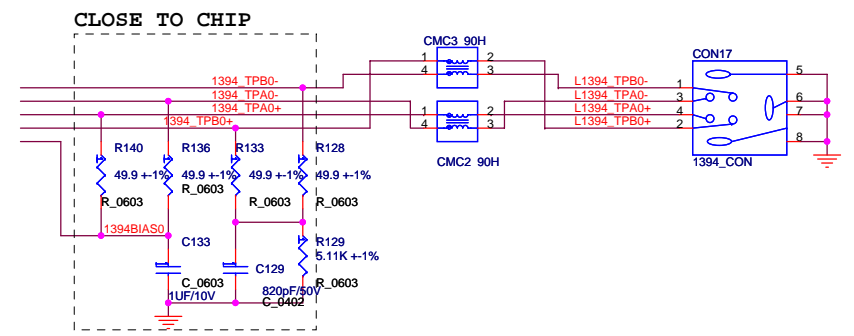
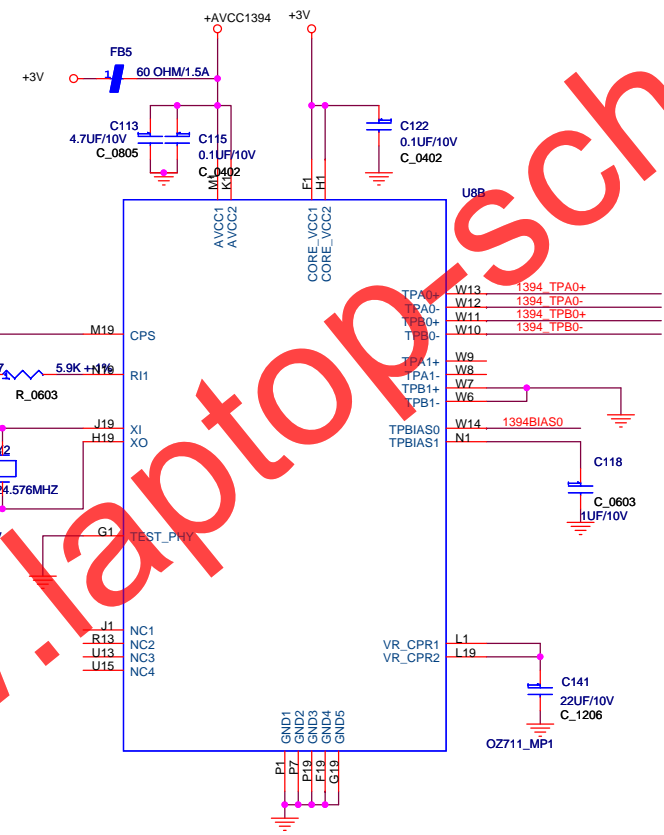


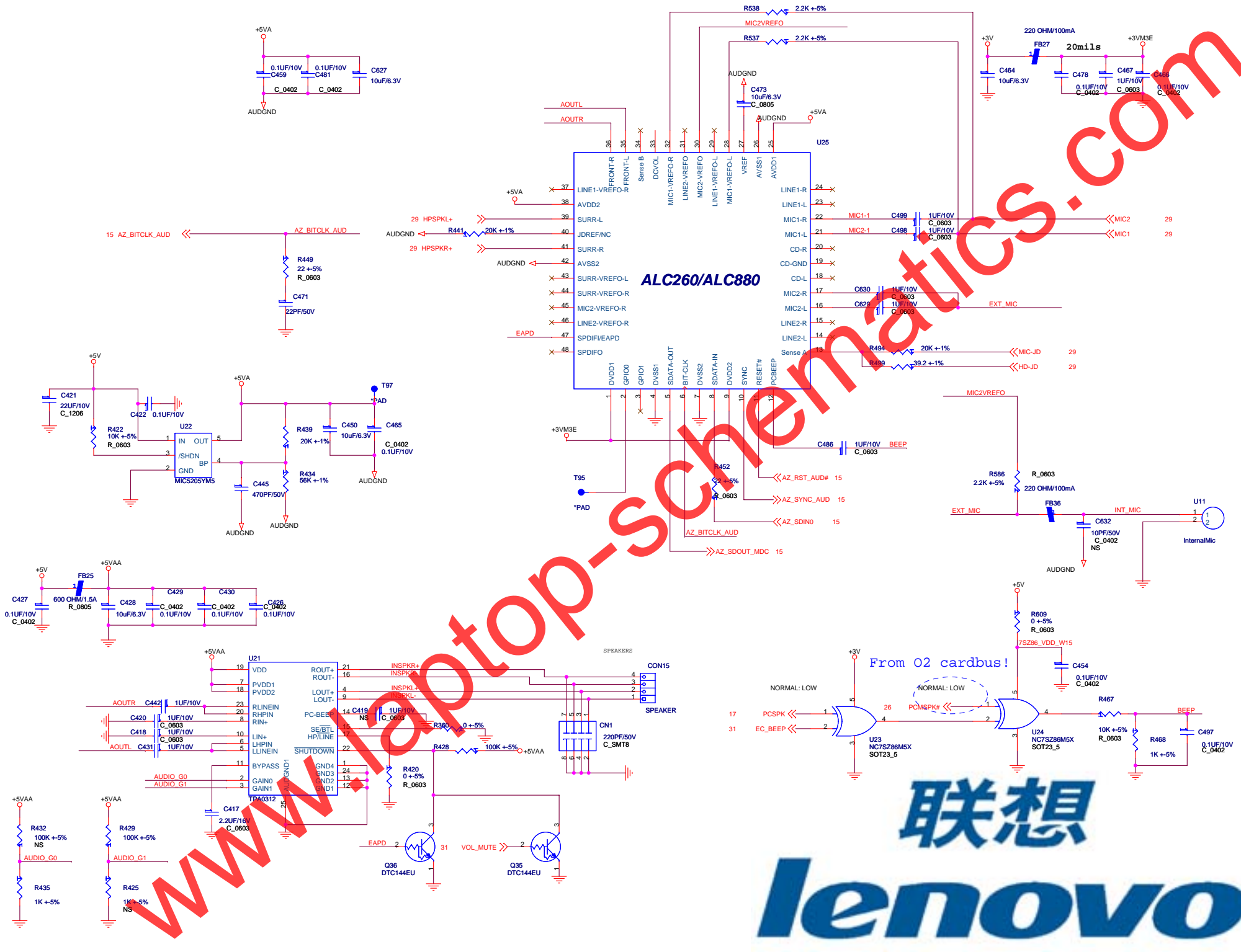
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Check MS_D0-D3!!!!





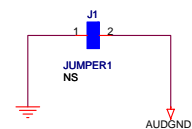
ALC260/ALC880

From O2 cardbus!

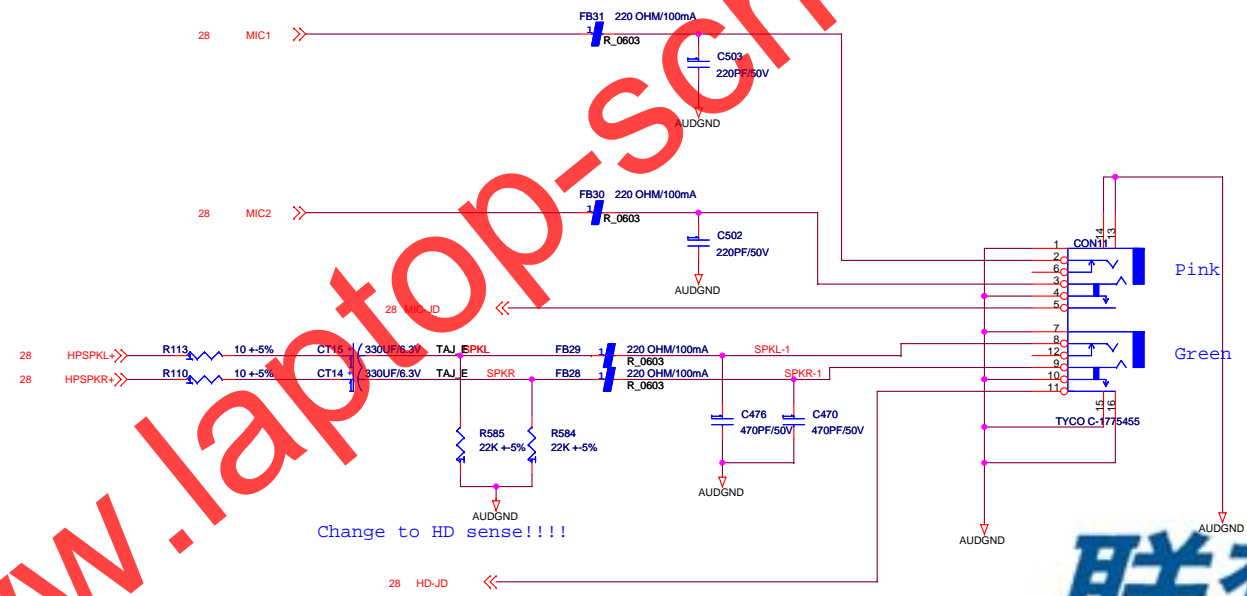


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J1 is only for Layout convenience. GND and AUDGND must be connected in inner layer.

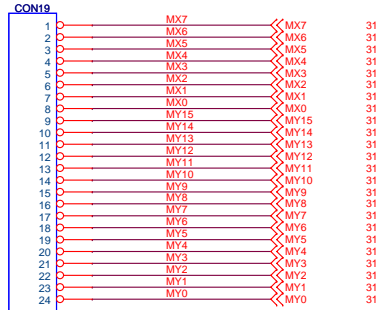


Change to HD sense!!!!

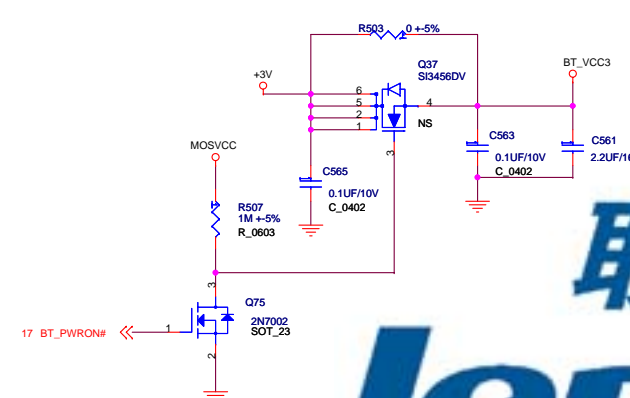
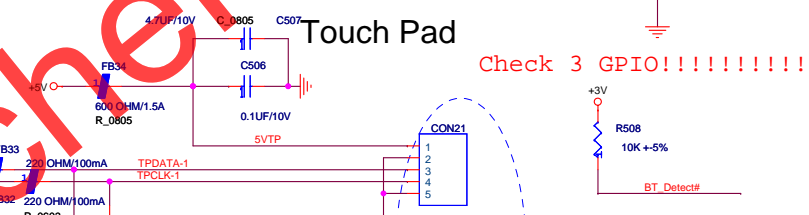
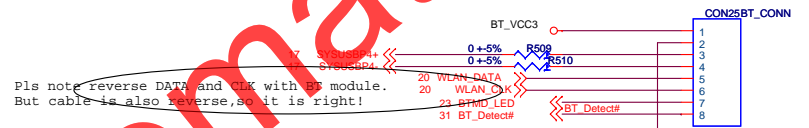
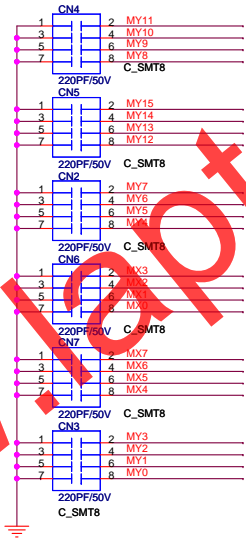
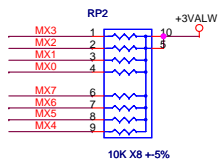
Consider to reserve PLUG signal in AC 97 platform



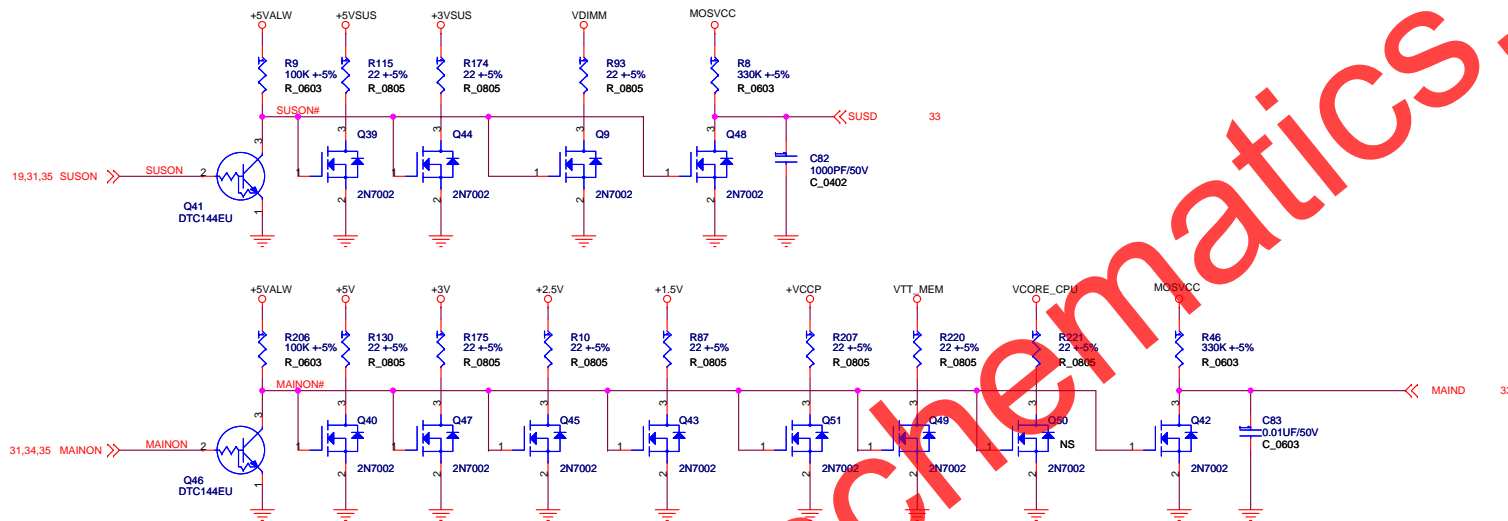
Check Keyboard matrix



KEYBOARD
NEED CHECK STICK POINTER PIN DEFINE

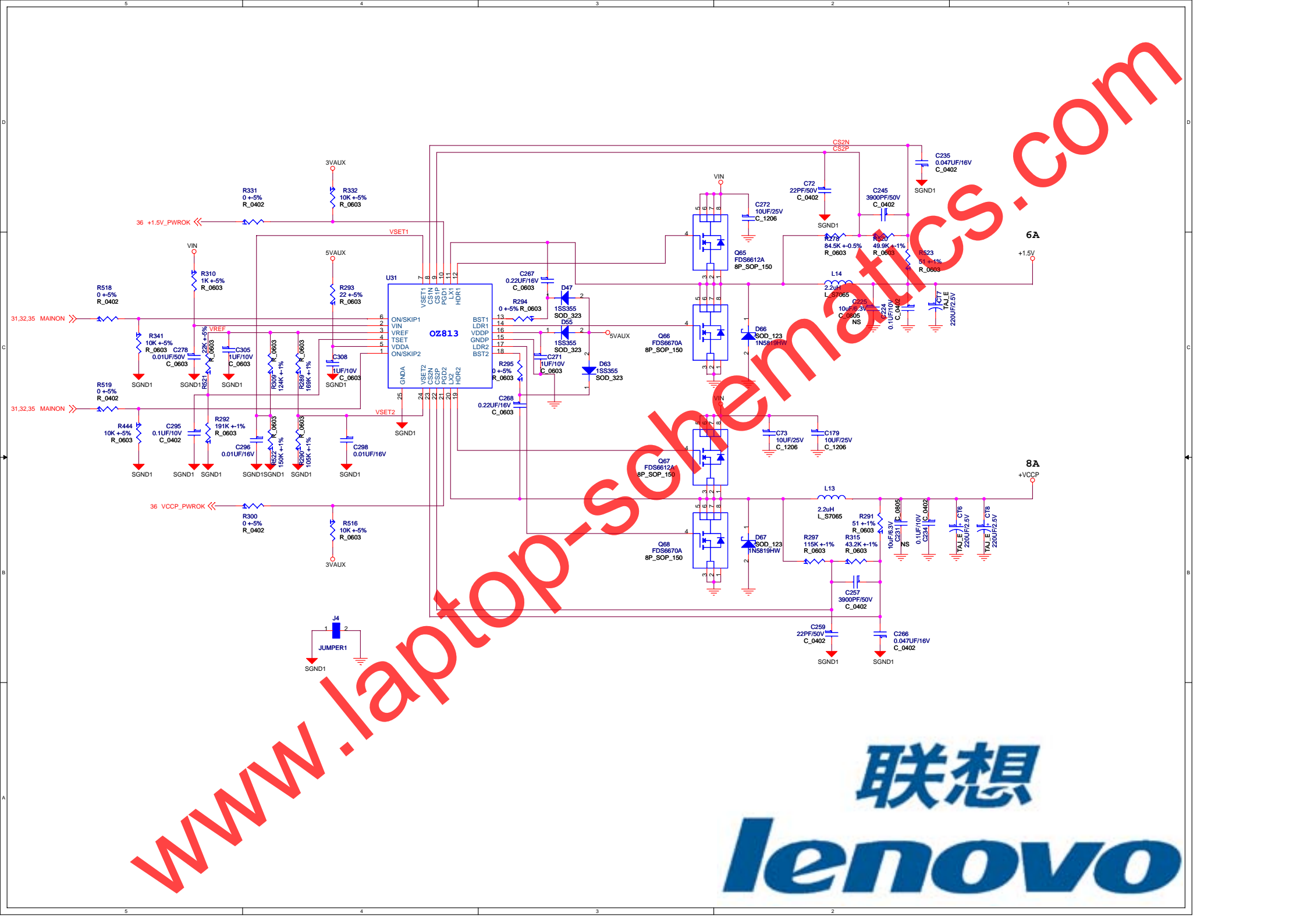


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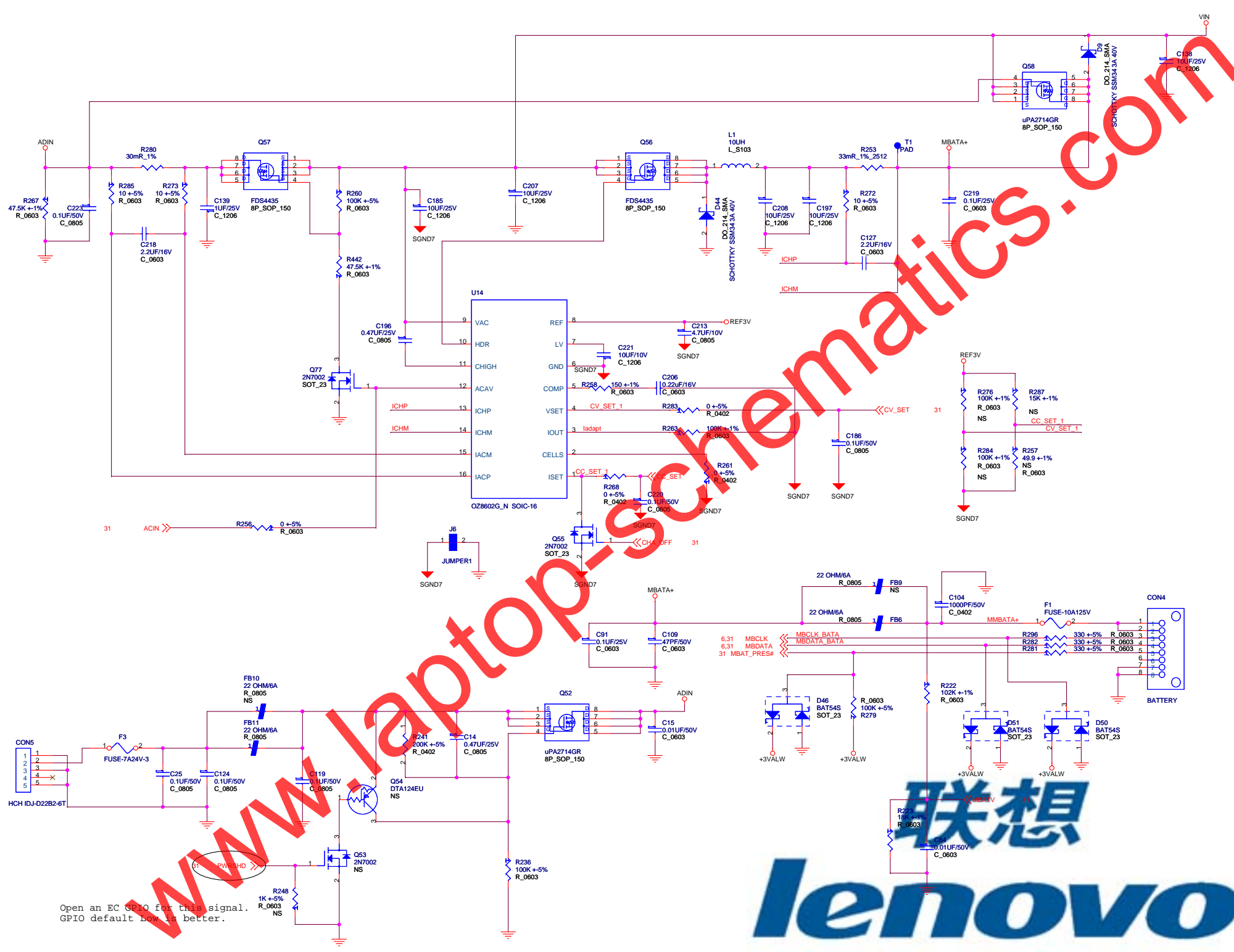
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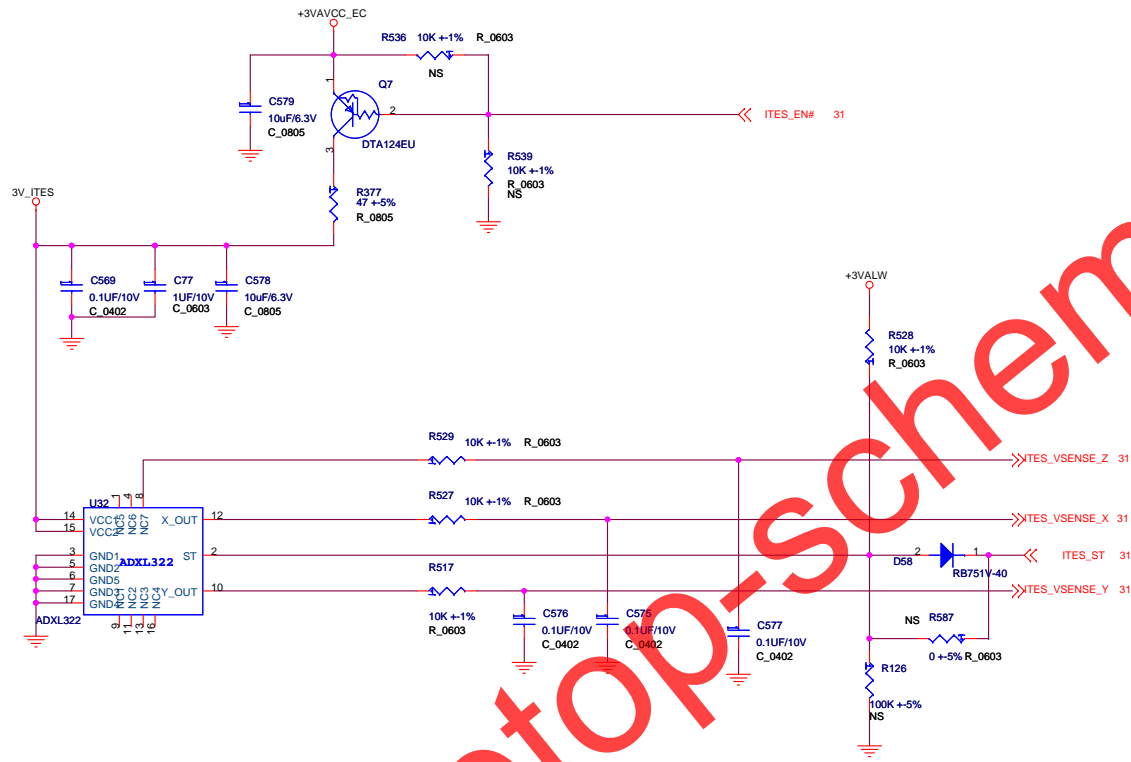




Open an EC GPIO for this signal.
GPIO default low is better.



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联想



联想

联想

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