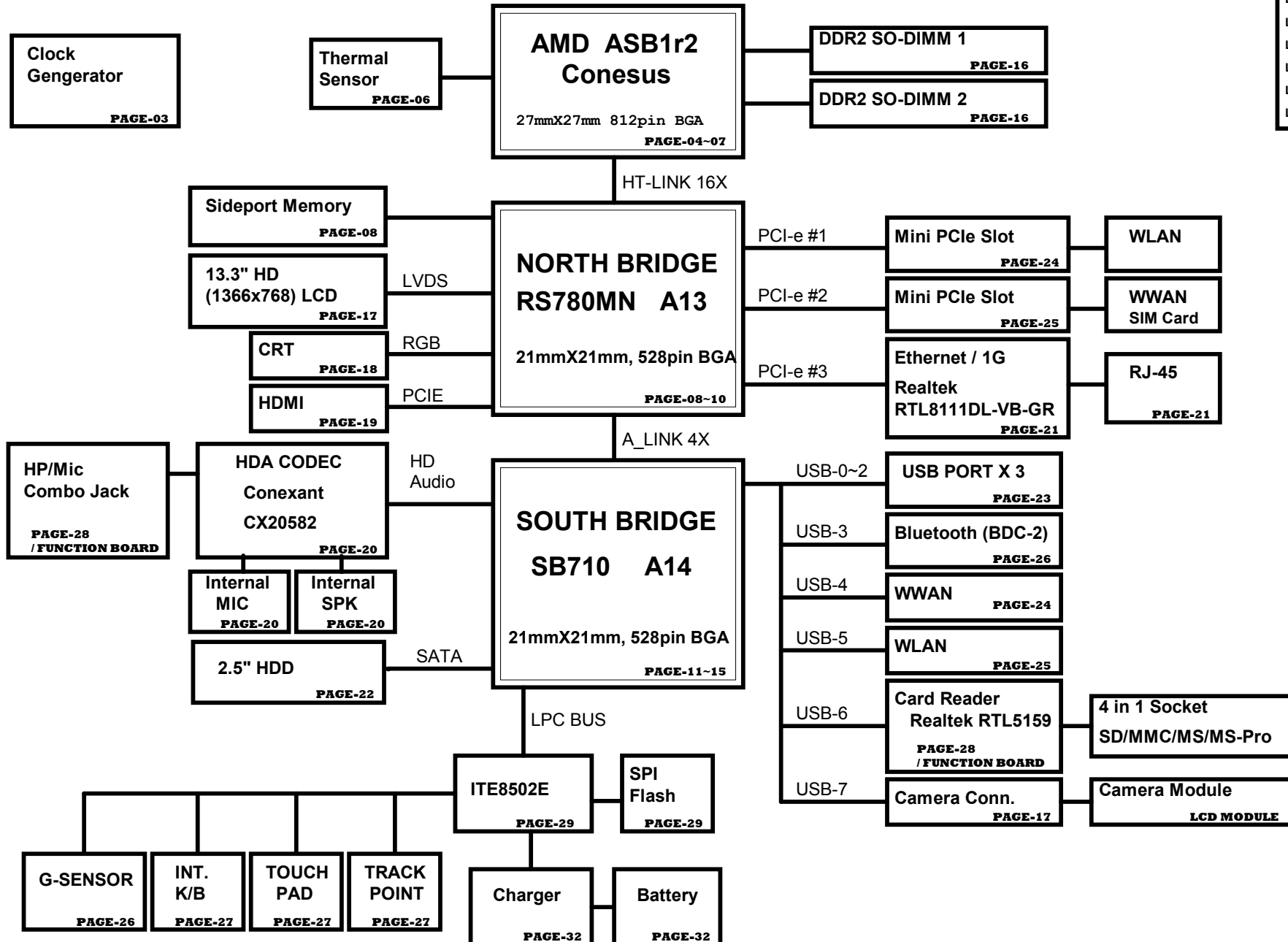
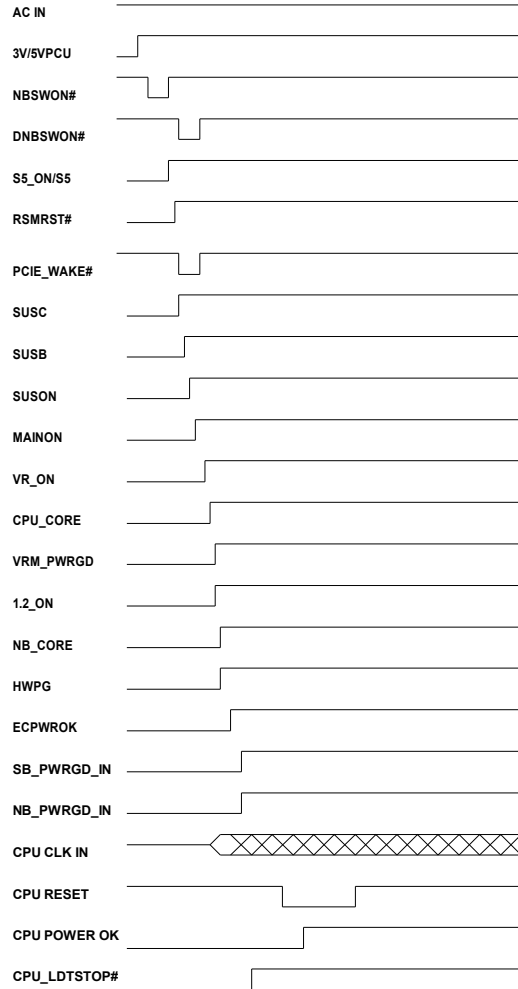


- LAYER 1 : TOP
- LAYER 2 : GND
- LAYER 3 : IN1
- LAYER 4 : VCC
- LAYER 5 : IN2
- LAYER 6 : IN3
- LAYER 7 : GND
- LAYER 8 : BOT



PAGE#	DESCRIPTION	NOTE
1	SCHEMATIC BLOCK DIAGRAM	
2	SYSTEM INFORMATION	
3	CLOCK GENERATOR_SLG8SP628	
4	K8G BGA HT I/F 1/4	
5	K8G BGA DDR2 MEMORY I/F 2/4	
6	K8G BGA CTRL & DEBUG 3/4	
7	K8G BGA PWR & GND 4/4	
8	RS780-HT LINK/PCIE/MEM I/F 1/3	
9	RS780-SYSTEM/STRAPS I/F 2/3	
10	RS780-POWER/GND 3/3	
11	SB710-PCIE/PCI/CPU/LPC 1/4	
12	SB710-ACP/GPIO/USB 2/4	
13	SB710-SATA/IDE/HWM/SPI 3/4	
14	SB710-PWR/DECOUPLING 4/4	
15	SB710-STRAPS & TERMINATOR	
16	SB710-STRAPS & PWRGD	
17	LCD/CAMERA	
18	CRT CONN	
19	HDMI CONN	
20	AUDIO (CX20582)	
21	LAN (RTL8111DL-VB-GR)	
22	SATA & FAN CONTROL	
23	USB	
24	WLAN	
25	WWAN	
26	BT, G-SENSOR	
27	KB, Touch Pad, Track Point	
28	Audio & Function CONN	
29	KBC IT8502E	
30	Screw Hole / EMI	
31	Discharge	
32	Charger	
33	1.8VSUS, +SMDDR_VTERM, +1.5V	
34	+1.2V, +1.1V	
35	3VPCU, 5VPCU	
36	CPU_CORE, +2.5V	
37	+1.8V, +1.2V_S5	
38	+NB_CORE	
39	POWER BLOCK DIAGRAM	
40	HISTORY (Pre A)	
41	HISTORY (A -> C)	
42	HISTORY (C -> C2)	
43	HISTORY (C2 -> MP)	

Power Sequence



RS780 SM BUS

RS780 I2C (S0)	I2C and AUX Function Define
DAC_SCL	CRT (+5V)
DAC_SDA	
I2C_CLK	LVDS (+3V)
I2C_DATA	
DDC_CLK0/AUX0N	HDMI (+5V)
DDC_DATA1/AUX0P	
DDC_CLK1/AUX1N	not used
DDC_DATA1/AUX1P	

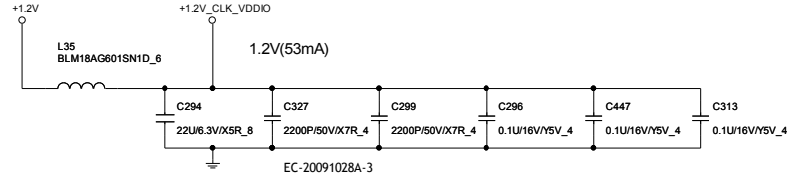
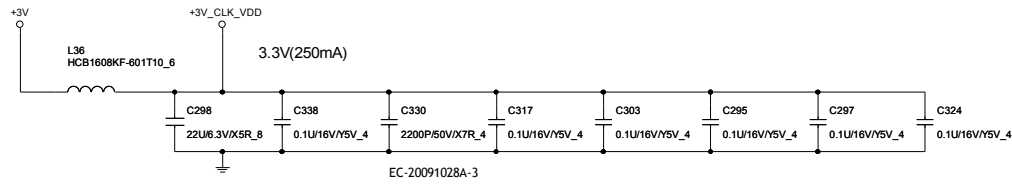
SB710 SM BUS

SB710 SMBUS	SMBUS Function Define
SMBCLK0 SMBDAT0 (+3V)	DDR / DDR THER / CLOCK GEN
SMBCLK1 SMBDAT1 (+3V_S5)	
SMBCLK2 SMBDAT2 (+3V_S5)	not used

KBC(EC) SM BUS

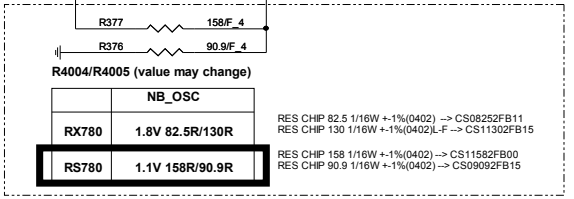
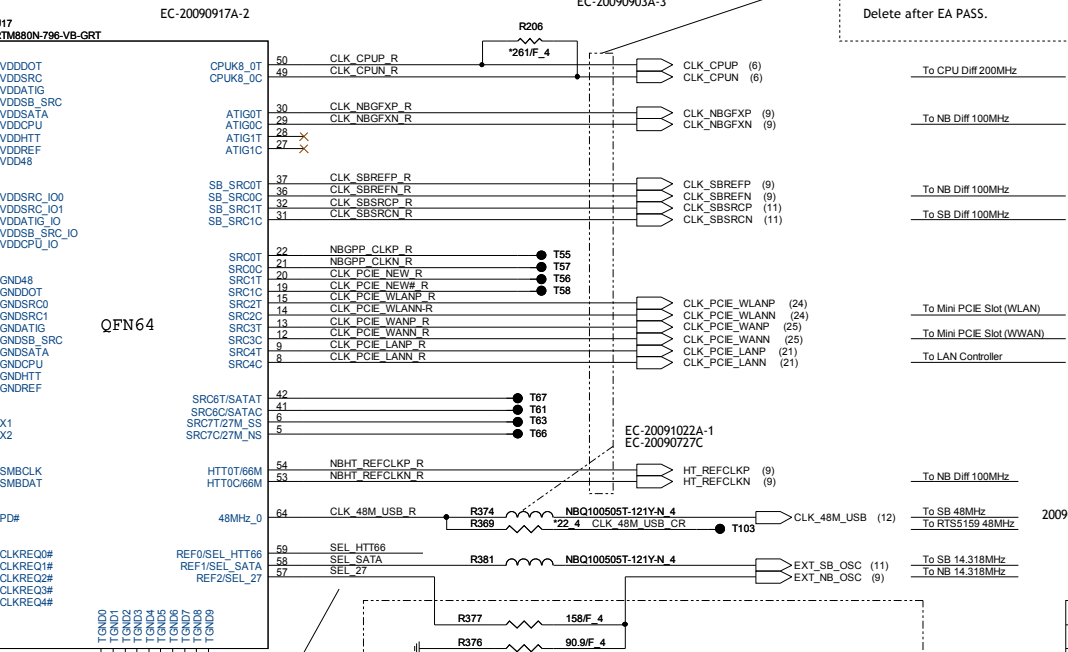
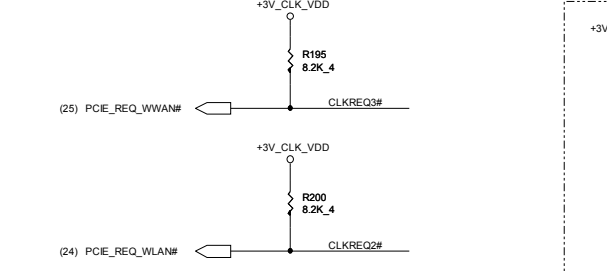
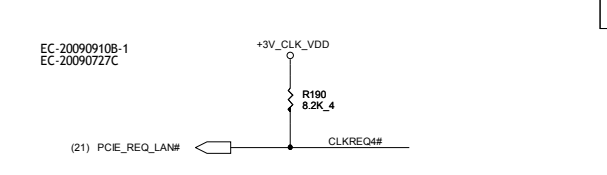
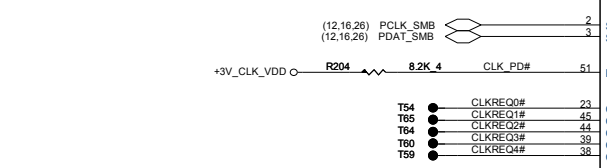
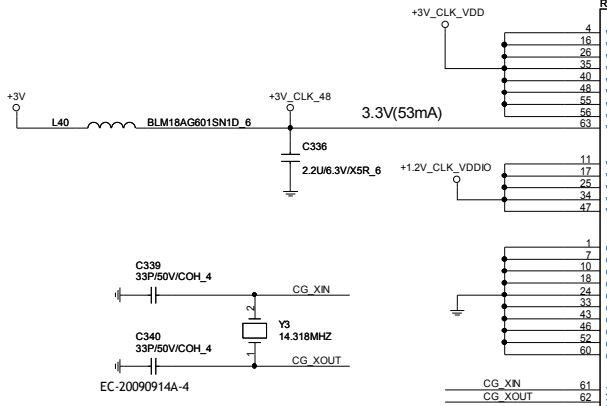
KBC SMBUS (+3VPCU)	SMBUS Function Define
MBCLK MBDAT	BATTERY (+3VPCU)
2ND_MBCLK 2ND_MBDATA	CPU THER SENSOR(+3V) EC EEPROM (+3VPCU)
3ND_MBCLK 3ND_MBDATA	G-SENSOR(+3VS5)

CLK_GEN



- ICS9LPRS480 P/N : ALPRS480000
- SLG8SP628 P/N : AL8SP628000
- RTM880N-796-VB-GRT P/N : AL000880001

Clock chip has internal serial terminations for differential pairs, external resistors are reserved for debug purpose.
Place within 0.5" of CLKGEN
Differential clock damping resistors
Delete after EA PASS.

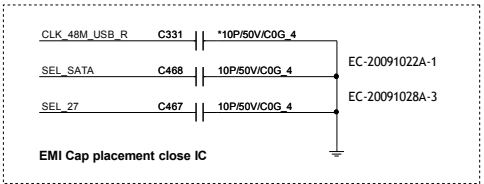


20090720A-T103 / Cancel for Card Reader frequency.

NB CLOCK INPUT TABLE

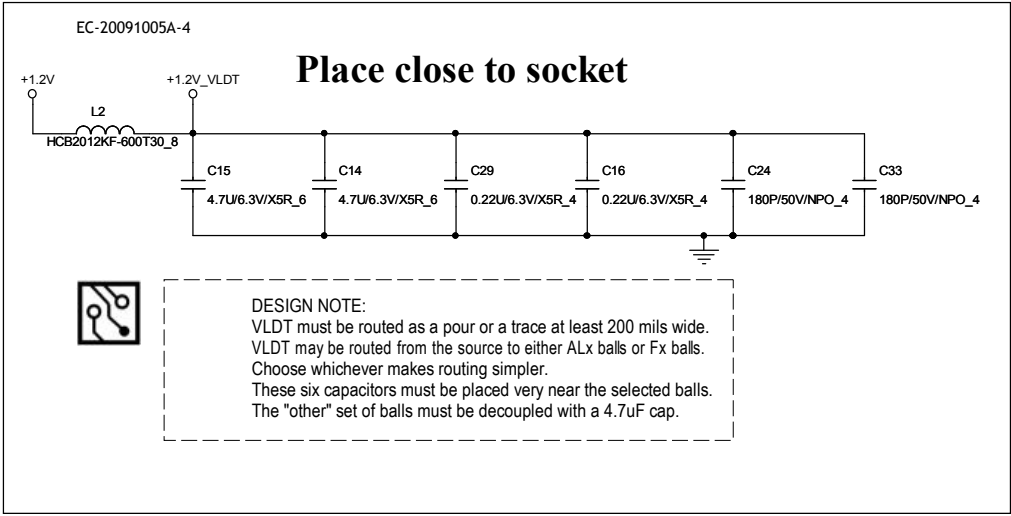
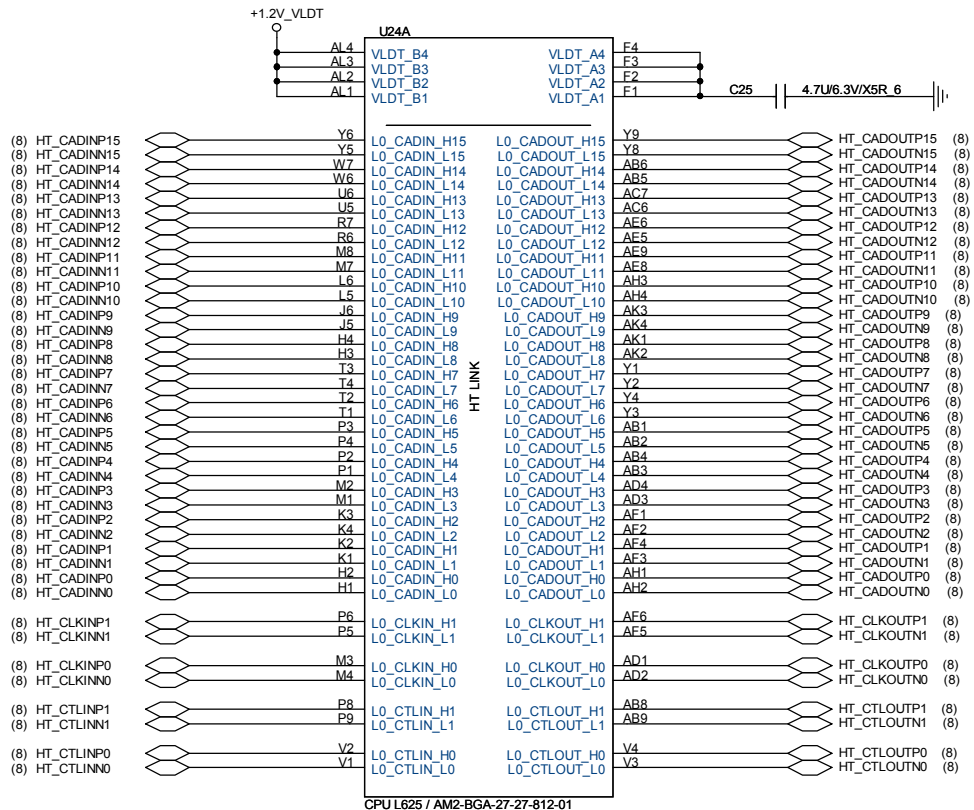
NB CLOCKS	RX780	RS780
HT_REFCLKP	100M DIFF	100M DFF
HT_REFCLKN	100M DFF	100M DFF
REFCLK_P	14M SE (1.8V)	14M SE (1.1V)
REFCLK_N	NC	vref
GFX_REFCLK	100M DIFF	100M DFF(IN/OUT)
GPP_REFCLK	100M DIFF	NC or 100M DFF OUTPUT
GPPSB_REFCLK	100M DIFF	100M DFF

SEL_HTT66	1	66 MHz 3.3V single ended HTT clock
SEL_HTT66	0*	100 MHz differential HTT clock
SEL_SATA	1	100 MHz non-spreading differential SRC clock
SEL_SATA	0 *	100 MHz spreading differential SRC clock
SEL_27	1	27MHz and 27M SS outputs
SEL_27	0*	100 MHz SRC clock

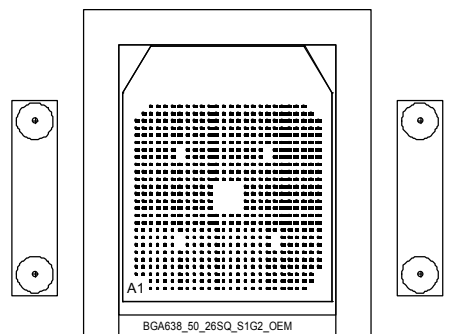


Quanta Computer Inc.
PS-Note (AMD)

Size	Document Number	Rev.
Custom	CLOCK GEN SLG8SP628	< MP >
Date:	10/29/2009, 04:46 PM	Sheet: 3 of 43



CPU

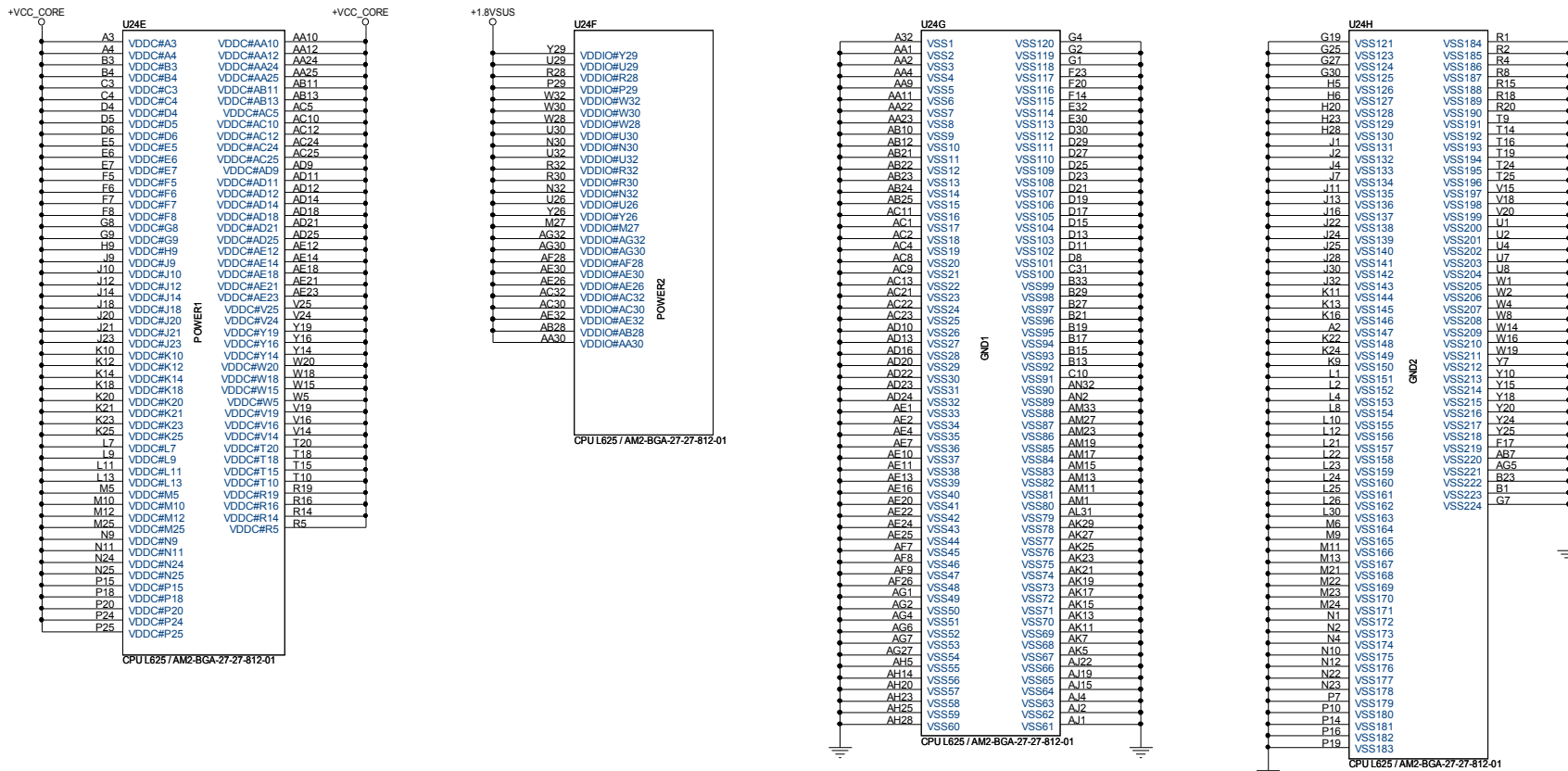


1.6GHz, 18W – Turion™ Neo X2 L625
 1.5GHz, 18W – Athlon Neo X2 L325
 1.6GHz, 15W – Athlon Neo MV-40

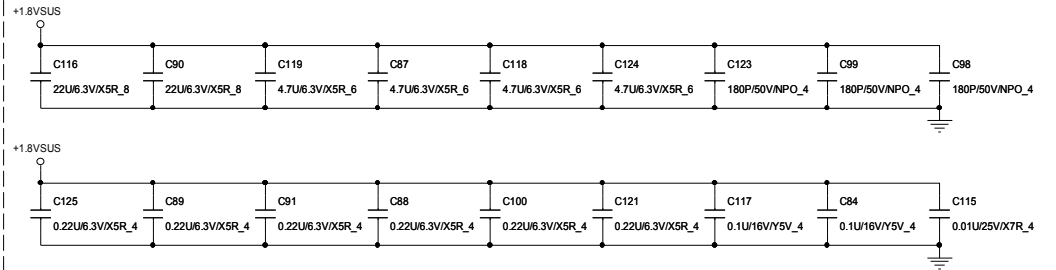
Quanta Computer Inc.
 PS-Note (AMD)

Size B	Document Number K8G BGA HT I/F 1/4	Rev. < MP >
Date:	10/29/2008, 04:48 PM	Sheet : 4 of 43

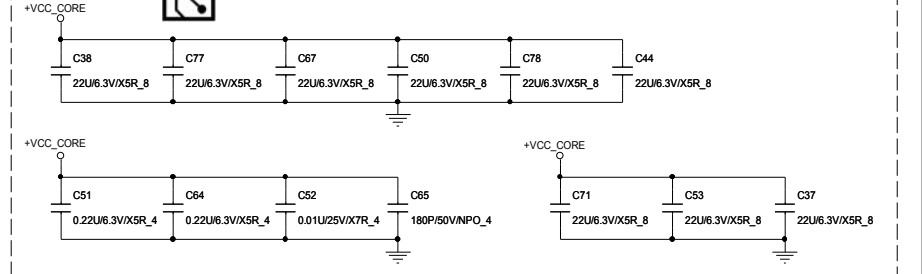
CPU PROCESSOR POWER AND GROUND



DECOUPLING BETWEEN PROCESSOR AND DIMMs PLACE CLOSE TO PROCESSOR AS POSSIBLE

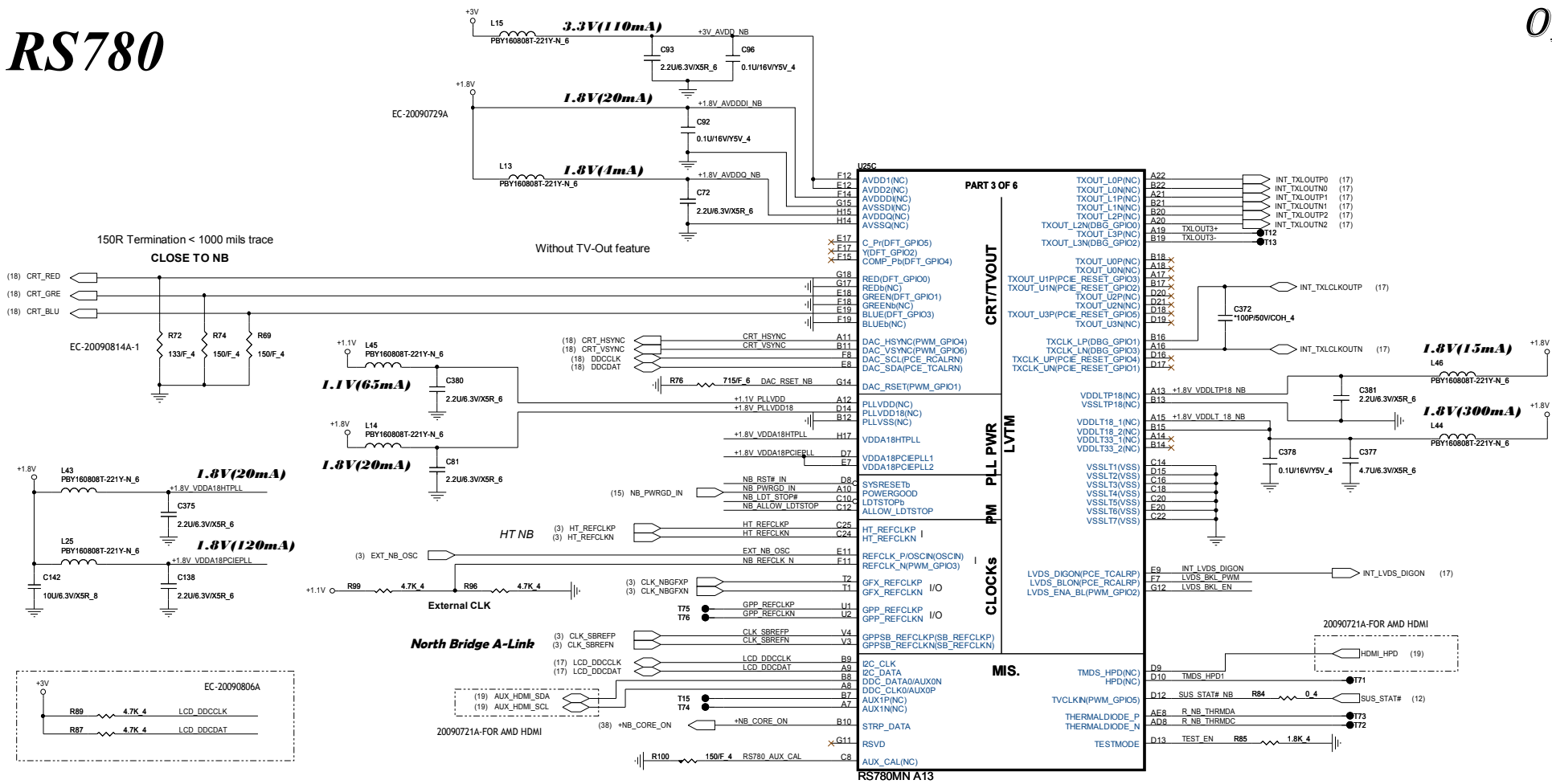


BOTTOM SIDE DECOUPLING

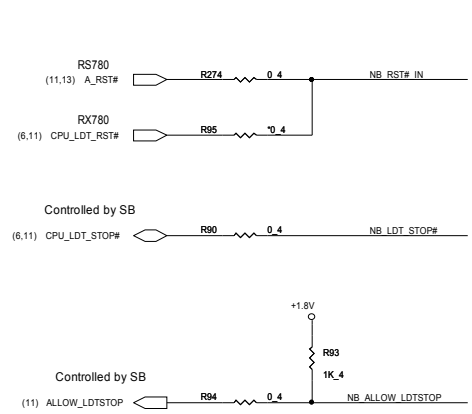


Quanta Computer Inc.
PS-Note (AMD)

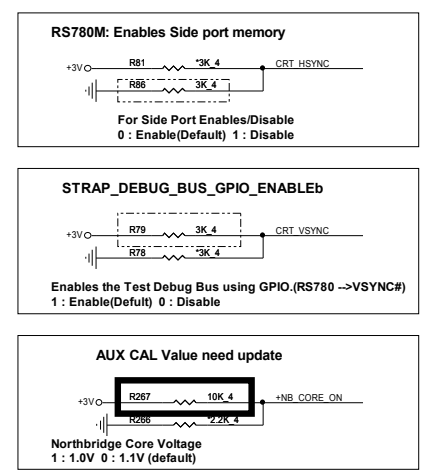
Size Custom	Document Number K8G BGA PWR & GND 4/4	Rev. <MP >
Date: 10/28/2009, 03:15 PM	Sheet: 7	of 43



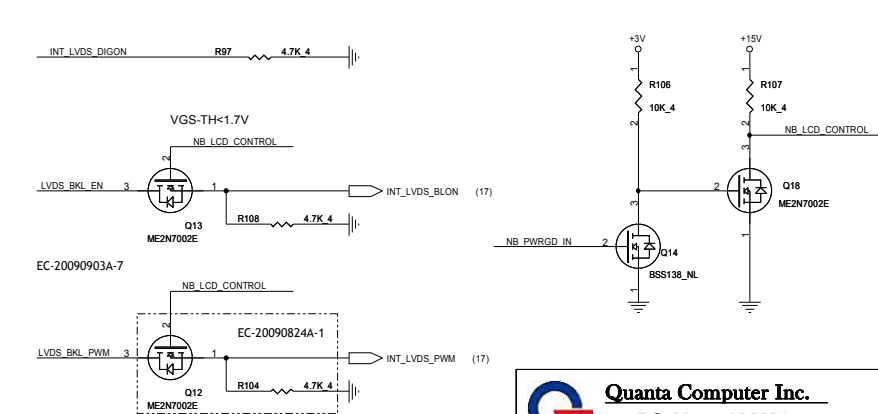
North Bridge RESET



STRAP DEBUG BUS GPIO

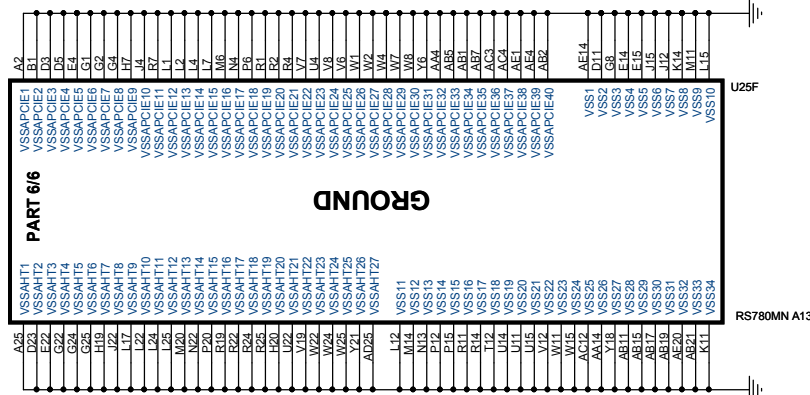


LVDS BLON



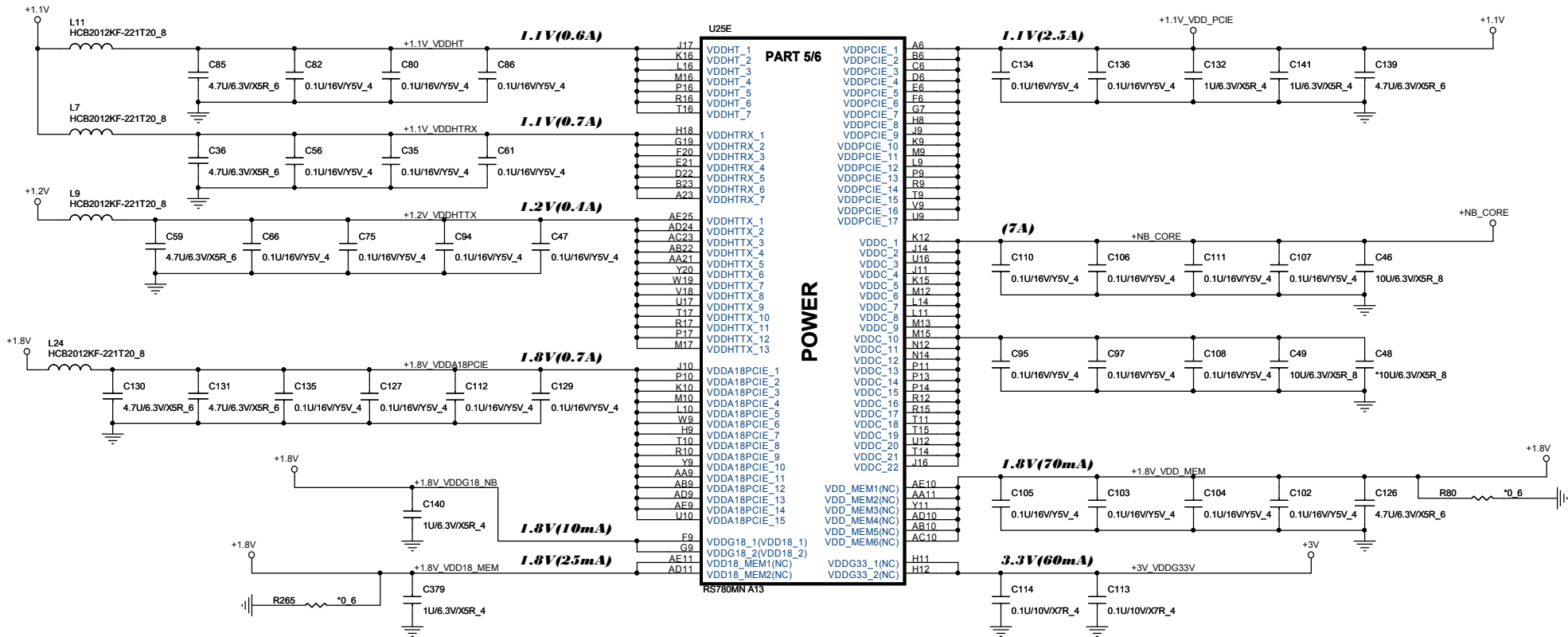
Quanta Computer Inc.
PS-Note (AMD)


Size	Document Number	Rev.
Custom	RS780-SYSTEM/STRAPS I/F 2/3	< MP >
Date:	10/29/2009, 04:48 PM	Sheet: 9 of 43



RS740/RX780/RS780 POWER DIFFERENCE TABLE

PIN NAME	RS740	RX780	RS780	PIN NAME	RS740	RX780	RS780
VDDHT	NC	+1.1V	+1.1V	IOPLLVD	+1.2V	NC	+1.1V
VDDHTRX	NC	+1.1V	+1.1V	AVDD	+3.3V	NC	+3.3V
VDDHTTX	+1.2V	+1.2V	+1.2V	AVDDDI	+1.8V	NC	+1.8V
VDDA18PCIE	NC	+1.8V	+1.8V	AVDDQ	+1.8V	NC	+1.8V
VDDG18	+1.8V	+1.8V	+1.8V	PLLVD	+1.2V	NC	+1.1V
VDD18_MEM	NC	NC	+1.8V	PLLVD18	+1.8V	NC	+1.8V
VDDPCIE	+1.2V	+1.1V	+1.1V	VDDA18PCIEPLL	+1.2V	+1.8V	+1.8V
VDDC	+1.2V	+1.1V	+1.1V	VDDA18HTPLL	+1.8V	+1.8V	+1.8V
VDD_MEM	+1.8V/1.5V	NC	+1.8V/1.5V	VDDLTP18	+1.8V	NC	+1.8V
VDDG33	+3.3V	NC	+3.3V	VDDL18	+1.8V	NC	+1.8V
IOPLLVD18	+1.8V	NC	+1.8V	VDDL23	+3.3V	NC	NC



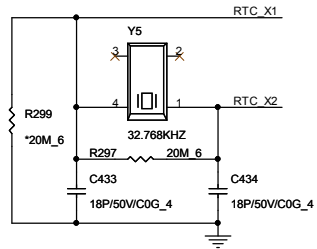


Quanta Computer Inc.
PS-Note (AMD)

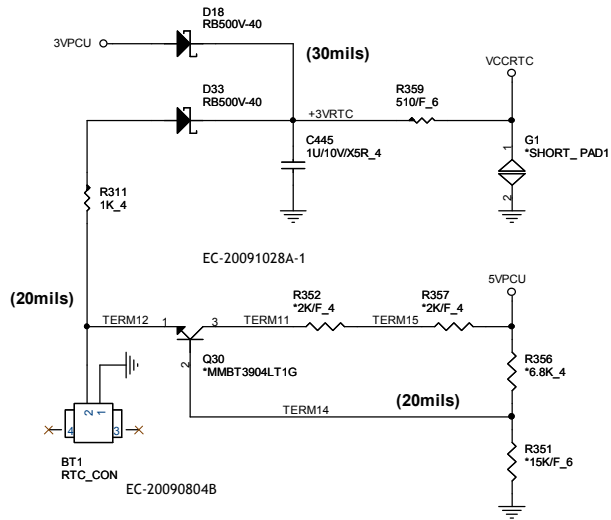
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SB710

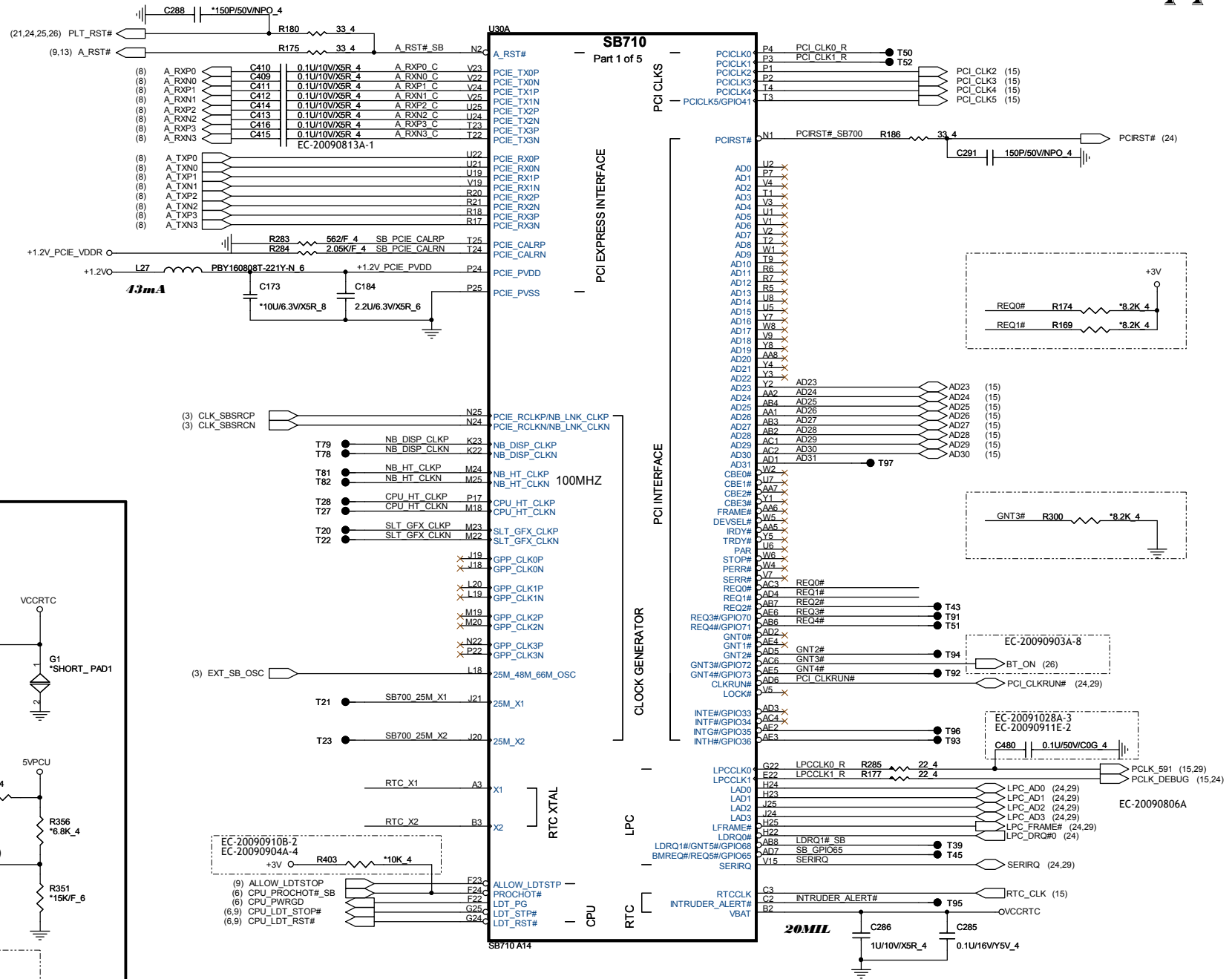
RTC XTAL



RTC

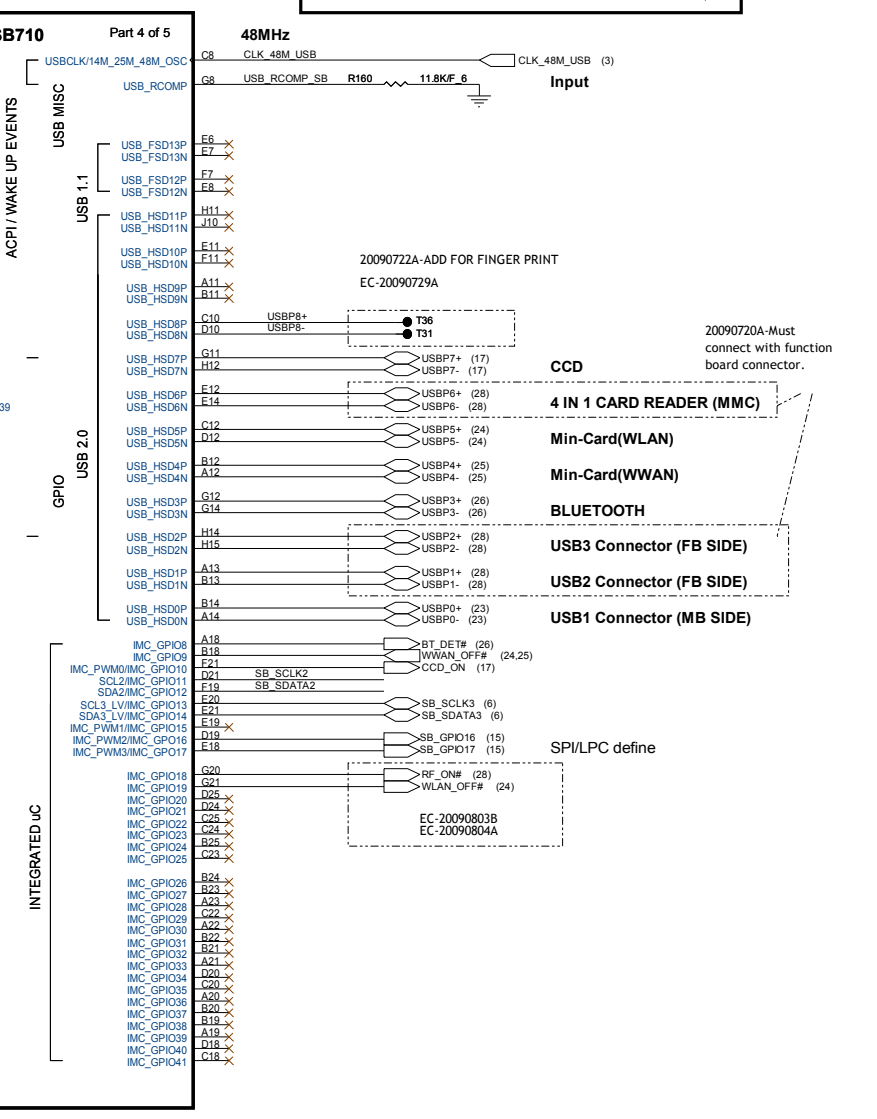
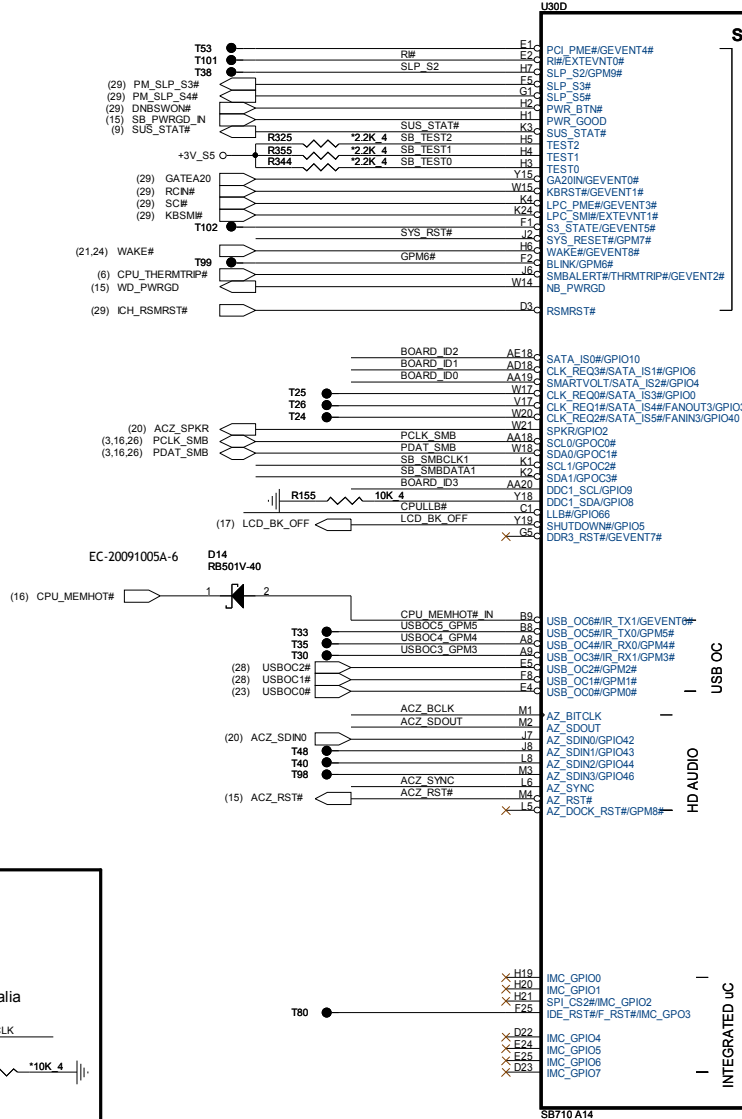
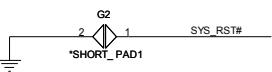
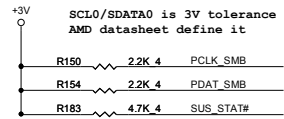
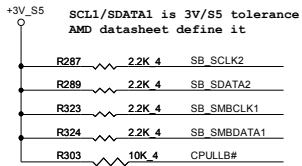
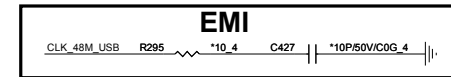


RTC P/N: AHL0300209 (CR2025)
Don't support chargeable Battery

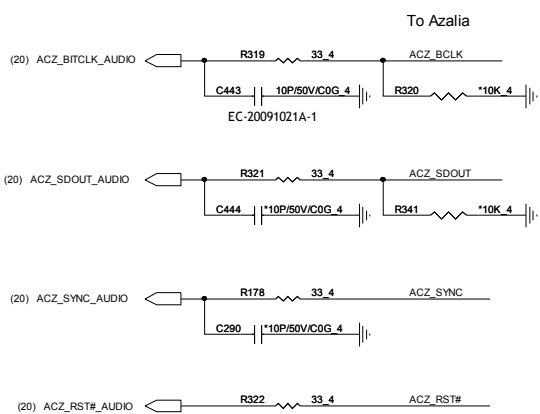


Quanta Computer Inc.
PS-Note (AMD)

Size Custom	Document Number SB710-PCIE/PCI/CPU/LPC 1/4	Rev. < MP >
Date: 10/29/2009, 04:48 PM	Sheet: 11	of 43



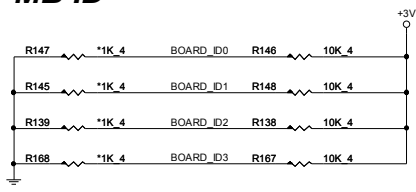
HD Audio Interface



MB ID Selection Table

BOARD_ID	BOARD_ID0	BOARD_ID1	BOARD_ID2	BOARD_ID3
Turion Neo X2 L625	H	H		
Athlon Neo MV-40	L	H		
Athlon Neo X2 L325	L	L		
W/ SIDE-PORT MEMORY			H	H
W/O SIDE-PORT MEMORY			L	H

MB ID

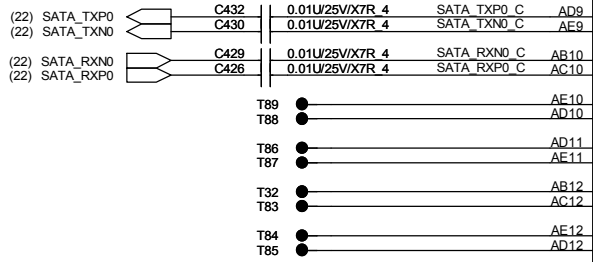


Quanta Computer Inc.
PS-Note (AMD)

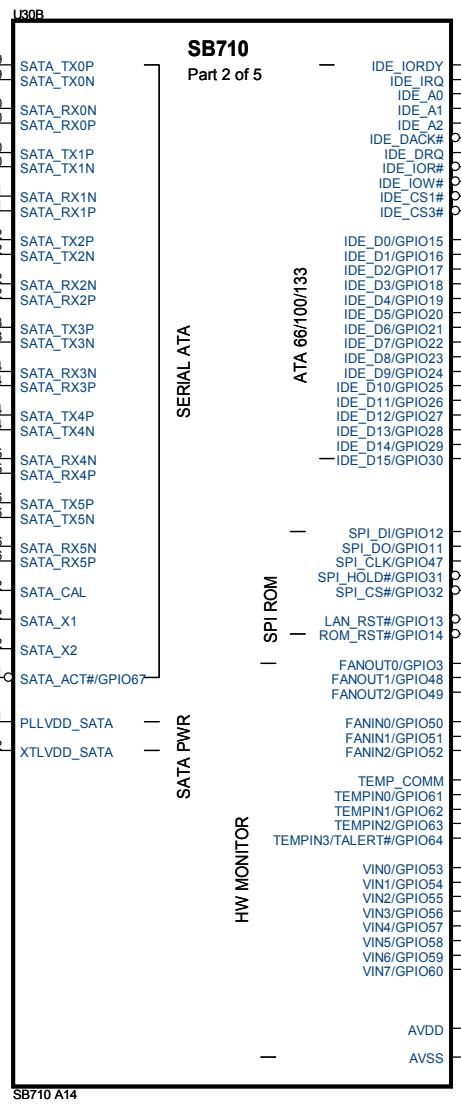
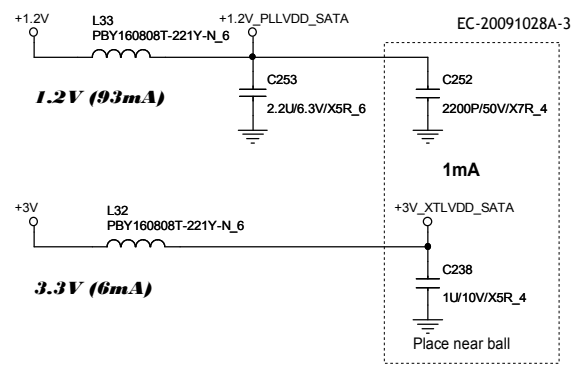
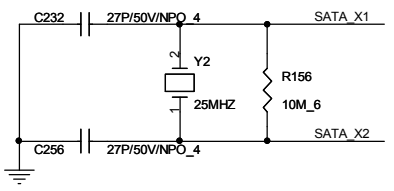
Size Custom	Document Number SB710-ACPI/GPIO/USB 2/4	Rev. <MP>
Date: 10/29/2009, 04:48 PM	Sheet: 12 of 43	

PLACE SATA AC COUPLING CAPS CLOSE TO SB710

SATA HDD



NOTE:
 Resistor IS 1K 1% FOR 25MHz XTAL, 4.99K 1% FOR 100MHz INTERNAL CLOCK



SERIAL ATA

ATA 66/100/133

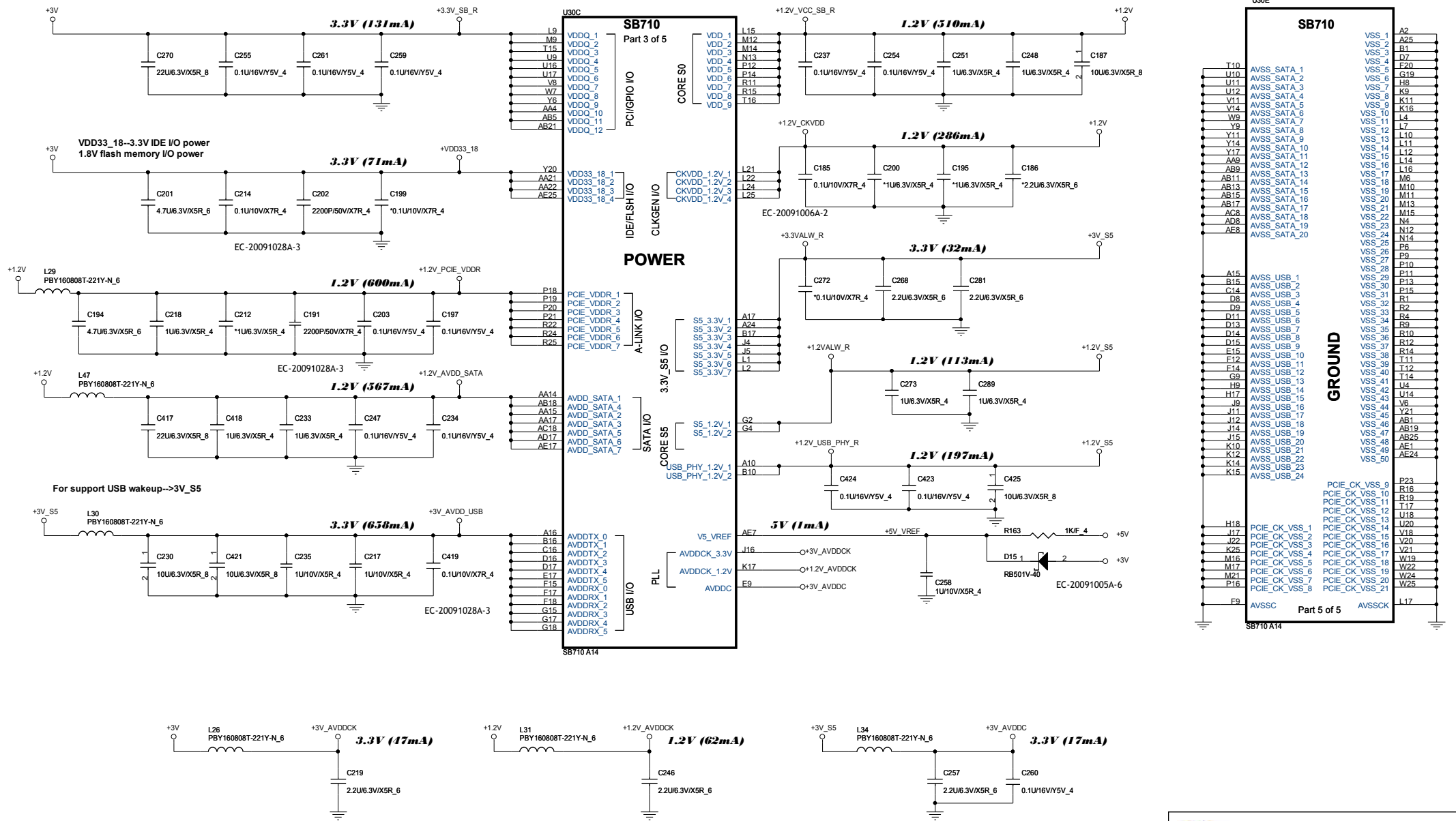
SPI ROM

HW MONITOR

Quanta Computer Inc.
 PS-Note (AMD)

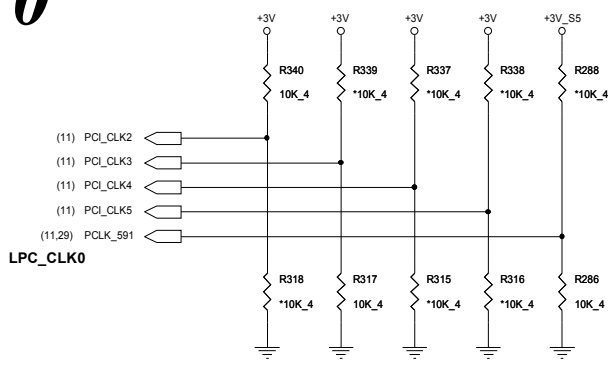
Size B	Document Number SB710-SATAI/IDE/HWM/SPI 3/4	Rev. < MP >
Date: 10/29/2009, 04:48 PM	Sheet: 13 of 43	

PLACE ALL THE DECOUPLING CAPS ON THIS SHEET CLOSE TO SB AS POSSIBLE.



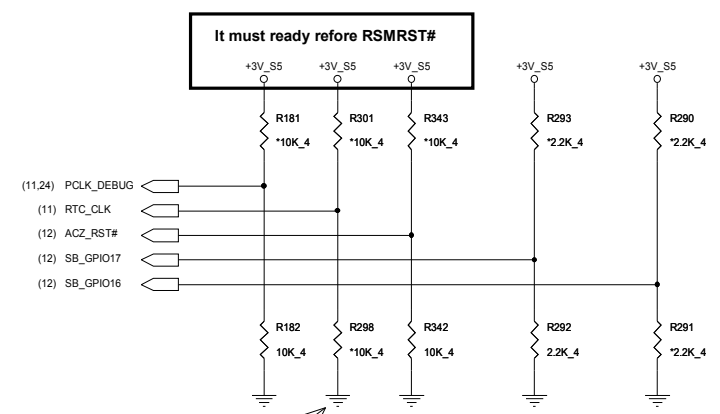
Quanta Computer Inc.
PS-Note (AMD)

Size	Document Number	Rev.
Custom	SB710-PWR/DECOUPLING 4/4	< MP >
Date:	10/28/2009, 03:15 PM	Sheet : 14 of 43



REQUIRED STRAPS

	PCI_CLK2	PCI_CLK3	PCI_CLK4	PCI_CLK5	LPC_CLK0
PULL HIGH	BOOTFAIL TIMER ENABLED <i>DEFAULT</i>	USE DEBUG STRAPS	RESERVED	RESERVED	EC ENABLED
PULL LOW	BOOTFAIL TIMER DISABLED	IGNORE DEBUG STRAPS <i>DEFAULT</i>			EC DISABLED <i>DEFAULT</i>

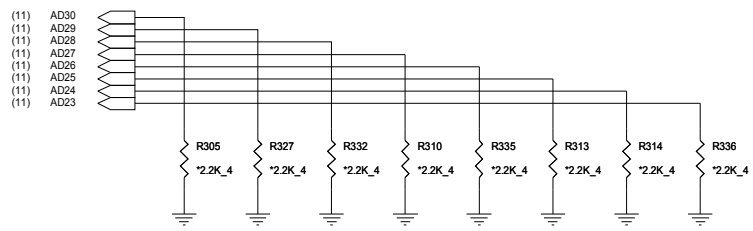


It must ready refore RSMRST#

NOTE: SB710 HAS INTERNAL 15K PULL UP RESISTOR FOR RTC_CLK

GPIO17 GPIO16
NOTE: SB710 HAS INTERNAL 15K PULL UP RESISTOR FOR SB_GPIO16,SB_GPIO17.

	LPC_CLK1	RTC_CLK	ACZ_RST#	GP17	GP16
PULL HIGH	CLKGEN ENABLED	INTERNAL RTC <i>DEFAULT</i>	ENABLE PCI MEM BOOT	ROM TYPE: H, H = Reserved H, L = SPI ROM	
PULL LOW	CLKGEN DISABLED <i>DEFAULT</i>	EXT. RTC (PD on X1, apply 32KHz to RTC_CLK)	DISABLE PCI MEM BOOT <i>DEFAULT</i>	L, H = LPC ROM <i>DEFAULT</i>	L, L = FWH ROM



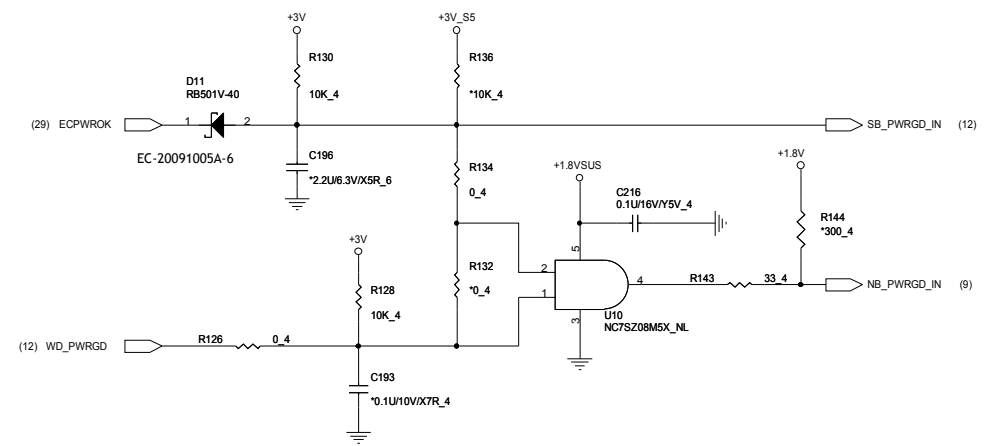
REQUIRED STRAPS

	PCI_AD28	PCI_AD27	PCI_AD26	PCI_AD25	PCI_AD24	PCI_AD23	PCI_AD29	PCI_AD30
PULL HIGH	USE LONG RESET <i>DEFAULT</i>	USE PCI PLL <i>DEFAULT</i>	USE ACPI BCLK <i>DEFAULT</i>	USE IDE PLL <i>DEFAULT</i>	USE DEFAULT PCIE STRAPS <i>DEFAULT</i>	RESERVED		
PULL LOW	USE SHORT RESET	BYPASS PCI PLL	BYPASS ACPI BCLK	BYPASS IDE PLL	USE EEPROM PCIE STRAPS		RESERVED	RESERVED

DEBUG STRAPS

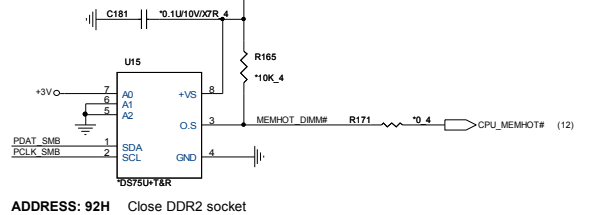
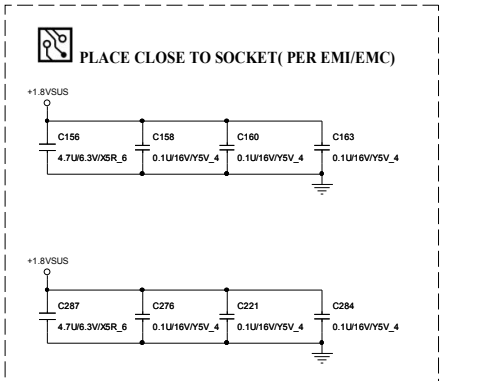
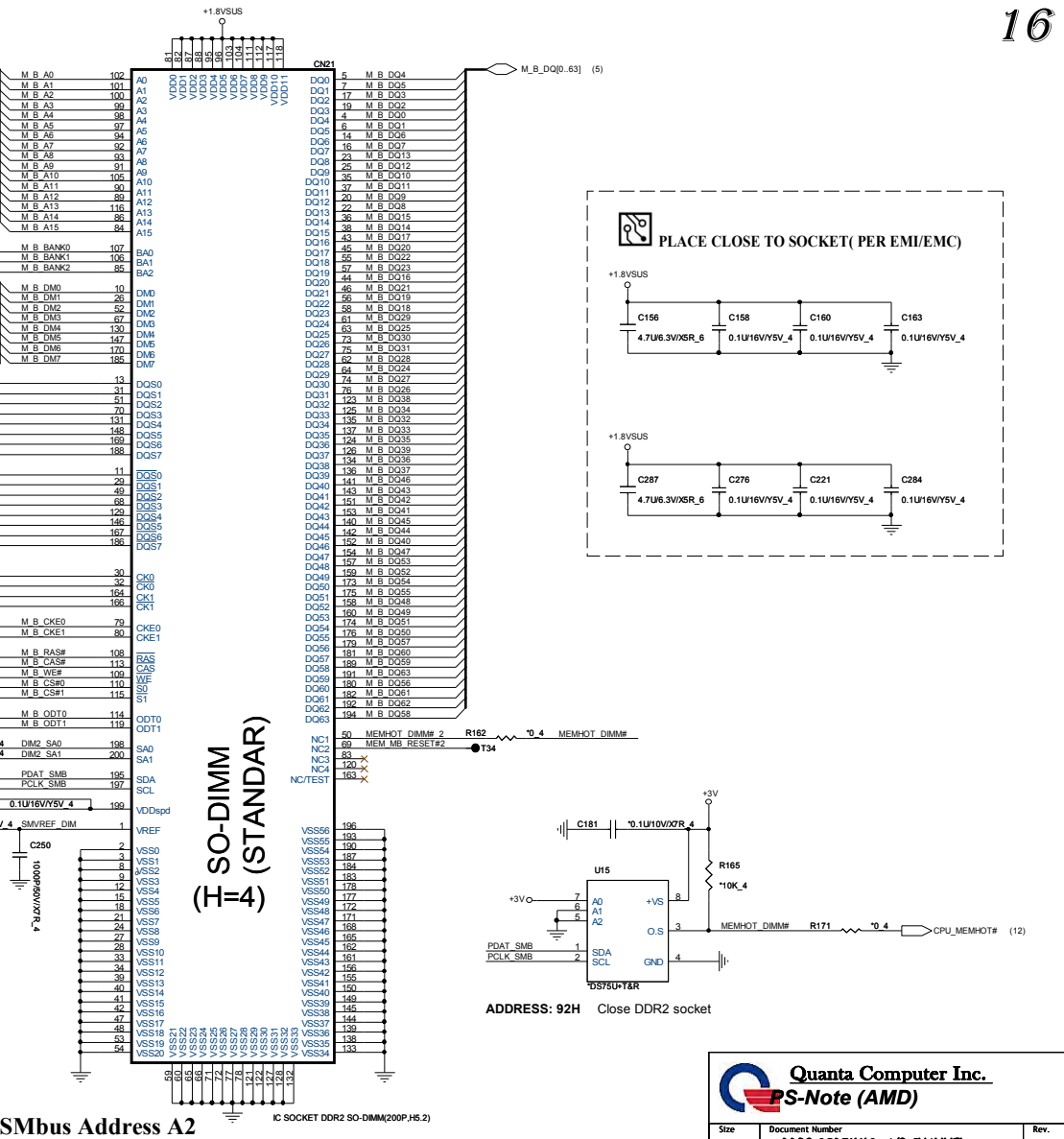
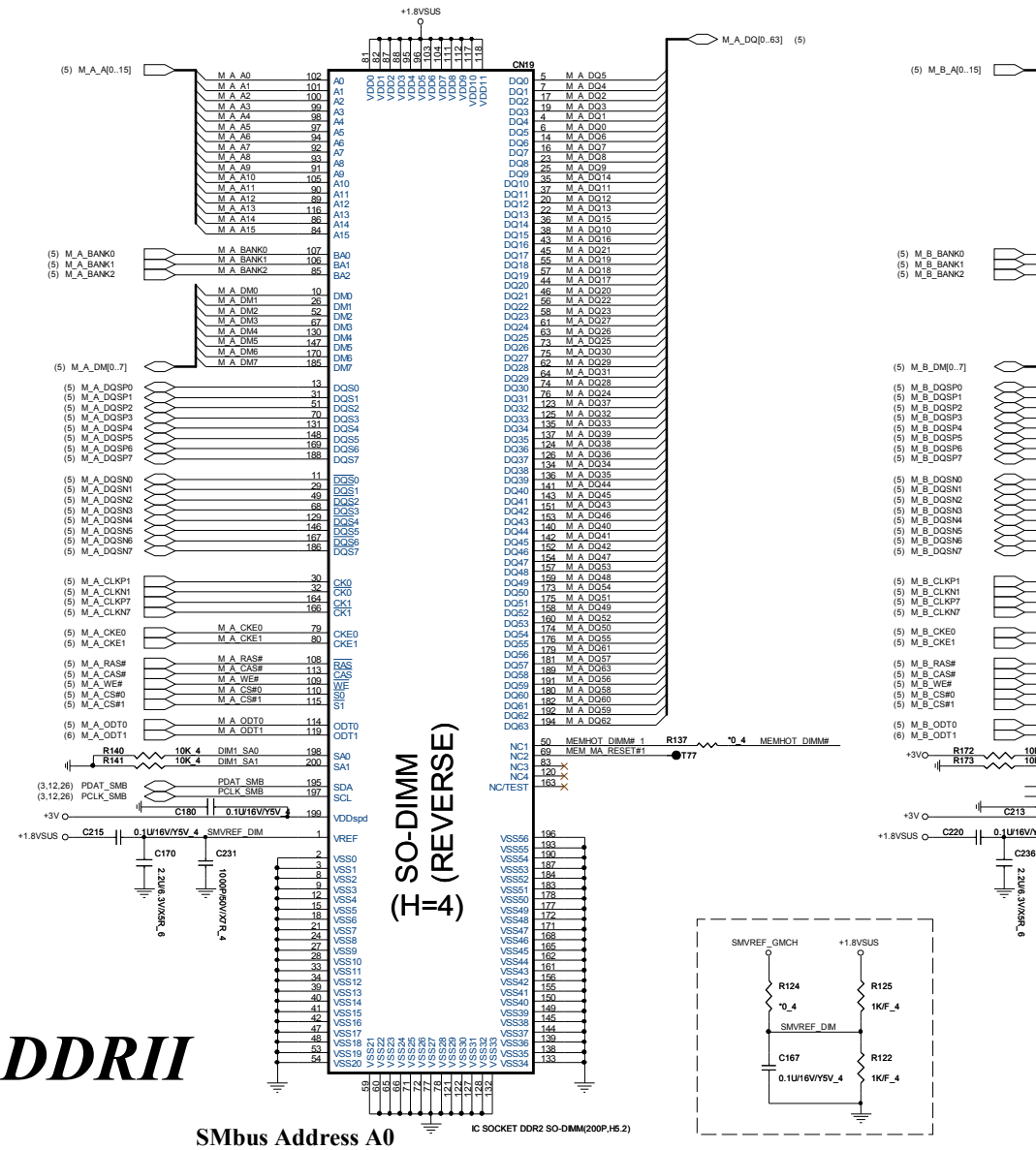
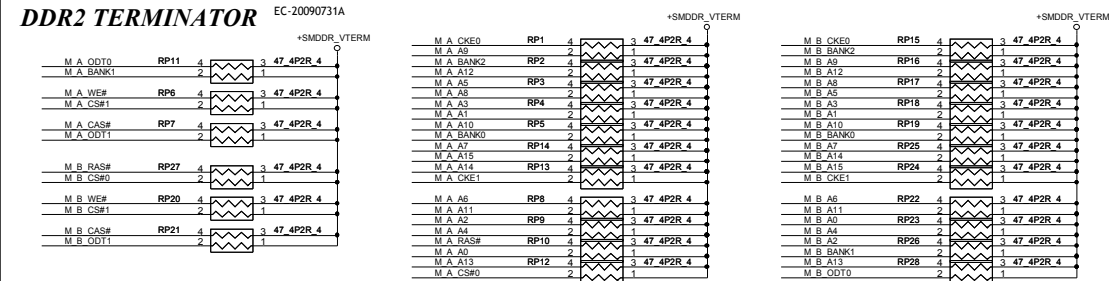
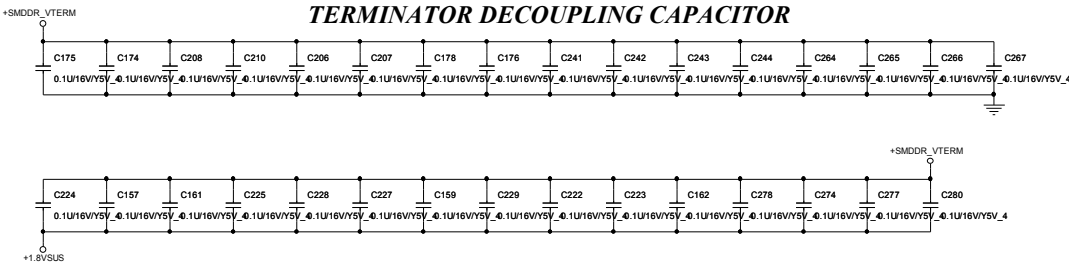
SB710 HAS 15K INTERNAL PU FOR PCI_AD[28:23]

NB/SB POWER GOOD CIRCUIT



Quanta Computer Inc.
PS-Note (AMD)

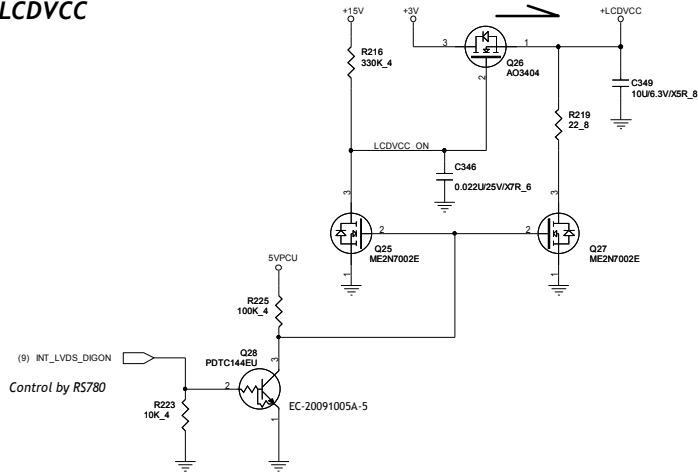
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Date: 10/29/2009 04:48 PM	Sheet: 15	of 43



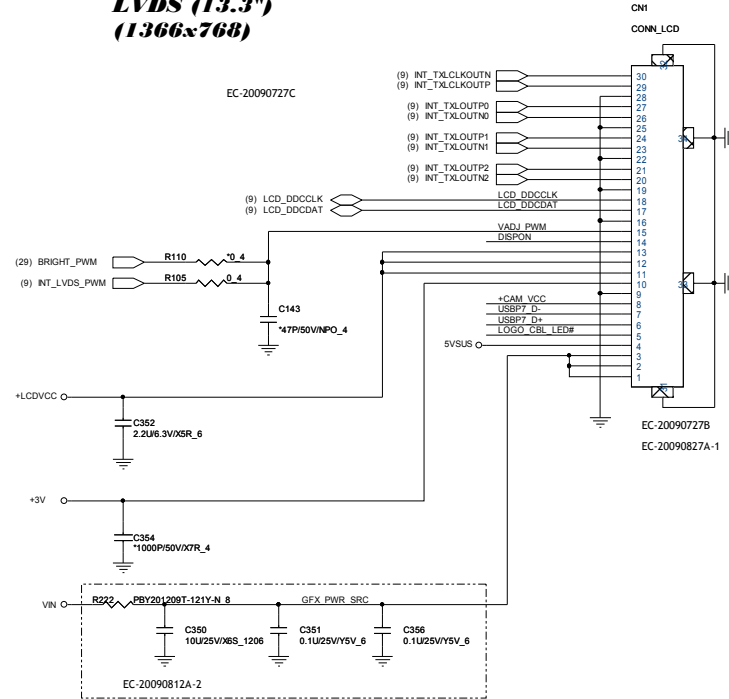
DDR2

LVDS PORT

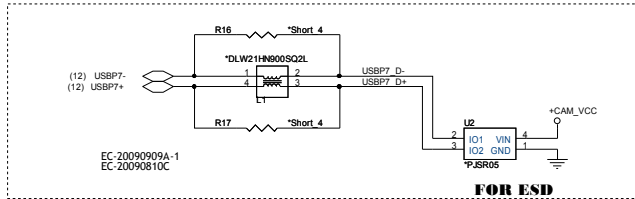
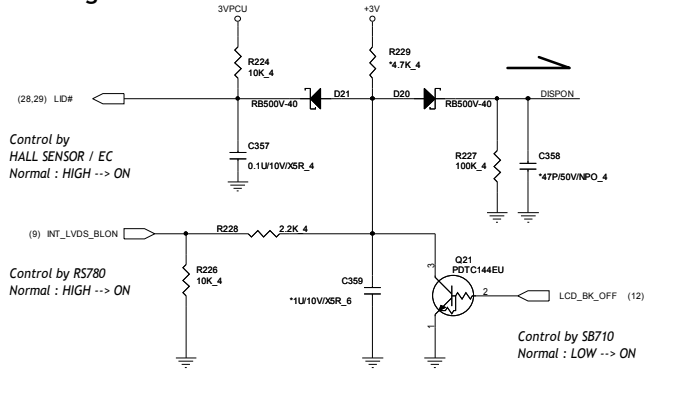
LCDVCC



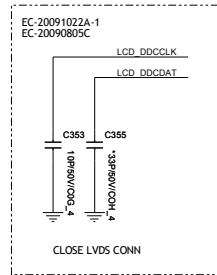
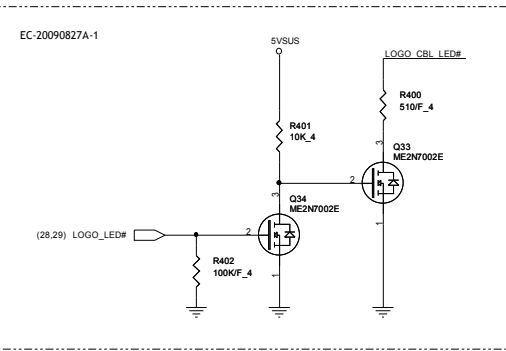
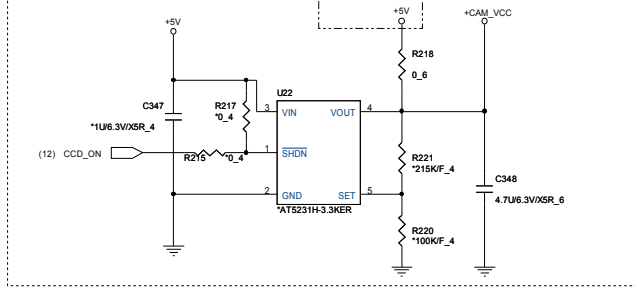
LVDS (13.3") (1366x768)



Back light



CAMERA VCC Control



CRT PORT

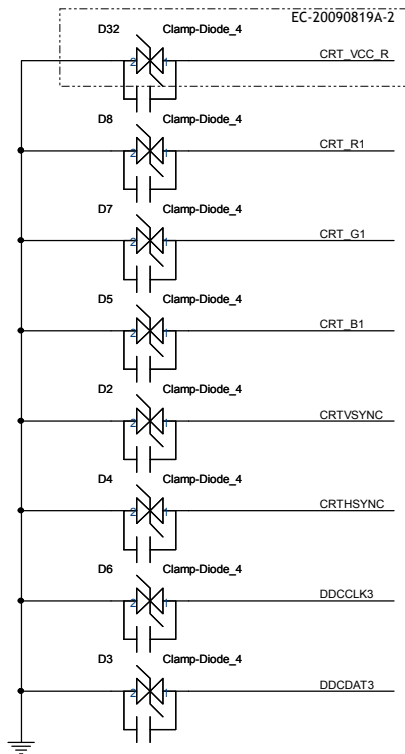
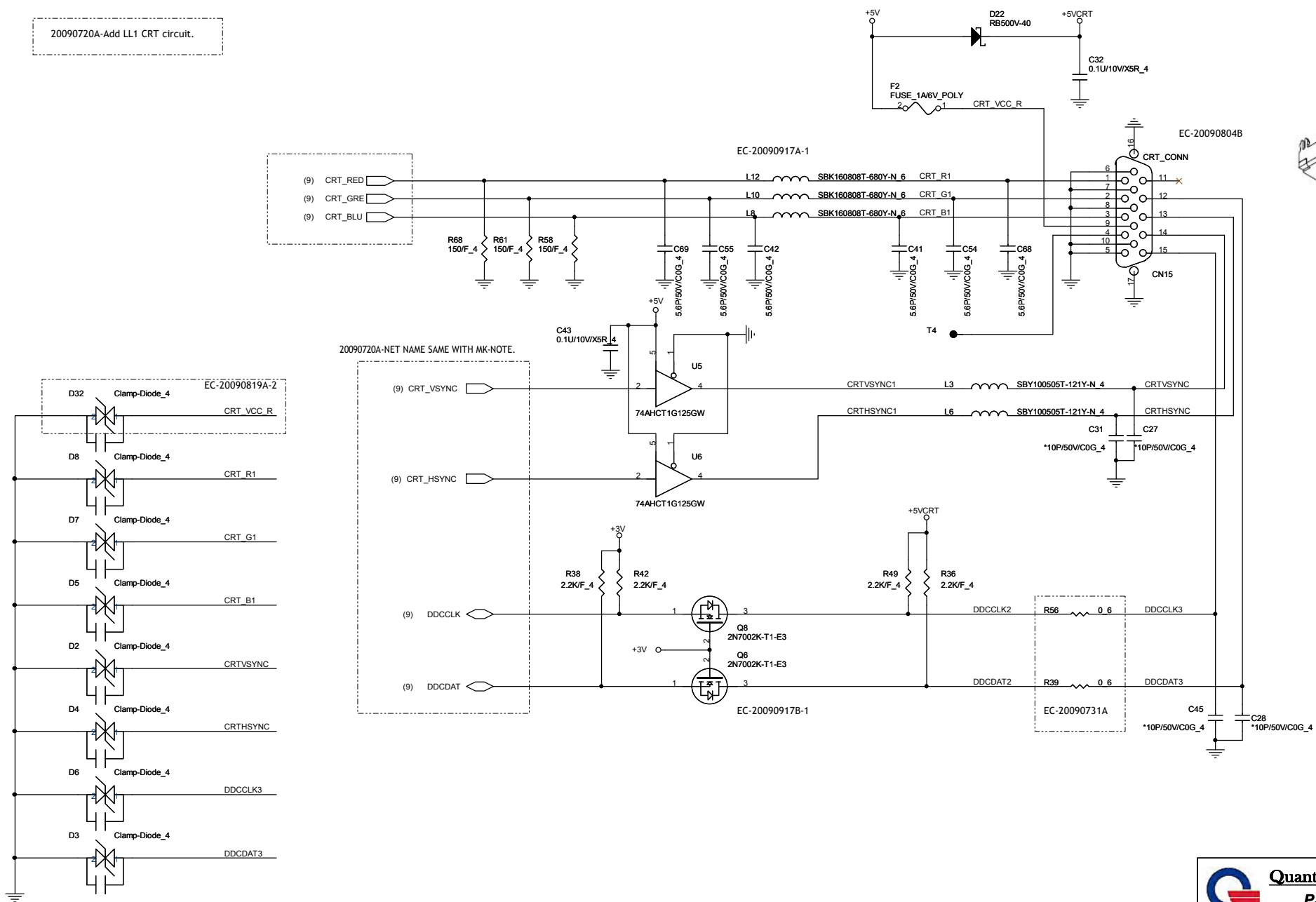
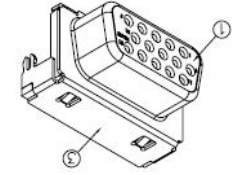
20090720A-Add LL1 CRT circuit.

(9) CRT_RED
(9) CRT_GRE
(9) CRT_BLU

EC-20090819A-2

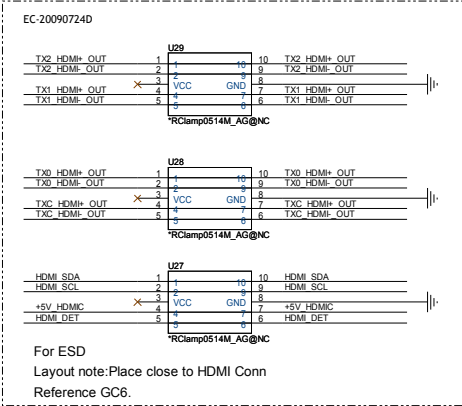
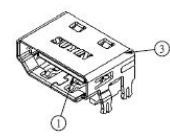
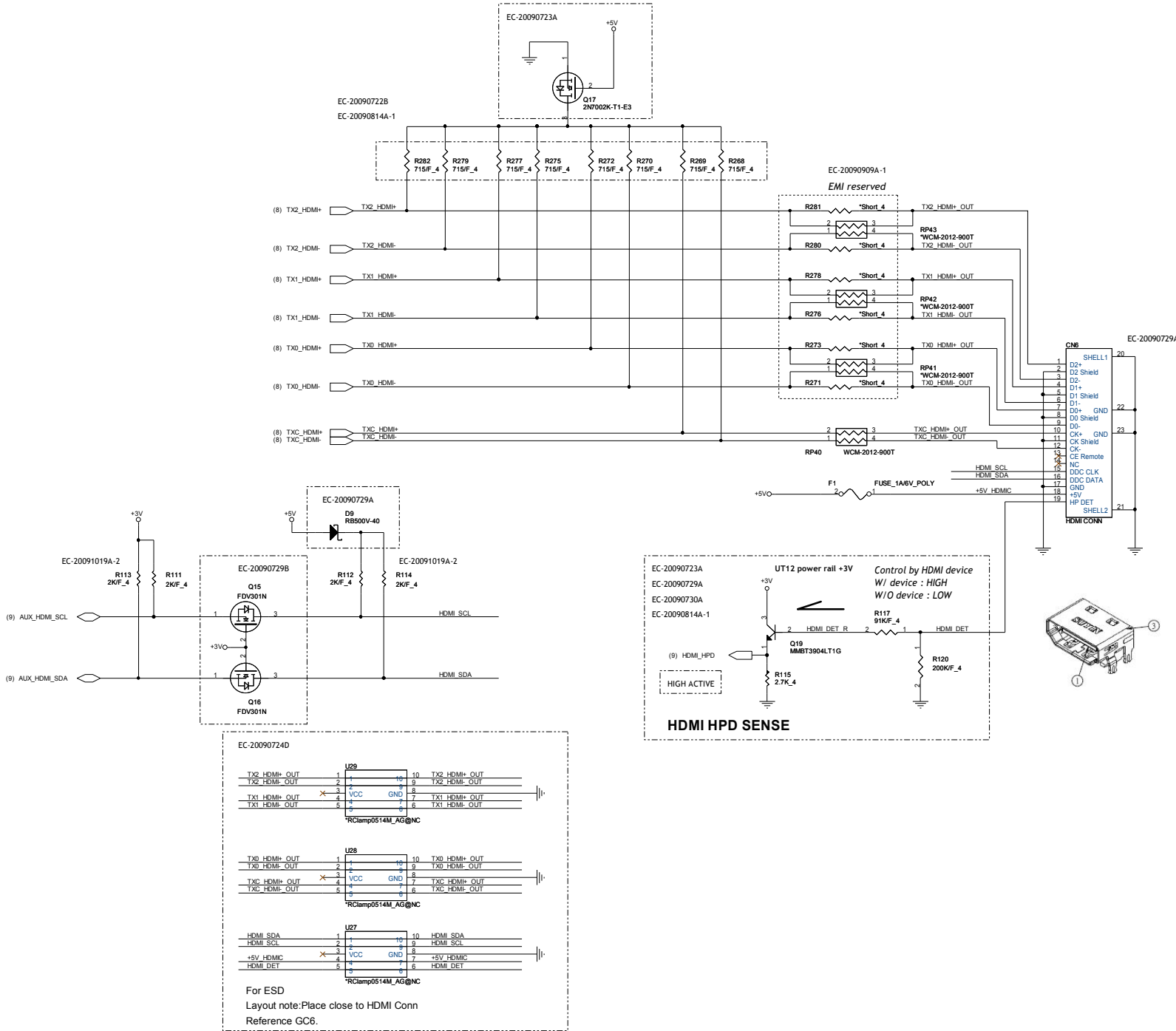
20090720A-NET NAME SAME WITH MK-NOTE.

EC-20090731A



EC-20090901A-3

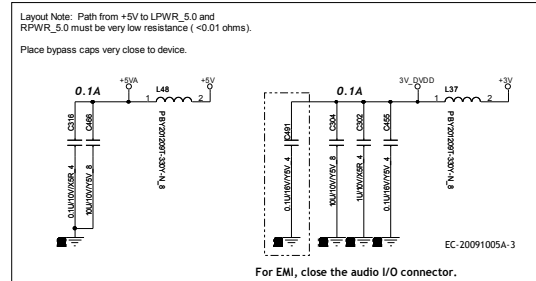
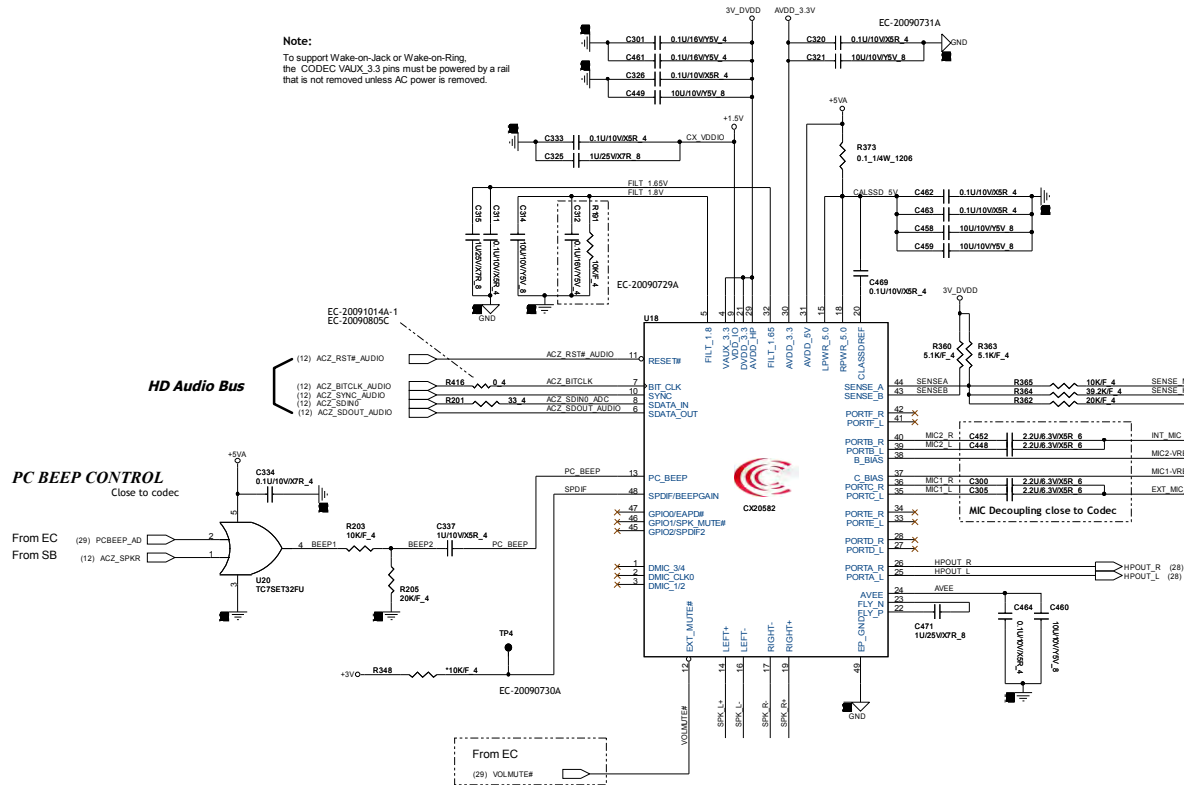
HDMI PORT



EC-20090730A
 EC-20090810C
 EC-20090724C

Note:
 To support Wake-on-Jack or Wake-on-Ring, the CODEC VALUX_3,3 pins must be powered by a rail that is not removed unless AC power is removed.

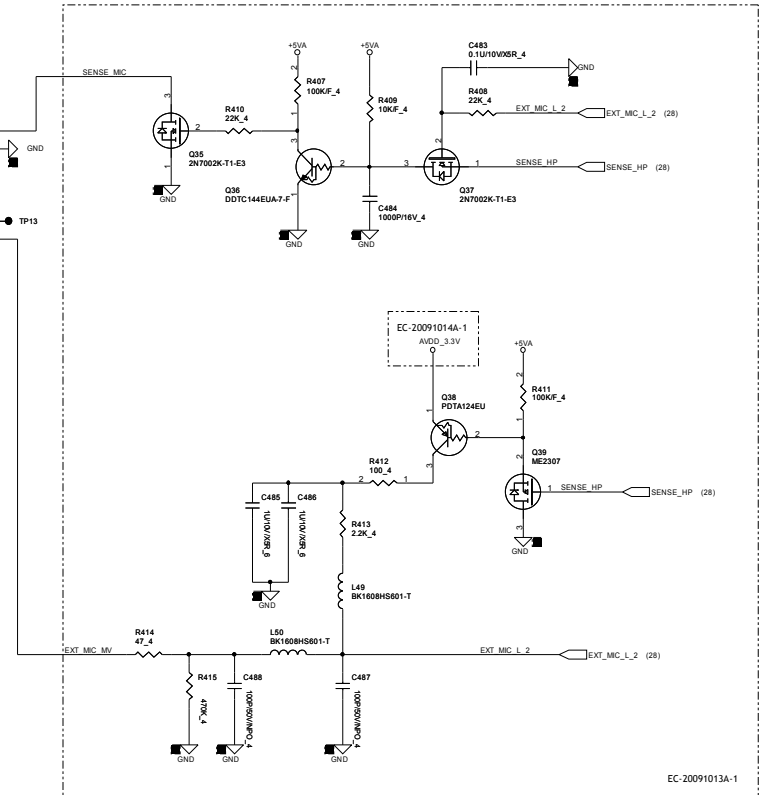
AVDD_3.3 pin is output of internal LDO. Do NOT connect to external supply.



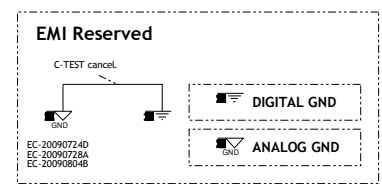
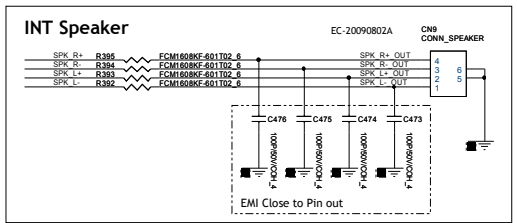
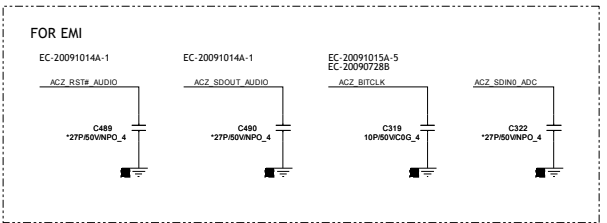
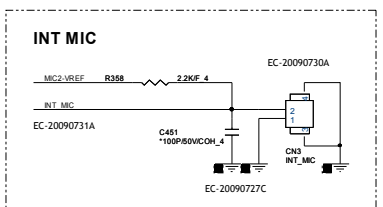
AUDIO Connector Pin Define

- <1> GND
- <2> EXT_MIC_L_2
- <3> GND
- <4> GND
- <5> HPOUT_L
- <6> HPOUT_R
- <7> SENSE_HP
- <8> GND

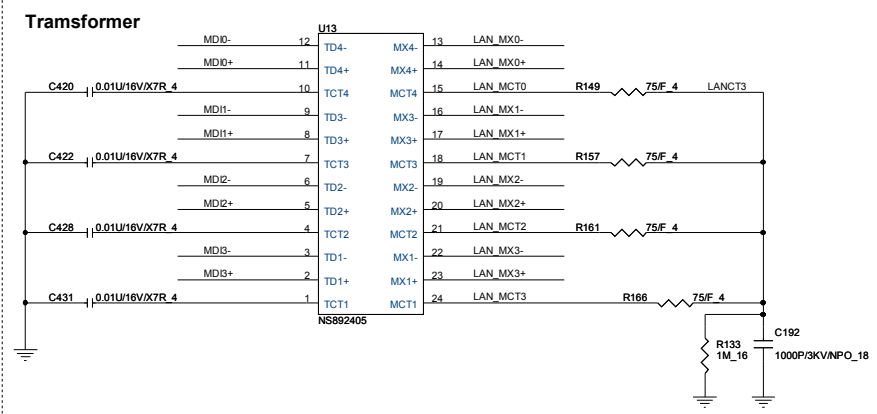
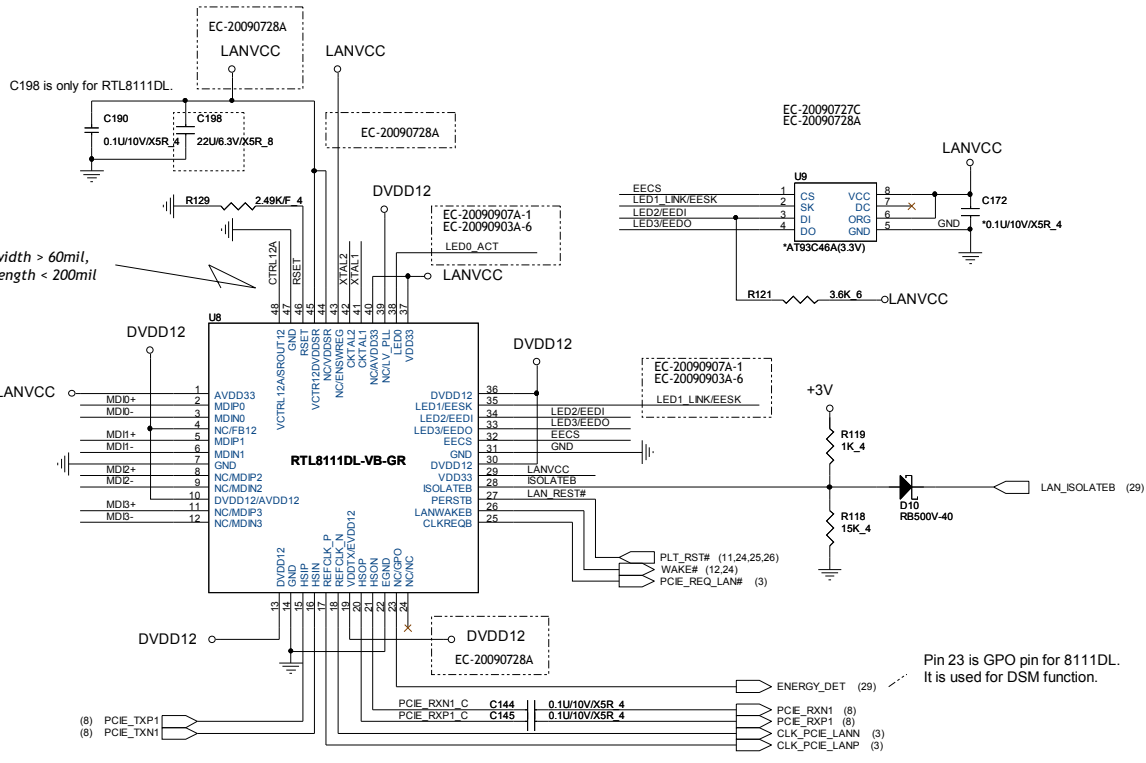
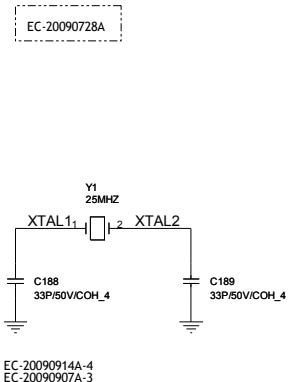
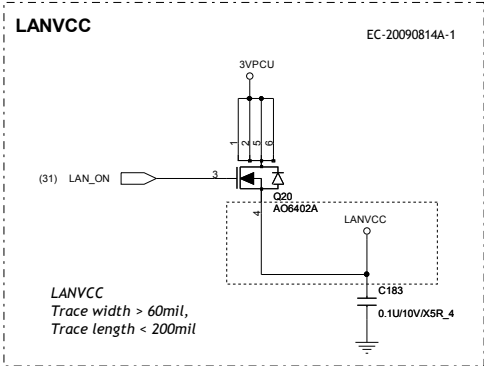
For EMI, close the audio I/O connector.



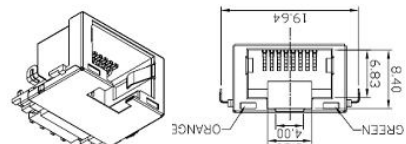
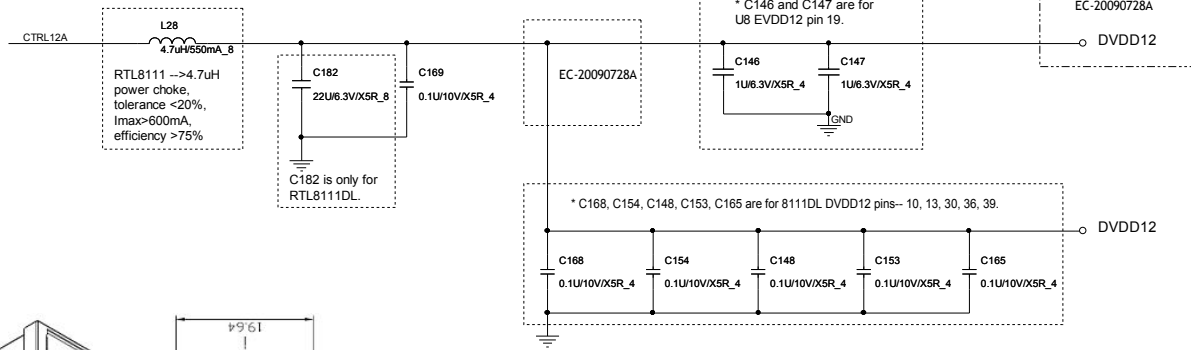
EC_MUTE	HP_PLUG	AMP_SHDN#
0	0	1
0	1	0
1	0	0
1	1	0



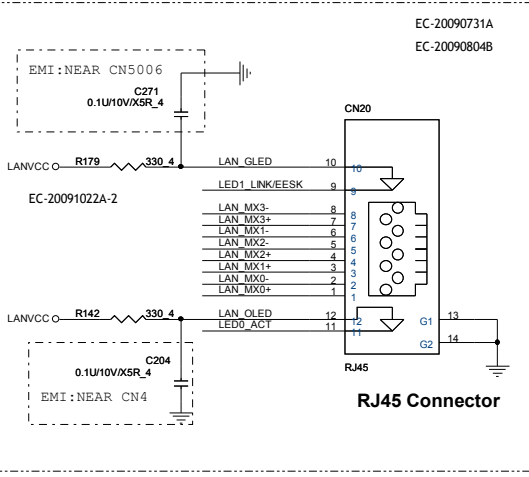
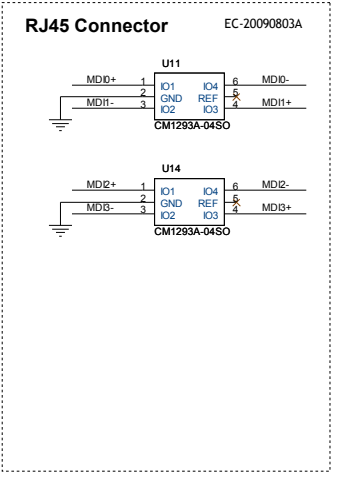
Spk:25*12 4Ω 2.0(W)/2.5(W)(Max)
 UL3302,28AWG / ACES 88267-0400

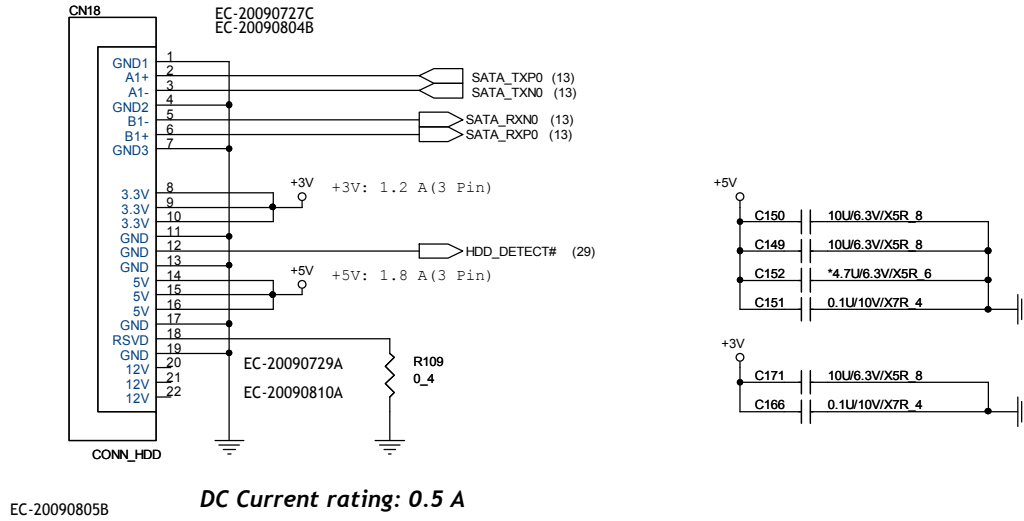


Note 1: The Trace length between L28 and 8111DL's Pin 1 must be within 0.5 cm. C182, C169 to L28 must be within 0.5cm. Refer to Layout guide for more detail.



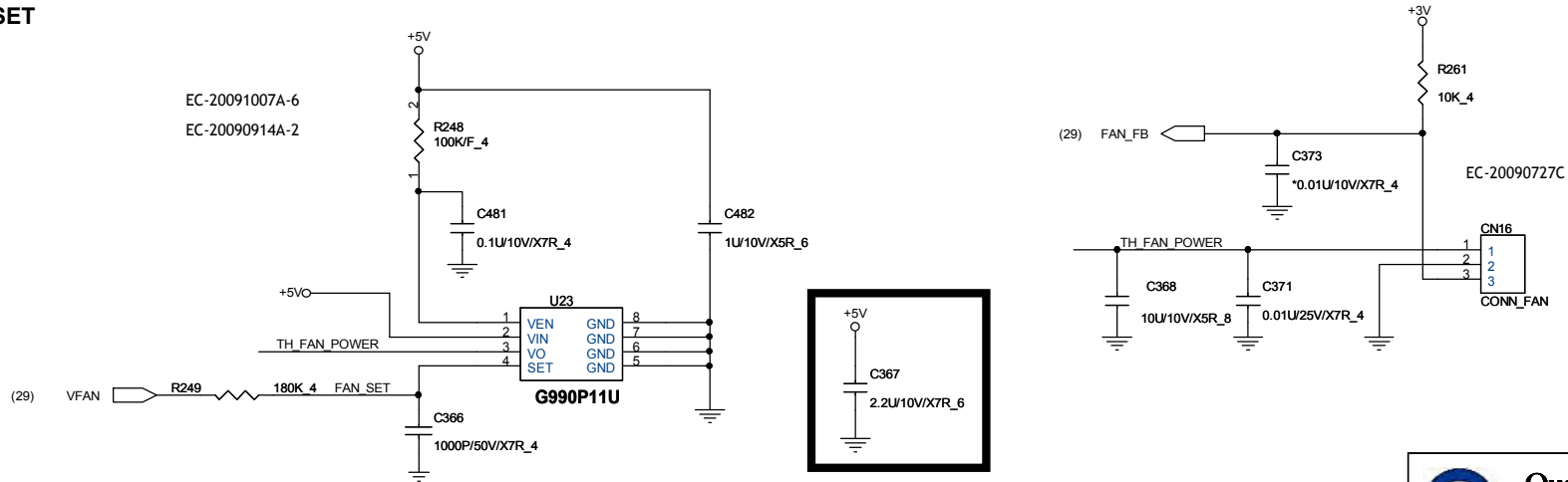
SANTA P/N:130451-E





CPU FAN

FANPWR = 1.6*VSET

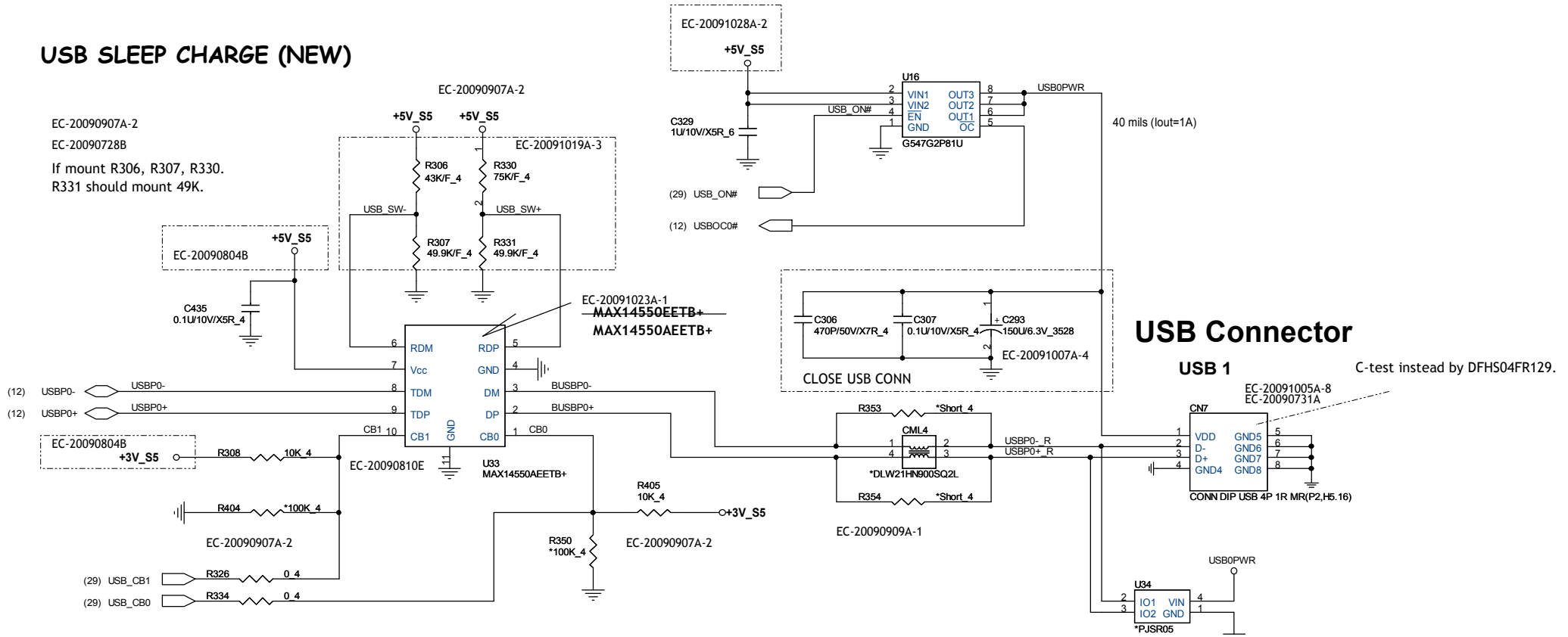


Quanta Computer Inc.
PS-Note (AMD)

Size B	Document Number SATA	Rev. < MP >
Date:	10/29/2008, 04:48 PM	Sheet : 22 of 43

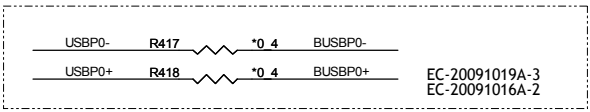
USB SLEEP CHARGE (NEW)

EC-20090907A-2
 EC-20090728B
 If mount R306, R307, R330,
 R331 should mount 49K.




USB Connector
USB 1
 C-test instead by DFHS04FR129.

CB0 / CB1	FUNCTION
0 0	Auto Mode
0 1	Force Short
1 0	Force Resistor
1 1	TDP = DP, TDM = DM.



20090720A-Change USB port *2 on function board.

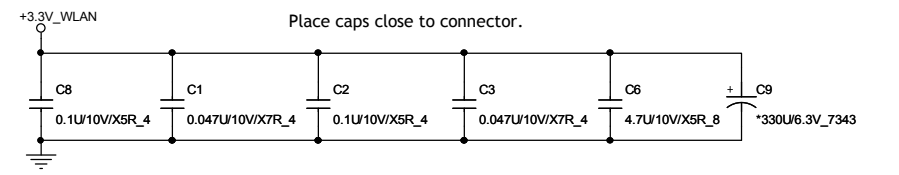
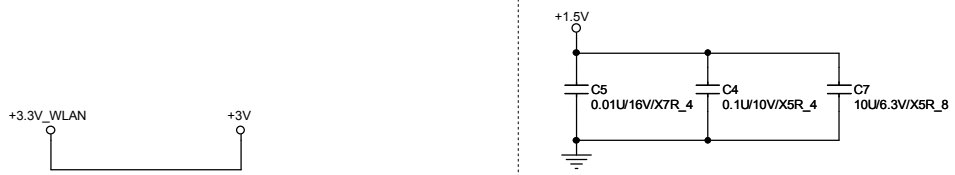
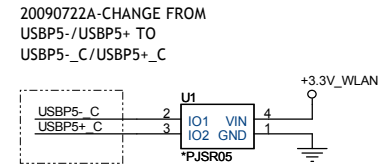
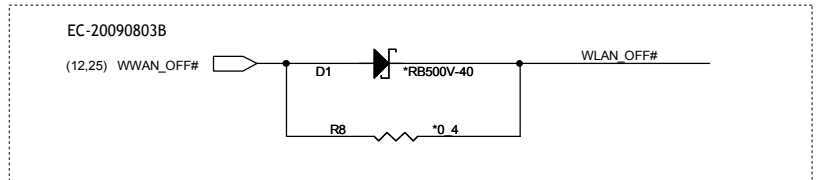
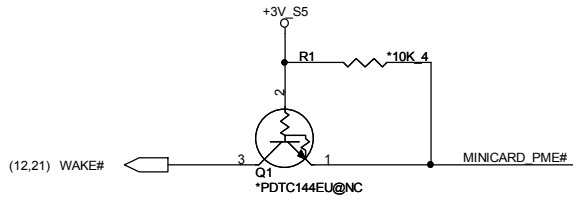
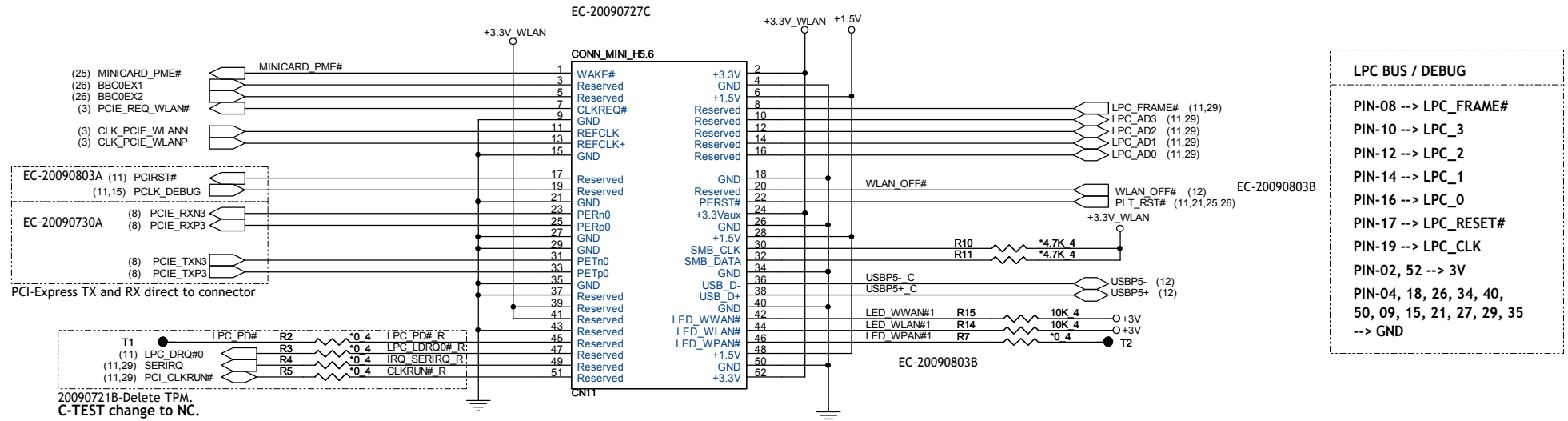
20090722A-Change USB port *2 Power Switch IC on function board.



Quanta Computer Inc.
 PS-Note (AMD)

Size B	Document Number USB x 3	Rev. < MP >
Date:	10/29/2009, 04:48 PM	Sheet : 23 of 43

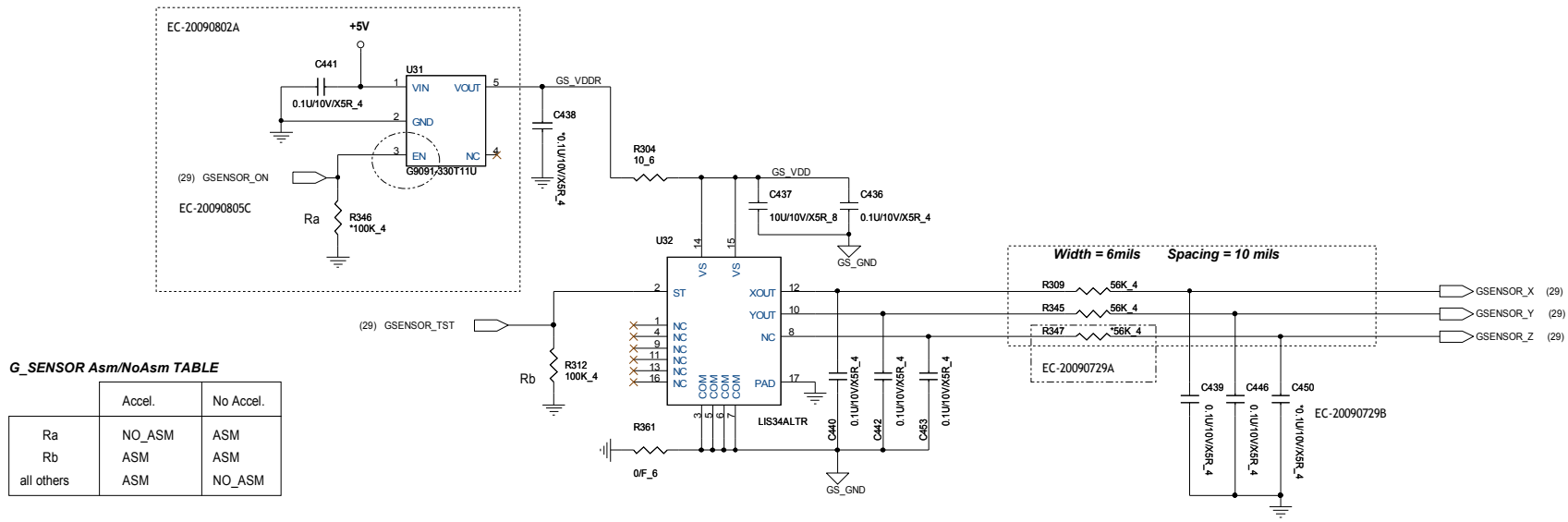
MiniCard WLAN connector



Quanta Computer Inc.

PS-Note (AMD)

Size Custom	Document Number WLAN	Rev. < MP >
Date: 10/29/2009, 04:48 PM	Sheet : 24	of 43

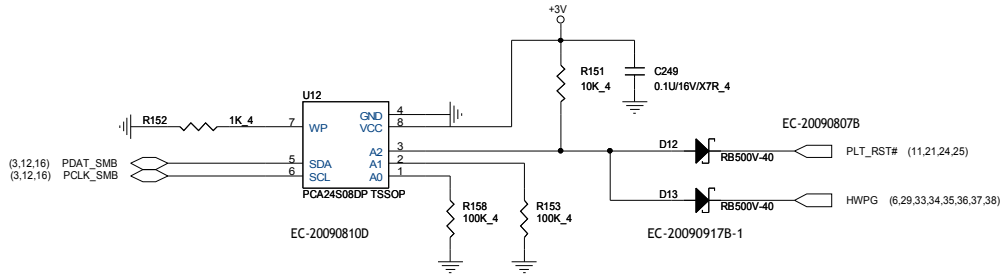


G_SENSOR Asm/NoAsm TABLE

	Accel.	No Accel.
Ra	NO_ASM	ASM
Rb	ASM	ASM
all others	ASM	NO_ASM

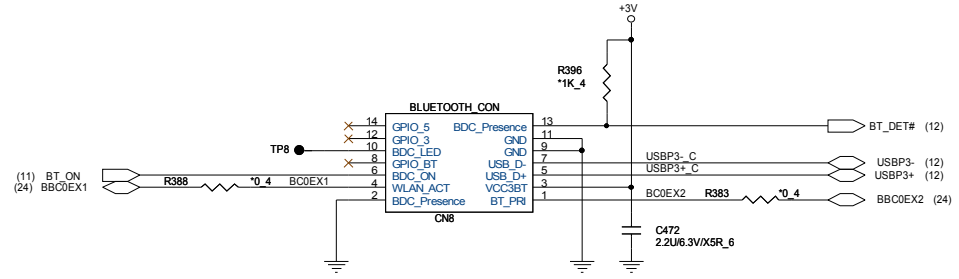
RFID EEPROM

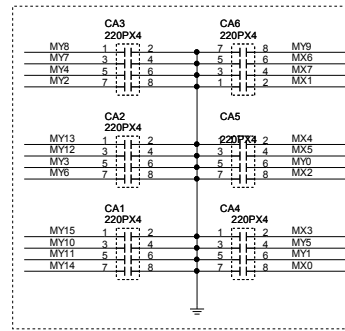
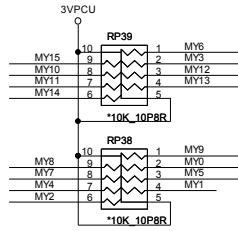
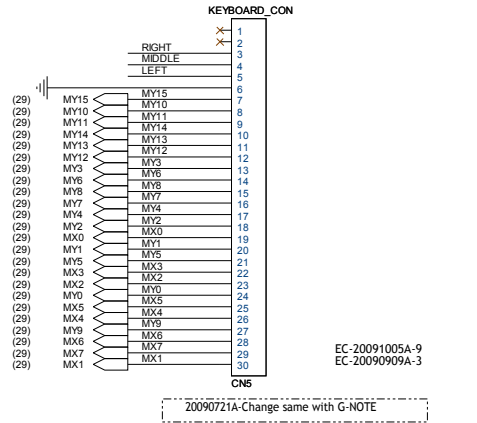
20090804B-GC7 RFIC CIRCUIT
EC-20090804B



AKE3CZ0KG00 / IC EEPROM(8P) PCA24S08DP (TSSOP) / NXP
AKE3CZBK000 / IC EEPROM(8P) BUL08-1FVJ-WE2 (TSSOP) / ROH

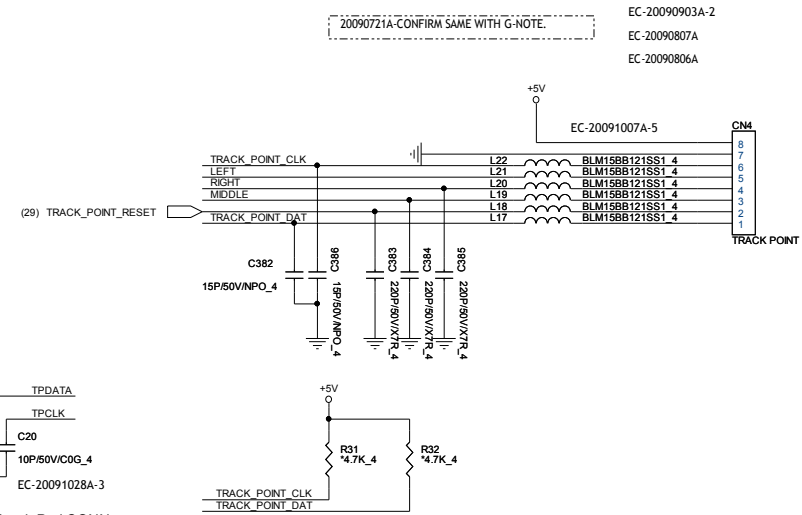
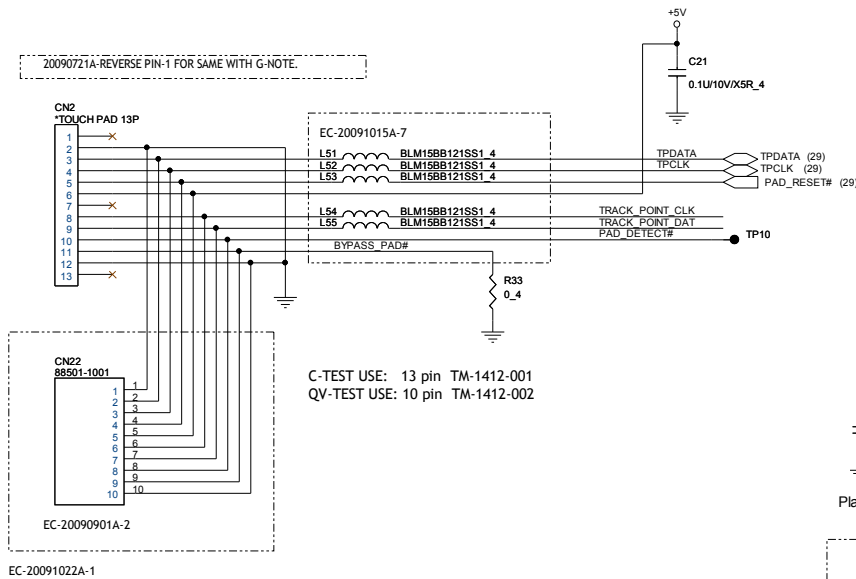
BLUETOOTH



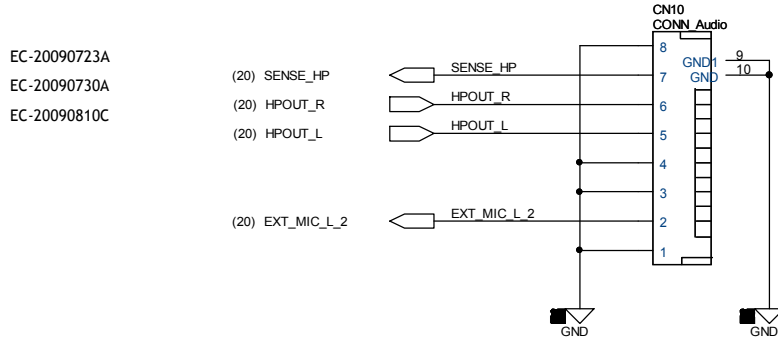


TOUCH PAD

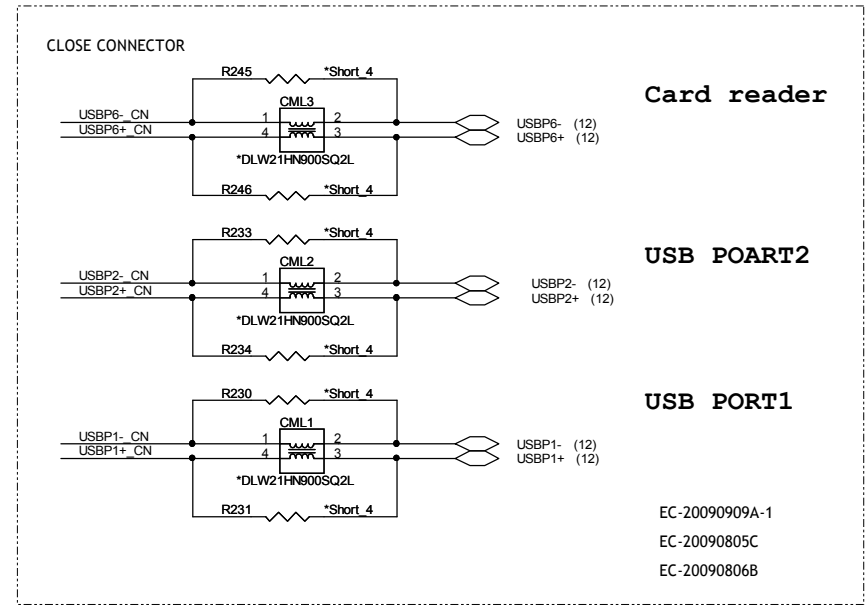
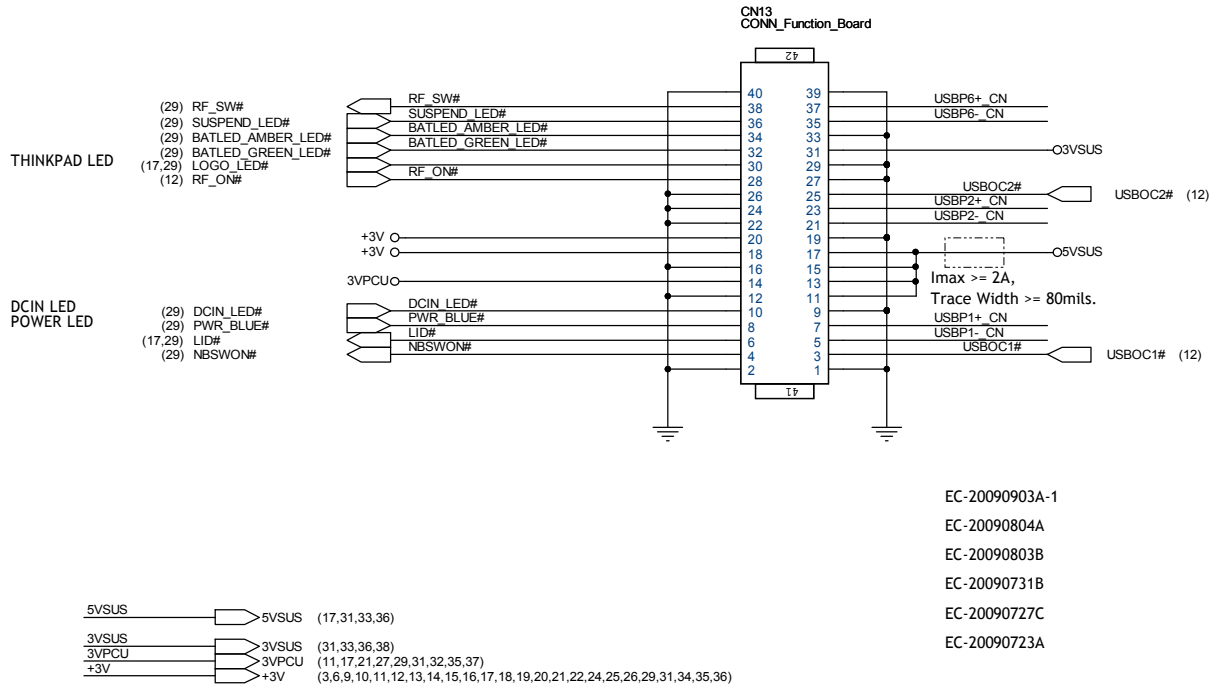
TRACK POINT



Audio Connector --> Function Board (8-PIN)

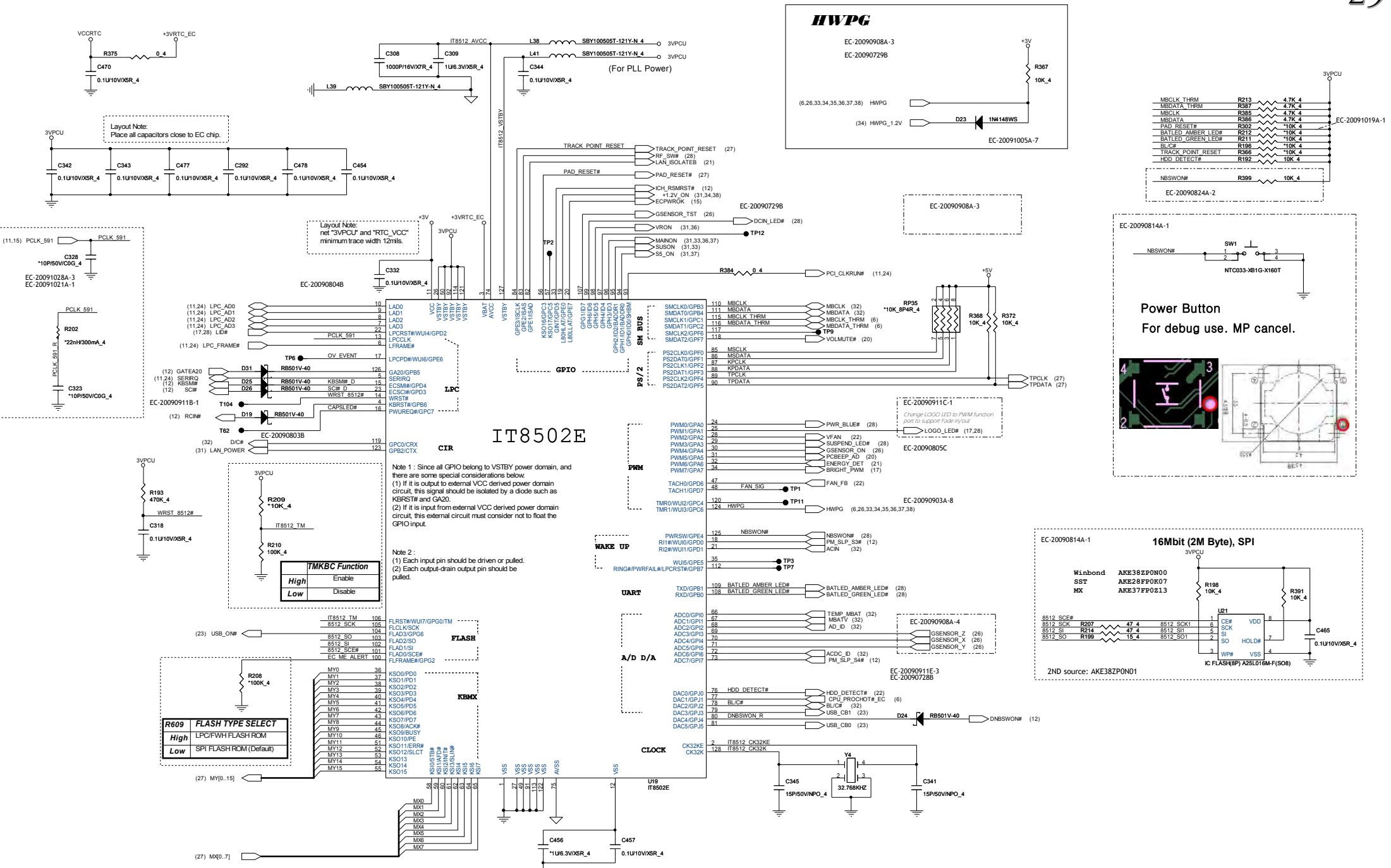


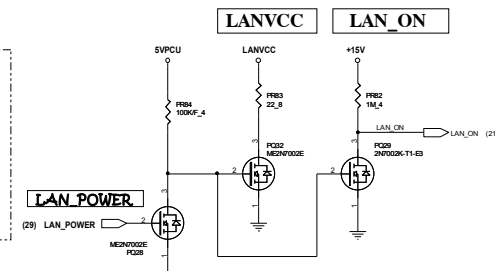
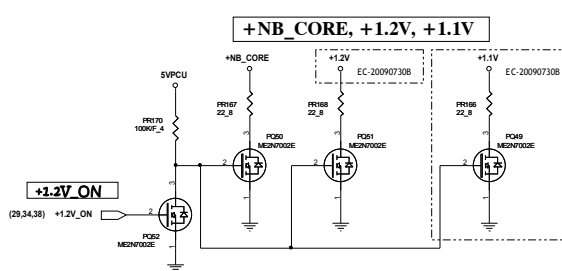
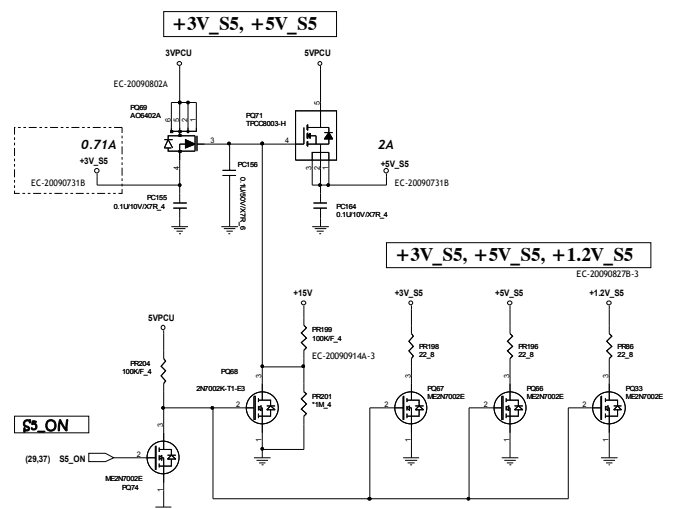
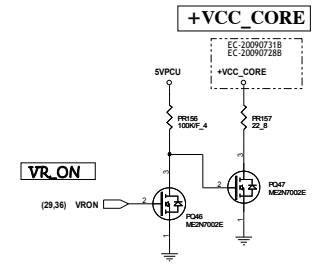
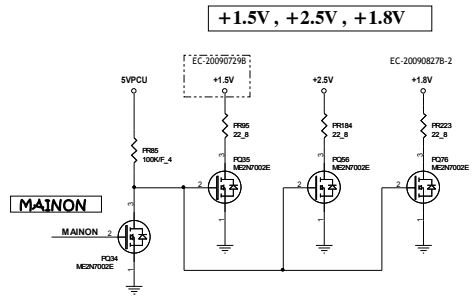
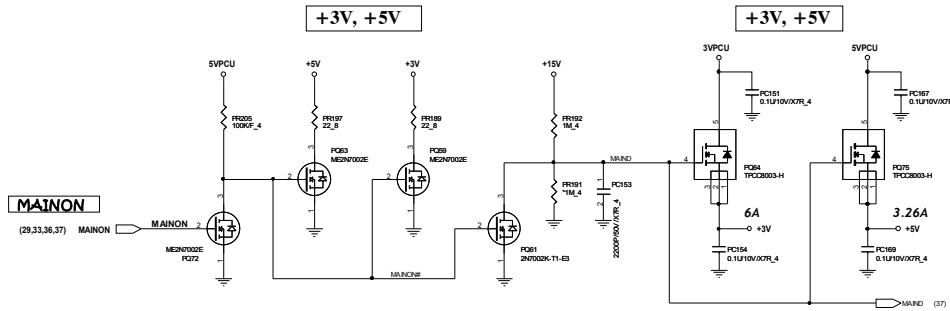
Function Connector --> Function Board (40-PIN)



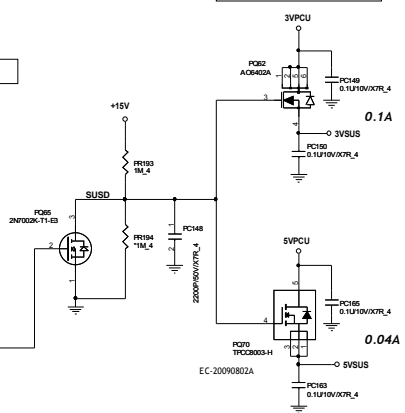
Quanta Computer Inc.
 PS-Note (AMD)

Size Custom	Document Number AUDIO & FUNCTION CONN	Rev. < MP >
Date: 10/29/2009, 04:48 PM	Sheet : 28 of 43	

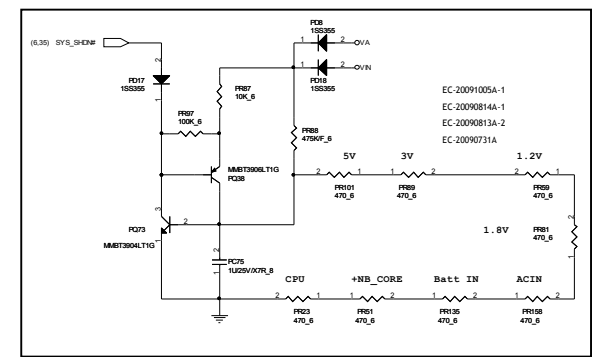
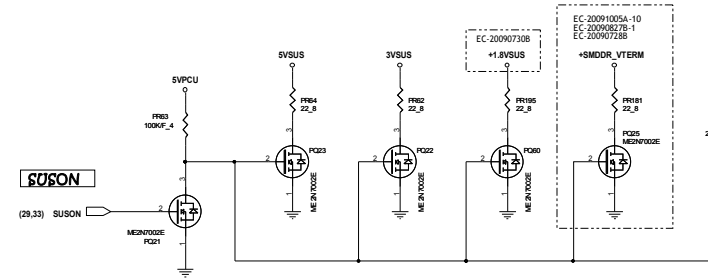


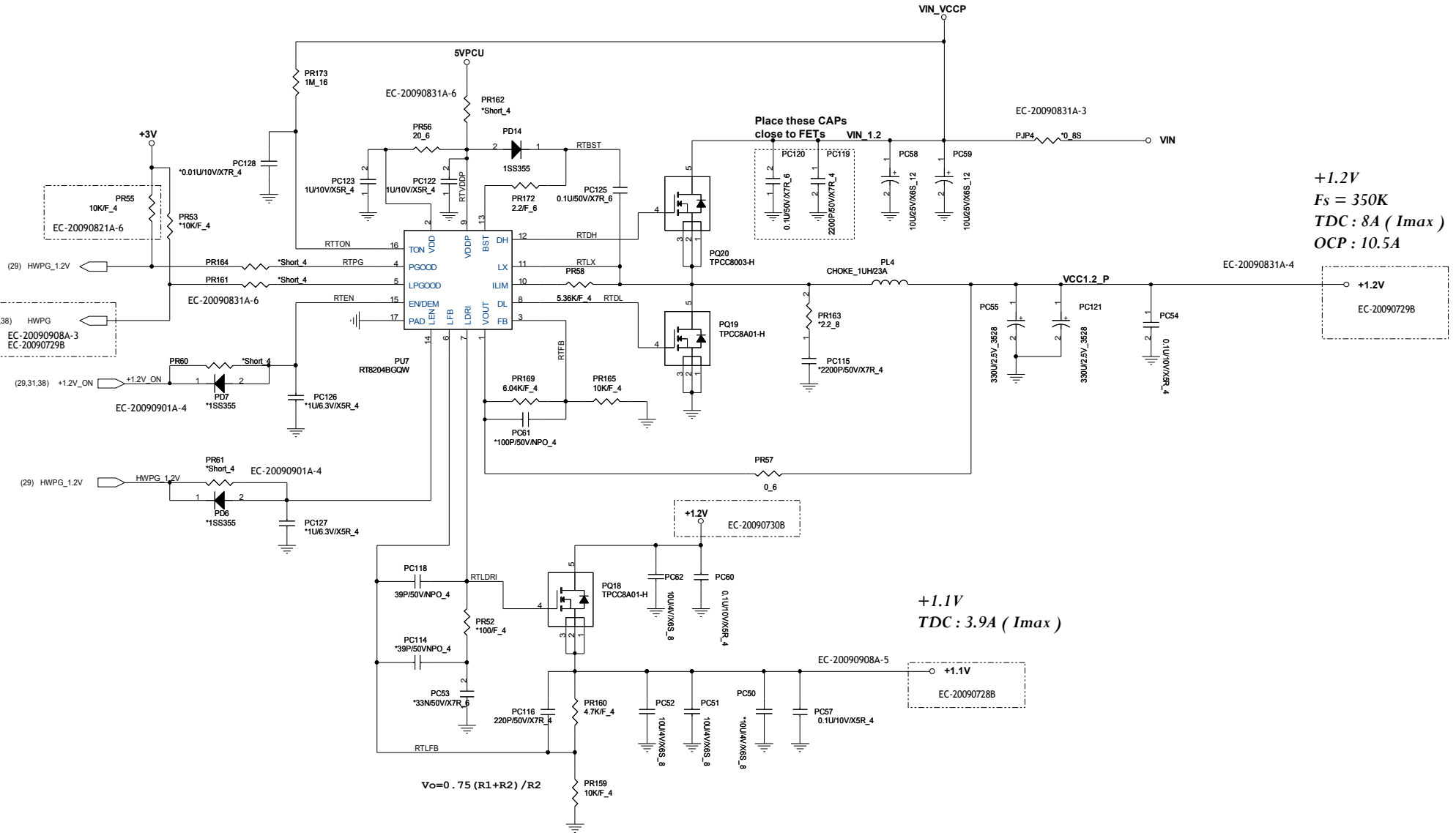


3VSUS, 5VSUS



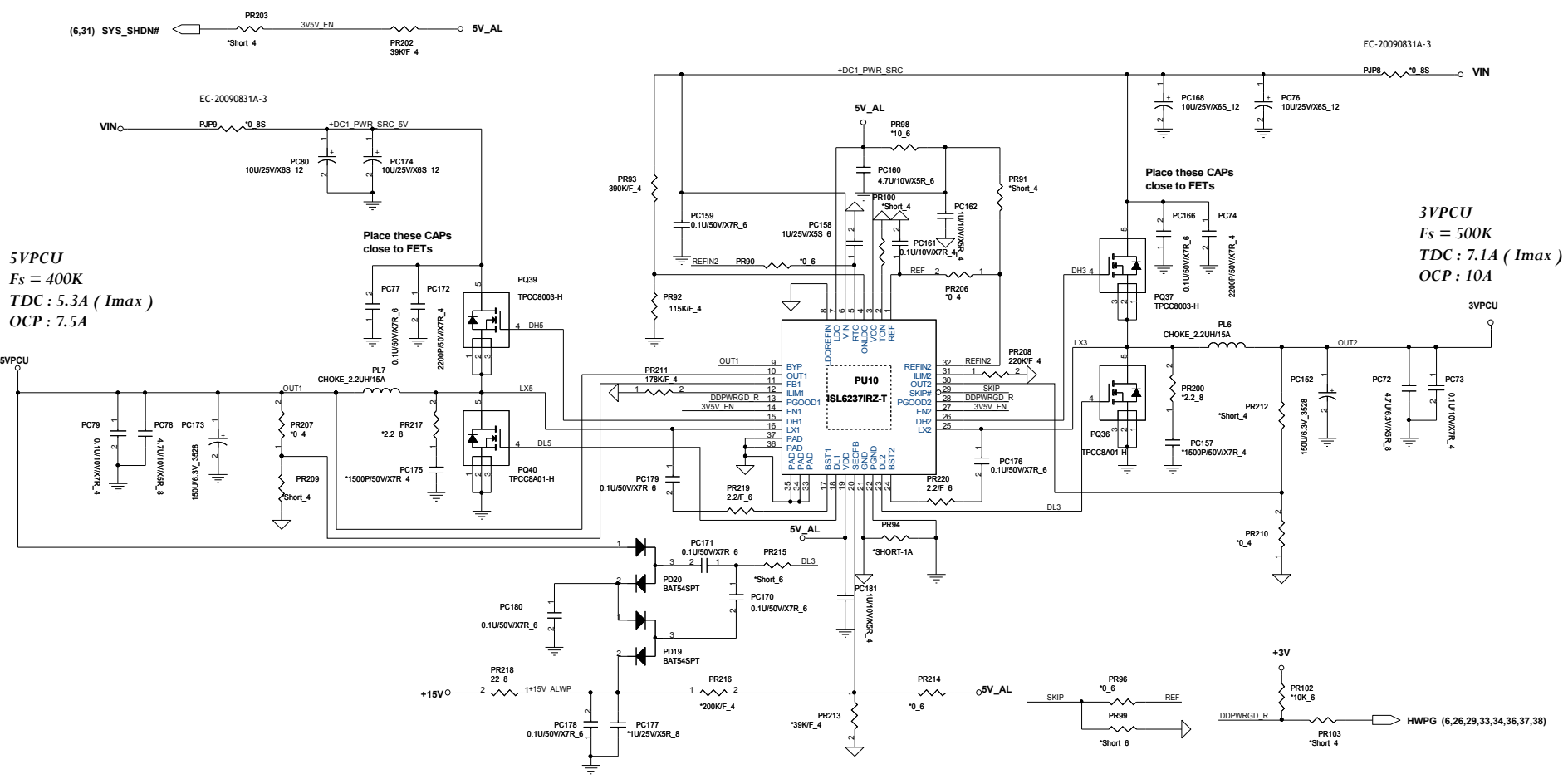
5VSUS, 3VSUS, +1.8VSUS, +SMDDR_VTERM

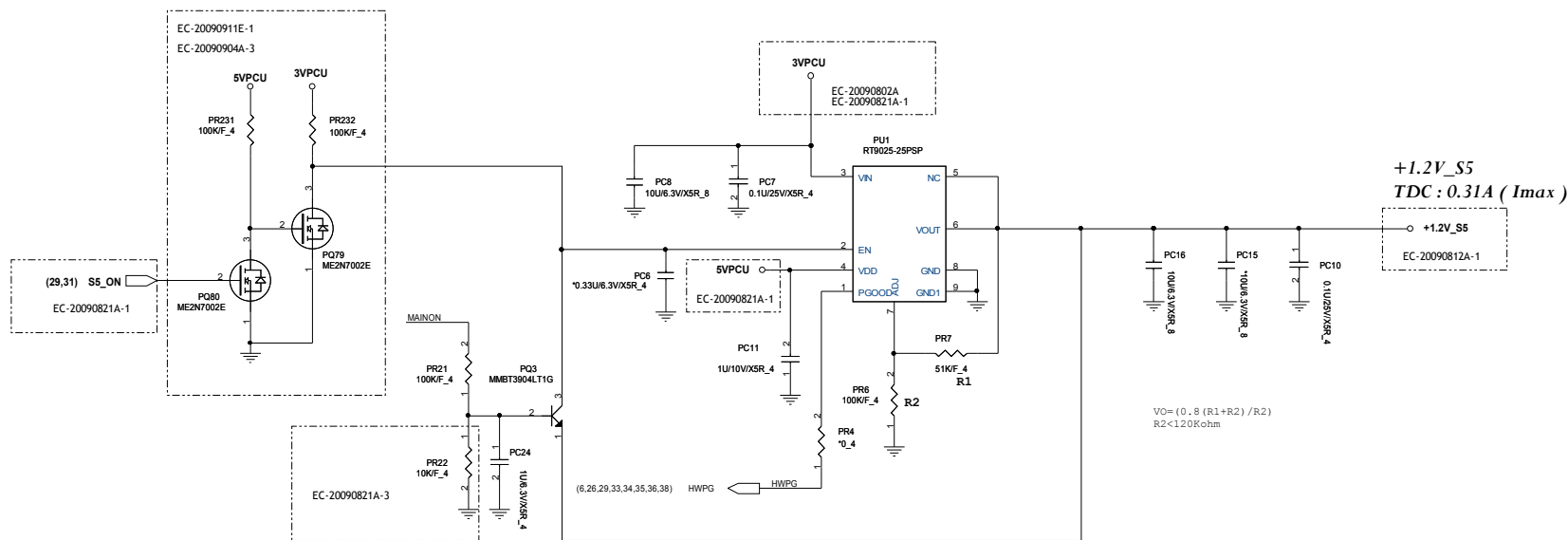
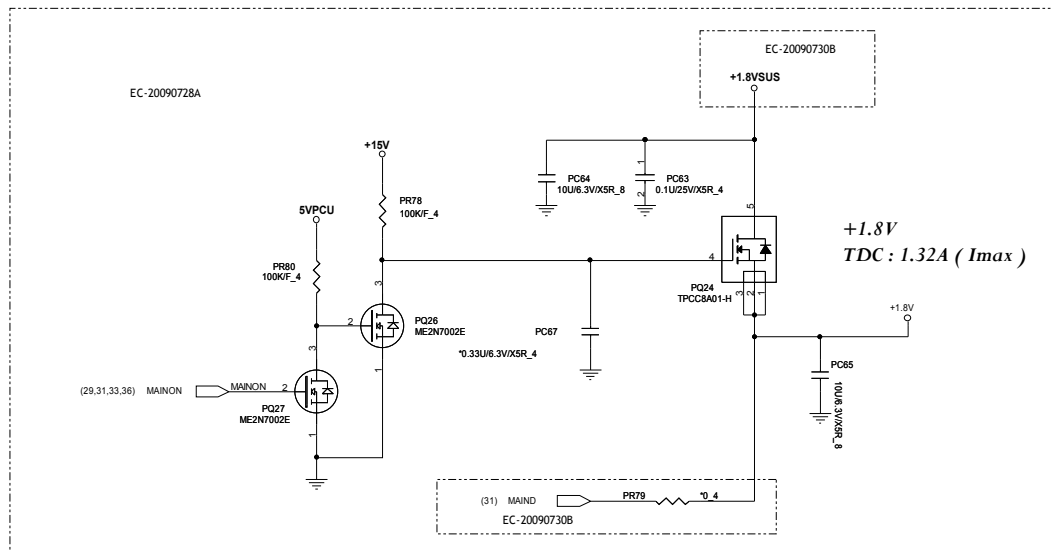


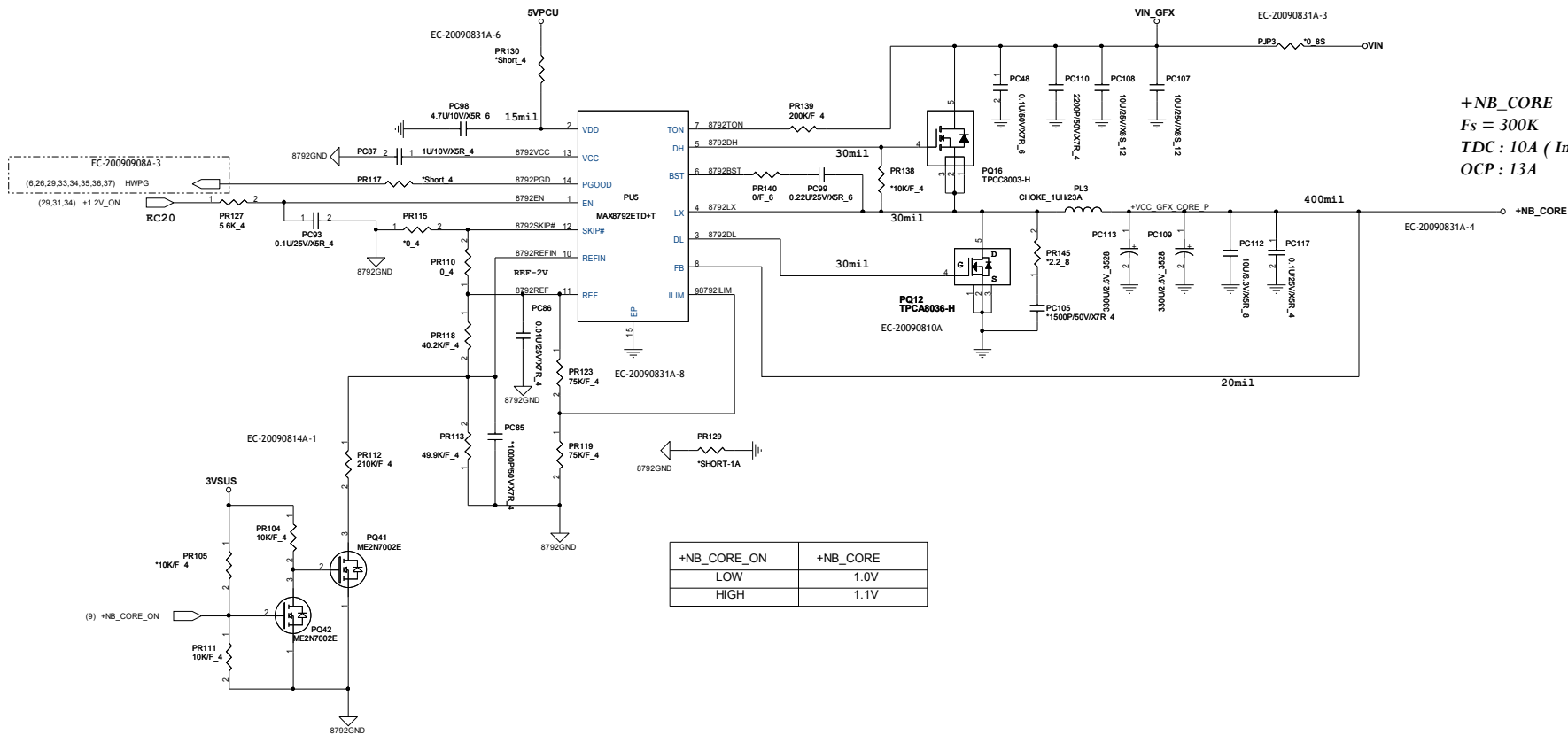


+1.2V
F_s = 350K
TDC : 8A (I_{max})
OCP : 10.5A

+1.1V
TDC : 3.9A (I_{max})

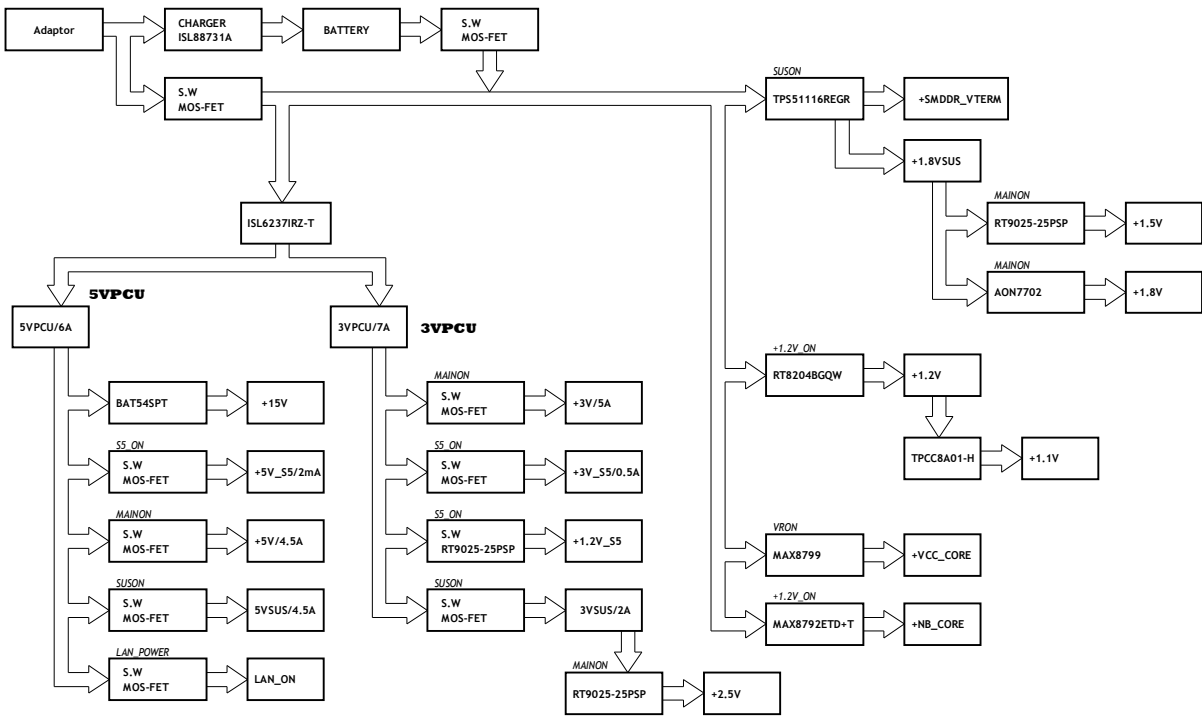




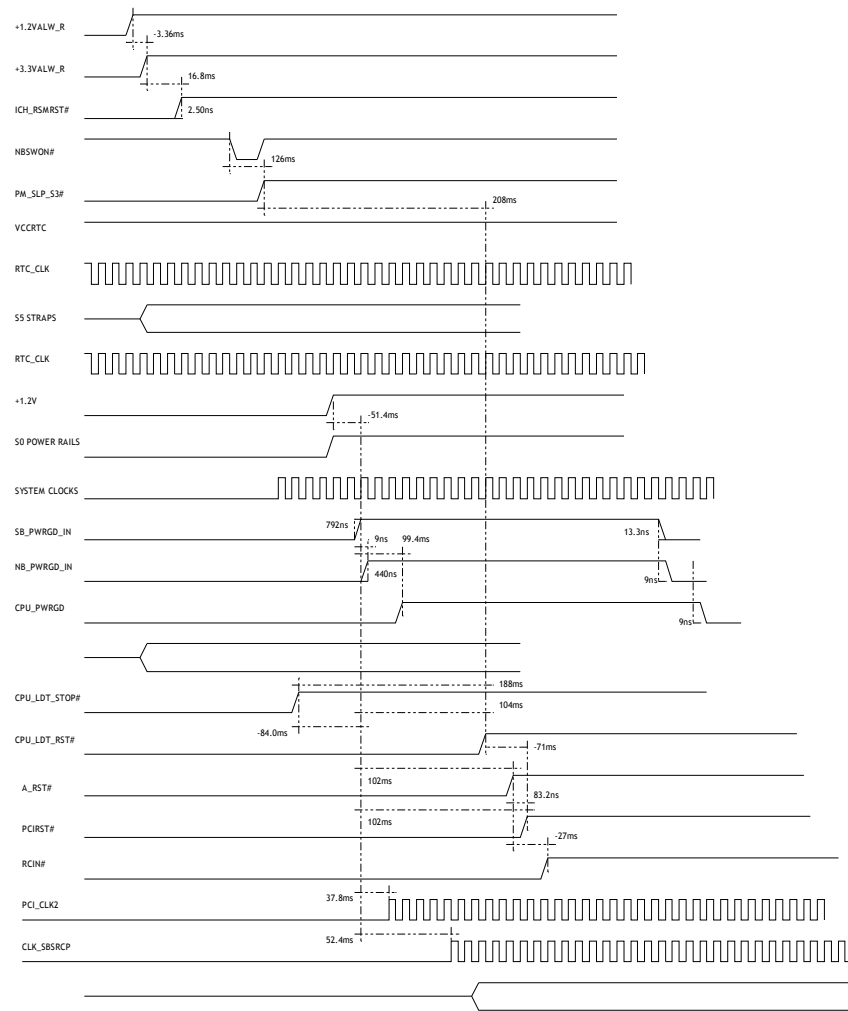


+NB_CORE
 $F_s = 300K$
 $TDC : 10A (I_{max})$
 $OCV : 13A$


PS1A SYSTEM POWER BLOCK DIAGRAM



PS1A POWER SEQUENCE



EC-20090723B	P19	AMD AE RECOMMAND	R078, R079, R080, R081, R082, R083, R084, R085 change from RES 490hm to RES 715hm.	EC-20090730A	P19	AMD AE RECOMMAND	RH127 change from 100khm to 91khm. RH129 change from 100khm to 2.7khm.	EC-20090805B	P28		SWAP CAL net for layout routing.
EC-20090723A	P28		Net connect for FUNCTION BOARD CONN / AUDIO CONN.	EC-20090730D	P20		INT. MIC CONN (CM9007) change footprint from 8626-020-2P-1 to 3800-02-2P-R	EC-20090804B	P11		RTC BATTERY CONN change footprint. (85204-0200-2p-1 -> 3800-02-2P-R)
	P19	AMD AE RECOMMAND	HDMI_DET circuit modify. DEL_Q012, Q011, R090. ADD UF (P/N: ALD01G125024)	EC-20090730D	P28, P20		Swap AUDIO wire cable connector (C9W004) pin define. Cancel NET: SPDIF (connect with TP)	EC-20090807A	P27		Delete S9011. Unnecessary component.
	P19	AMD AE RECOMMAND	Meet HDMI compliance requirements. Delete Q9006, Q9007, Q9008.	EC-20090730D	P24, P25		For layout routing. WLAN CONN change from PCIe LANE 2 to LANE 3. WWAN CONN change from PCIe LANE 3 to LANE 2.	EC-20090807B	P26		Change net name: PLTRST7 -> PLT_RST7
	P19	AMD AE RECOMMAND	Q905 pin 2 connect change from -1V to -5V.	EC-20090730D	P20, P28		Separate AGND & GND.	EC-20090801A	P39		Rds/on not enough. -NB_CORE low side MOS. PQ152 change footprint. (power_3_3a3_3-45-5p -> 50-8FL-5P)
EC-20090723B	P20		Change audio codec circuit from ALIC269 to CX2082.	EC-20090730B	P35, P32		Power rail VCC1.2 change to +1.2V. (+1.1V source)	EC-20090801A	P22		HDD CONN PH-18 resistor change from NC to STUFF. Follow P51.
EC-20090724B	P33	POC Request.	Battery CONN / PJP4 change footprint from bat-200028r007g1051-7p-1g-1 to bat-200028r007g1051-7p-1	EC-20090730B	P32		Add +1.1V discharge circuit.	EC-20090801A	P31		HOLE11 - HOLE12 - HOLE14 - HOLE15 change footprint. (H-TC1678C75D59P2 -> H-TC1678C75D51P2. HOLE11 - HOLE12 - HOLE13 change footprint. (H-TC1686C77079P2 -> H-TC1686C766D7Q2)
EC-20090724C			Change audio codec circuit. (CX2082) base on L11.	EC-20090730B	P38		NC NET: MAIND control -1.8V MDSFET.	EC-20090810C	P17		L2 mirror vertically for layout.
EC-20090724D	P19		HDMI add ESD components (P9012, U9013, U9014)	EC-20090730B	P32, P34, P38		Power rail 1.8V3US change to -1.8V3US.	EC-20090810C	P20, P28		Change AGND to GND.
	P20		Add RES 0-ohm "C" (R9120, R9121) between AGND & GND.	EC-20090731A	P33		P31 pin define reverse for layout spacing.	EC-20090810D			Modify power BDN.
EC-20090727B	P17		Change LVDS footprint same with L11.	EC-20090731A	P32		Cancel PR366 for charger circuit.	EC-20090810D	P33		Cancel NET: ADPIN.
EC-20090727C	P17		Change LVDS pin define same with P51.	EC-20090731A	P33		Change BAT connector same with LEV. (P/N:DPHD07M023). Footprint: BAT-C19966(1)-7P-L)	EC-20090810D	P26		Change REFID part number. (AK63C28000 -> AK63C28000)
EC-20090727C	P20		Change INT. MIC same with P51.	EC-20090731A	P31		Add Bluetooth SCREW HOLE.	EC-20090810D	P31		HOLE30 change footprint. (H-TC1388C236D118P2 -> H-TC13158C236D118P2)
EC-20090727C	P20		Change SATA HDD CONN B pin define same with P51.	EC-20090731A	P16		DDR2 net SWAP for layout routing.	EC-20090810E	P23		MAX14550 add bottom pad connect with GND.
EC-20090727C	P24		Change WLAN CONN B pin define same with P51.	EC-20090731A	P21		Change R445 footprint. (rj45-100731012133a112p-v -> (rj45-130451-e-12p-v)	EC-20090811C	P31		Add Track Point Hole symbol.
EC-20090727C	P28		Change WWAN CONN / SIM SOCKET B pin define same with P51.	EC-20090731A	P21		C94, C95 change NU to stuff. (R445 EM)				
EC-20090727C	P28		Change FUNCTION BOARD CONN pin define same with P51.	EC-20090731A	P23		Change M8 USB footprint. (usb-020173m04655514p-r-v -> (usb-c1078P-10A05-14p-r-v)				
EC-20090727C	P29		Change FAN CONN footprint same with P51.	EC-20090731A	P23		Modify USB SLEEP CHARGER circuit (SAME WITH MK-NOTE)				
EC-20090727C	P37		Net: COREEFB-V change net name to CPU_VDD_RUN_FB_H. Net: COREEFB change net name to CPU_VDD_RUN_FB_L.	EC-20090731A	P19		HDMI CONN / R9039, R9040, R9041, R9043, R9045, R9046 change from SHORT PAD to RES 0ohm (RC0402).				
EC-20090727C	P34		Add Net: CPU_VDDIO_FB_H & CPU_VDDIO_FB_L reserve circuit.	EC-20090731A	P20		AUDIO / R9091 RH10 change from SHORT PAD to RES 0ohm (RC0402).				
EC-20090727C	P3		Follow IOT recommend. (Q19, Q21, R16, R167) change from NU to stuff. (R8153) change from stuff to NU.	EC-20090731A	P18		CRT DDC / R9040, R9061 change from SHORT PAD to RES 0ohm (RC0603).				
EC-20090727C	P3		Follow IOT recommend. R1234 change from RES 22ohm to 33ohm for ONLY ONE PORT of CLK_48M_USB.	EC-20090731B	P32		Power name change. (VY_S5 -> -VY_S5) (VY_S5 -> -VY_S5)				
EC-20090727C	P21		Follow REALTEK recommend. R158 change from RES 10khm to RES 16khm for NET: EEC3. RTL8110D & NU.	EC-20090731B	P32, P37		Power name change. (CPU_CORE -> -VCC_CORE)				
EC-20090727C	P34		Change power net: SMDOR_VTERM to SMDOR_VTERM.	EC-20090731B	P13		SIM DETECT (net: SIM_DET) connect to SB710 (GPIO3 / A4)				
EC-20090727D	P23		Modify USB S5 charge circuit same with C67.	EC-20090731B	P28		Wire cable connector PIN-28 change net name from RF_ON1 to WLAN_OFF1.				
EC-20090728A	P38		Change +1.8V MDSFET circuit / PD not enough.	EC-20090802A	P38		Power rail 1.5V3US change to -1.8V3US.				
EC-20090728A	P33		P31 pin define reverse for layout spacing.	EC-20090802A	P26		G-SENSOR add LDD circuit for clear power.				
EC-20090728A	P21		Cancel R55, R54, R158, R56, R58, R61, R152 location. Change Power net name CTR115/VDD33 & CTR12/VDD to LANVCC. Change Power net name EVDD12 & CTR12A_X to VDD12.	EC-20090802A	P32		PQ119 change footprint. (power_3_3a3_3-45-5p -> tsq6-2_9-1)				
EC-20090728A	P20		Cancel R9120, R9121 between AGND & GND.	EC-20090802A	P32		PQ128 change footprint. (tsq6-2_9-1 -> power_3_3a3_3-45-5p)				
EC-20090728B	P34		PC102, PC103 change footprint for layout spacing not enough. Change all AGND to GND.	EC-20090802A	P32		Speaker connector pin 1 reverse.				
EC-20090728B	P20		NC NET: BIT_CLK_AUDIO change to ACZ_B_TCLK_AUDIO	EC-20090803A	P6		NET: SB_SCLK1, SB_SDATA3, CPU_S_C reserve resistor to pull high or pull low. Follow MK-NOTE.				
EC-20090728B	P35		NC NET: -1.1V_NB change to -1.1V	EC-20090803A	P21		Net SWAP for layout routing. (R445 ESD)				
EC-20090728B	P23		Cancel net name: RUSBP3 -> RUSBP3. Chaner USBP3 -> USBP3 to USBP0 / USBP0.	EC-20090803A	P24		Delete R132, R139, and NET: PCL1RST / PCL1_DEBUG connect direct with MINI_CARD.				
EC-20090728B	P34		NC NET: 1.5V3US_P change to 1.8V3US_P.	EC-20090803B	P24, P25		Separate MINI_CARD_LED pullhigh circuit.				
EC-20090728B	P32		NC NET: SMDOR_VTERM change to -SMDOR_VTERM.	EC-20090803B	P12, P24, P25, P28		Define SB710 GPIO to control RF_ON1 (RF SW), WLAN_OFF1 (WLAN enable), WWAN_OFF1 (WWAN enable).				
EC-20090728B	P32		NC NET: VCC_CORE change to CPU_CORE.	EC-20090803B	P30		NET: CAPLEDF connect with TP.				
EC-20090728B	P30		NC NET: USB_C80 change to USB_AD_SEL0. USB_C81 change to USB_AD_SEL1.	EC-20090803B	P13		NET: SATA_LED connect with TP.				
EC-20090728B	P34		NC NET: -0.9V5VMREF change to 5VMREF_GMCH.	EC-20090803B	P37		PJ15 PIN 9 add pull high with +3V. PIN 24 add pull high with +5V.				
EC-20090728B	P5		NC NET: CPU_VTT_SENSE change to VTT.	EC-20090803C	P37		Power: -VCC_CODE input (VIN) add one CAP (10u/25V, 1206).				
EC-20090728B	P8		Add HDMI net name between R8780 & CAP for const rans1 define.	EC-20090804A	P27		ESD request to add varistor on Touch Pad CLK & DATA.				
EC-20090729A	P22		SATA HDD CONN pin-18 modify (W/D connect with GND)	EC-20090804A	P12, P13		Add RES 0ohm on NET: SIM_DET, RF_ON1, WLAN_OFF1 for GPIO debug use.				
EC-20090729A	P20		NET: FILT_1.8V add RES 10khm & CAP 0.1uF. Cancel C910W (RES 16khm).	EC-20090804A	P28		Wire CONN more add one pin for 5V3US.				
EC-20090729A	P19		For leakage current. HDMI detect pin & DDC circuit modify.	EC-20090804B	P26		Add REFID circuit. (Refer G67)				
EC-20090729A	P12		Double net name. Add net USBP6 / USBP6.	EC-20090804B	P20		RH131 change footprint. (SHR07R003 -> RC0603)				
EC-20090729A	P26		G-sensor from 3-axial change to 2-axial. R9030 (NET: GSENSOR_Z) change to NU.	EC-20090804B	P18		CRT CONN change footprint. (dsub-07054680153262r-15p-v -> dsub-07054680153222r-15p-v)				
EC-20090729A	P19		HDMI CONN footprint from (hdmi-10004g0119xk48-19p-kv-v) change to (hdmi-hdmi014-k09-19p-r-v-s-1e1) (same with RL1)	EC-20090804B	P22		SATA HDD CONN change footprint. (hds-127072f022qz20r-22p-r -> sata-c166h1-12204-22p-r)				
EC-20090729A	P09		BB064 footprint from short pad (short1063) change to RES CHIP 0.1/10W -15 (0603).	EC-20090804B	P21		SWAP NET. (R445 PIN10/LAN_LED, PIN9/LEDO_ACT, PIN12/LAN_LED, PIN11/LEDD1/LAN_EEK)				
EC-20090729A	P20		Add headphone sense circuit.	EC-20090804B	P30		Change EC footprint. (LQFP128-16X16-4 -> TQFP128-16X16-4)				
EC-20090729B	P16		C337 change to NU. (G-sensor from 2-axial)	EC-20090804B	P31		Modify HOLE type.				
EC-20090729B	P2		SB710 USB port 8 add TP for reserve.	EC-20090804B	P23		USB sleep charge power plan change. (5VPCU -> +VY_S5) (3VPCU -> -3V_S5)				
EC-20090729B	P30		Cancel NET: +1.5V_PG, HWPG_2.5V, HWPG_3V5, HWPG_1.2V_S5. NET: HWPG_1.5V change to HWPG. Delete D24, D28, D21, D25.	EC-20090805A			UPDATE PART NUMBER & VALUE.				
EC-20090729B	P37		NET: CPU_COREPG change to VBM_PWROD.	EC-20090805B	P34		Change +1.5V symbol.				
EC-20090729B	P35		Power rail VCC1.2 change to +1.2V.	EC-20090805B	P34		SATA HDD CONN change same with L11. (Footprint change: sata-c166h1-12204-22p-r -> hds-127072f022qz20r-22p-r)				
EC-20090729B	P35		NET: HWPG_1.1V_NB change to HWPG_NB_1.1V.	EC-20090805C	P20	EMI RECOMMAND	NET: ACZ_B_TCLK_AUDIO add one RES 0ohm.				
EC-20090729B	P30		EC PIN 97 change to control LOGO_LED EC PIN 99 change to control DCIN_LED	EC-20090805C	P17	EMI RECOMMAND	Add CAP * 3PCS (r0.11U/10V_4) (Between VIN & GND)				
EC-20090729B	P32, P34		Power rail VCC1.5V change to -1.5V.	EC-20090805C	P31	EMI RECOMMAND	Add CAP * 2PCS (33P/50V_4) on NET: LCD_DDCCLK / LCD_DDCDAT.				
EC-20090729B	P17		Add net name: 879VCC between PC285 & PR379 to connect with PR463.	EC-20090805C	P28, P30	EMI RECOMMAND	Add COIL Check * 3PCS (FLW21H9905Q2L) for USB pair.				
EC-20090729B	P19	AMD AE RECOMMAND	SMBUS Q909 & Q910 change form (2H70206-T1-E3) to (TRAW MOSFET FDW301N_NL(25V_200mA))	EC-20090806A	P09		Change NET: GSENSOR_ON1 -> GSENSOR_ON.				
				EC-20090806A	P11		NET: LCD_DDCCLK / LCD_DDCDAT add pull high circuit. (add RES 4.7khm with +3V) Follow MK-NOTE.				
				EC-20090806A	P23		1. HOLE18 change to H-C234098P2 2. HOLE20 change to H-TC1388C336D118P2 3. HOLE24 change to H-TC1978C154D118P2				
				EC-20090806A	P27		Track Point CONN add BLM158D12151_479x5.				



PS-Note (AMD)

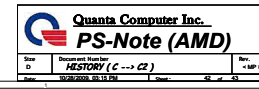
 Date: 5/26/2008 09:15:58 Rev: 43 / 53

EC-2009017B-1	P37, P26, P18	Change part number to match BOM. -> PC18, PC42, PC108 (PH: C18100A0291 / CAP CHIP 10U 22V1-22K,33K,100K) -> PH: C18100A0291 / CAP CHIP 10U 22V1-22K,33K,100K) -> D12, D13 (PH: BC8000D214 / DIODE CH5001 40PT145V0.1A,9V,45C,76) -> PH: BC8000D214 / DIODE SMD R850V 4040V,0.1A,5C,0.042T1) -> Q8, Q8 (PH: BAW7002002 / TRANS MOSFET 2N7002P-T1 E360V50T23) -> PH: BAW7002002 / TRANS MOSFET 2N7002P-T1 E360V50T23)	EC-20091013A-1	P20	Modify Audio circuit for COMBO Jack. (Headphone & Ext. MIC) -1- Delete HP & MIC sense circuit. Q31, Q32 (PH: BAW7002002 / TRANSISTOR MOSFET 2N7002E60V,0.25A) R370, R371 (PH: C314702J821 / RES CHIP 47K 1/16W -5% (0402)) -2- Delete MIC VREF circuit. D16, D17 (PH: C18144E200 / DIODE SMD 1N4148W57V,200MA) R187, R188 (PH: C322202F808 / RES CHIP 2.2K 1/16W -5% (0402)) -3- Modify COMBO JACK circuit. (Audio Connector) Change CH10 pin-7 net name from EXT_HP_SLP103 to SENSE_HP. Change CH10 pin-2 net name from EXT_MIC_L_1 to EXT_MIC_L_2. Cancel CH10 pin-4 / EXT_MIC_PLUG pin-5 / EXT_MIC_L_2 -4- Create COMBO JACK circuit. (Sensor Circuit) Add Q33, Q37 (PH: BAW7002002 / TRANS MOSFET 2N7002P-T1 E360V50T23) Add R808, R410 (PH: C32202J828 / RES CHIP 22K 1/16W -5% (0402)) -> PH: C32202J828 / RES CHIP 22K 1/16W -5% (0402)) Add C483 (PH: C1H1002B030 / CAP CHIP 0.1U 10V1-105,33K,0402) Add R409 (PH: C31002F824 / RES CHIP 10K 1/16W -5% (0402)) Add C484 (PH: C1H201K1804 / CAP CHIP 1000PF 16V +-10%,37K,0402) Add C394 (PH: BAW7002002 / TRANSISTOR SMD P0TC144E1 (50V,30MA)) Add R407, R411 (PH: C341002F828 / RES CHIP 100K 1/16W -5% (0402)) Add C319 (PH: BAW7002002 / TRANS MOSFET ME3207:30V,2.5A,50D721) Add C318 (PH: BAW7002002 / TRANSISTOR SMD P0TC144E1 (50V,30MA)) Add R412 (PH: C311002J832 / RES CHIP 100 1/16W -5% (0402)) Add R413 (PH: C32202J818 / RES CHIP 2.2K 1/16W -5% (0402)) Add R49, R20 (PH: C3181002B10 / FILTER CHIP B610B8501-T200MA,600) Add C485, C486 (PH: C1H1002B996 / CAP CHIP 100P 16V +-10%,37K,0603) Add C487, C488 (PH: C1H1002B030 / CAP CHIP 100P 30V +-5%,30P,0402) Add R414 (PH: C34702J815 / RES CHIP 47K 1/16W -5% (0402)) Add R414 (PH: C34702J818 / RES CHIP 47 1/16W -5% (0402))
EC-20091005A-1	P32	Change part number to match C1 stage BOM. -> PR23, R51, PR59, PR81, PR89, PR101, PR135, PR158 (PH: C1J47090003 / THERMISTOR 470-5000603/TM1547/PI059) -> PH: C1J47090003 / THERMISTOR 470-5000603/TM1547/PI059) -> PH: C1J47090002 / THERMISTOR 470-5000603/TM1547/PI059)	EC-20091005A-2	P6, P8, P9, P11, P13, P14	Change part number to match C1 stage BOM. -> L4, L5, L13, L14, L15, L16, L26, L28, L27, L29, L30, L31, L32, L33, L34, L42, L44, L45, L46, L47 (PH: C3221T0001 / EMF FILTER RES1608F 21732022,200MA) -> PH: C3221T0001 / EMF FILTER RES1608F 21732022,200MA) -> PH: C3221T0001 / EMF FILTER RES1608F 21732022,200MA)
EC-20091005A-3	P20	Change part number to match C1 stage BOM. -> L37, L48 (PH: CX097131028 / EMF FILTER RBY201209T 1317 N130,2.5A) -> PH: CX097131027 / EMF FILTER RBY201209T 1317 N130,2.5A)	EC-20091005A-4	P4	Change part number to match C1 stage BOM. -> L2 (PH: C300740000 / EMF FILTER CHIP HC60212F 30074510,3.5A) -> PH: C300740000 / EMF FILTER CHIP HC60212F 30074510,3.5A) -> PH: C3600730010 / EMF FILTER CHIP HC60212F 60073010,3.0A)
EC-20091005A-5	P17	Change part number to match C1 stage BOM. -> Q38 (PH: BAW7002002 / TRANSISTOR SMD P0TC144E1 (50V,30MA)) -> PH: BAW7002002 / TRANSISTOR SMD P0TC144E1 (50V,30MA)) -> PH: BAW7002002 / TRANSISTOR SMD P0TC144E1 (50V,30MA))	EC-20091014A-1	P20	-1- BT_CLK should have a series-terminating resistor at controller side. Add R416 (PH: C30002J838 / RESISTOR CHIP 0 1/16W -5% (0402)) -2- Reserve for EMF, ISO, bus issue. Net: AC2_RSTP_AUDIO add C489 (NU). Net: AC2_S00UF_AUDIO add C490 (NU). -3- Change external bias power source from 3V_DVDD to AUDV_3V.
EC-20091005A-6	P12, P14, P15	Change part number to match C1 stage BOM. -> D11, D14, D15 (PH: BC8000D214 / DIODE SMD R850V 4040V,0.1A) -> PH: BC8000D214 / DIODE SMD R850V 4040V,0.1A) -> PH: BC8000D214 / DIODE SMD R850V 4040V,0.1A)	EC-20091015A-1	P20	For EMU request. -1- Add C491 on 3V_DVDD, and close the audio I/O connector CH10. (PH: C1H1002B33 / CAP CHIP 0.1U 10V1-105,33K,0402)
EC-20091005A-7	P30	Change part number to match C1 stage BOM. -> D23 (PH: BC8000D214 / DIODE SMD R850V 4040V,0.1A) -> PH: BC8000D214 / DIODE SMD R850V 4040V,0.1A) -> PH: BC8000D214 / DIODE SMD R850V 4040V,0.1A)	EC-20091015A-2	P12, P17	For EMU request. -1- P4P1, P4P2 change footprint & part number. Footprint: SHW07055 - RC8B05. Part number: NU -> PH: C1T2180000 / EMF FILTER CHIP UPB21212 800Y-NB0,5A)
EC-20091005A-8	P23	Change MB USB connector's part number for final part number. -> C17 (PH: DFHD04M153 / CONN DIP USB 4P 1R MB/P2,HS,12) -> PH: DFHD04M152 / CONN DIP USB 4P 1R MB/P2,HS,12)	EC-20091015A-3	P17	For CCD power change. -CAM_VCC source change from -3V to -5V.
EC-20091005A-9	P27	Change MB connector part number same with P51. -> C45 (PH: DFCC30P241 / CONN SMD PFC 30P 1R 1R (P1,D,H),5) -> PH: DFCC30P242 / CONN SMD PFC 30P 1R 1R (P1,D,H),5)	EC-20091015A-4	P31	For manufacture request to modify the bluetooth NUT type B footprint. HDL11 change footprint. (H1TE197BC154D118P2 -> H1TC238AC154D118P2)
EC-20091006A-1	P6	Follow MK-NOTE circuit. (base on BOM). -> C34 change from NU to stuff. (PH: C1H1002B996 / CAP CHIP 10U 16V1-105,33K,0603) -> R00, R238, R240, R241, R252, R258 change from NU to stuff. (PH: C313002J820 / RES CHIP 300 1/16W -5% (0402)) -> R37 change from NU to stuff. (PH: C321002J834 / RES CHIP 1K 1/16W 5% (0402))	EC-20091015A-5	P20	For EMU request. -1- C119 change from NU to stuff. (PH: C1H1002B08 / CAP CHIP 10P 50V1-55,COG,0402)
EC-20091006A-2	P14	Follow MK-NOTE circuit. (base on BOM). -> C185 change from NU to stuff. (PH: C1H102K1803 / CAP CHIP 0.1U 10V1-105,37K,0402)	EC-20091015A-6	P37	For EMU request. -1- PR48 change from NU to stuff. (PH: C32204K137 / RES CHIP 2.2 1/16W -5% (0805)) -> PC46 change from NU to stuff. (PH: C1E1506M14 / CAP CHIP 1500PF 50V +-10%,X7R,0402)
EC-20091007A-1	P6	-1- Cancel NET_SMBALERT to connect with FAH control IC VEN. -2- Add R406 (NU) (PH: C30002J838 / RESISTOR CHIP 0 1/16W -5% (0402)) between CPU_PROCHOT_L4 & SMBALERT. -3- Connect NET_CPU_PROCHOT_L4 from R406 to U24-AN6.	EC-20091016A-1	P31	Add symbol of GROUND SHAPE. (PAD1, PAD2, PAD3, PAD4, PAD5, PAD6, PAD7, PAD8, PAD9) Add symbol of optics point. (PAD13, PAD14)
EC-20091007A-2	P6	-1- Delete R43 (NU / RES Ohm) location. -2- C26 change part number. (PH: CH2204R816 / CAP CHIP 2200P 50V(-10%,X7R,0402)) -> PH: CH2100B1810 / CAP CHIP 1000P 50V(-5%,X7R,0402))	EC-20091016A-2	P31	For USB charger IC will upgrade the chipset version. -1- Add R417 between USBP0 / BUSBP0 - (PH: C30002J838 / RESISTOR CHIP 0 1/16W -5% (0402)) -2- Add R418 between USBP0 / BUSBP0 - (PH: C30002J838 / RESISTOR CHIP 0 1/16W -5% (0402))
EC-20091007A-3	P6	-1- Q3 PH-1 & PH-3 swap. -2- C26 change part number. (PH: CH2204R816 / CAP CHIP 2200P 50V(-10%,X7R,0402)) -> PH: CH2100B1810 / CAP CHIP 1000P 50V(-5%,X7R,0402))	EC-20091016A-3	P31	For USB charger IC will upgrade the chipset version. -1- Add R417 between USBP0 / BUSBP0 - (PH: C30002J838 / RESISTOR CHIP 0 1/16W -5% (0402)) -2- Add R418 between USBP0 / BUSBP0 - (PH: C30002J838 / RESISTOR CHIP 0 1/16W -5% (0402))
EC-20091007A-4	P23	-1- Q3 PH-1 & PH-3 swap. -2- C26 change part number. (PH: CH2204R816 / CAP CHIP 2200P 50V(-10%,X7R,0402)) -> PH: CH2100B1810 / CAP CHIP 1000P 50V(-5%,X7R,0402))	EC-20091016A-4	P31	For USB charger IC will upgrade the chipset version. -1- Add R417 between USBP0 / BUSBP0 - (PH: C30002J838 / RESISTOR CHIP 0 1/16W -5% (0402)) -2- Add R418 between USBP0 / BUSBP0 - (PH: C30002J838 / RESISTOR CHIP 0 1/16W -5% (0402))
EC-20091007A-5	P27	-1- Delete F3 (Trace Point Power Fuse) location. (PH: DK300VU000 / FUSE SMD 3A,24V(AJT,LJL,CSA,0603)) -2- Delete Q29 location. (NU) (PH: BAW7002002 / TRANS MOSFET ME3207:30V,2.5A,50D721) -3- Delete NET_SMBALERT & CPU_FAN_ON. -4- Change R48 part number. (PH: C311002J828 / RES CHIP 10K 1/16W 5% (0402)) -> PH: C341002F828 / RES CHIP 100K 1/16W -5% (0402)) -5- Add C481 (PH: C1H102K1803 / CAP CHIP 0.1U 10V1-105,37K,0402) between U23 pin-1 & R248. -6- Add C482 (PH: C1H1002B996 / CAP CHIP 10U 16V1-105,33K,0603) between -VB & GND.	EC-20091016A-5	P19	For HDMI test item 7-13 failed issue. -1- R112, R114 change part number. (PH: C32402J817 / RES CHIP 6.8K 1/16W -5% (0402)) -> PH: C322002R817 / RES CHIP 2K 1/16W -5% (0402)) -2- R111, R113 change part number. (PH: C32402J818 / RES CHIP 4.7K 1/16W -5% (0402)) -> PH: C322002R818 / RES CHIP 2K 1/16W -5% (0402))
EC-20091008A-1	P4 - P7	Change CPU (L24) footprint (bga812 and 4584 -> bga812 and 4584-1)	EC-20091019A-3	P23	For USB charger IC upgrade version from MAX14550ETB to MAX14550A. Stuff the external resistors for iPod support. -1- R306 change from NU to stuff. (NU -> PH: C34302F809 / RES CHIP 43K 1/16W -5% (0402)) -2- R330 change from NU to stuff. (NU -> PH: C33702R12 / RES CHIP 75K 1/16W -5% (0402)) -3- R307 change from NU to stuff. (PH: C33492F810 / RES CHIP 49.9K 1/16W -5% (0402)) -4- R331 change part number. (PH: C30002J838 / RESISTOR CHIP 0 1/16W -5% (0402)) -> PH: C34492F810 / RES CHIP 49.9K 1/16W -5% (0402)) -5- R417, R418 change stuff to NU. (PH: C30002J838 / RESISTOR CHIP 0 1/16W -5% (0402)) -> NU)

MB ID Selection Table

BOARD_ID	BOARD_ID0	BOARD_ID1	BOARD_ID2	BOARD_ID3
Turion Neo X2 L625	H	H		
Athlon Neo MV-40	L	H		
Athlon Neo X2 L325	L	L		
WIDE-PORT MEMORY			H	H
WIDE-PORT MEMORY			L	H

CPU TYPE	MB PIN	BOARD_ID0	BOARD_ID1	BOARD_ID2	BOARD_ID3
Turion Neo X2 L625	31PS1MB0030	H (R146 -> STUFF, R147 -> NU)	H (R148 -> STUFF, R145 -> NU)	H (R138 -> STUFF, R139 -> NU)	H (R167 -> STUFF, R168 -> NU)
Athlon Neo MV-40	31PS1MB0050	L (R146 -> STUFF, R147 -> STUFF)	H (R148 -> STUFF, R145 -> NU)	H (R138 -> STUFF, R139 -> NU)	H (R138 -> STUFF, R139 -> NU)
Athlon Neo X2 L325	31PS1MB0040	L (R146 -> NU, R147 -> STUFF)	L (R148 -> NU, R145 -> STUFF)	H (R138 -> STUFF, R139 -> NU)	H (R138 -> STUFF, R139 -> NU)



EC-20091022A-1	P3, P17	<p>For RF solution.</p> <ul style="list-style-type: none"> -1- C488 change from NU to stuff. (SEL_SATA) (P#: CH10106A808 / CAP CHIP 10P 50V(+5%,C0G,0402)) -2- C333 change from NU to stuff. (LCO_DDCCLK) (P#: CH10106A808 / CAP CHIP 10P 50V(+5%,C0G,0402)) -3- R381 change part number. (EXT_SL_DSC) (P#: C503102A829 / RES CHIP 33 1/16W -5% (0402)) → P#: CA057121001 / EMF FILTER HQE1005051-121Y-N(120,300MA) -4- R374 change part number. (CLK_48M_USB) (P#: C503102A829 / RES CHIP 33 1/16W -5% (0402)) → P#: CA057121001 / EMF FILTER HQE1005051-121Y-N(120,300MA)
EC-20091022A-2	P21	<p>For LED brightness.</p> <ul style="list-style-type: none"> -1- R142, R179 change the resistor value. (P#: C511502A822 / RES CHIP 150 1/16W -5% (0402)) → P#: C513302A821 / RES CHIP 330 1/16W -5% (0402))
EC-20091023A-1	P23	<p>USB port charger function. Charger IC upgrade function B version.</p> <ul style="list-style-type: none"> -1- U33 change part number. (P#: ALD1455000 / I C OTHER(10P) MAX14550EETB- 10V(TDFN)) → P#: ALD1455000 / I C OTHER(10P) MAX14550AEEETB- (TDFN))
EC-20091028A-1	P11	<p>RTC battery change from W / chargeable to W / O chargeable. So remove the RTC charger circuit & add one diode to prevent charger for RTC battery.</p> <ul style="list-style-type: none"> -1- Delete Q30. (P#: BAC39040047 / TRANS SMD MHMT790LTI(G40V,200MA)) -2- Delete R352, R357. (P#: C326802B19 / RES CHIP 28 1/16W -1% (0402)) -3- Delete R356. (P#: C526802B11 / RES CHIP 6.8K 1/16W -1% (0402)) -4- Delete R351. (P#: C531503P939 / RESISTOR CHIP 18K 1/10W -1% (0603)) -5- Add D33. (P#: BC28000V229 / DIODE SMD RES00V-40V4V(1A,5C40T70V)) (CHECK TOP or BOTT)
EC-20091028A-2	P23	<p>MB USB power switch (U16) change power source from 5VPCU to +5V_S5. (+5V_S5 will control turn ON or OFF by BIOS/EC CMOS setup for USB charger function selection.)</p>
EC-20091028A-3	P11, P03, P27, P29, P14, P03, P13, P14, P13.	<p>For RF solution. (C2 ALREADY IMPROVE)</p> <ul style="list-style-type: none"> -1- C480 change from NU to stuff. (P#: CH10106A808 / CAP CHIP 10P 50V(+5%,C0G,0402)) -2- C467 change from NU to stuff. (P#: CH10106A808 / CAP CHIP 10P 50V(+5%,C0G,0402)) -3- C23 change from NU to stuff. (P#: CH10106A808 / CAP CHIP 10P 50V(+5%,C0G,0402)) -4- C128 change from stuff to NU. (P#: CH10106A808 / CAP CHIP 10P 50V(+5%,C0G,0402)) -5- C214 change from NU to stuff. (P#: CH4102K803 / CAP CHIP 0.1U 10V(+10%,X7R,0402)) -6- C419 change from NU to stuff. (P#: CH4102K803 / CAP CHIP 0.1U 10V(+10%,X7R,0402)) -7- C299, C330, C327 change part number. (P#: CH4103Z833 / CAP CHIP 0.1U 16V(+80%,-20%,Y5V,0402)) → P#: CH22206816 / CAP CHIP 2200P 50V(+10%,X7R,0402)) -8- C191, C202, C252 change from NU to stuff. (P#: CH22206816 / CAP CHIP 2200P 50V(+10%,X7R,0402)) -9- C262 change from NU to stuff. (P#: CH22206817 / CAP CHIP 2200P 50V(+10%,X7R,0402))