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FCB

SE000008600

Compal Confidential

Everest Schematics Document

Intel Merom Processor with Crestline + DDRII + ICH8M

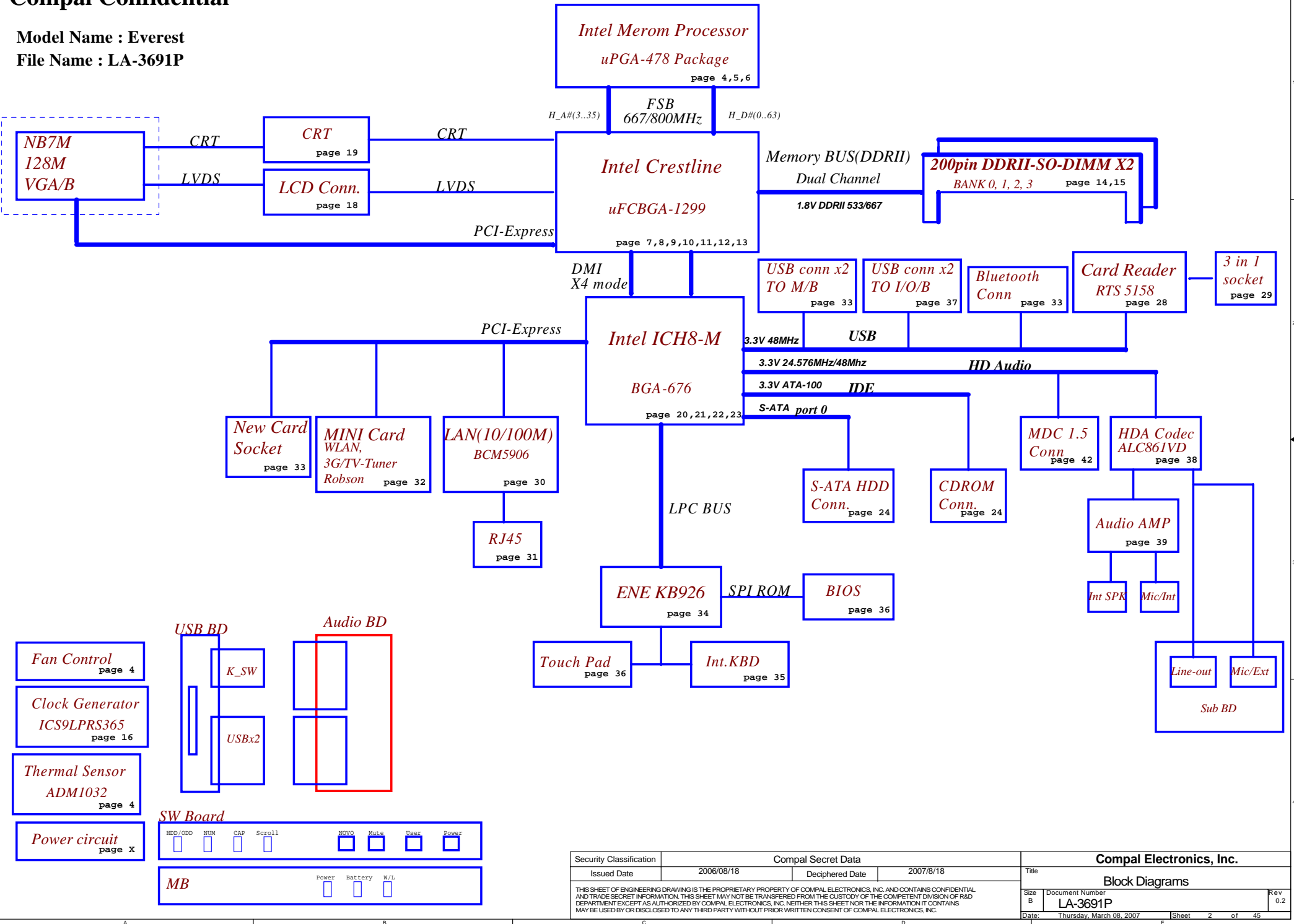
2007-03-05

REV: 0.2

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Model Name : Everest
File Name : LA-3691P



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				Block Diagrams	
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Voltage Rails

Power Plane	Description	S1	S3	S5
VIN	Adapter power supply (19V)	NA	NA	NA
B+	AC or battery power rail for power circuit.	NA	NA	NA
+CPU_CORE	Core voltage for CPU	ON	OFF	OFF
+0.9VS	0.9V switched power rail for DDR terminator	ON	OFF	OFF
+1.05VS	1.05V switched power rail	ON	OFF	OFF
+1.25VS	1.25V switched power rail	ON	OFF	OFF
+1.5VS	1.5V switched power rail	ON	OFF	OFF
+1.8V	1.8V power rail for DDR	ON	ON	OFF
+1.8VS	1.8V switched power rail	ON	OFF	OFF
+2.5VS	2.5V switched power rail	ON	OFF	OFF
+3VALW	3.3V always on power rail	ON	ON	ON*
+3VS	3.3V switched power rail	ON	OFF	OFF
+5VALW	5V always on power rail	ON	ON	ON*
+5VS	5V switched power rail	ON	OFF	OFF
+VSB	VSB always on power rail	ON	ON	ON*
+RTCVCC	RTC power	ON	ON	ON

Note : ON* means that this power plane is ON only with AC power available, otherwise it is OFF.

External PCI Devices

DEVICE	IDSEL #	REQ/GNT #	PIRQ
--------	---------	-----------	------

No PCI Device

STATE	SIGNAL								
		SLP_S1#	SLP_S3#	SLP_S4#	SLP_S5#	+VALW	+V	+VS	Clock
Full ON	HIGH	HIGH	HIGH	HIGH	HIGH	ON	ON	ON	ON
S1 (Power On Suspend)	LOW	HIGH	HIGH	HIGH	HIGH	ON	ON	ON	LOW
S3 (Suspend to RAM)	LOW	LOW	HIGH	HIGH	HIGH	ON	ON	OFF	OFF
S4 (Suspend to Disk)	LOW	LOW	LOW	HIGH	HIGH	ON	OFF	OFF	OFF
S5 (Soft OFF)	LOW	LOW	LOW	LOW	LOW	ON	OFF	OFF	OFF

EC SM Bus1 address

EC SM Bus2 address

Device	Address	Device	Address
Smart Battery	0001 011X b	GMT-781	1001 100X b
EEPROM(24C16/02)	1010 000X b	NVIDIA NB8X	

ICH8M SM Bus address

Device	Address
Clock Generator (ICS9LPRS325AKLFT_MLF72)	1101 001Xb
DDR DIMM0	1010 000Xb
DDR DIMM1	1010 010Xb

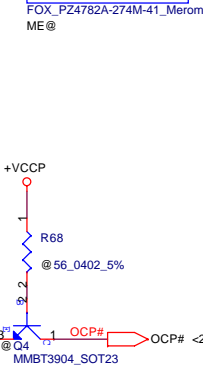
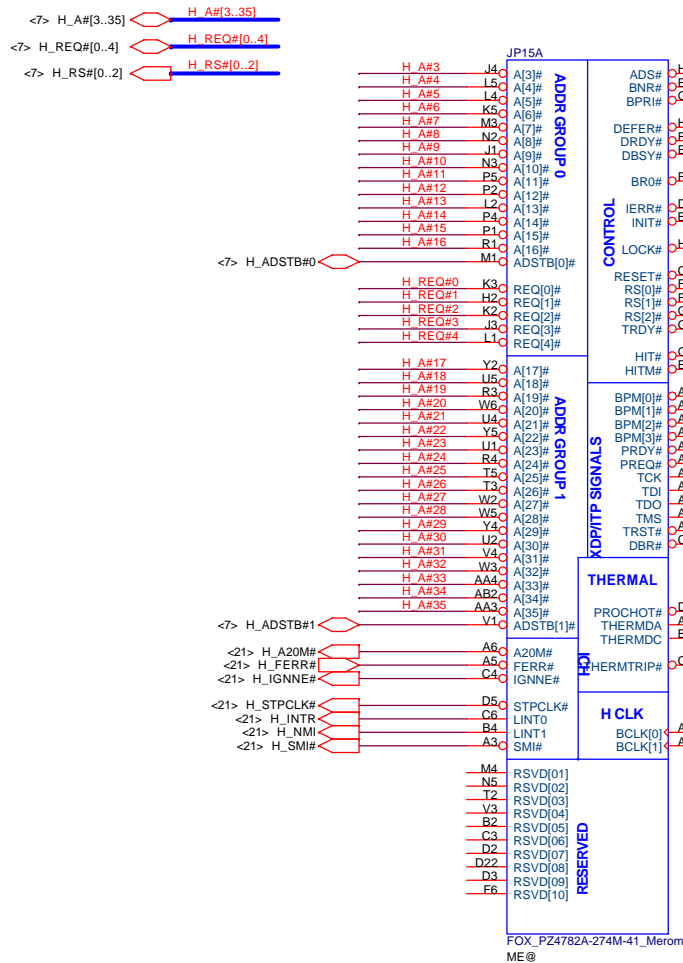
BOARD ID Table

ID1	ID0	TEST
0(R744)	0(R745)	A-TEST
0(R744)	1(R742)	B-TEST
1(R741)	0(R745)	C-TEST

PANEL ID Table

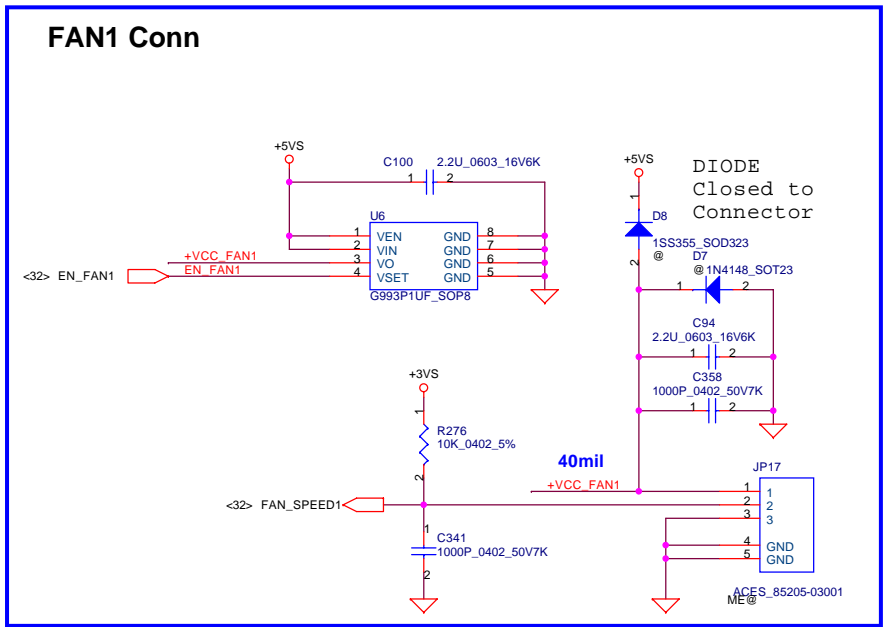
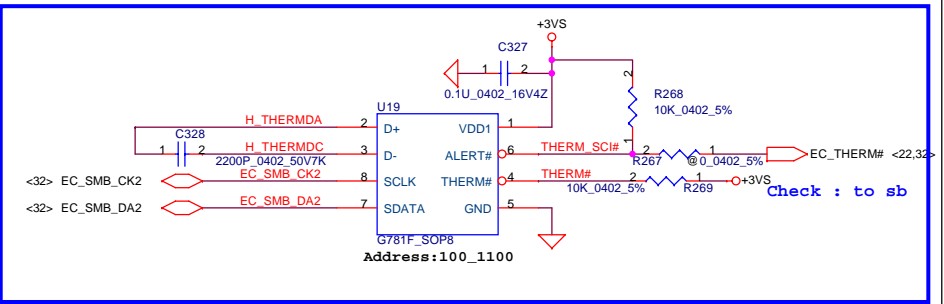
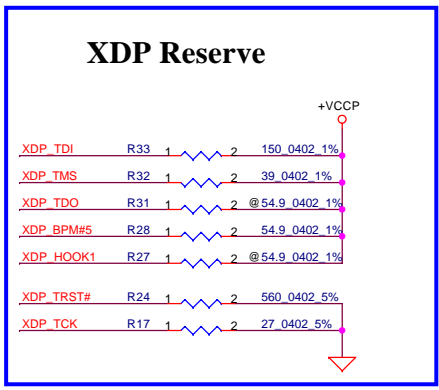
R	Size
Ra (R743)	15W
Rb (R740)	14W

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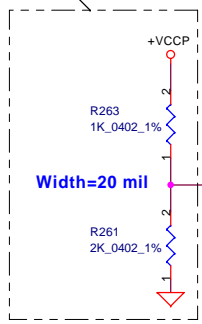
**H_THERMDA, H_THERMDC routing together,
Trace width / Spacing = 10 / 10 mil**

Place close to CPU within 500mil



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Close to CPU pin AD26 within 500mils.



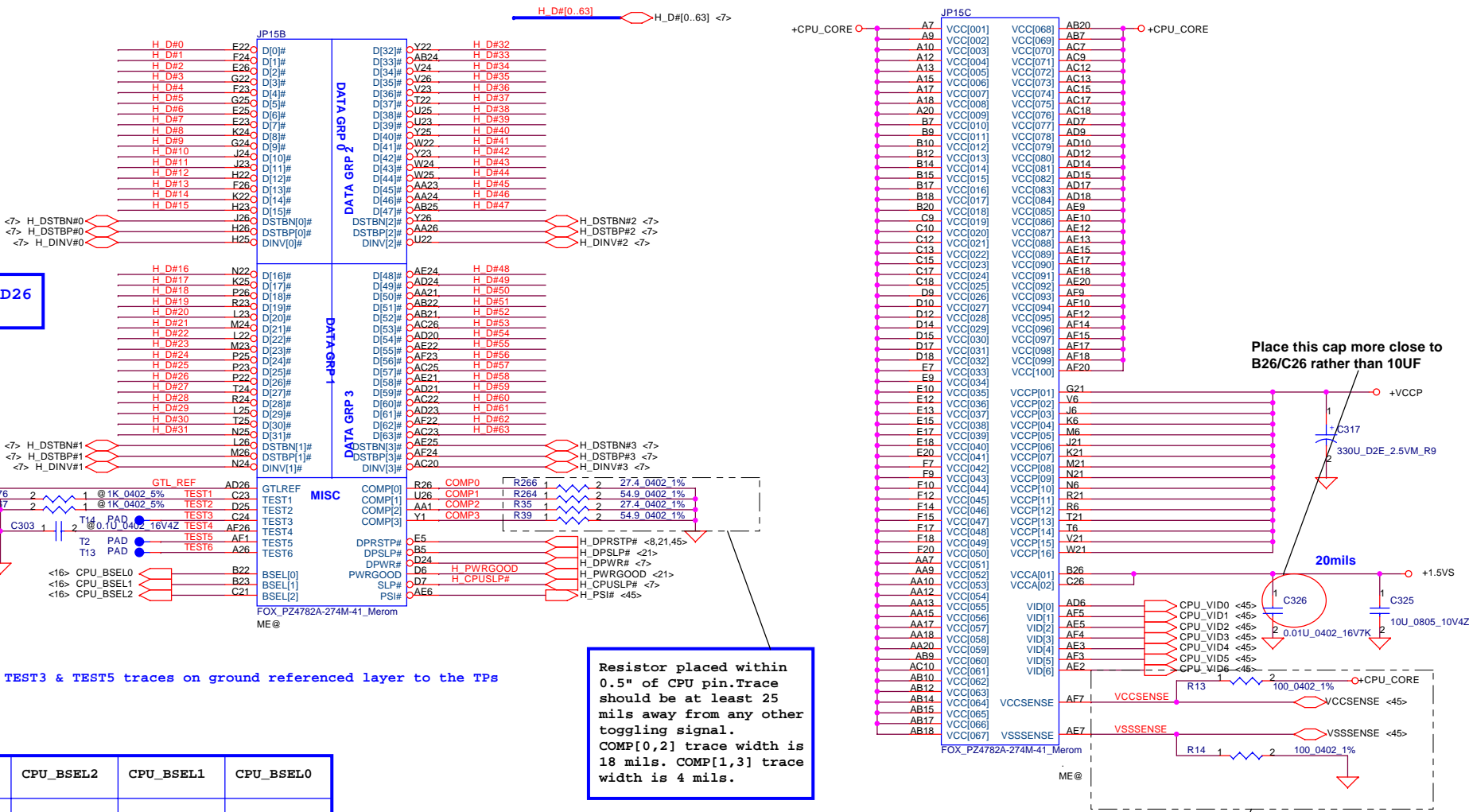
layout note: Route TEST3 & TEST5 traces on ground referenced layer to the TPs

CPU_BSEL	CPU_BSEL2	CPU_BSEL1	CPU_BSEL0
166	0	1	1
200	0	1	0

Resistor placed within 0.5" of CPU pin. Trace should be at least 25 mils away from any other toggling signal. COMP[0,2] trace width is 18 mils. COMP[1,3] trace width is 4 mils.

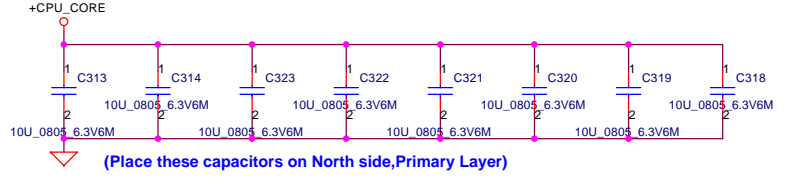
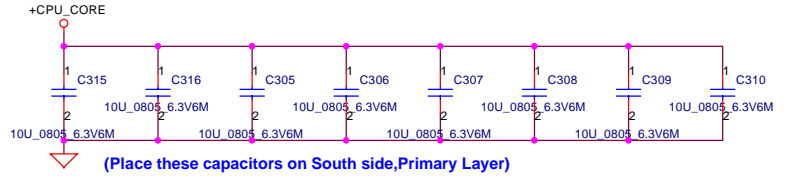
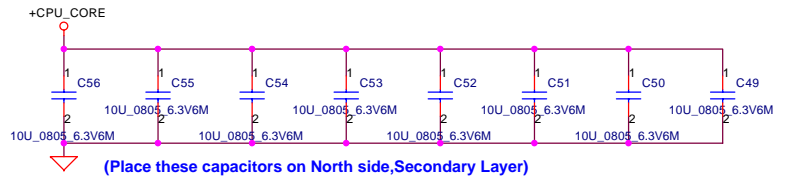
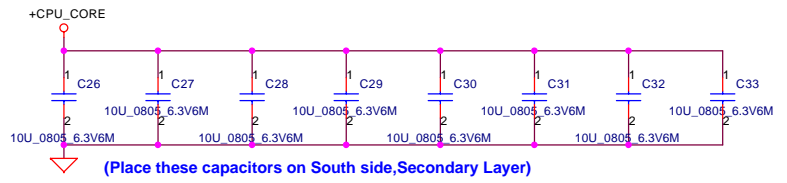
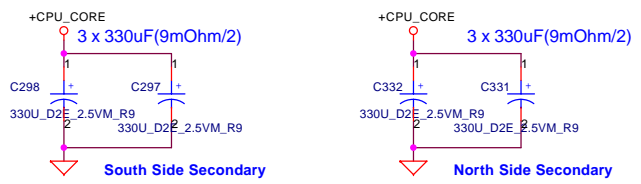
Length match within 25 mils. The trace width/space/other is 20/7/25.

Close to CPU pin within 500mils.

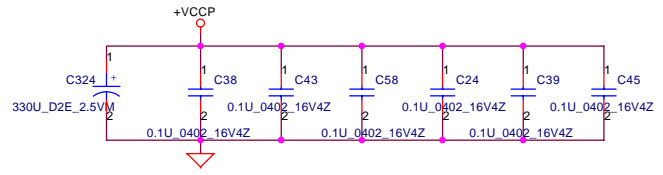


JP15D		
A4	VSS[001]	D6
A8	VSS[002]	P21
A11	VSS[003]	P24
A14	VSS[004]	R2
A16	VSS[005]	R5
A19	VSS[006]	R22
A23	VSS[007]	R25
AF2	VSS[008]	T1
B6	VSS[009]	T4
B8	VSS[010]	T23
B11	VSS[011]	T26
B13	VSS[012]	U3
B16	VSS[013]	U6
B19	VSS[014]	U21
B21	VSS[015]	U24
B24	VSS[016]	V2
C5	VSS[017]	V5
C8	VSS[018]	V22
C11	VSS[019]	V25
C14	VSS[020]	W1
C16	VSS[021]	W4
C19	VSS[022]	W23
C2	VSS[023]	W26
C22	VSS[024]	Y3
C25	VSS[025]	Y6
D1	VSS[026]	Y21
D4	VSS[027]	Y24
D8	VSS[028]	AA2
D11	VSS[029]	AA5
D13	VSS[030]	AA8
D16	VSS[031]	AA11
D19	VSS[032]	AA14
D23	VSS[033]	AA16
D26	VSS[034]	AA19
E3	VSS[035]	AA22
E6	VSS[036]	AA25
E8	VSS[037]	AB1
E11	VSS[038]	AB4
E14	VSS[039]	AB8
E16	VSS[040]	AB11
E19	VSS[041]	AB13
E21	VSS[042]	AB16
E24	VSS[043]	AB19
F5	VSS[044]	AB23
F8	VSS[045]	AB26
F11	VSS[046]	AC3
F13	VSS[047]	AC6
F16	VSS[048]	AC8
F19	VSS[049]	AC11
F2	VSS[050]	AC14
F22	VSS[051]	AC16
F25	VSS[052]	AC19
G4	VSS[053]	AC21
G1	VSS[054]	AC24
G23	VSS[055]	AD2
G26	VSS[056]	AD5
H3	VSS[057]	AD8
H6	VSS[058]	AD11
H21	VSS[059]	AD13
H24	VSS[060]	AD16
J2	VSS[061]	AD19
J5	VSS[062]	AD22
J22	VSS[063]	AD25
J25	VSS[064]	AE1
K1	VSS[065]	AE4
K4	VSS[066]	AE8
K23	VSS[067]	AE11
K26	VSS[068]	AE14
L3	VSS[069]	AE16
L6	VSS[070]	AE19
L21	VSS[071]	AE23
L24	VSS[072]	AE26
M2	VSS[073]	A2
M5	VSS[074]	A5
M22	VSS[075]	A8
M25	VSS[076]	AF6
N1	VSS[077]	AF8
N4	VSS[078]	AF11
N23	VSS[079]	AF13
N26	VSS[080]	AF16
P3	VSS[081]	AF19
		AF21
		A25
		AF25

FOX_PZ4782A-274M-41_Merom
ME@



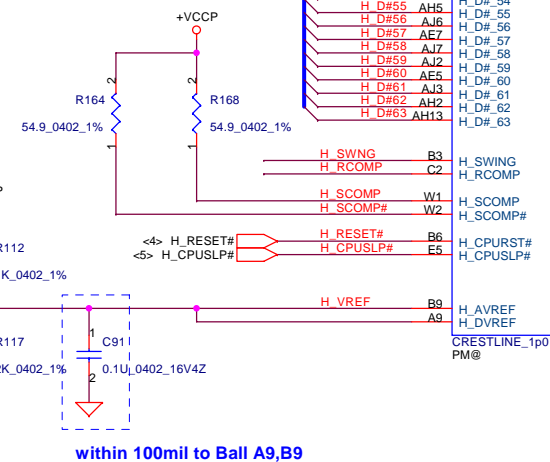
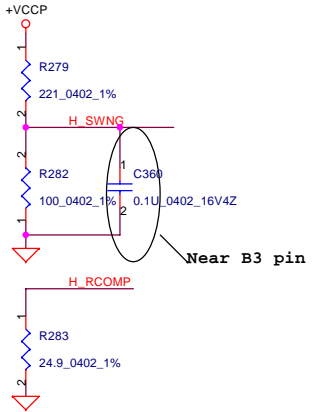
+CPU-CORE Decoupling	C, uF	ESR, mohm	ESL, nH
SPCAP, Polymer	6X330uF	9m ohm/6	1.8nH/6
MLCC 0805 X5R	32X22uF	3m ohm/32	0.6nH/32
	32X10uF	3m ohm/32	0.6nH/32



9/25 10U checked. OK for use!

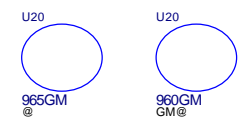
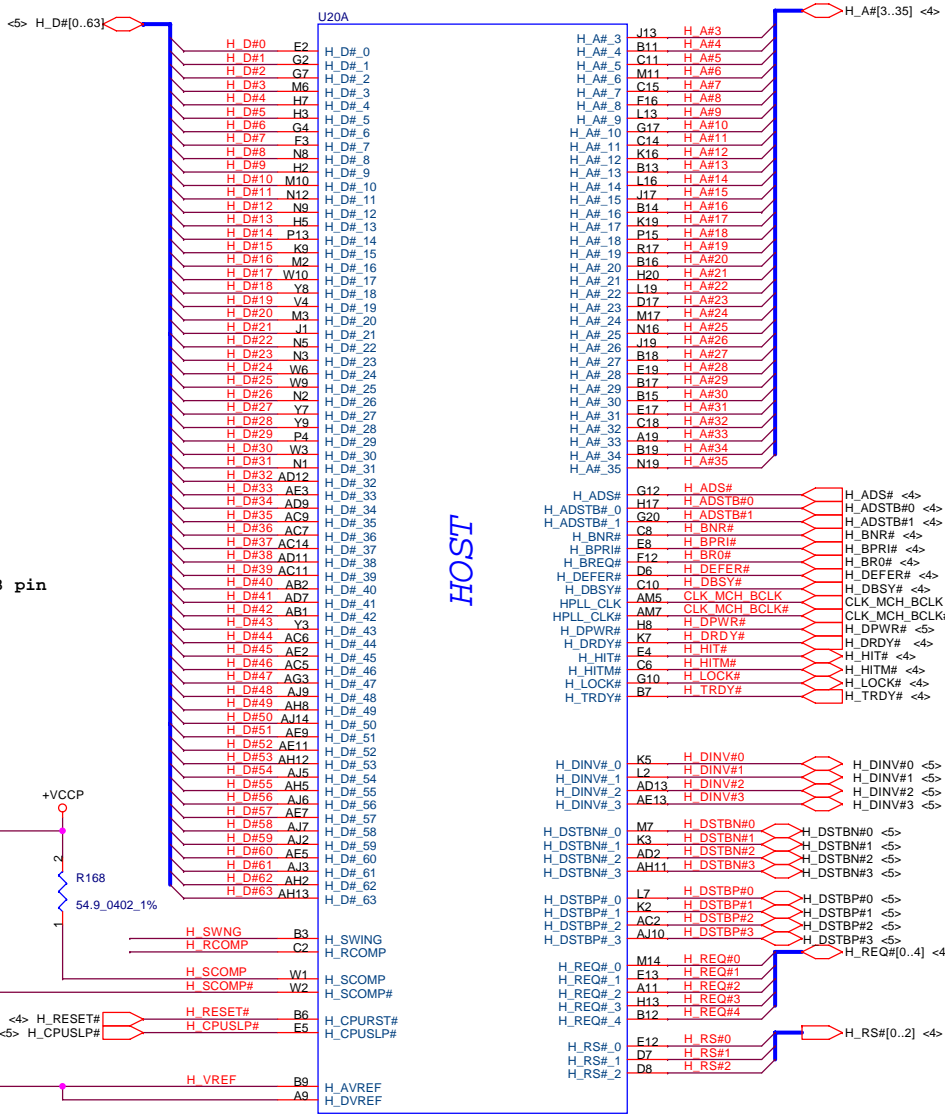
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layout note:
Route H_SCOMP and H_SCOMP# with trace width, spacing and impedance (55 ohm) same as FSB data traces

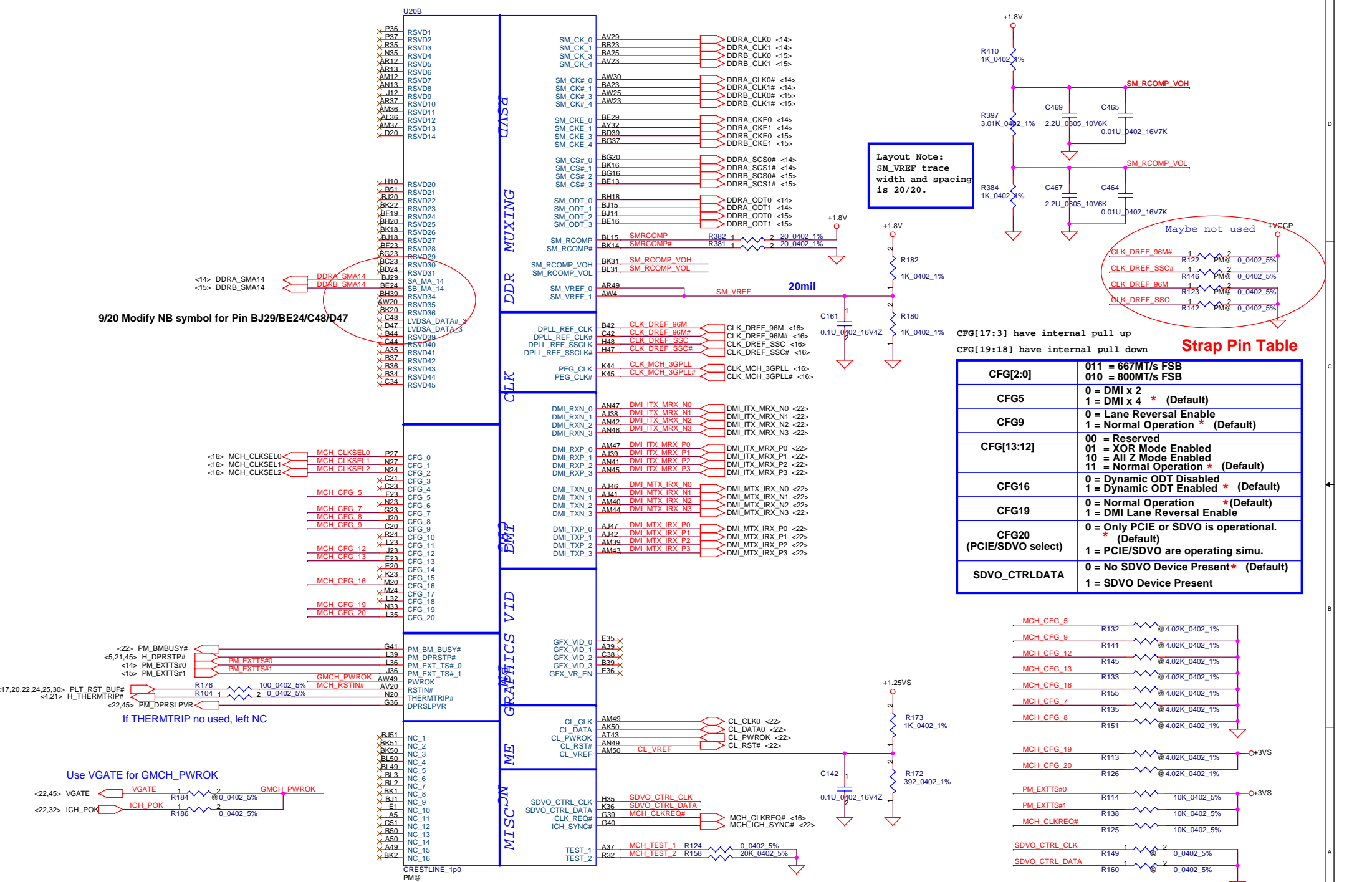


within 100mil to Ball A9,B9

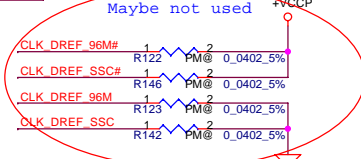
Layout Note:
H_RCOMP / H_VREF / H_SWNG trace width and spacing is 10/20



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				Crestline (1/7)-GTL	
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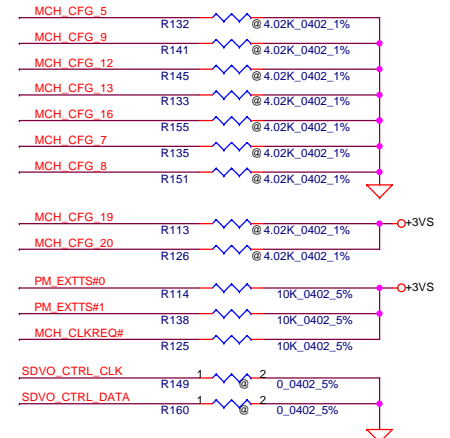
Layout Note:
SM_VREF trace width and spacing is 20/20.



CFG[17:3] have internal pull up
CFG[19:18] have internal pull down

Strap Pin Table

CFG[2:0]	011 = 667MT/s FSB 010 = 800MT/s FSB
CFG5	0 = DMI x 2 1 = DMI x 4 * (Default)
CFG9	0 = Lane Reversal Enable 1 = Normal Operation * (Default)
CFG[13:12]	00 = Reserved 01 = XOR Mode Enabled 10 = All Z Mode Enabled 11 = Normal Operation * (Default)
CFG16	0 = Dynamic ODT Disabled * (Default) 1 = Dynamic ODT Enabled *
CFG19	0 = Normal Operation * (Default) 1 = DMI Lane Reversal Enable
CFG20 (PCIe/SDVO select)	0 = Only PCIe or SDVO is operational. * (Default) 1 = PCIe/SDVO are operating simul.
SDVO_CTRLDATA	0 = No SDVO Device Present * (Default) 1 = SDVO Device Present

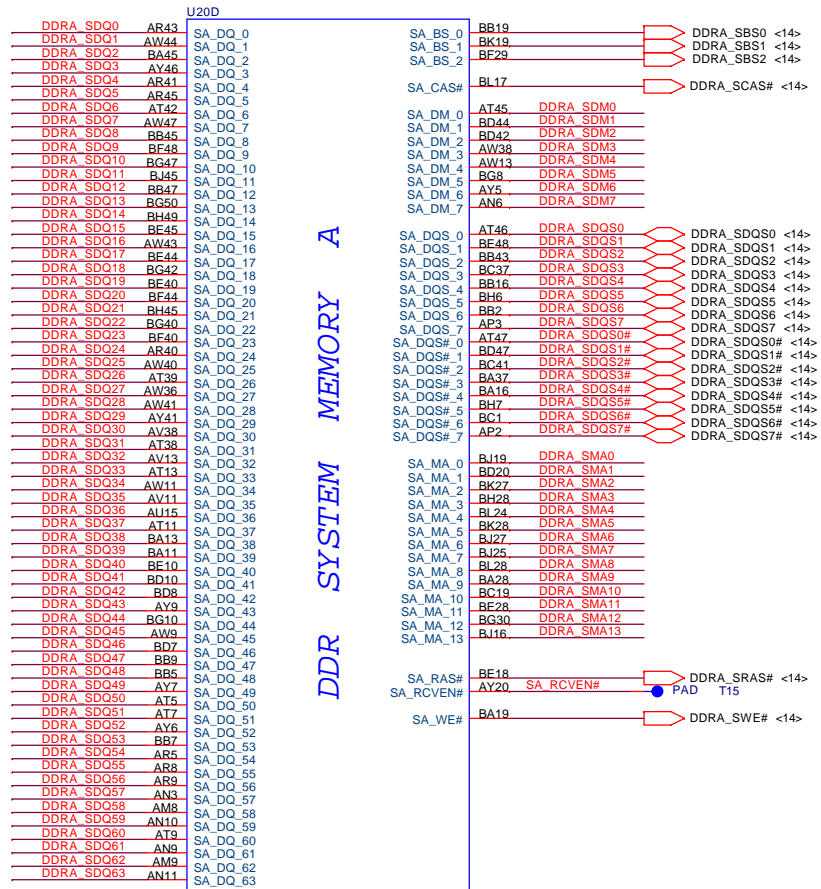


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Title Crestline (27)-DMI/DDR		
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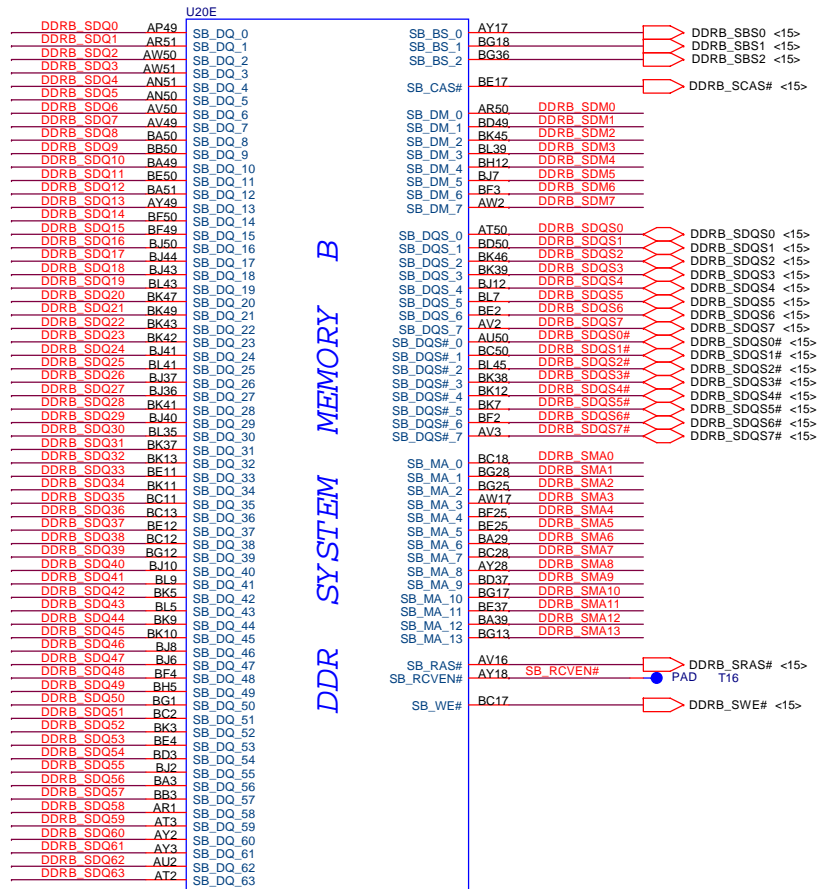
<14> DDRA_SDQ[0..63] ↔ DDRA_SDQ[0..63]
 <14> DDRA_SDM[0..7] ↔ DDRA_SDM[0..7]
 <14> DDRA_SMA[0..13] ↔ DDRA_SMA[0..13]

<15> DDRB_SDQ[0..63] ↔ DDRB_SDQ[0..63]
 <15> DDRB_SDM[0..7] ↔ DDRB_SDM[0..7]
 <15> DDRB_SMA[0..13] ↔ DDRB_SMA[0..13]



DDR SYSTEM MEMORY A

CRESTLINE_1p0
PM@

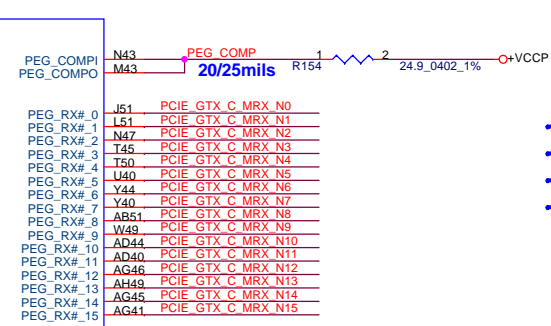
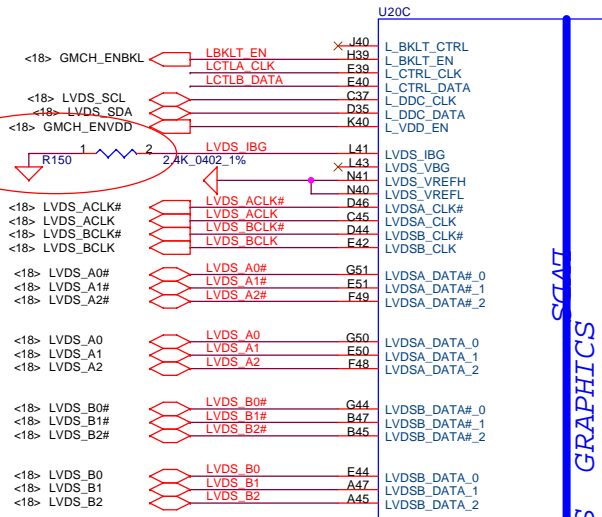


DDR SYSTEM MEMORY B

CRESTLINE_1p0
PM@

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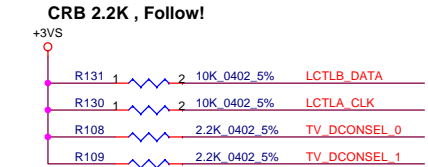
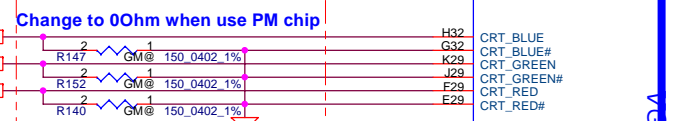
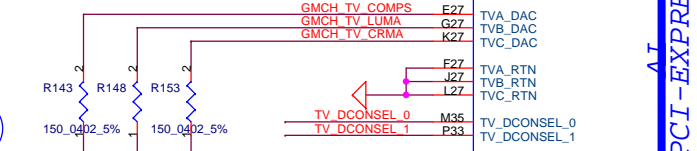
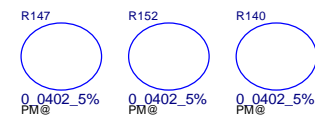
CRB 2.37K_1% to GND



- PCIE_MTX_C_GRX_N0[0..15] <17>
- PCIE_MTX_C_GRX_P0[0..15] <17>
- PCIE_GTX_C_MRX_N0[0..15] <17>
- PCIE_GTX_C_MRX_P0[0..15] <17>

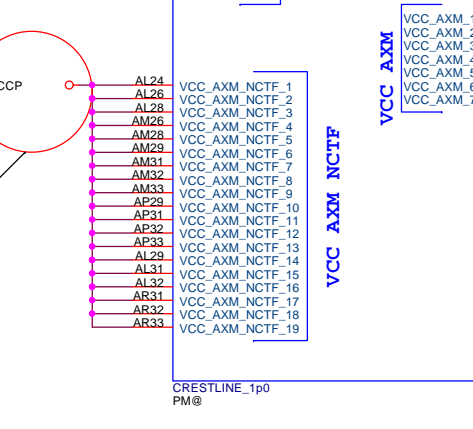
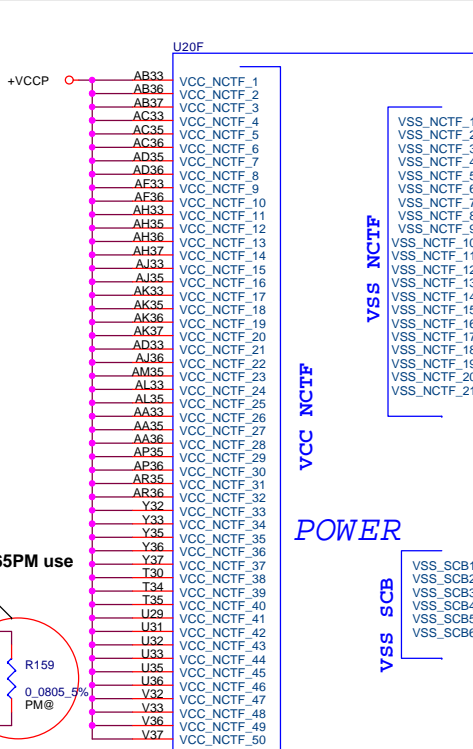
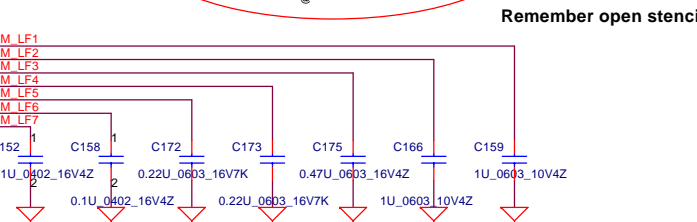
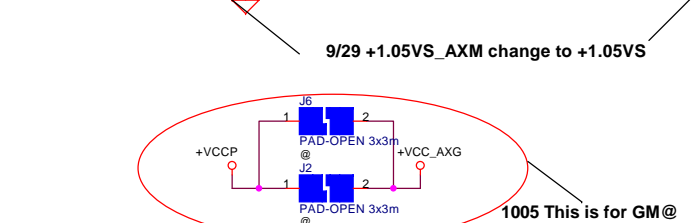
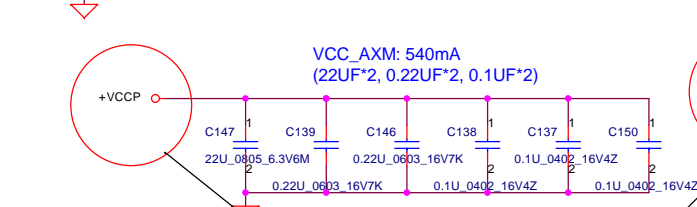
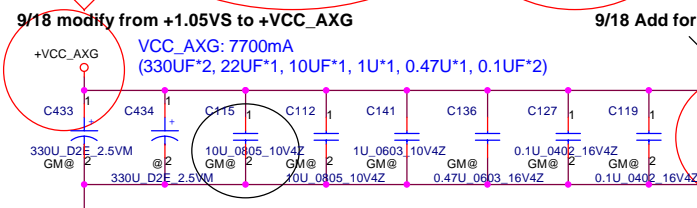
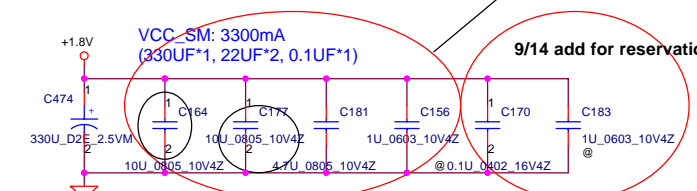
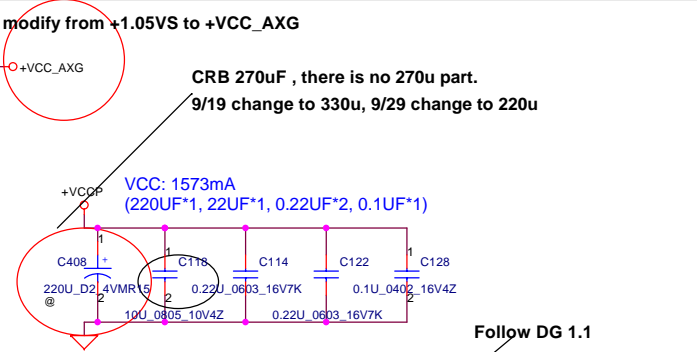
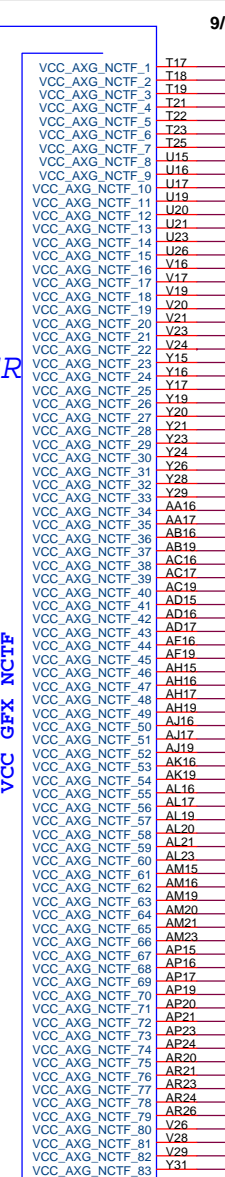
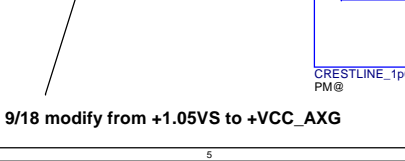
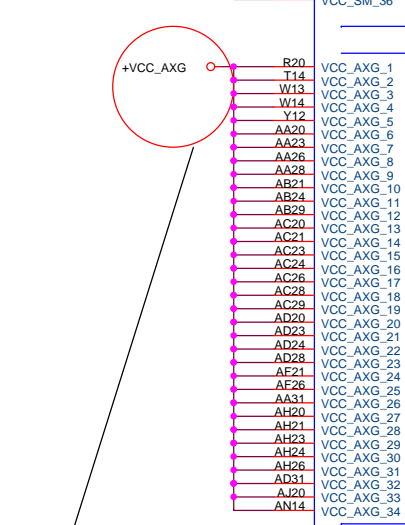
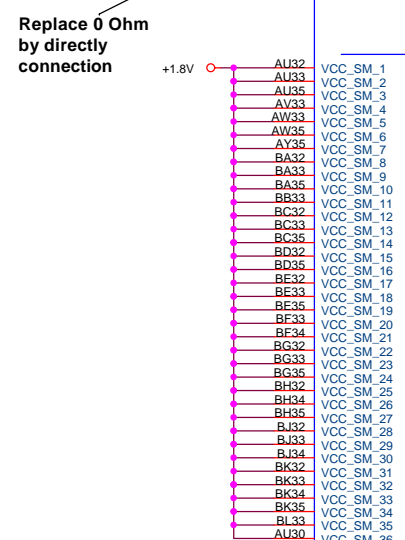
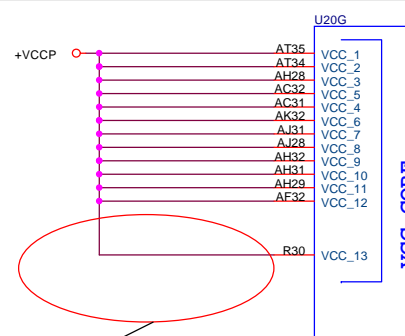
- PEG_RX#_0 J51 PCIE GTX C MRX N0
- PEG_RX#_1 L51 PCIE GTX C MRX N1
- PEG_RX#_2 T45 PCIE GTX C MRX N2
- PEG_RX#_3 T50 PCIE GTX C MRX N3
- PEG_RX#_4 U40 PCIE GTX C MRX N4
- PEG_RX#_5 U44 PCIE GTX C MRX N5
- PEG_RX#_6 Y40 PCIE GTX C MRX N6
- PEG_RX#_7 Y44 PCIE GTX C MRX N7
- PEG_RX#_8 AB51 PCIE GTX C MRX N8
- PEG_RX#_9 W49 PCIE GTX C MRX N9
- PEG_RX#_10 AD44 PCIE GTX C MRX N10
- PEG_RX#_11 AD40 PCIE GTX C MRX N11
- PEG_RX#_12 AG46 PCIE GTX C MRX N12
- PEG_RX#_13 AH49 PCIE GTX C MRX N13
- PEG_RX#_14 AG45 PCIE GTX C MRX N14
- PEG_RX#_15 AG41 PCIE GTX C MRX N15

PEG_TX#	Component	Value	Notes
PEG_TX#_0	N45	PCIE MTX GRX N0	C124 1 2 PM@ 0.1U 0402 10V7K PCIE_MTX_C_GRX_N0
PEG_TX#_1	U39	PCIE MTX GRX N1	C396 1 2 PM@ 0.1U 0402 10V7K PCIE_MTX_C_GRX_N1
PEG_TX#_2	U47	PCIE MTX GRX N2	C130 1 2 PM@ 0.1U 0402 10V7K PCIE_MTX_C_GRX_N2
PEG_TX#_3	N51	PCIE MTX GRX N3	C400 1 2 PM@ 0.1U 0402 10V7K PCIE_MTX_C_GRX_N3
PEG_TX#_4	R50	PCIE MTX GRX N4	C140 1 2 PM@ 0.1U 0402 10V7K PCIE_MTX_C_GRX_N4
PEG_TX#_5	T42	PCIE MTX GRX N5	C410 1 2 PM@ 0.1U 0402 10V7K PCIE_MTX_C_GRX_N5
PEG_TX#_6	W48	PCIE MTX GRX N6	C145 1 2 PM@ 0.1U 0402 10V7K PCIE_MTX_C_GRX_N6
PEG_TX#_7	W38	PCIE MTX GRX N7	C422 1 2 PM@ 0.1U 0402 10V7K PCIE_MTX_C_GRX_N7
PEG_TX#_8	AD39	PCIE MTX GRX N8	C425 1 2 PM@ 0.1U 0402 10V7K PCIE_MTX_C_GRX_N8
PEG_TX#_9	AC46	PCIE MTX GRX N9	C157 1 2 PM@ 0.1U 0402 10V7K PCIE_MTX_C_GRX_N9
PEG_TX#_10	AC49	PCIE MTX GRX N10	C430 1 2 PM@ 0.1U 0402 10V7K PCIE_MTX_C_GRX_N10
PEG_TX#_11	AC42	PCIE MTX GRX N11	C165 1 2 PM@ 0.1U 0402 10V7K PCIE_MTX_C_GRX_N11
PEG_TX#_12	AC41	PCIE MTX GRX N12	C432 1 2 PM@ 0.1U 0402 10V7K PCIE_MTX_C_GRX_N12
PEG_TX#_13	AE49	PCIE MTX GRX N13	C171 1 2 PM@ 0.1U 0402 10V7K PCIE_MTX_C_GRX_N13
PEG_TX#_14	AH44	PCIE MTX GRX N14	C442 1 2 PM@ 0.1U 0402 10V7K PCIE_MTX_C_GRX_N14
PEG_TX#_15	AH44	PCIE MTX GRX N15	C442 1 2 PM@ 0.1U 0402 10V7K PCIE_MTX_C_GRX_N15



PCI-EXPRESS GRAPHICS

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Size B	Document Number	Rev		0.2	
	LA-3691P				
Date:	Thursday, March 08, 2007	Sheet	10	of	45



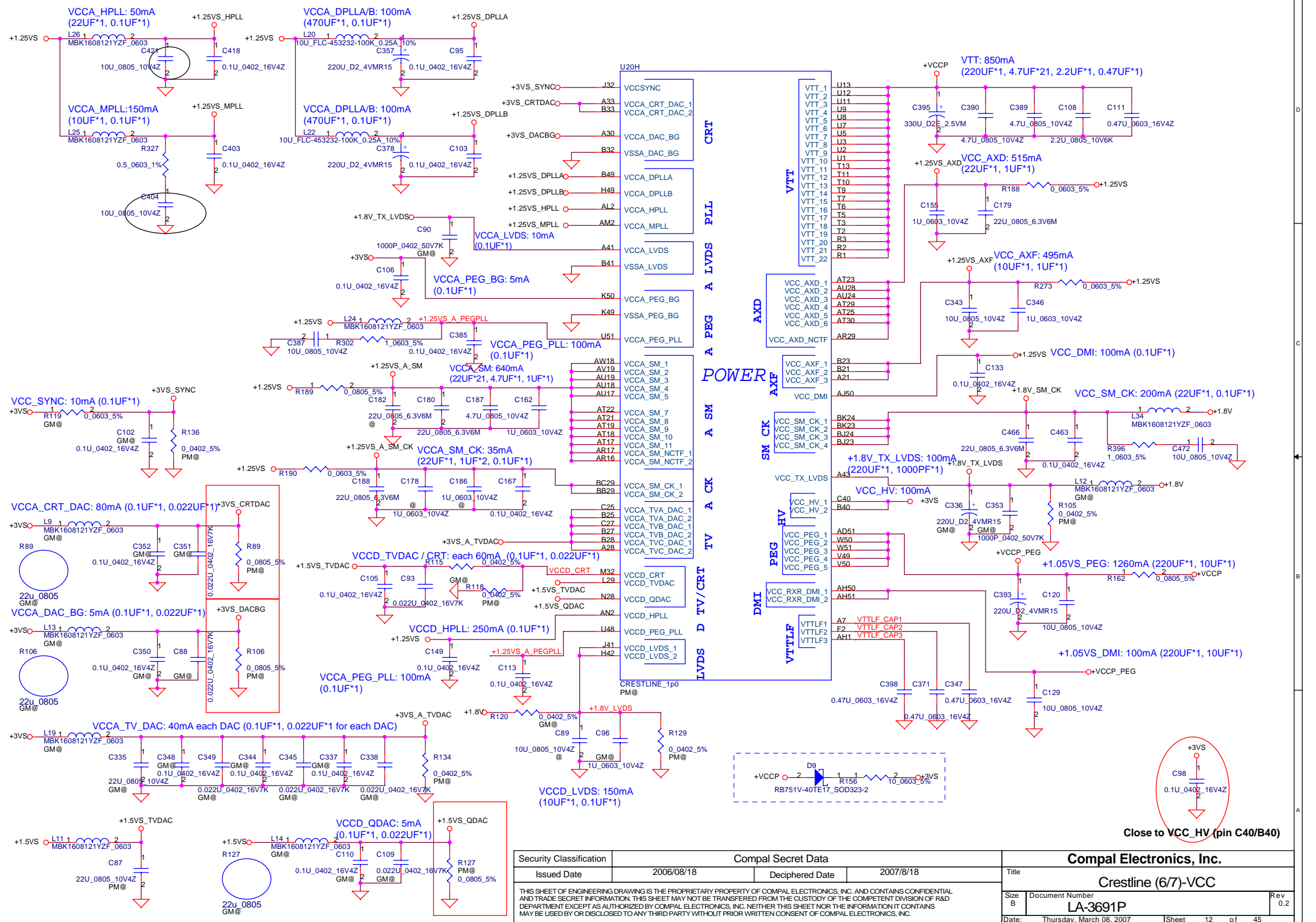
Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2006/08/18	Deciphered Date	2007/8/18	Title	
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Size	Document Number	Date:		Sheet	Rev
B	LA-3691P	Thursday, March 08, 2007		11	0.2
				of	45

9/18 modify from +1.05VS to +VCC_AXG

9/18 modify from +1.05VS to +VCC_AXG

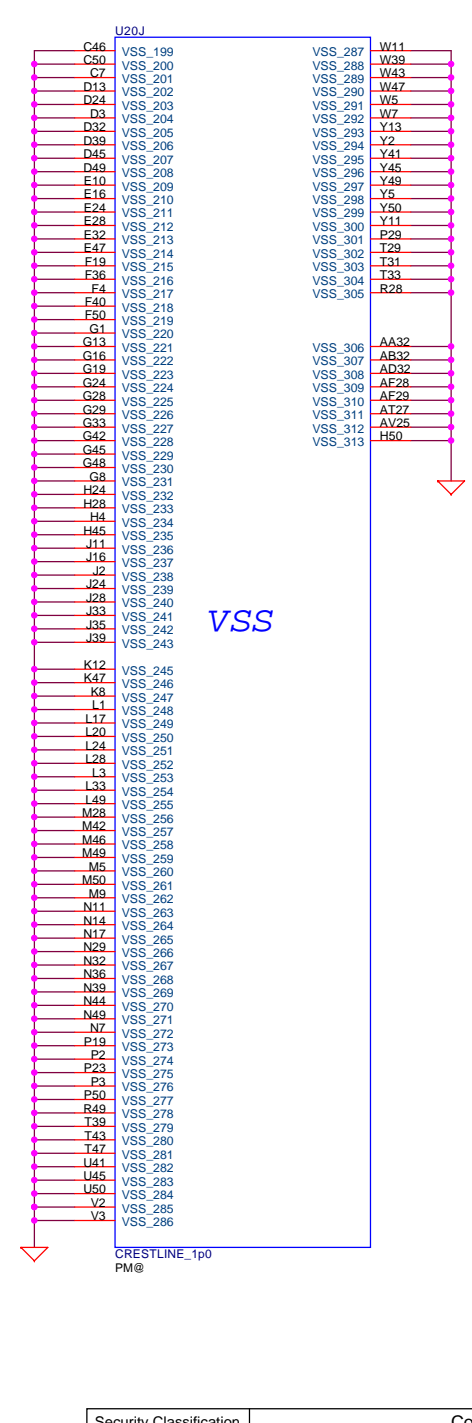
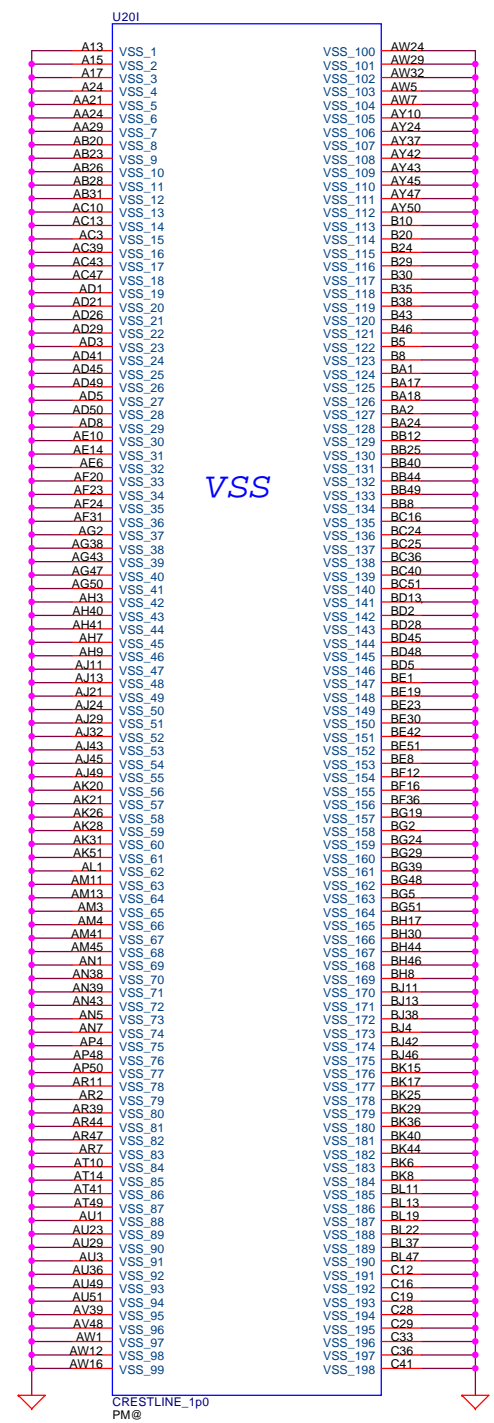
9/29 +1.05VS_AXM change to +1.05VS

9/29 +1.05VS_AXM change to +1.05VS



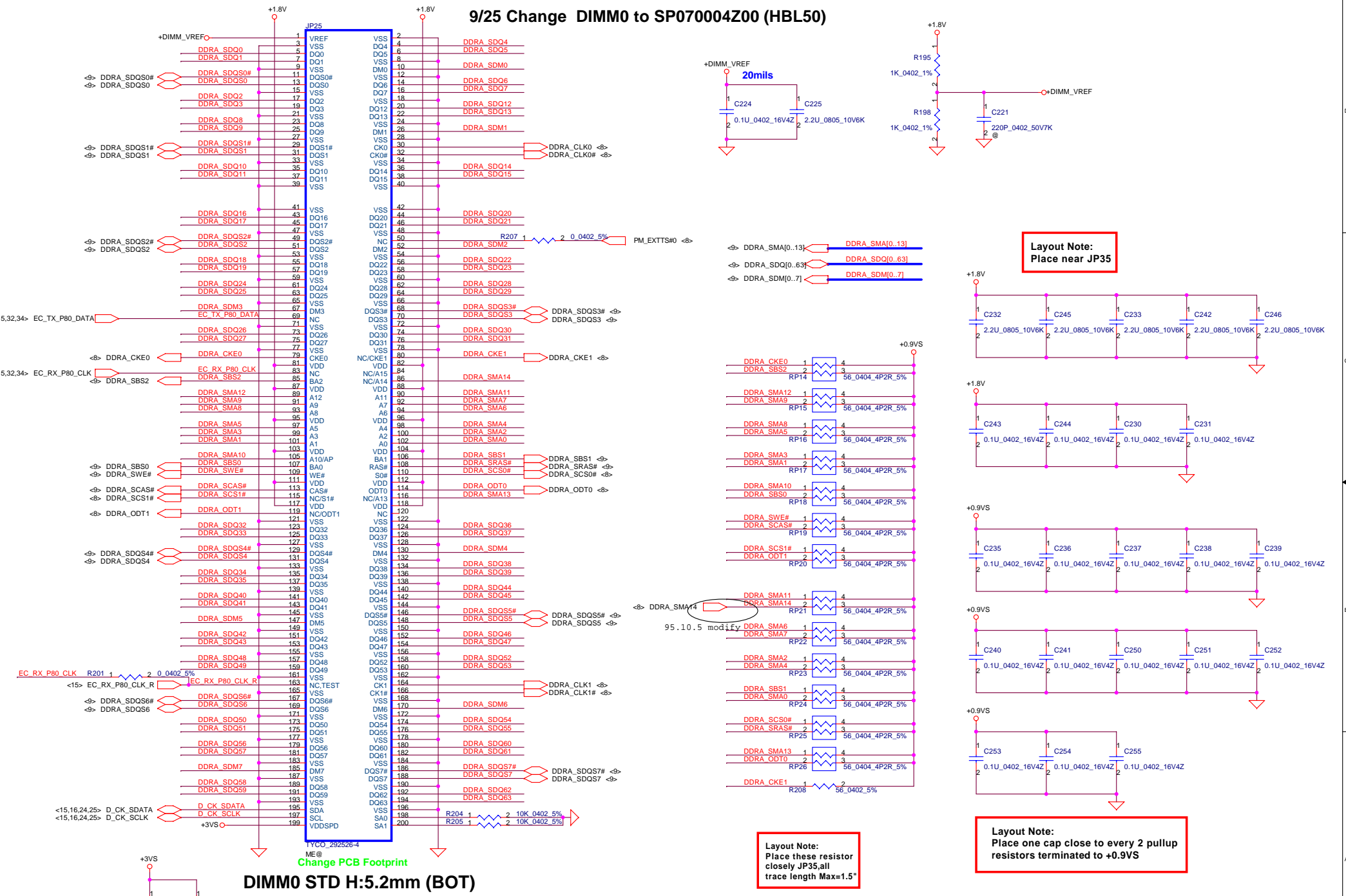
Security Classification		Compal Secret Data	
Issued Date	2006/08/18	Deciphered Date	2007/8/18
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Compal Electronics, Inc.			
Title			
Crestline (6/7)-VCC			
Size	Document Number	Rev	
B	LA-3691P	0.2	
Date:	Thursday, March 08, 2007	Sheet	12 of 45



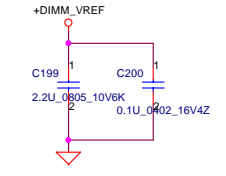
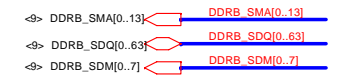
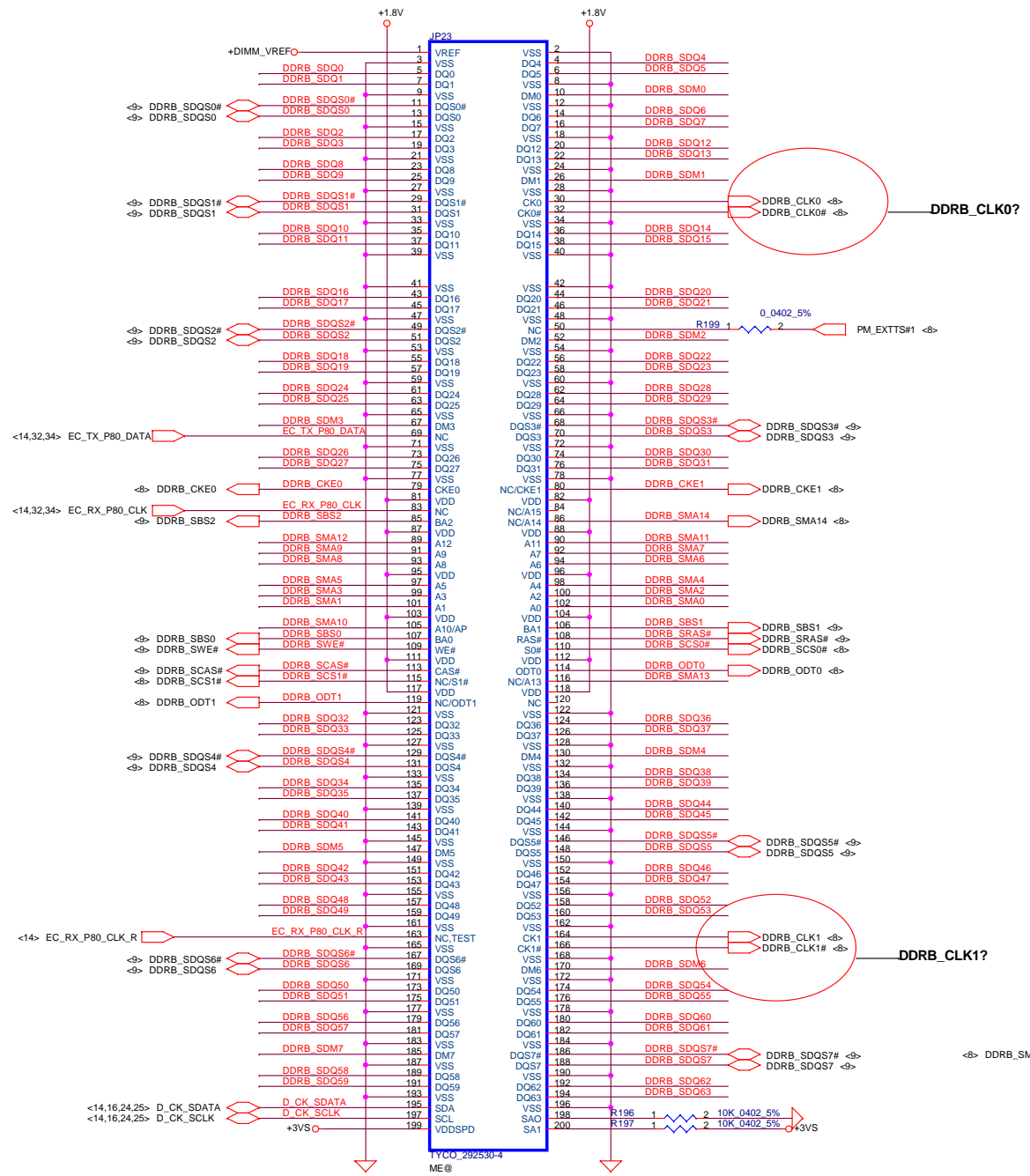
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Issued Date	2006/08/18	Deciphered Date	2007/8/18	Title Crestline (7/7)-GND	
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Date: Thursday, March 08, 2007				Sheet	13 of 45

9/25 Change DIMM0 to SP070004Z00 (HBL50)

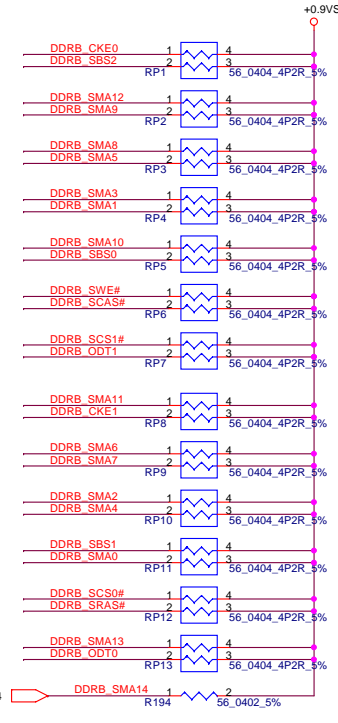
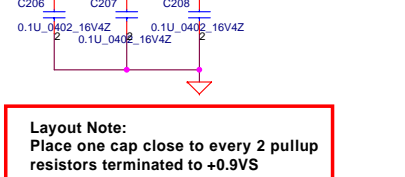
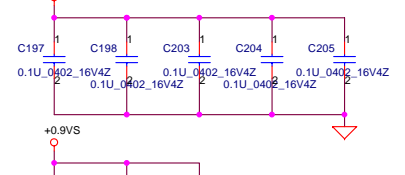
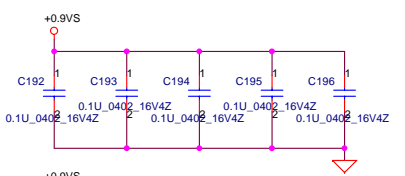
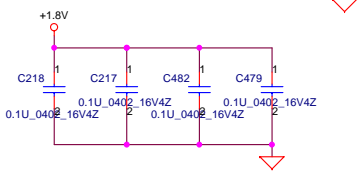
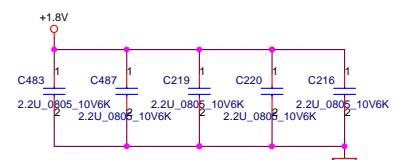


Security Classification		Compal Secret Data		Title	
Issued Date	2006/08/18	Deciphered Date	2007/8/18	DDR2-SODIMM0	
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Size B	Document Number	Date	Thursday, March 08, 2007	Sheet	14 of 45
	LA-3691P				Rev 0.2

9/25 Change DIMM1 to SP070006F00



Layout Note:
Place near JP34



Layout Note:
Place these resistor closely JP35, all trace length Max=1.5"

Layout Note:
Place one cap close to every 2 pullup resistors terminated to +0.9VS

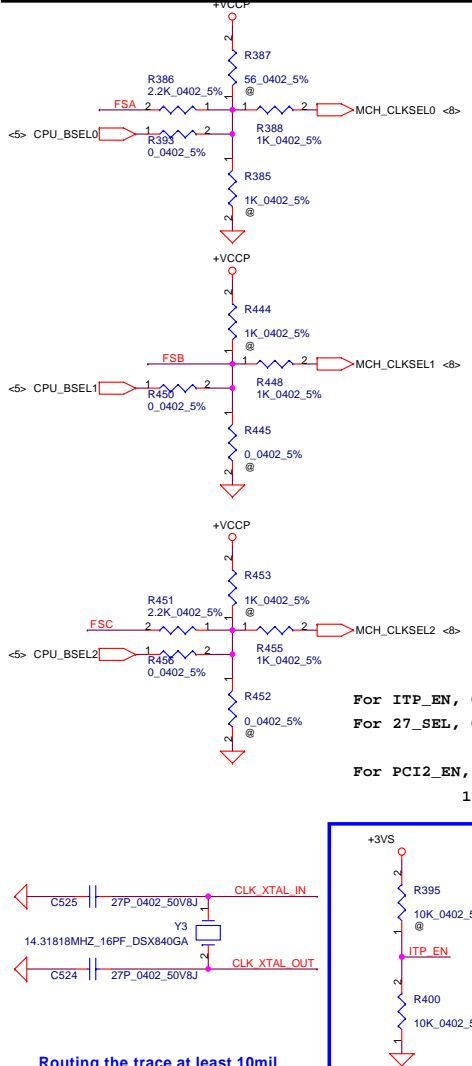
DIMM1 STD H:9.2mm (BOT)

Security Classification	Compal Secret Data		Title	
Issued Date	2006/08/18	Deciphered Date	2007/8/18	DDRII-SODIMM1
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Size B	Document Number LA-3691P	Date Thursday, March 06, 2007	Sheet 15	of 45

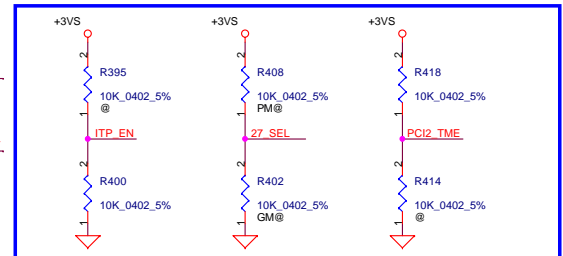
FSLC	FSLB	FSLA	CPU	SRC	PCI
CLKSEL2	CLKSEL1	CLKSEL0	MHz	MHz	MHz
0	1	0	200	100	33.3
0	1	1	166	100	33.3

FSB Frequency Selet:

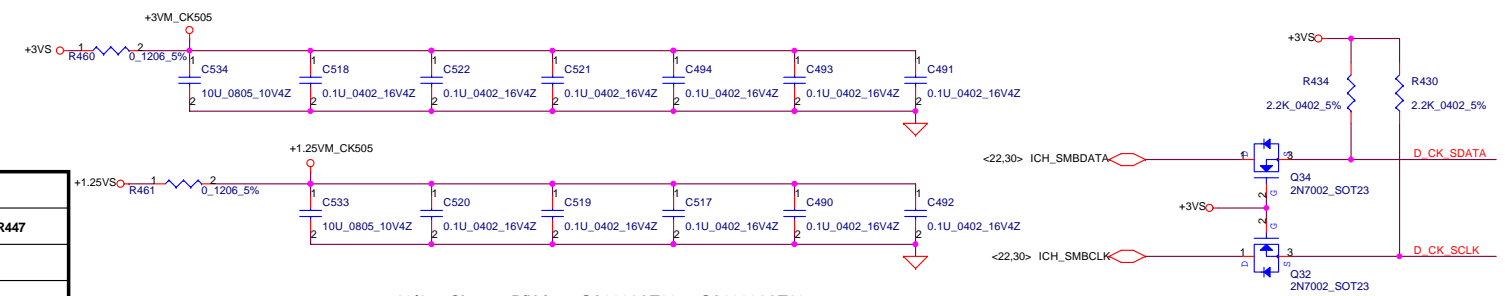
CPU Driven	Stuff	R401	R408	R417	R430	R438	R447
*(Default)	No Stuff	R401	R408	R417	R430	R438	R447
	Stuff	R401	R417	R447			
667MHz	No Stuff	R408	R430	R438			
	Stuff	R408	R417	R447			
800MHz	No Stuff	R401	R430	R438			
	Stuff	R401	R430	R438			



For ITP_EN, 0 = SRC8/SRC8#, 1 = ITP/ITP#
 For 27_SEL, 0 = Enable DOT96 & SRC1, 1 = Enable SRC0 & 27MHz
 For PCI2_EN, 0 = Overclocking of CPU and SRC Allowed, 1 = Overclocking of CPU and SRC NOT allowed

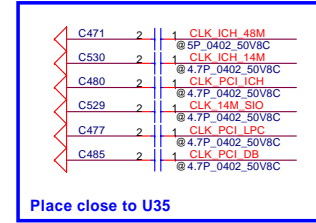
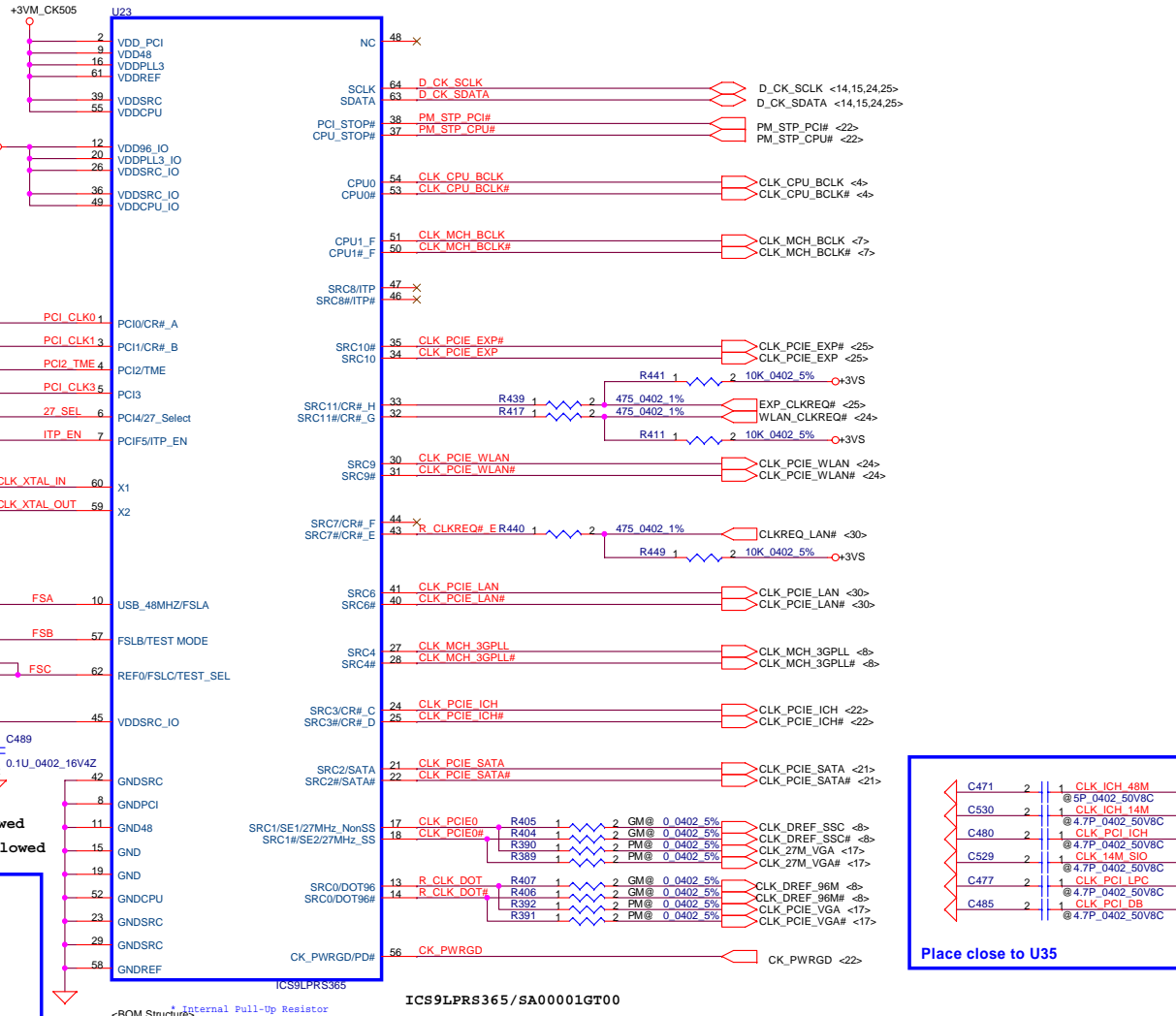


Routing the trace at least 10mil



10/17 : Change P/N from SA0001GT00 to SA00001GT10

Need to update Symbol



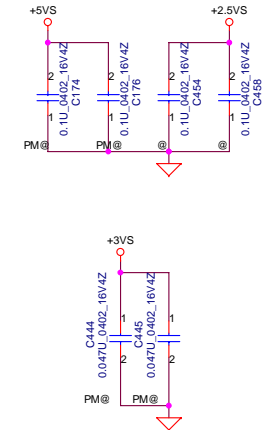
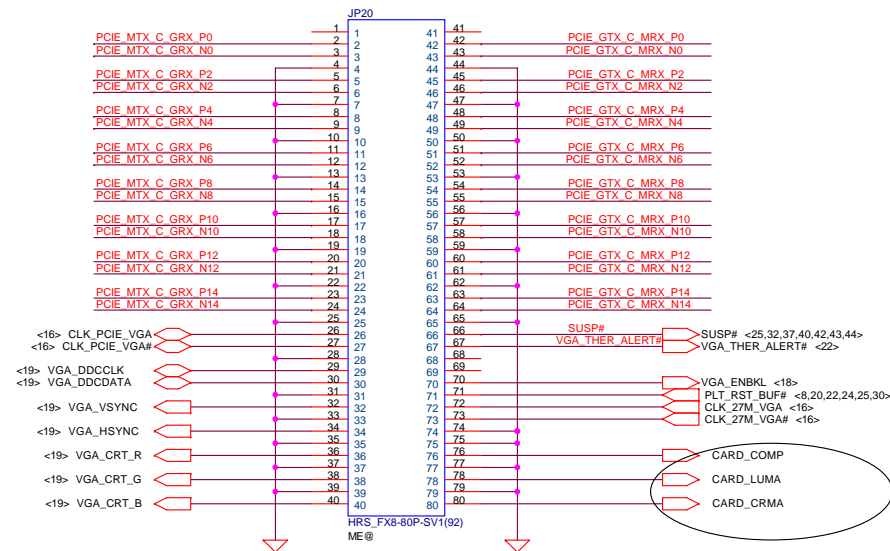
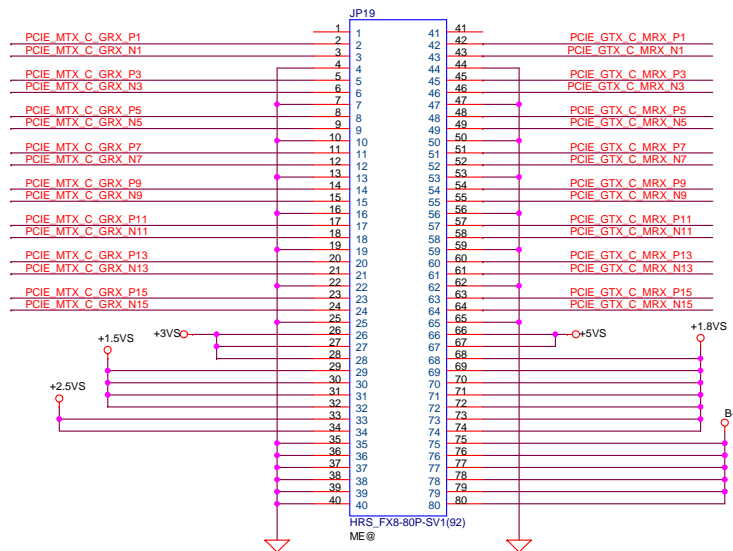
Security Classification	Compal Secret Data	Title
Issued Date	2006/08/04	Deciphered Date
2006/10/06		2006/10/06
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Size	Document Number	Rev
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MAX. 4.06A @ 1.8V

MAX. 130mA @ 2.5V

MAX. 655mA @ 3.3V

- <10> PCIE_MTX_C_GRX_N[0..15] PCIE_MTX_C_GRX_N[0..15]
- <10> PCIE_MTX_C_GRX_P[0..15] PCIE_MTX_C_GRX_P[0..15]
- <10> PCIE_GTX_C_MRX_N[0..15] PCIE_GTX_C_MRX_N[0..15]
- <10> PCIE_GTX_C_MRX_P[0..15] PCIE_GTX_C_MRX_P[0..15]



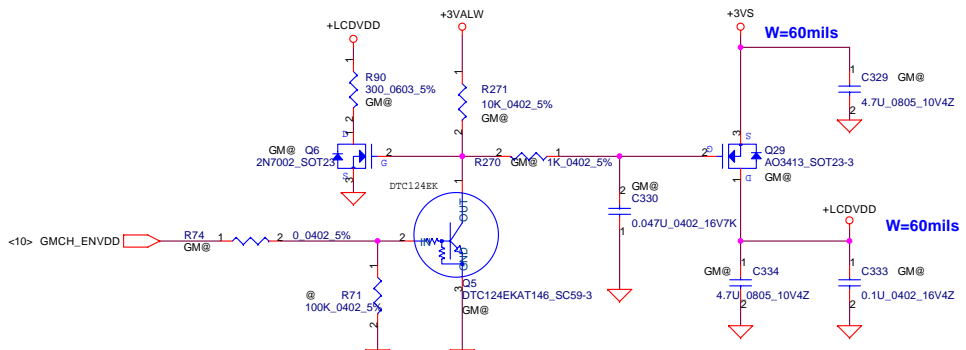
Compal Electronics, Inc.

VGA/B connector

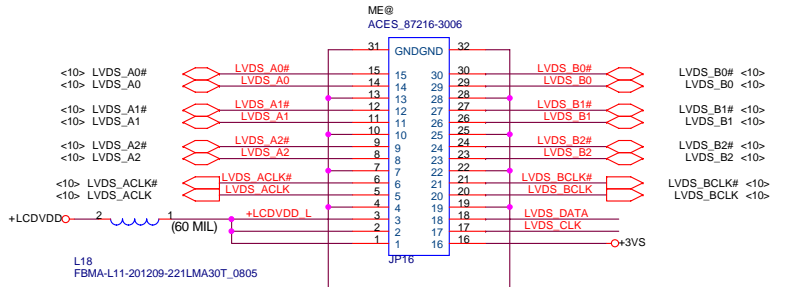
Title		
Size	Document Number	Rev
Customer	IEL10 LA-3451P	0.2
Date	Thursday, March 08, 2007	Sheet 17 of 45

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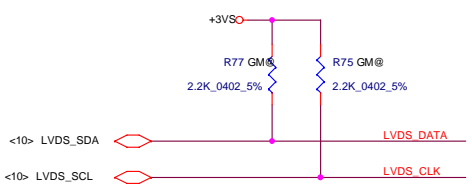
LCD POWER CIRCUIT



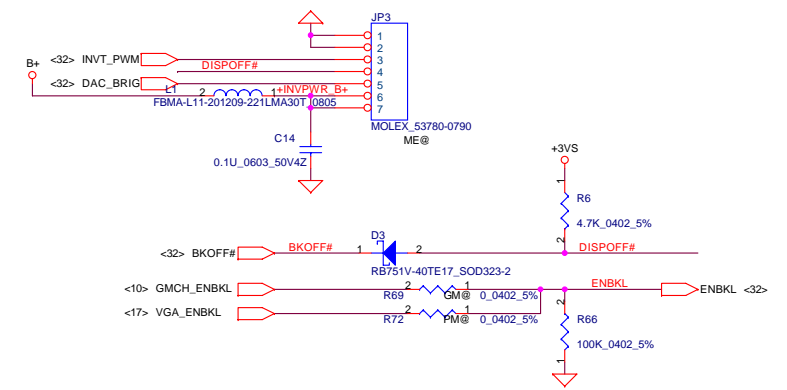
LCD/PANEL BD. Conn.



Follow HEL80's pin definition
Except pin 29



INVERTER Conn.

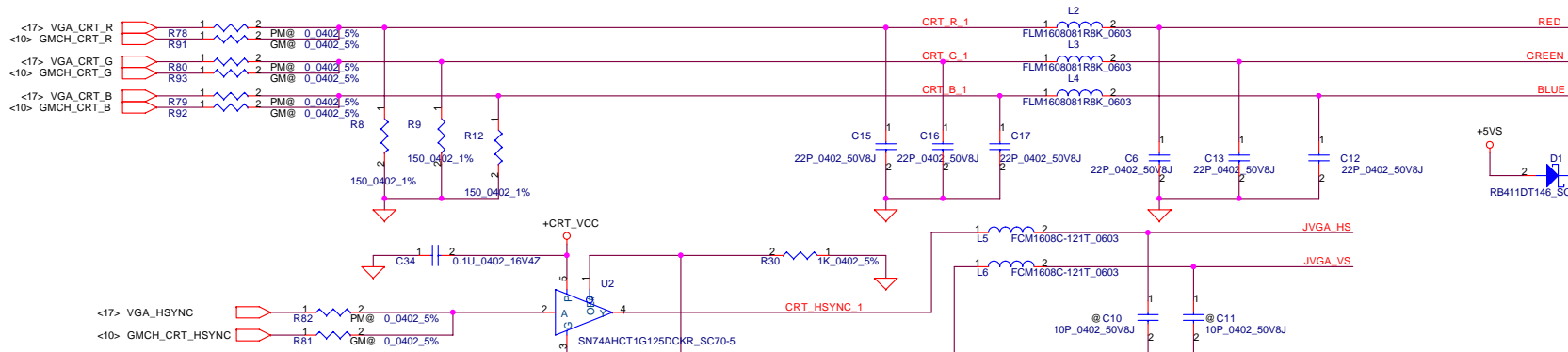


Security Classification	Compal Secret Data			Compal Electronics, Inc.		
Issued Date	2006/08/18	Deciphered Date	2007/8/18	Title		
				LVDS & DVI Connector		
				Size	Document Number	Rev
				B	LA-3691P	0.2
				Date:	Thursday, March 06, 2007	Sheet 18 of 45

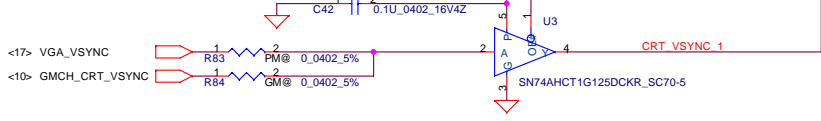
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CRT Connector

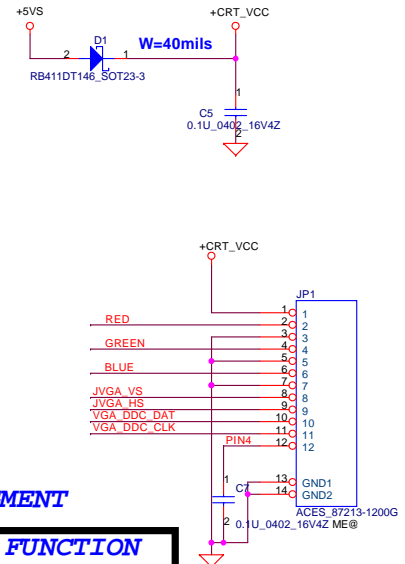
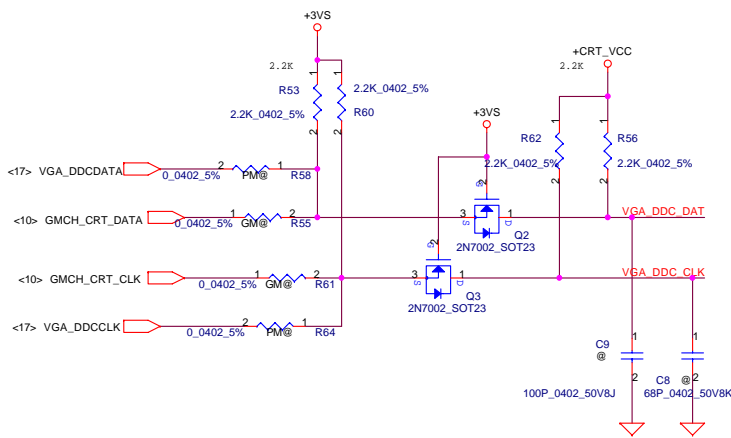
Place closed to chipset



Place closed to chipset



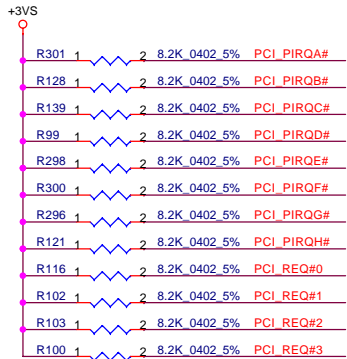
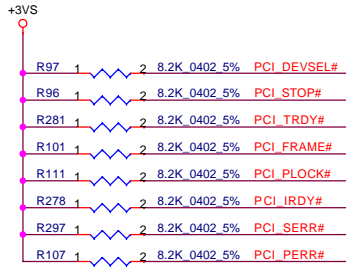
Update Footprint



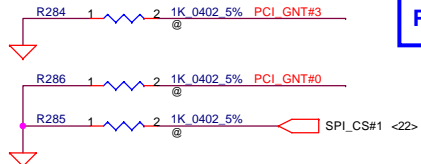
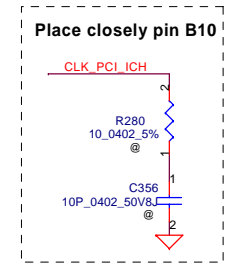
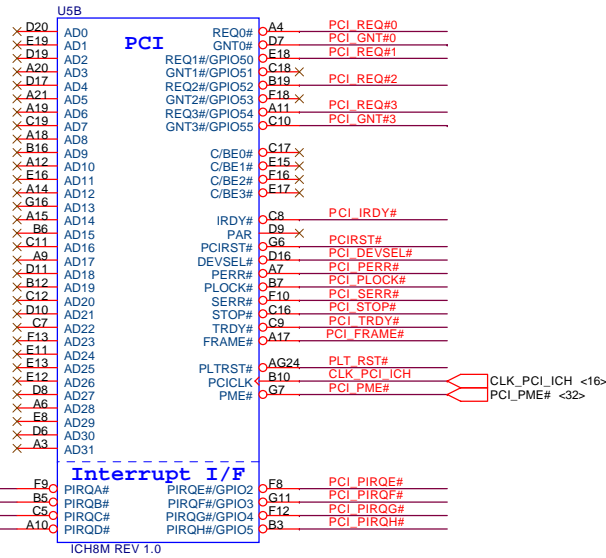
PIN ASSIGNMENT

PIN	D-SUB	FUNCTION
1	9	+CRT_VCC
2	1	RED
3	6	GND
4	2	GREEN
5	7	GND
6	3	BLUE
7	8	GND
8	14	VSYNC
9	10	GND
	11	SENSE
10	12	SM_DAT
11	15	SM_CLK
12	4	PIN4

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				LA-3691P
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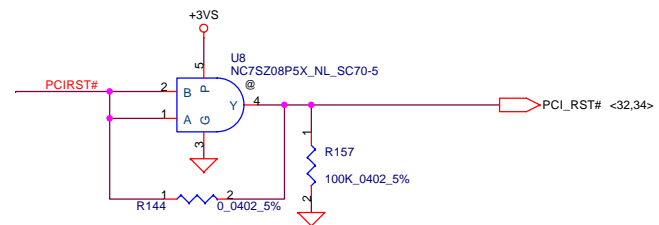
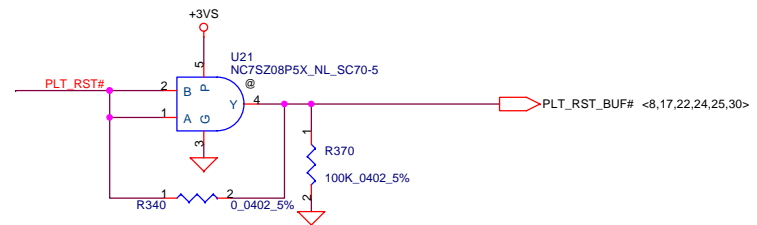


10/17 : Change P/N from SA000010G00 to SA00001JU10
 10/17 : FootPrint : SA000010G00
 BOM : SA00001JU10

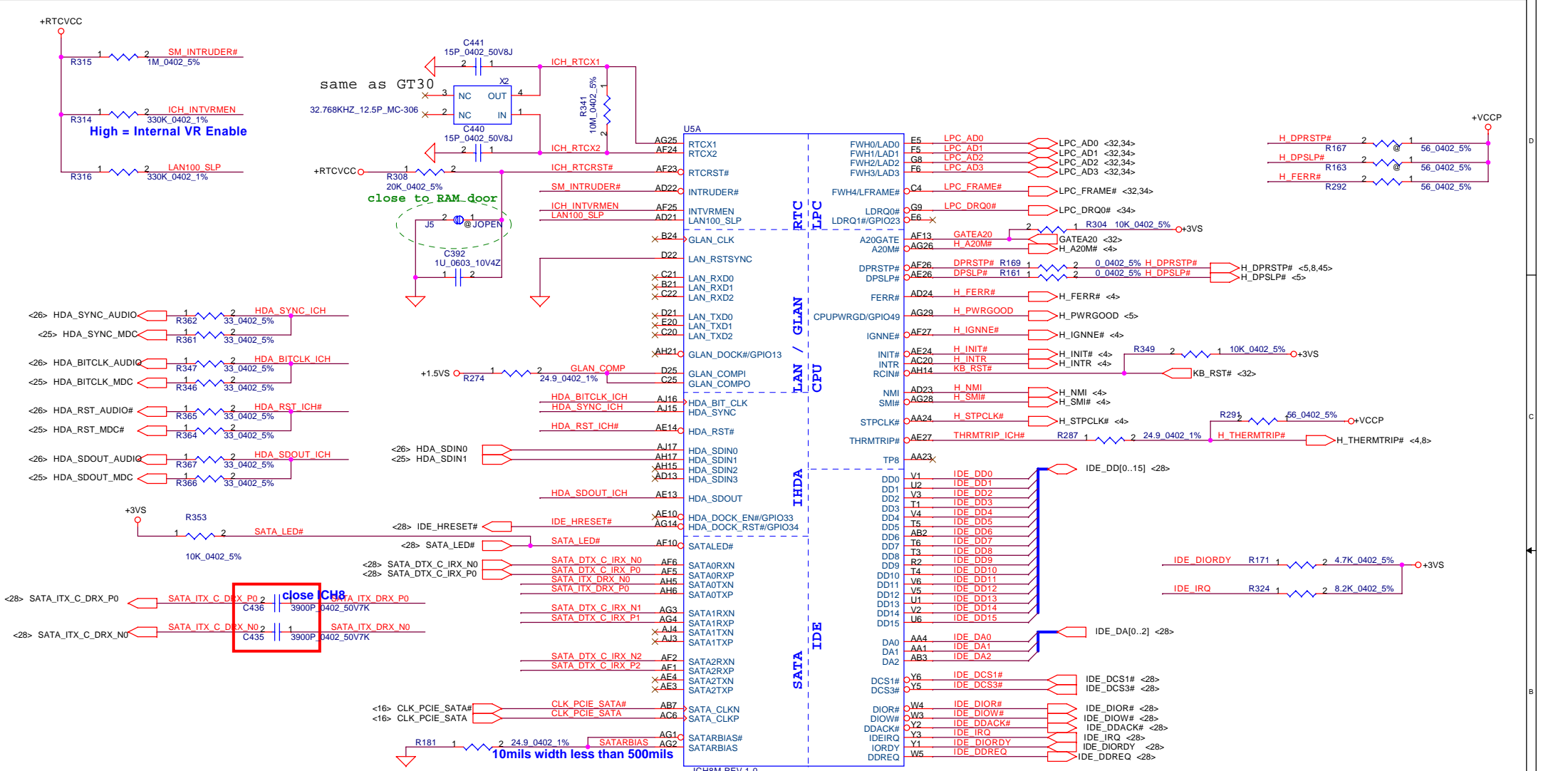


A16 Swap Override Strap
 PCI_GNT#3 Low= A16 swap override Enable
 High= Default*

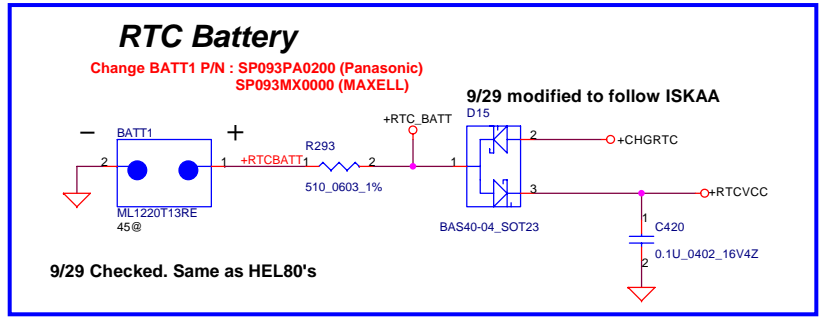
Boot BIOS Strap		
PCI_GNT#0	SPI_CS#1	Boot BIOS Location
0	1	SPI
1	0	PCI
1	1	LPC*

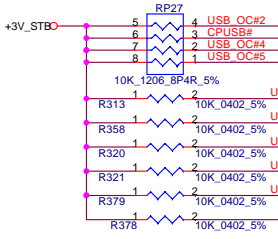
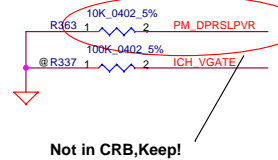
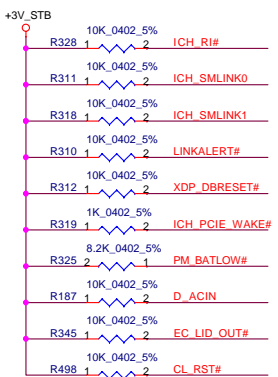
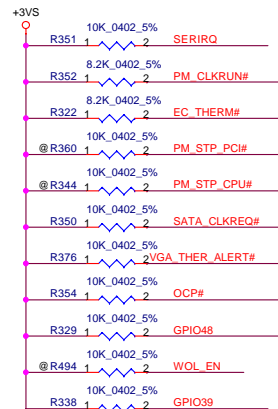


Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2006/08/18	Deciphered Date	2007/8/18	Title	
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Size	Document Number			Rev	0.2
LA-3691P		Date: Thursday, March 08, 2007		Sheet	20 of 45

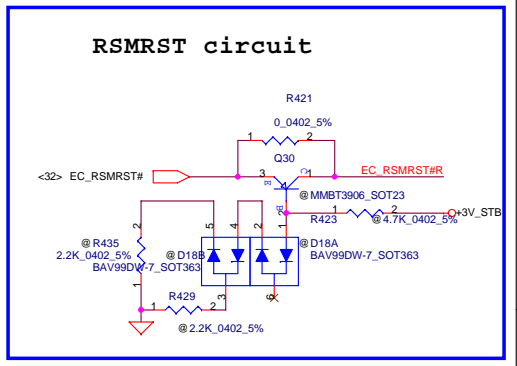
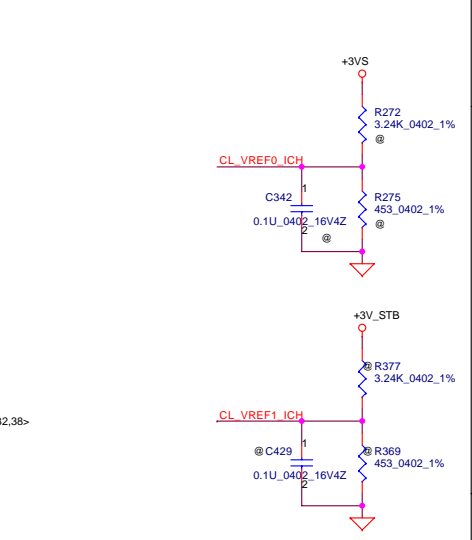
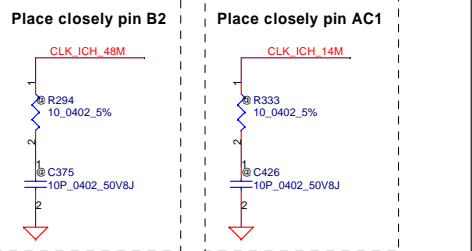
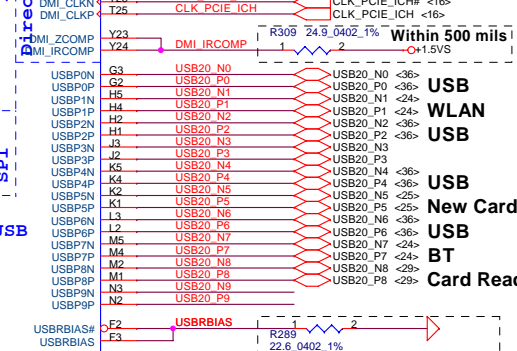
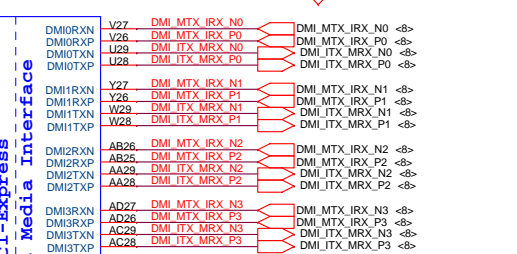
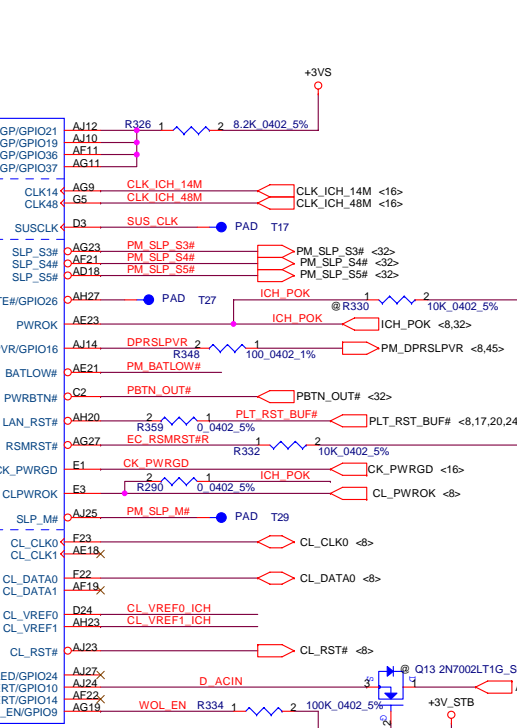
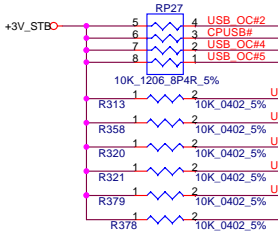
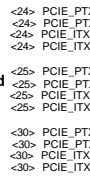
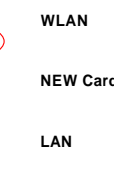
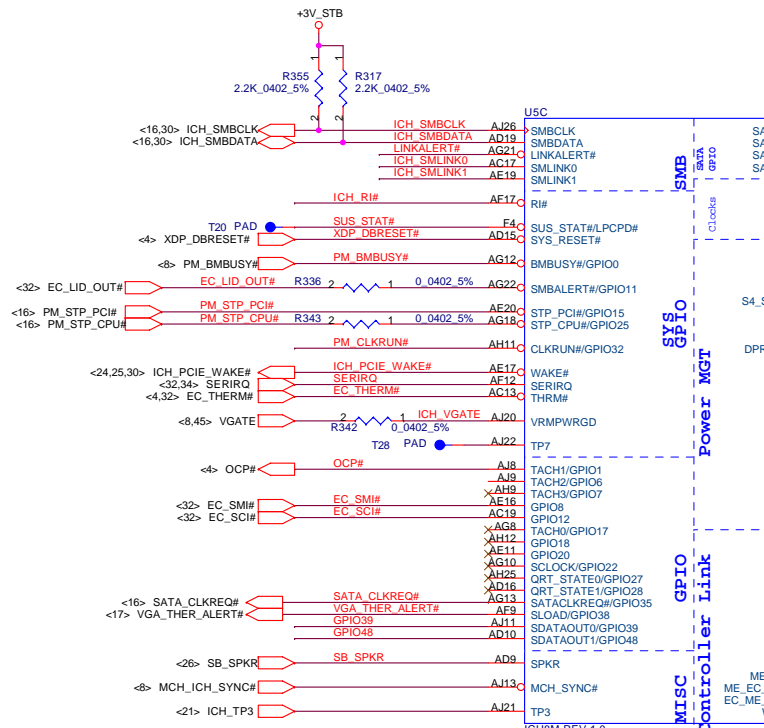


XOR Chain Entrance Strap		
ICH_TP3	HDA_SDOUT	Description
0	0	RSVD
0	1	Enter XOR Chain
1	0	Normal Operation
1	1	Set PCIE port config bit 1





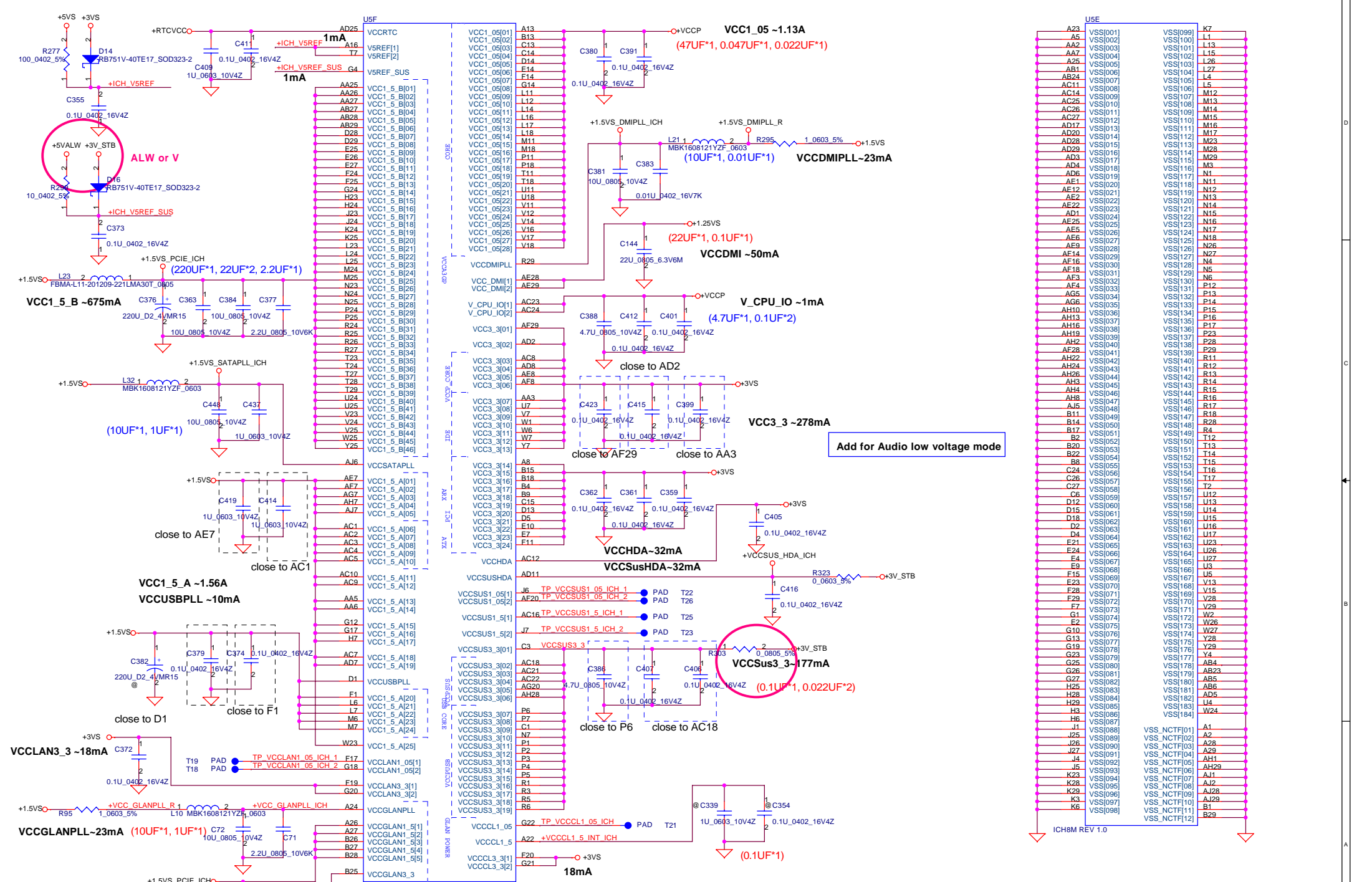
Not in CRB, Keep!



Security Classification	Compal Secret Data
Issued Date	2006/08/18
Deciphered Date	2007/8/18

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Compal Electronics, Inc.	
Title	ICH8M(3/4)-USB,GPIO,PCIE
Size	Document Number
Customer	LA-3691P
Date	Thursday, March 08, 2007
Sheet	22 of 45
Rev	0.2

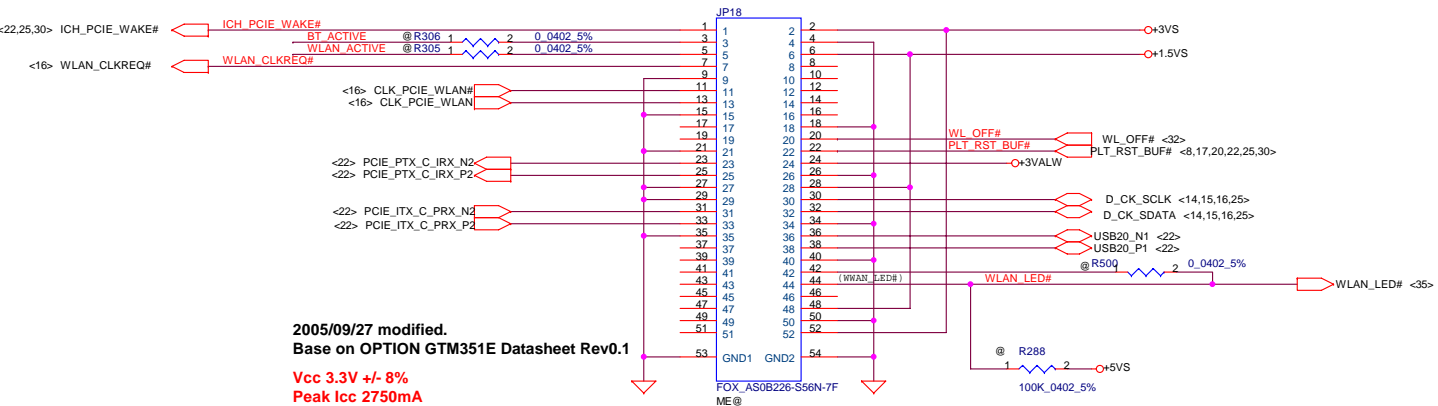
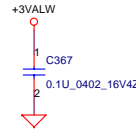
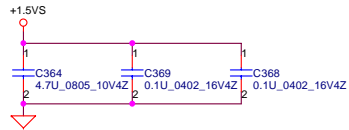
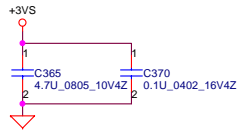


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Issued Date	2006/08/18	Deciphered Date	2007/8/18
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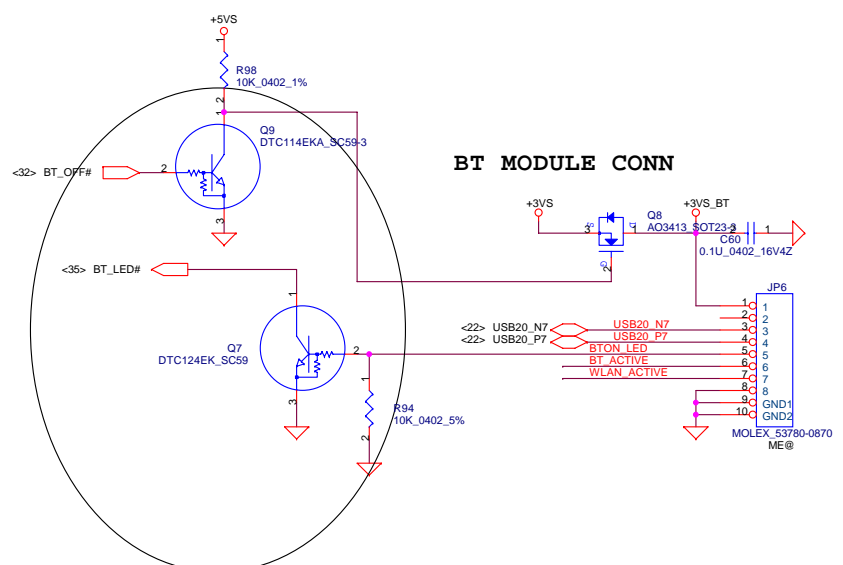
Compal Electronics, Inc.			
Title	IFTXX MB LA-3541P Schematic		
Size	Document Number	Rev	0.2
Customer	LA-3691P		
Date:	Thursday, March 08, 2007	Sheet	23 of 45

Mini-Express Card for 3G Or TV Tuner

Mini-Express Card for WLAN



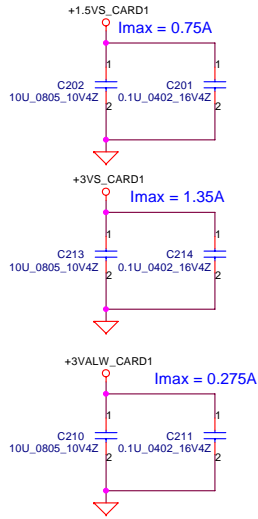
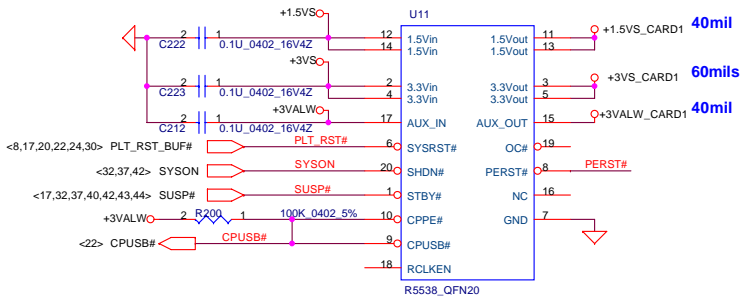
2005/09/27 modified.
 Base on OPTION GTM351E Datasheet Rev0.1
Vcc 3.3V +/- 8%
Peak Icc 2750mA
with max supply droop 50mA
Average Icc 1000mA



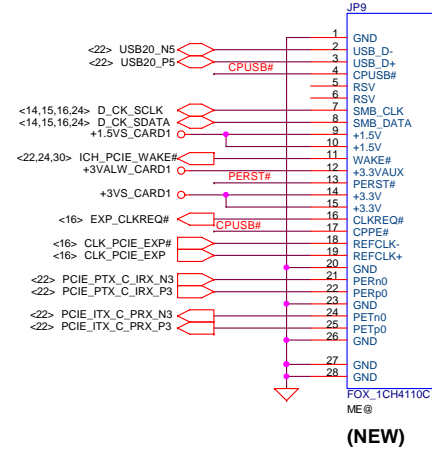
Security Classification	Compal Secret Data			Title		
Issued Date	2006/08/05	Deciphered Date	2007/08/05	Compal Electronics, Inc.		
				Mini-Card/3G/FeliCa/FP		
				Size	Document Number	Rev
				LA-3691P		0.2
				Date:	Thursday, March 08, 2007	Sheet 24 of 45

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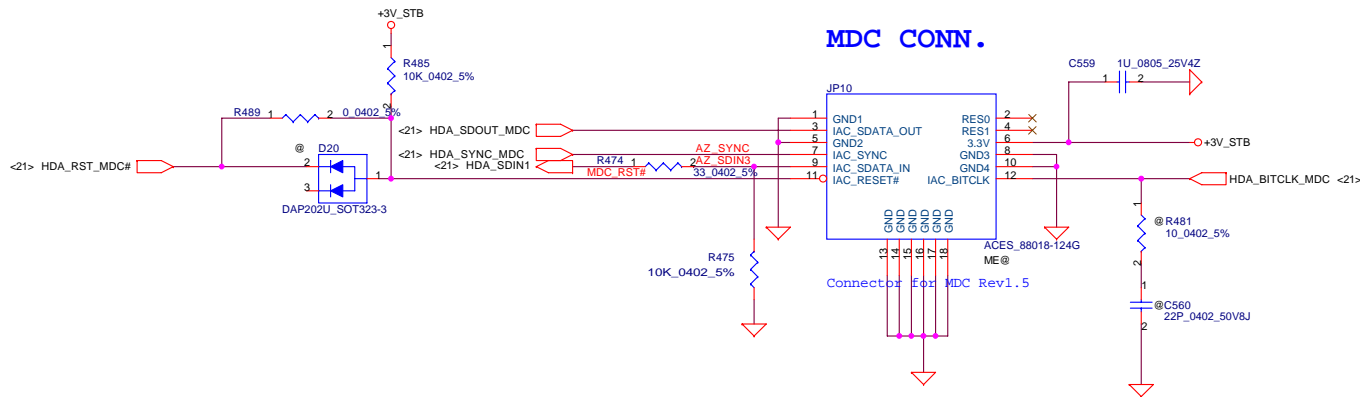
Express Card Power Switch



New Card Socket (Left/TOP)



MDC CONN.

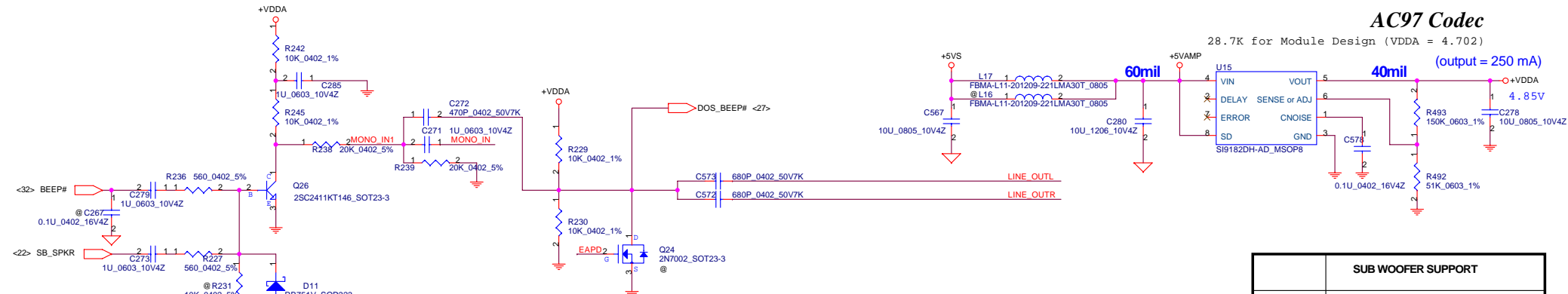


Security Classification	Compal Secret Data		Title	
Issued Date	2006/08/18	Deciphered Date	2007/8/18	NEW CARD & USB Connector
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Date:	Thursday, March 06, 2007	Sheet	25 of 45	

AC97 Codec

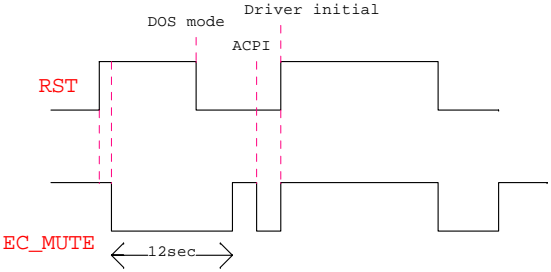
28.7K for Module Design (VDDA = 4.702)

(output = 250 mA)

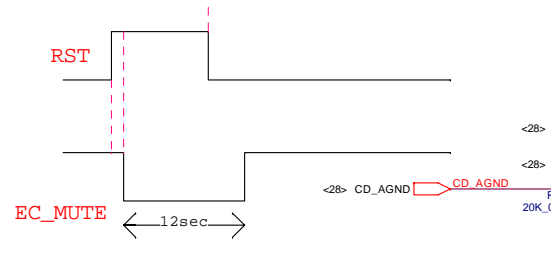


SUB WOOFER SUPPORT	
ALC262	
ALC861D	

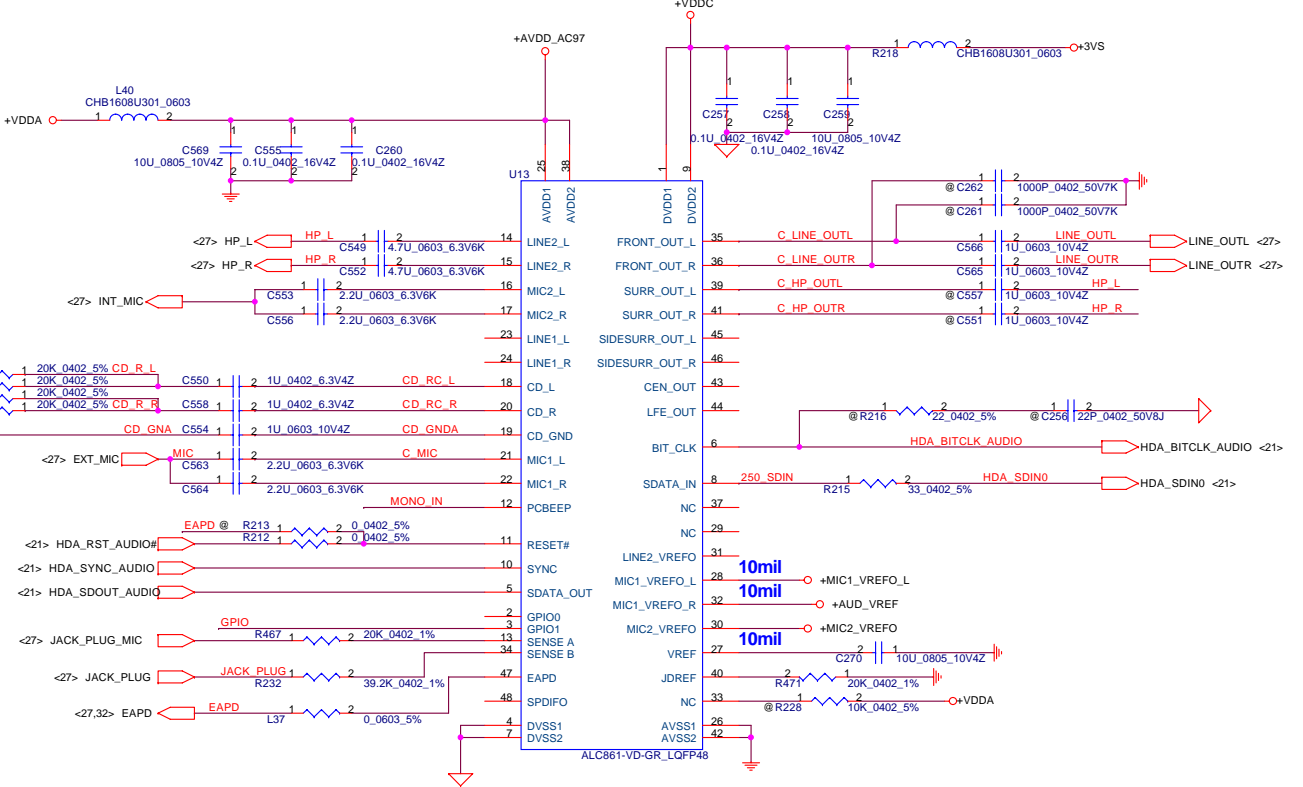
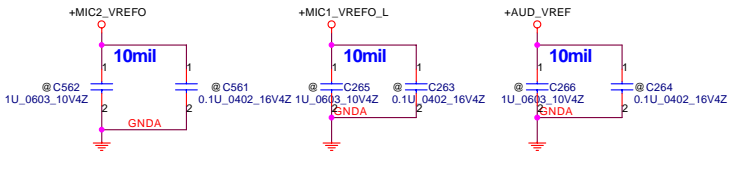
Window mode



DOS mode



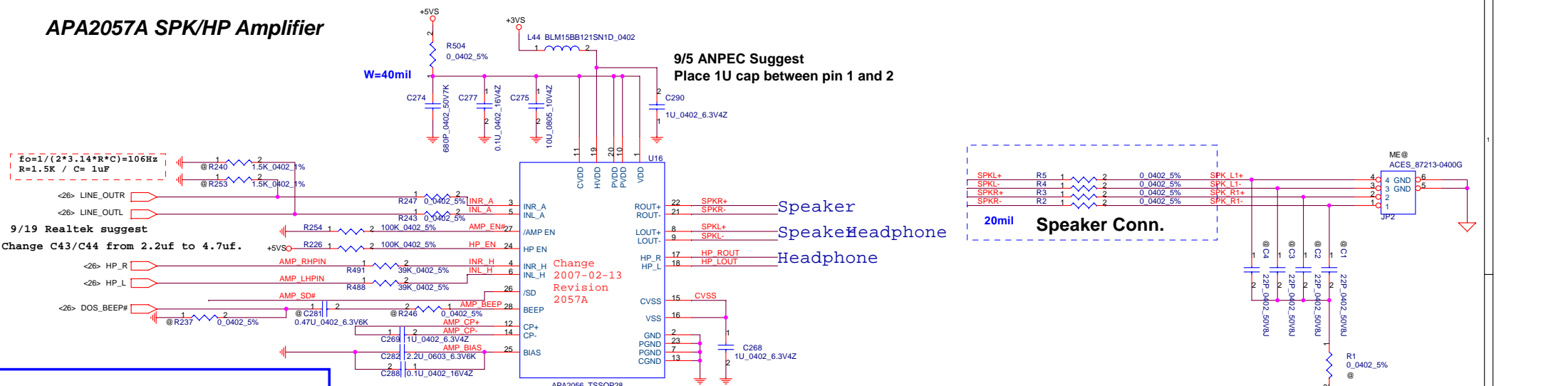
GND GNDA



Security Classification	Compal Secret Data	
Issued Date	2006/08/04	Deciphered Date
		2006/10/06
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Compal Electronics, Inc.	
Title	ALC861 VD Codec
Size	Document Number
Custom	IEL10 LA-3451P
Date:	Thursday, March 08, 2007
Sheet	26 of 45

APA2057A SPK/HP Amplifier

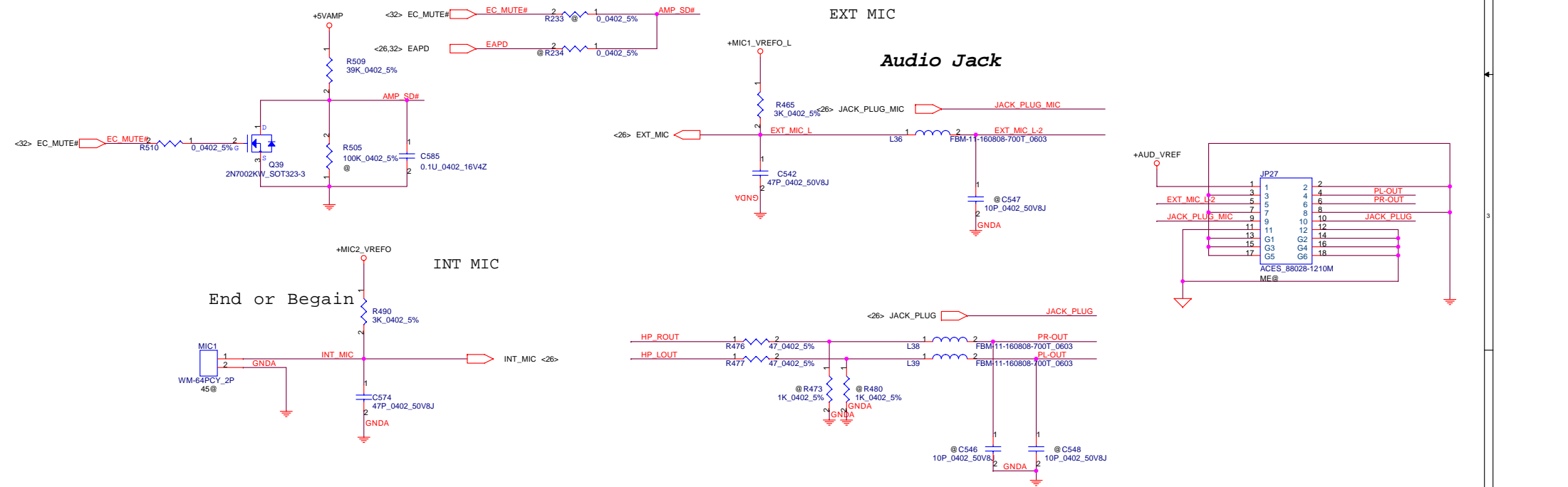


9/5 If implement AMP BEEP, Swap C155 and R79.
R79 change from 0 Ohm to 47K

IN_A Gain = 10dB (Internal Speaker)
IN_H Gain = 0dB (Headphone)

EXT MIC

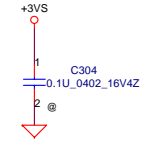
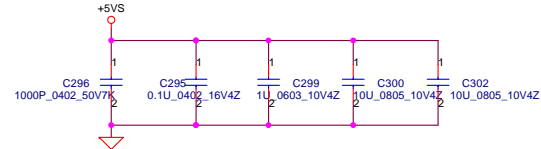
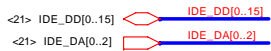
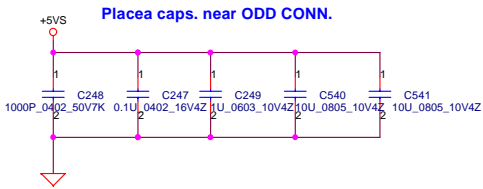
Audio Jack



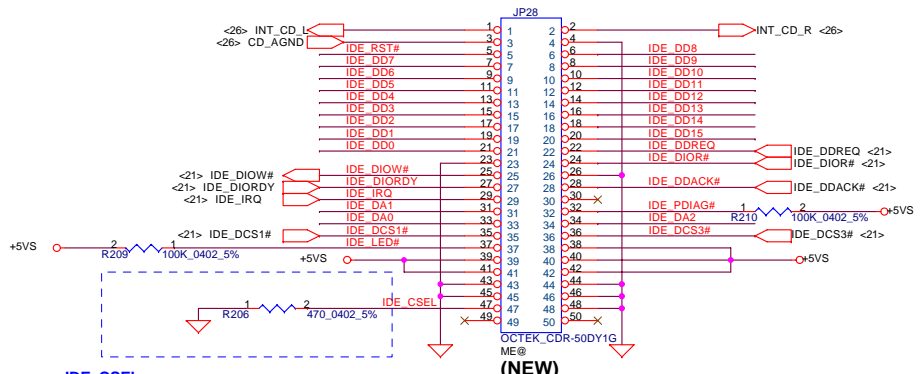
Security Classification	Compal Secret Data	
Issued Date	2006/08/05	Deciphered Date
		2007/08/05

Compal Electronics, Inc.			
Title			
AMP/V/R/Audio Jack/MIC			
Size	Document Number		Rev
Custom	LA-3691P		0.2

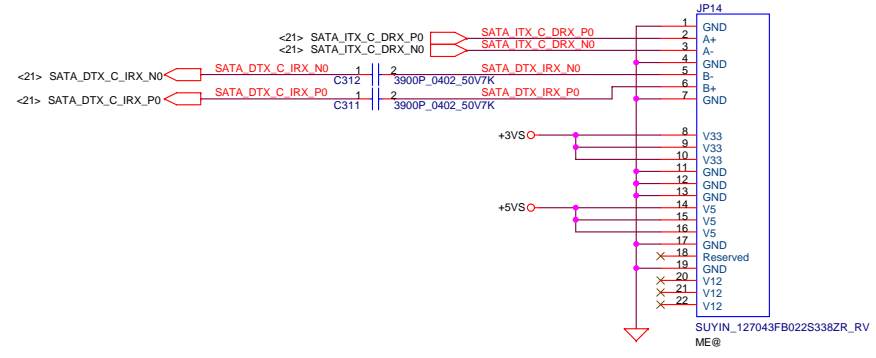
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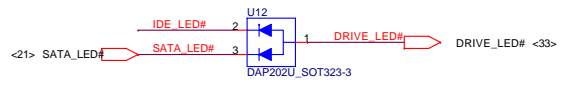
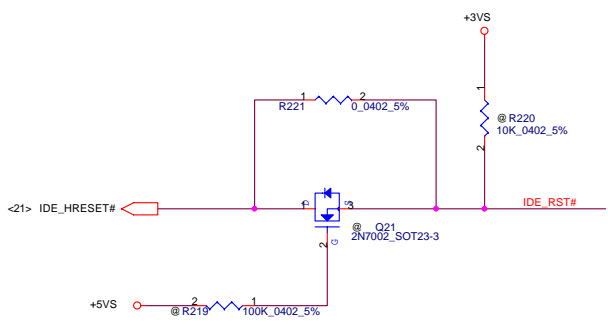
SATA HDD Conn.



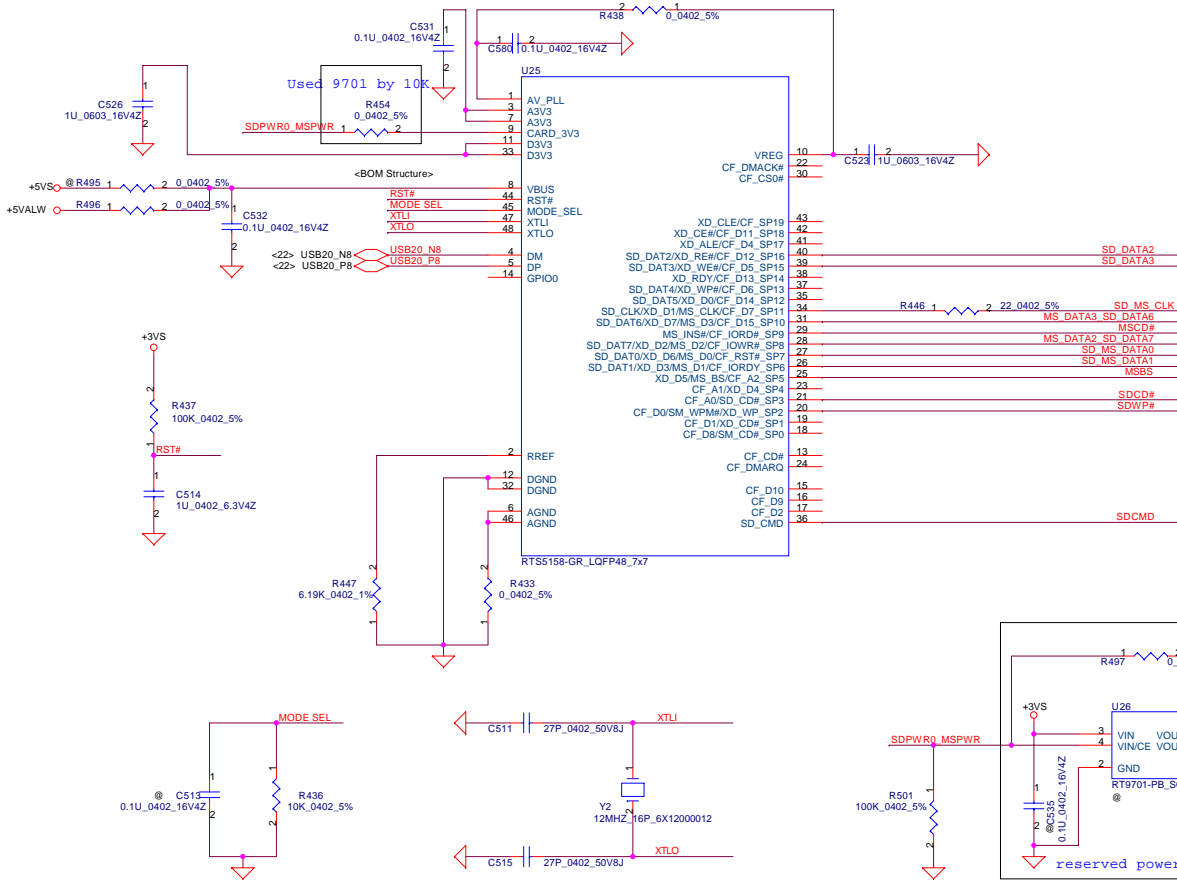
IDE_CSEL
Grounding for Master (When use SATA HDD)
Open or High for Slaver (Normal)



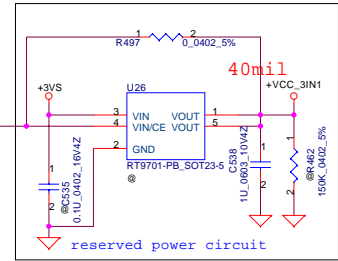
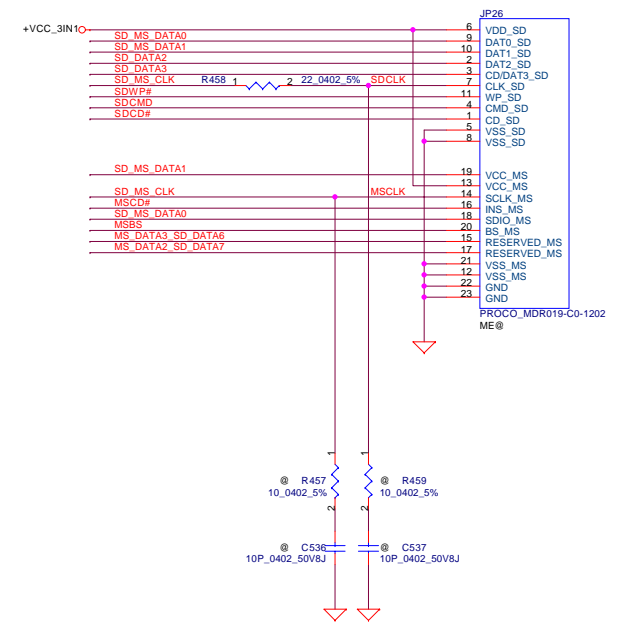
(NEW)
Change Library



Security Classification		Compal Secret Data		Title	
Issued Date	2006/08/18	Deciphered Date	2007/8/18	HDD & ODD Connector	
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				Date: Thursday, March 06, 2007	Sheet 28 of 45

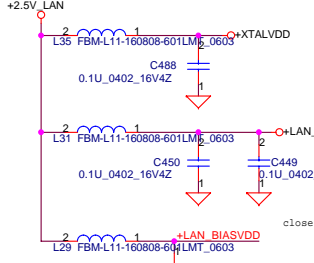


3 in 1 Card Reader

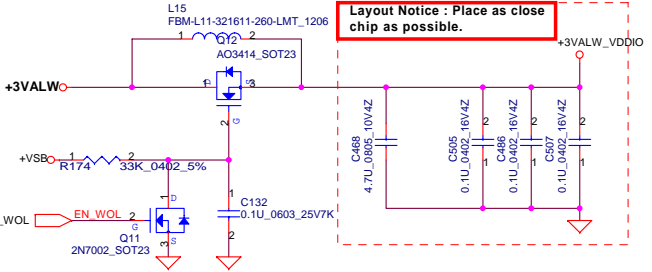


Security Classification	Compal Secret Data		Title	
Issued Date	2006/08/04	Deciphered Date	2006/10/06	1394+3 in 1 Card
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Date: Thursday, March 08, 2007				Sheet 29 of 45

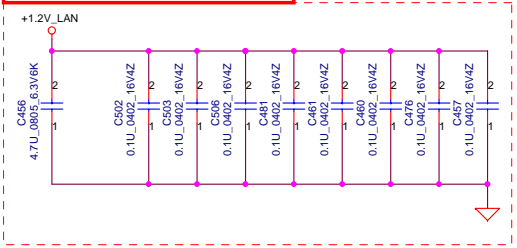
Layout Notice : Filter place as close chip as possible.



Layout Notice : Place as close chip as possible.

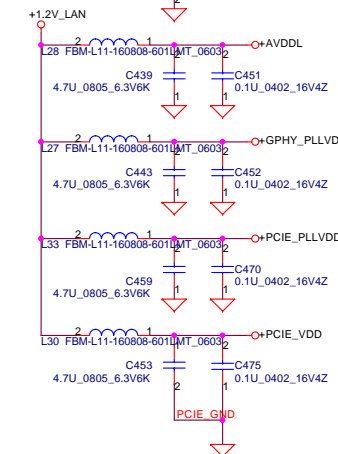


Layout Notice : 1.2V filter. Place as close chip as possible.

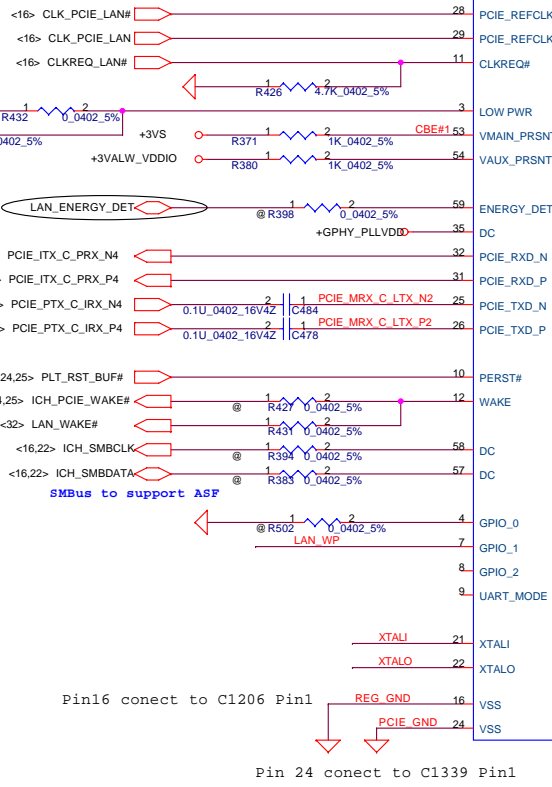
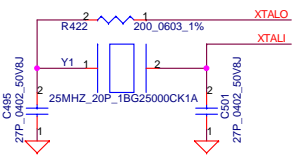


close to each of the pins 38, 45, and 52

(CLKREQ#) and (ENERGY_DET) are only supported in BCM5787M

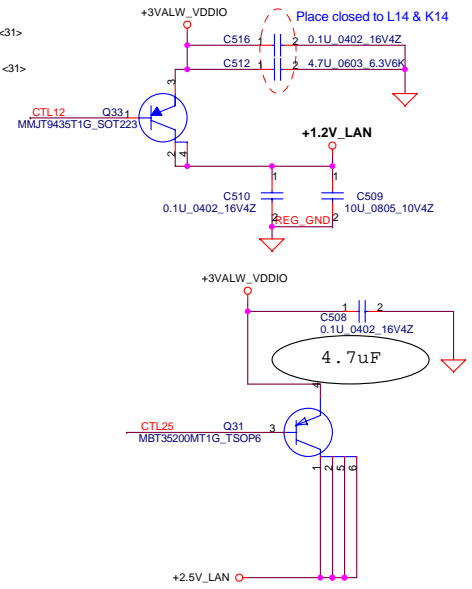
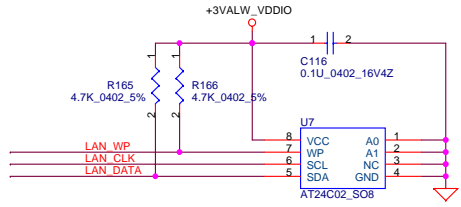


No CIS symbol



Pin16 connect to C1206 Pin1

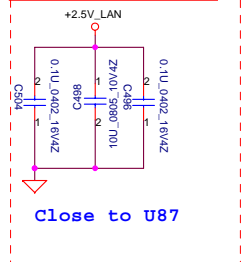
Pin 24 connect to C1339 Pin1



Notice : 4.7u 6.3V capacitor Thickness 1.25mm

Layout Notice : Filter place as close chip as possible.

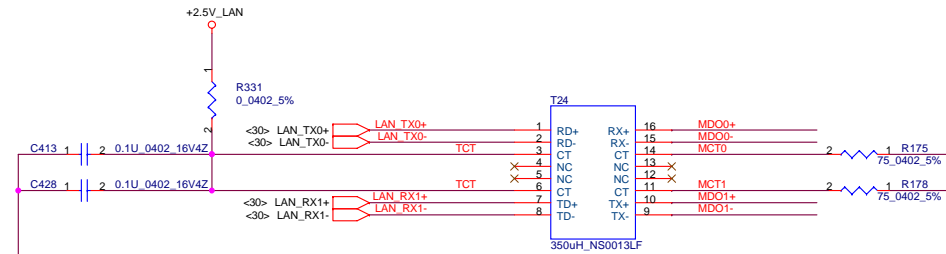
Layout Notice : Place as close chip as possible.



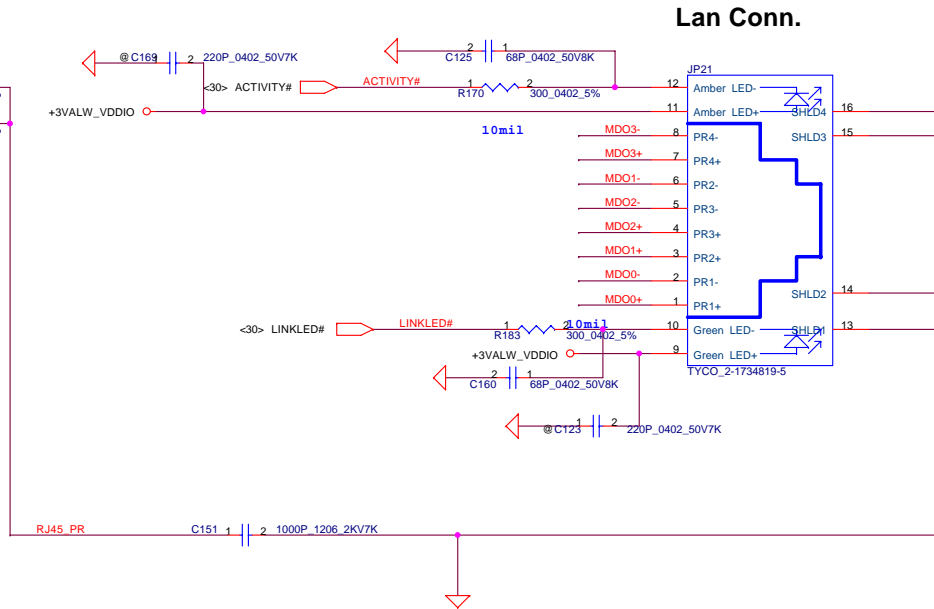
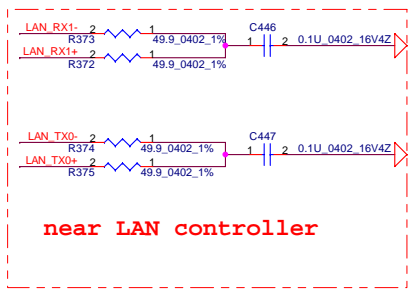
Close to U87

Security Classification	Compal Secret Data	
Issued Date	2006/08/04	Deciphered Date
		2006/10/06
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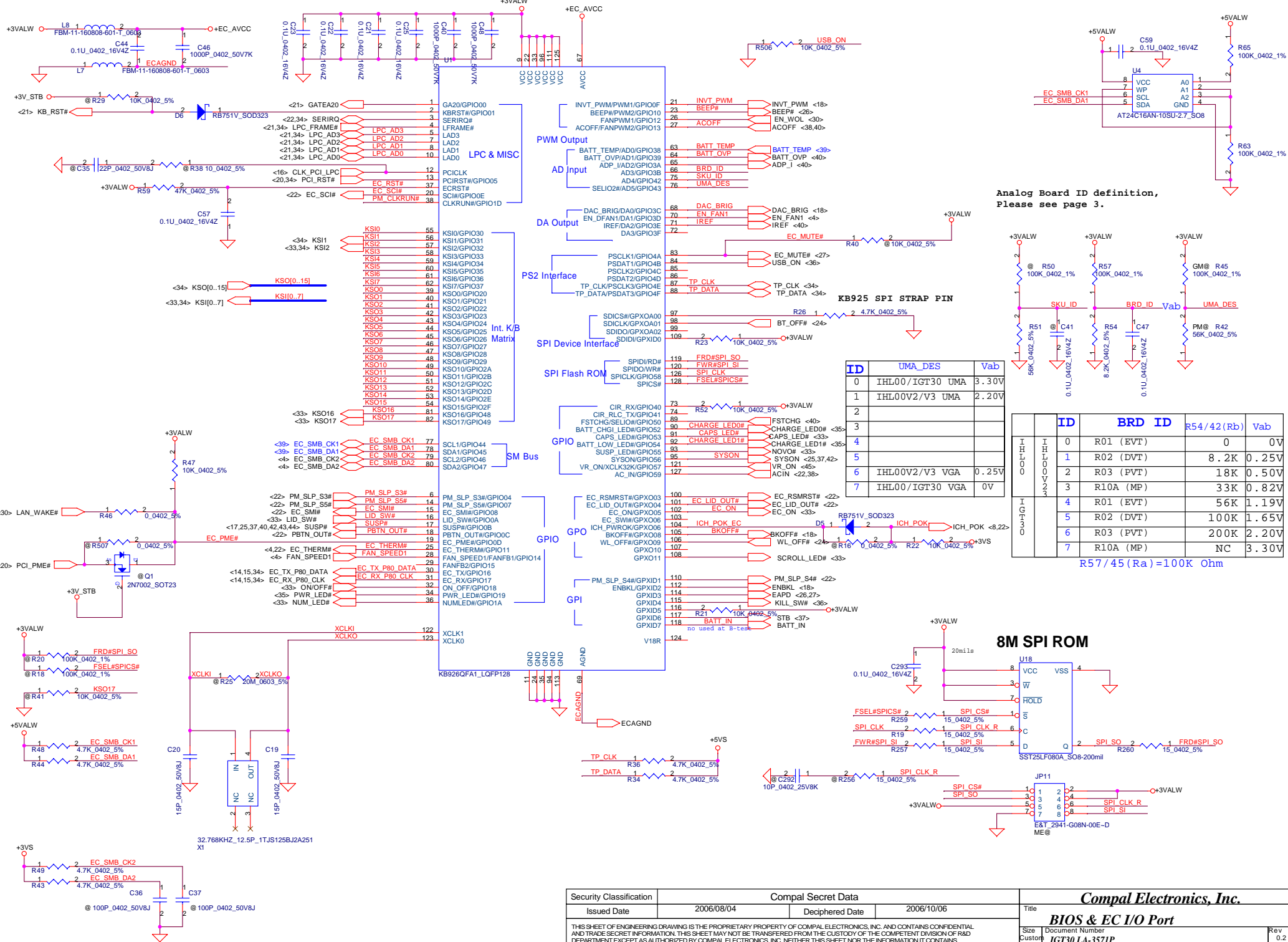
Compal Electronics, Inc.		
Title	BCM5787M-GLAN	
Size	Document Number	Rev
Customer	IEL20 LA-3471P	0.2
Date	Thursday, March 08, 2007	Sheet 30 of 45



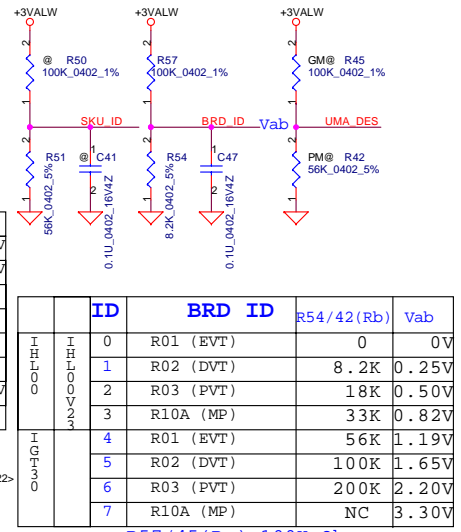
Change C468,C470,C473,C474,C475,C476 from 0.01uF to 0.1uF



Security Classification	Compal Secret Data			Compal Electronics, Inc.		
Issued Date	2006/08/04	Deciphered Date	2006/10/06	Title LAN CONTROLLER		
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Date:	Thursday, March 08, 2007	Sheet	31	of	45	

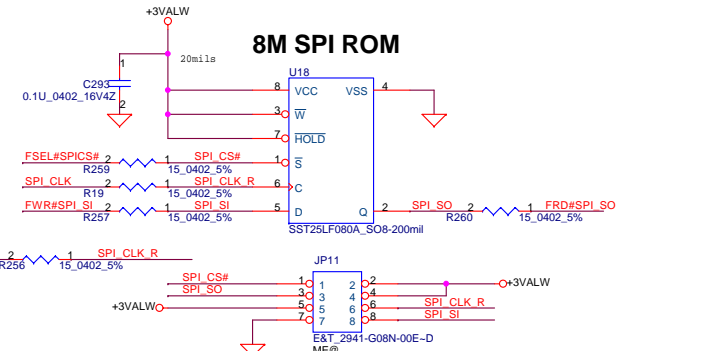


Analog Board ID definition, Please see page 3.



ID	UMA_DES	Vab
0	IHL00/IGT30 UMA	3.30V
1	IHL00V2/V3 UMA	2.20V
2		
3		
4		
5		
6	IHL00V2/V3 VGA	0.25V
7	IHL00/IGT30 VGA	0V

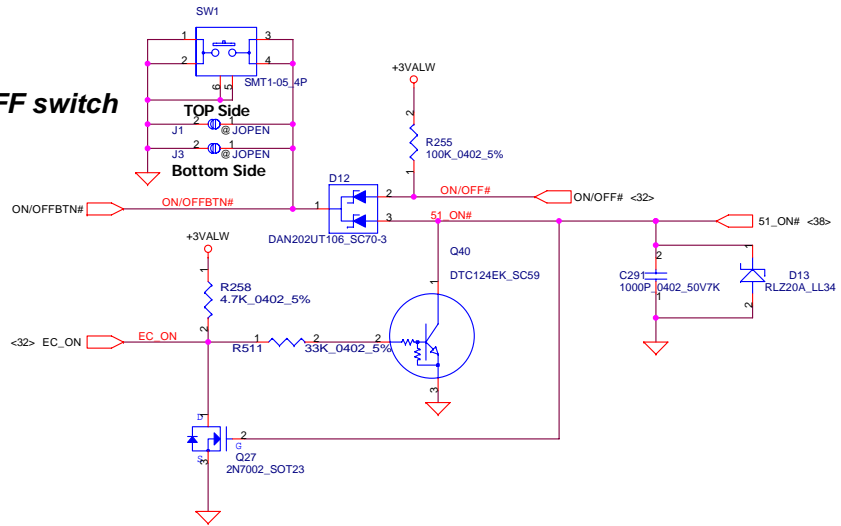
8M SPI ROM



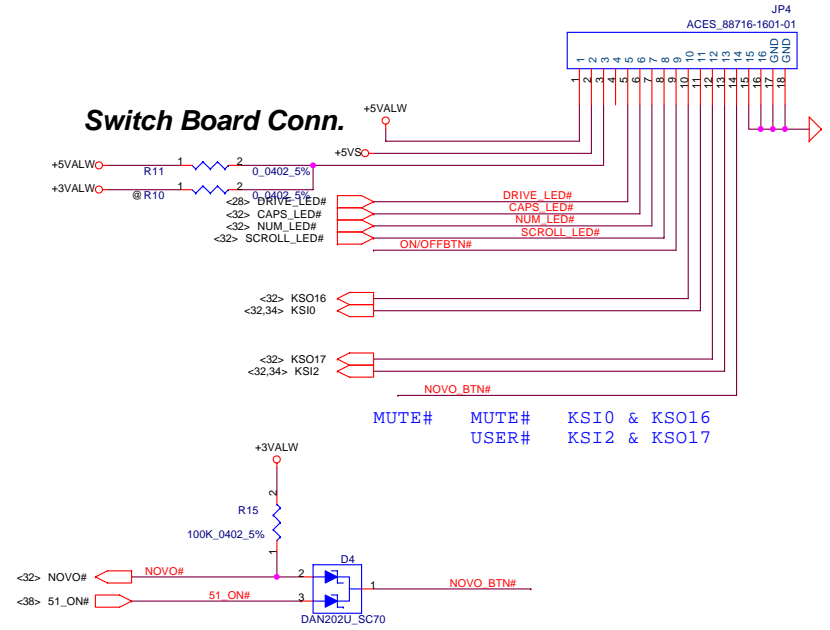
Security Classification		Compal Secret Data		<p align="center">Compal Electronics, Inc.</p> <p align="center">BIOS & EC I/O Port</p>	
Issued Date	2006/08/04	Deciphered Date	2006/10/06		
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Power Button

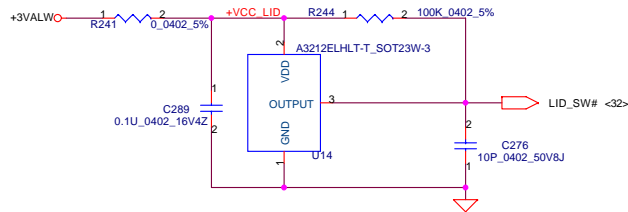
ON/OFF switch



Switch Board Conn.

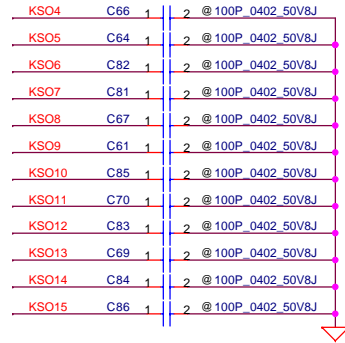
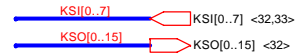


Lid Switch

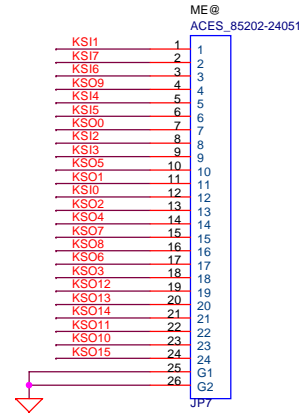


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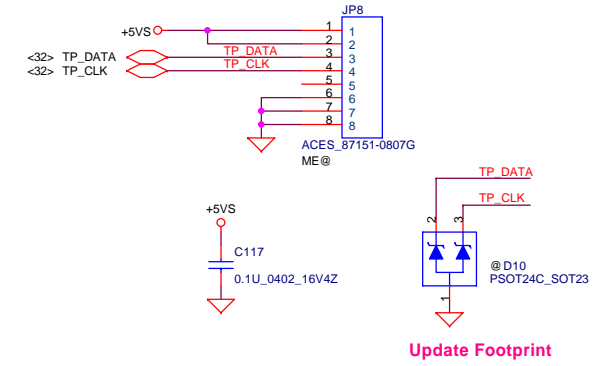
INT_KBD Conn.



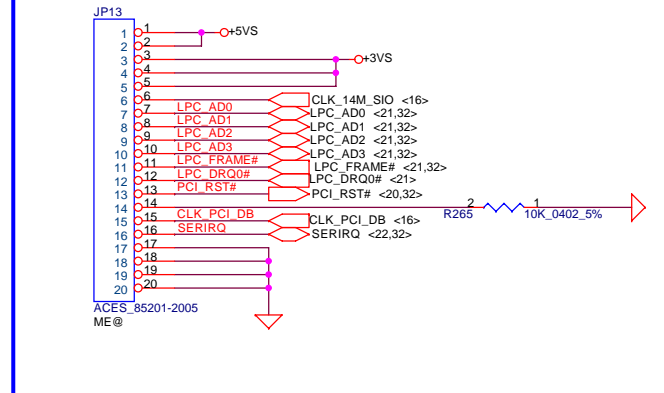
For IHL00



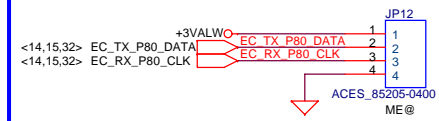
To TP/B Conn.



FOR LPC SIO DEBUG PORT

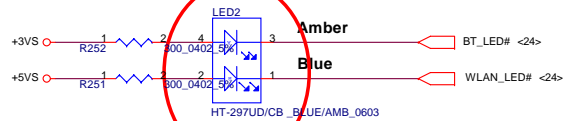
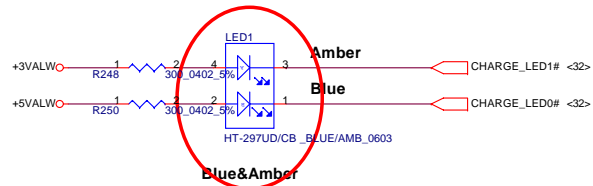
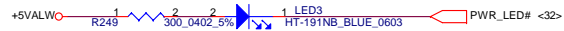


EC DEBUG PORT



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LED

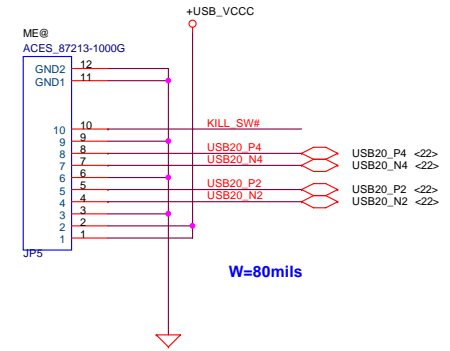
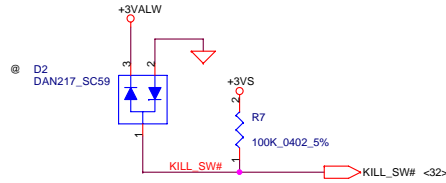


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				Size	Document Number	Rev
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USB Conn.

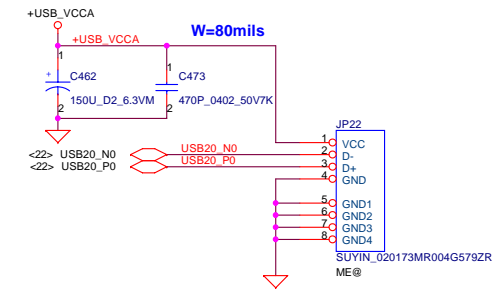
W=80mils

Kill SWITCH

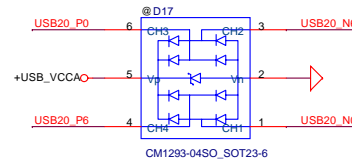


W=80mils

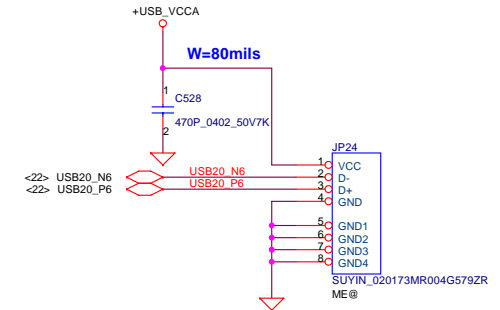
USB CONN. 1



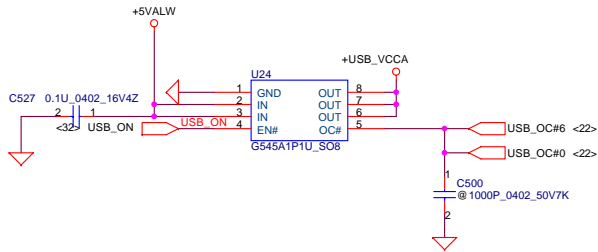
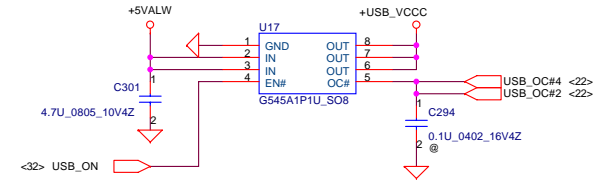
W=80mils



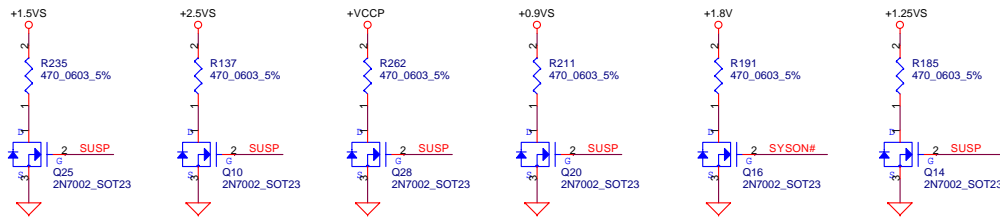
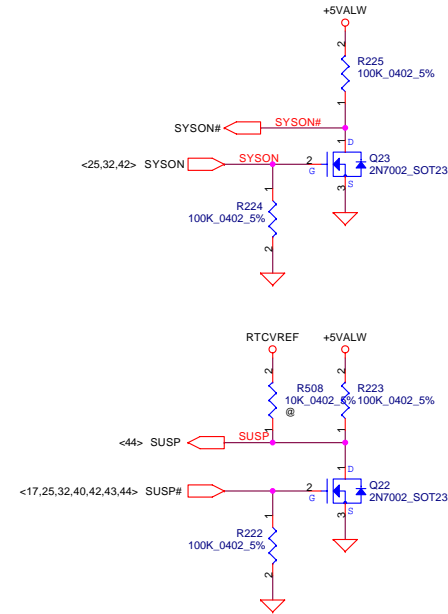
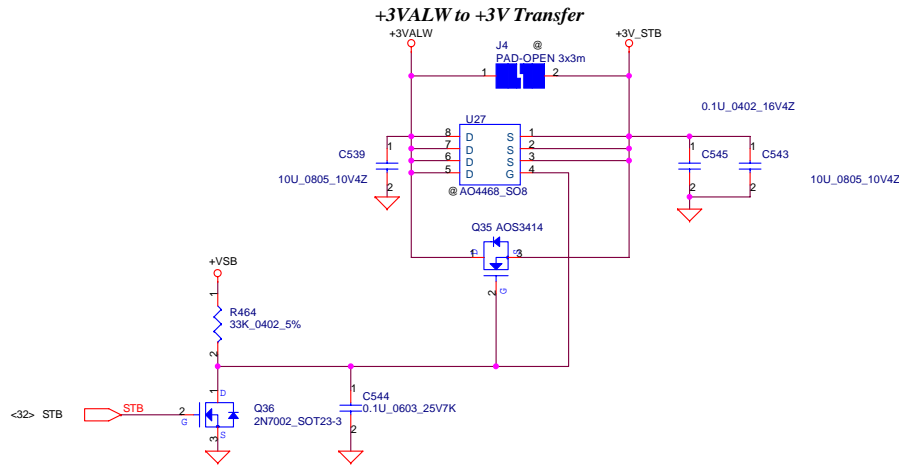
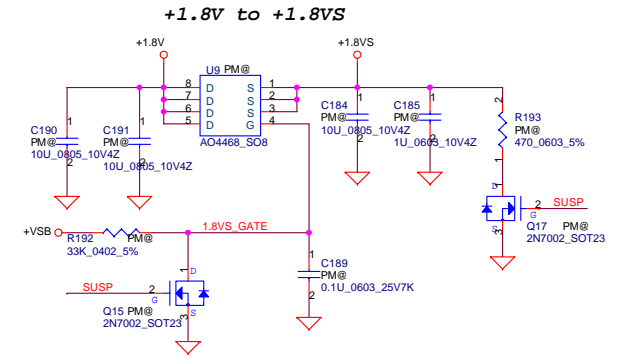
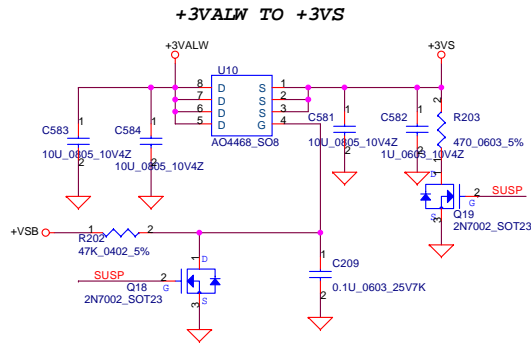
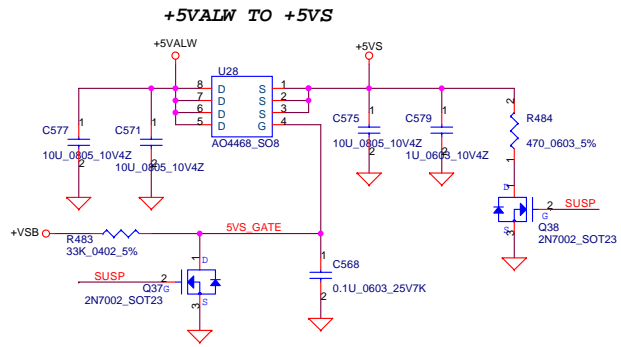
USB CONN. 2



W=80mils

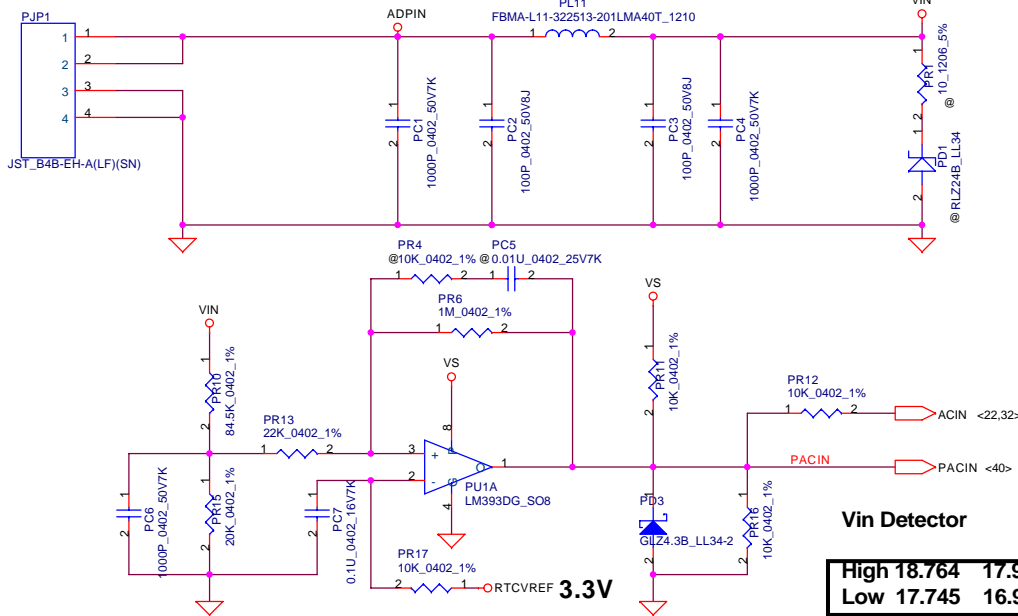


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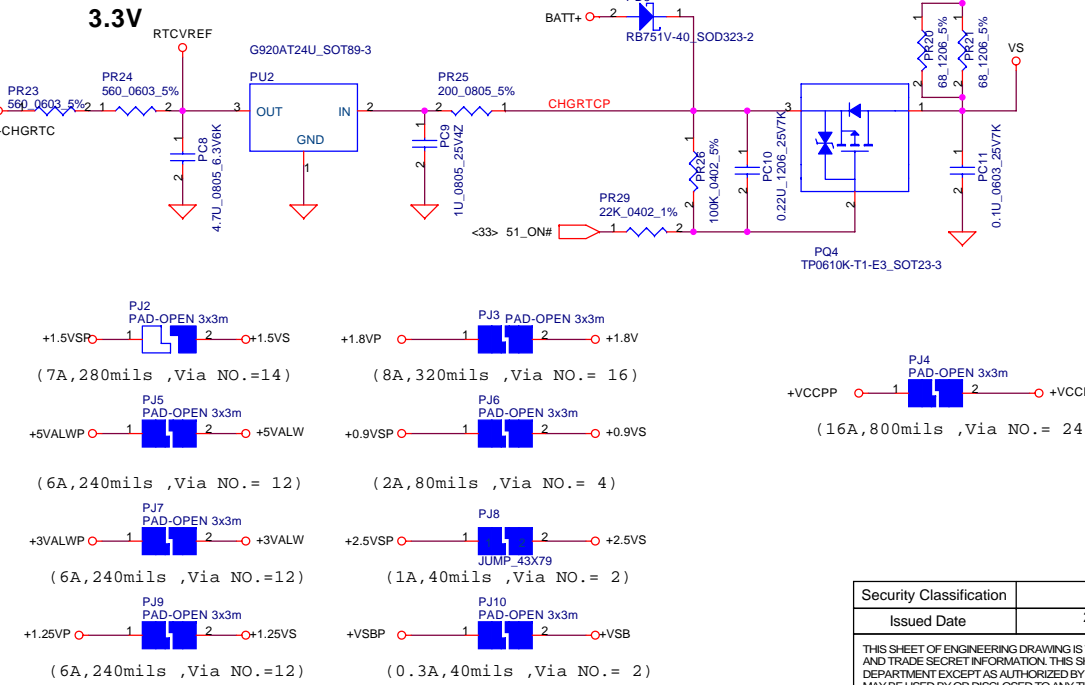
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Issued Date	2006/08/18	Deciphered Date	2007/8/18	DC Interface	
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DC030005Q00



Vin Detector

High	18.764	17.901	17.063
Low	17.745	16.9	16.03

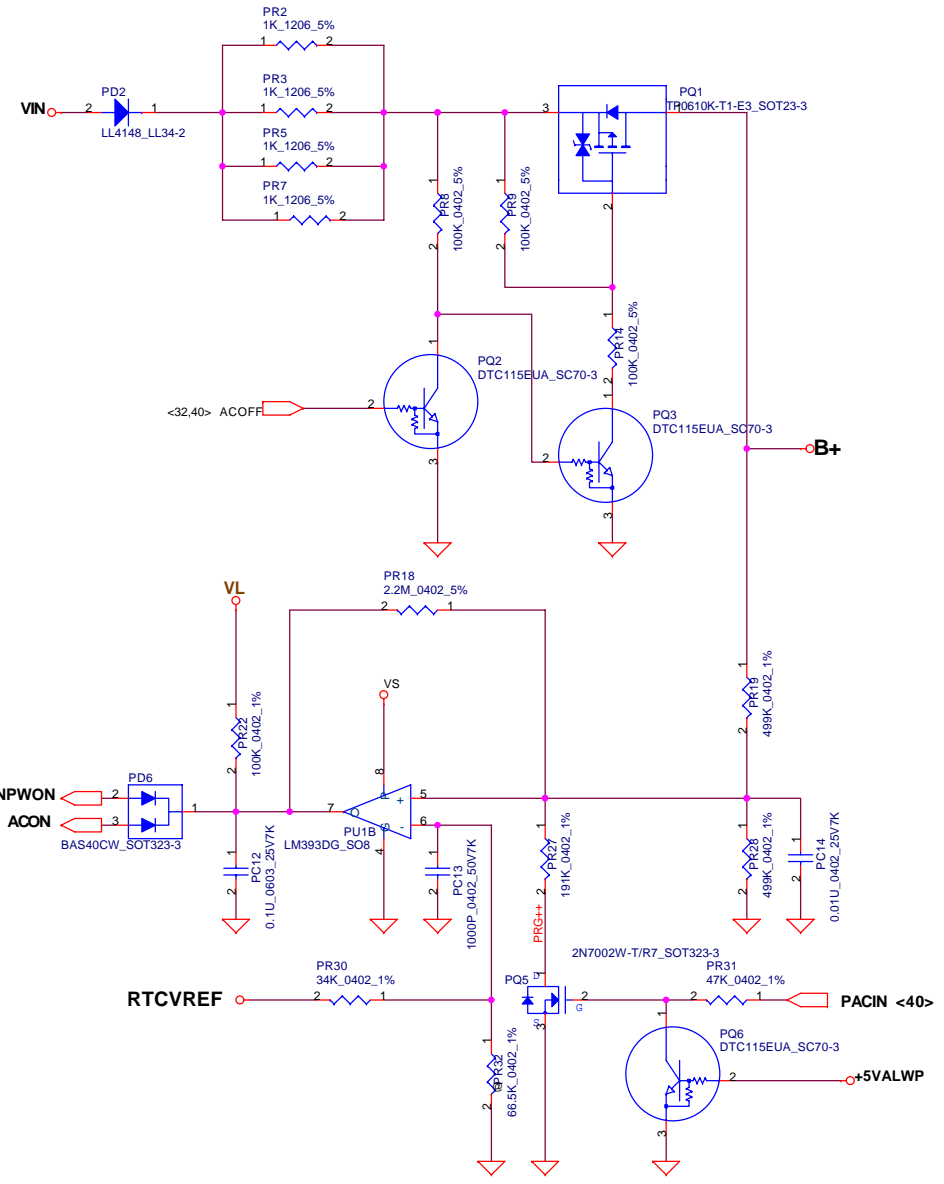


ACIN

	Min.	typ.	Max.
H->L	14.589V	14.84V	15.243V
L->H	15.562V	15.97V	16.388V

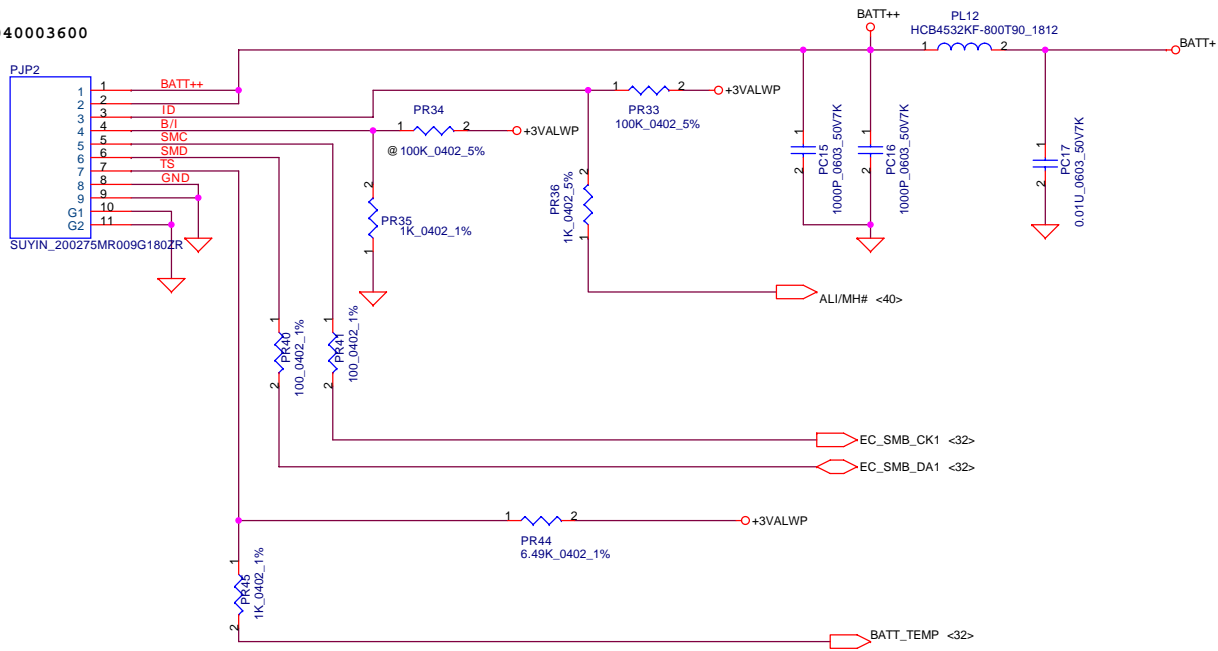
BATT ONLY

	Min.	typ.	Max.
H->L	6.138V	6.214V	6.359V
L->H	7.196V	7.349V	7.505V

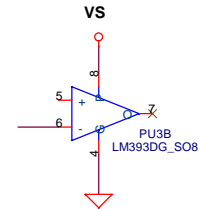
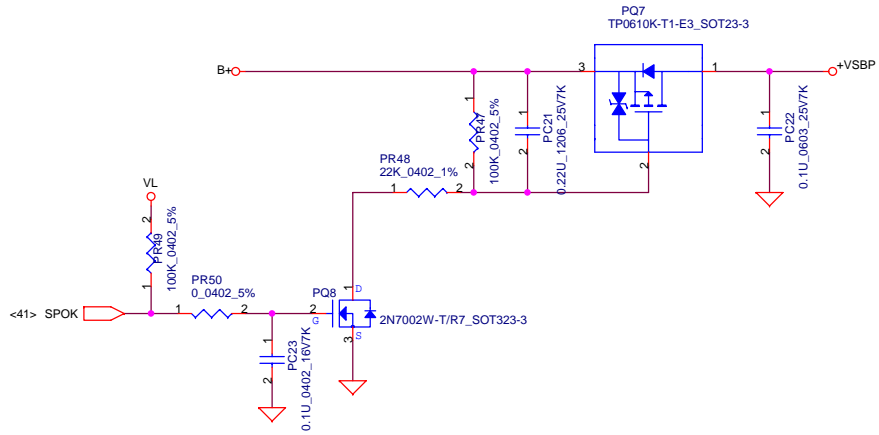
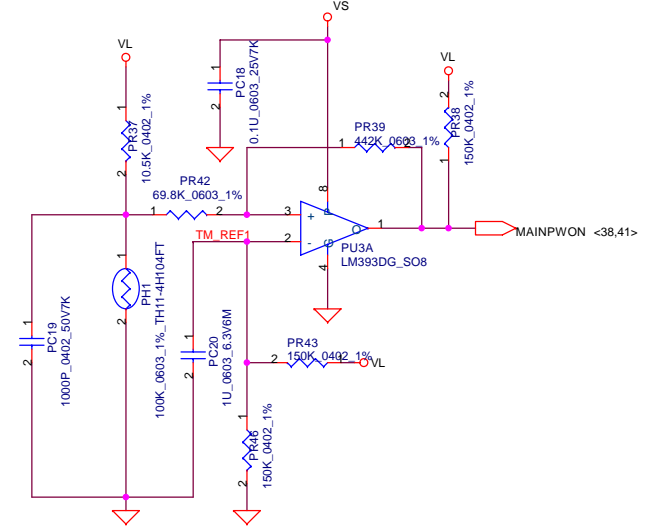


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Title DCIN/DECTOR			
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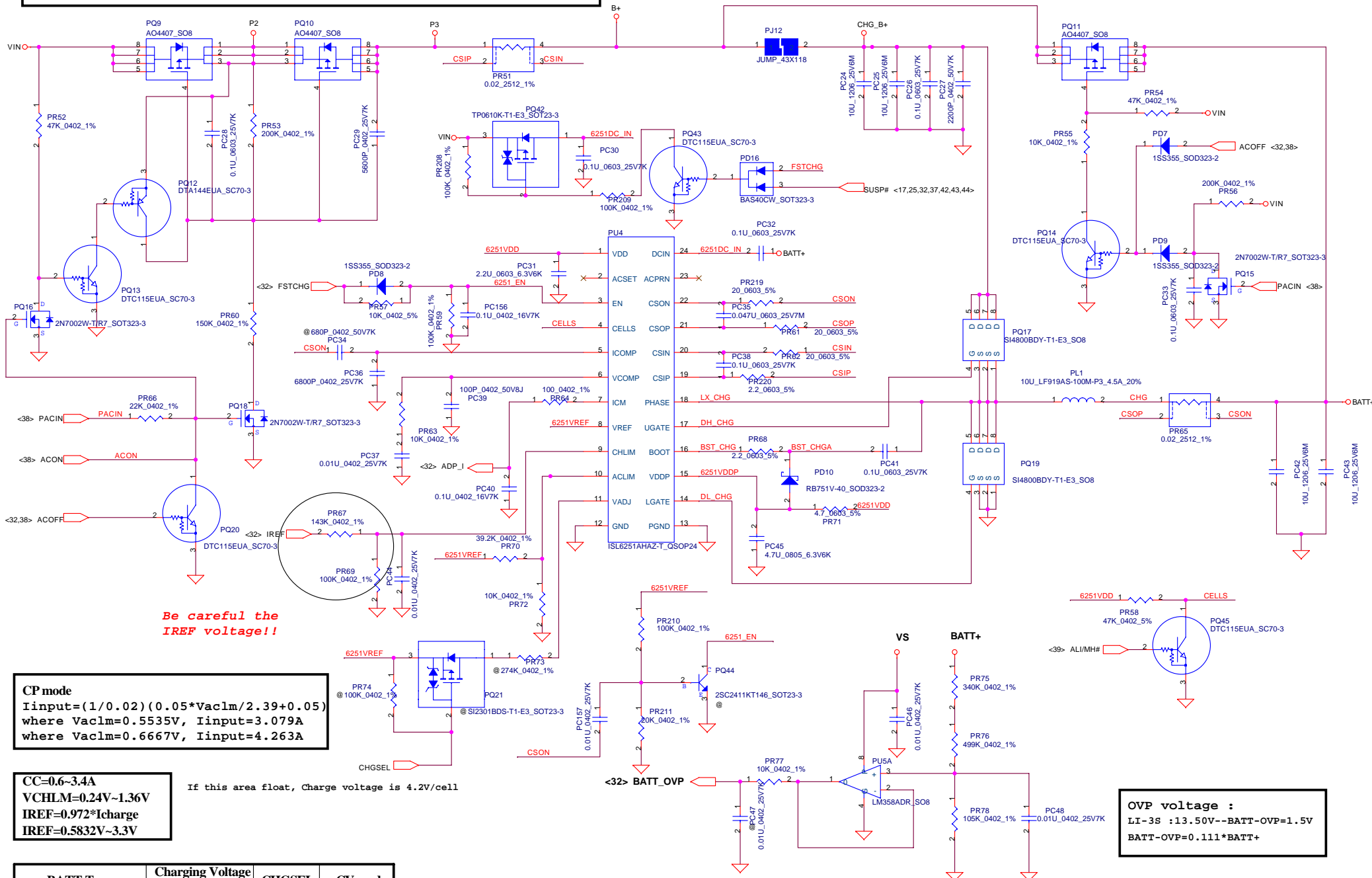
PH1 under CPU bottom side :
 CPU thermal protection at 87 degree C
 Recovery at 70 degree C



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65W, Iadapter=0~3.42A, Current sense=0.02ohm, PR70=39.2K, CP=3.079A
 90W, Iadapter=0~4.74A, Current sense=0.015ohm, PR70=28.7K, CP=4.263A

$$ADP_I = 19.9 * I_{adapter} * R_{sense}$$



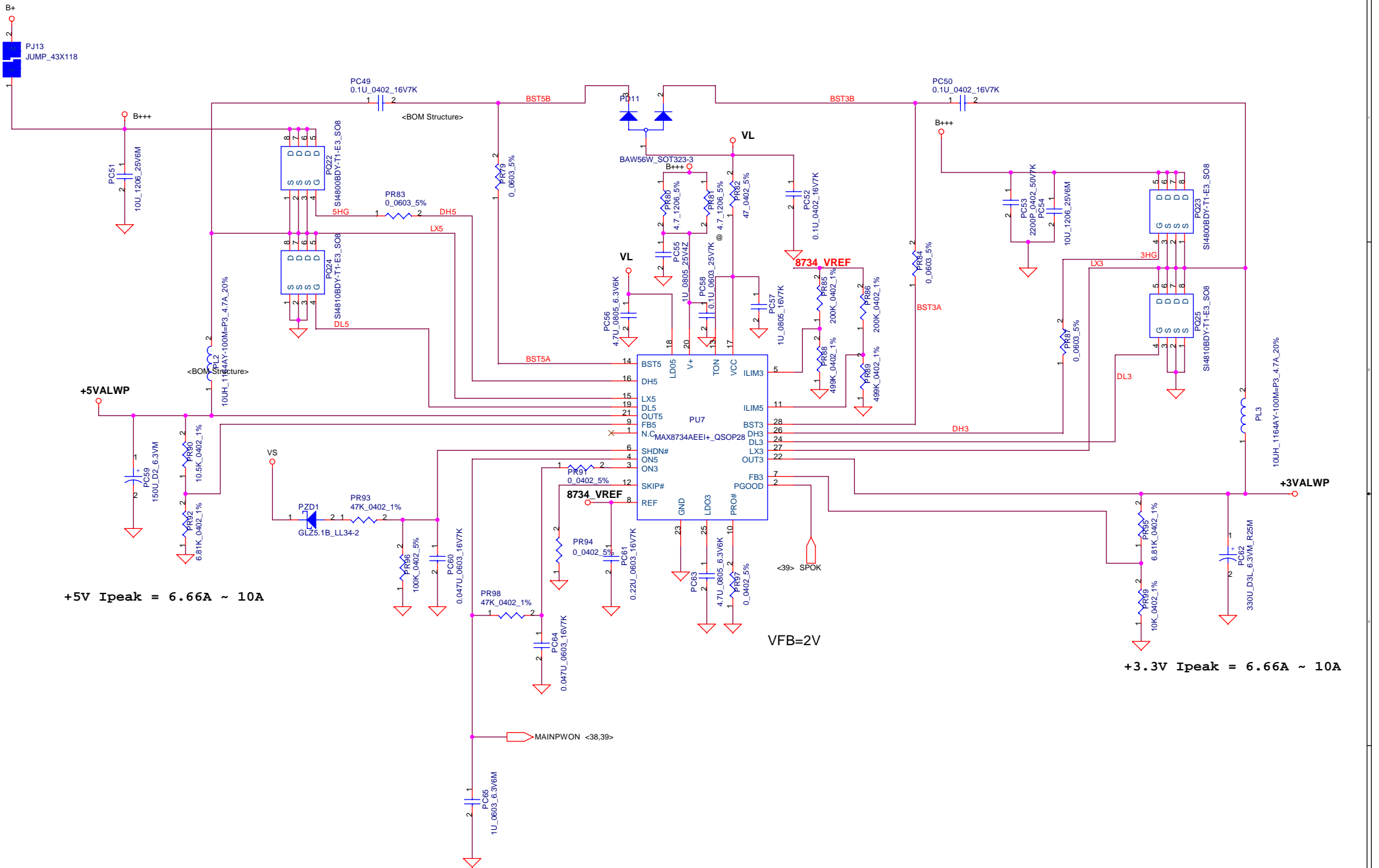
Be careful the IREF voltage!!

CP mode
 $I_{input} = (1/0.02) (0.05 * V_{aclm} / 2.39 + 0.05)$
 where $V_{aclm} = 0.5535V$, $I_{input} = 3.079A$
 where $V_{aclm} = 0.6667V$, $I_{input} = 4.263A$

CC=0.6~3.4A
 $V_{CHLM} = 0.24V \sim 1.36V$
 $I_{REF} = 0.972 * I_{charge}$
 $I_{REF} = 0.5832V \sim 3.3V$
 If this area float, Charge voltage is 4.2V/cell

OVP voltage :
 $LI-3S : 1.3.50V \sim BATT-OVP = 1.5V$
 $BATT-OVP = 0.111 * BATT+$

BATT Type	Charging Voltage (0x15)	CHGSEL	CV mode
2800mAH 3S pack	13050mV	LOW	12.90V
Normal 3S LI-ON Cells	12600mV	HIGH	12.60V

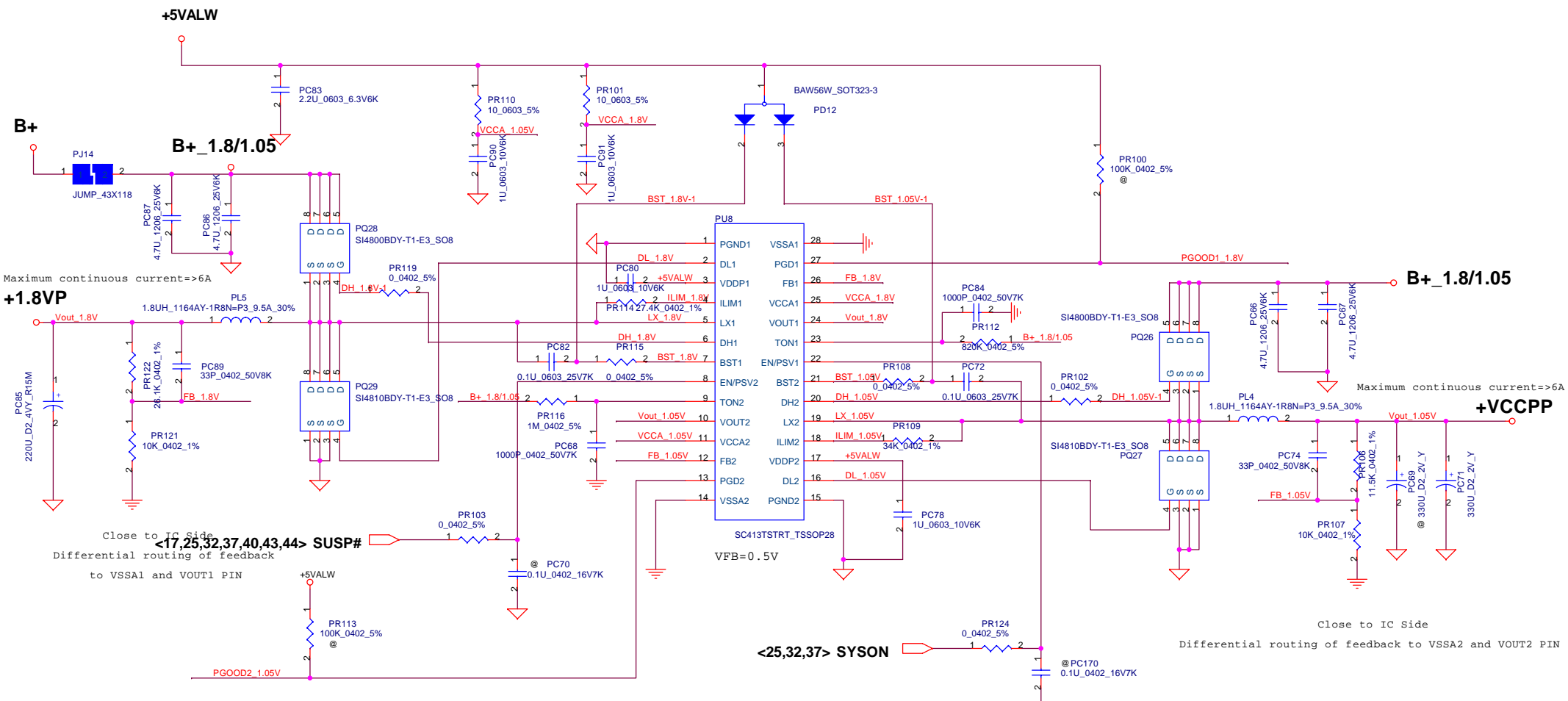


+5V Ipeak = 6.66A ~ 10A

+3.3V Ipeak = 6.66A ~ 10A

VFB=2V

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Maximum continuous current=>6A

Maximum continuous current=>6A

Close to IC Side
Differential routing of feedback
to VSSA1 and VOUT1 PIN

Close to IC Side
Differential routing of feedback to VSSA2 and VOUT2 PIN

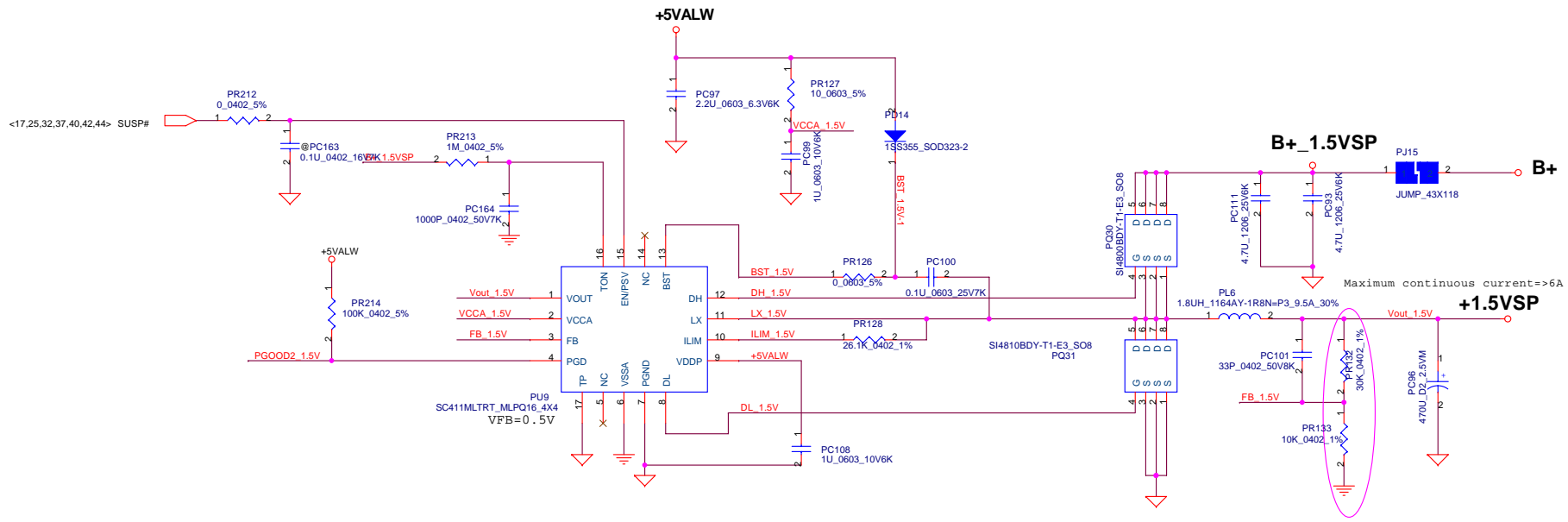
VFB=0.5V
 $V_o = VFB * (1 + PR122 / PR127) = 1.805V$
Ipeak=12.17A, Imax=8.519A
 $Ton = (3.3E-12 * (PR121 + 37K) * (Vout / VBat)) + 50ns$
 $= 3.3 * 10e-12 * (820K + 37K) * (1.8 / 19) + 50ns = 0.3179us$
 FDS6670AS:Rds(on)=>Typ:9 mOhm
 Max:11.5 mOhm
 $Iocp = Ivalley + Iripple / 2$
 $Iripple = (vin - vout) * (Ton / L) = 5.467A, 1/2 Iripple = 2.734A$
 $Ivalleymin = 10E-6 * (PR120 / Rds(ON)max * 1.5)$
 $= 9 * 10e-6 * (27.4K / 0.0115 * 1.5) = 14.295A > 11.73 * 1.2 = 14.076A$
 $Ivalleymax = 10E-6 * (PR120 / Rds(ON)typ * 1.2)$
 $= 11 * 10e-6 * (27.4K / 0.009 * 1.2) = 27.907A$
 OCP=>17.029A~30.641A

VFB=0.5V
 $V_o = VFB * (1 + PR129 / PR130) = 1.5V$
Ipeak=5.16A, Imax=3.612A
 $Ton = (3.3E-12 * (PR125 + 37K) * (Vout / VBat)) + 50ns$
 $= 0.3201us$
 AO4916 Rds(on)=>Typ:21 mOhm
 Max:27 mOhm
 $Ivalleymin = 9 * 10u * (29.4K / 0.027 * 1.4) = 7A$
 $Ivalleymax = 11 * E-6 * (29.4K / 0.021 * 1.1) = 12.833A$
 $Iripple = (vin - vout) * (Ton / L) = 2.546A, 1/2 Iripple = 1.273A$
 $Iocp = Ivalley + Iripple / 2$
 OCP=>8.273A~14.106A

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B+ 1.5VSP

Maximum continuous current=>6A

+1.5VSP

Vout 1.5V

Close to IC Side
Differential routing of feedback to VSSA2 and VOUT2 PIN

VFB=0.5V, Ipeak=14.02A, Imax=9.814A

The current rating of +1.05VSP include +VCC_GFX current.

$V_o = V_{FB} * (1 + PR146 / PR147) = 1.05V$

$Ton = (3.3E-12 * (PR142 + 37K) * (Vout / VBat)) + 50ns = 0.2391us$

SI4810BDY:Rds(on) => Typ: 9mOhm
Max: 11.5 mOhm

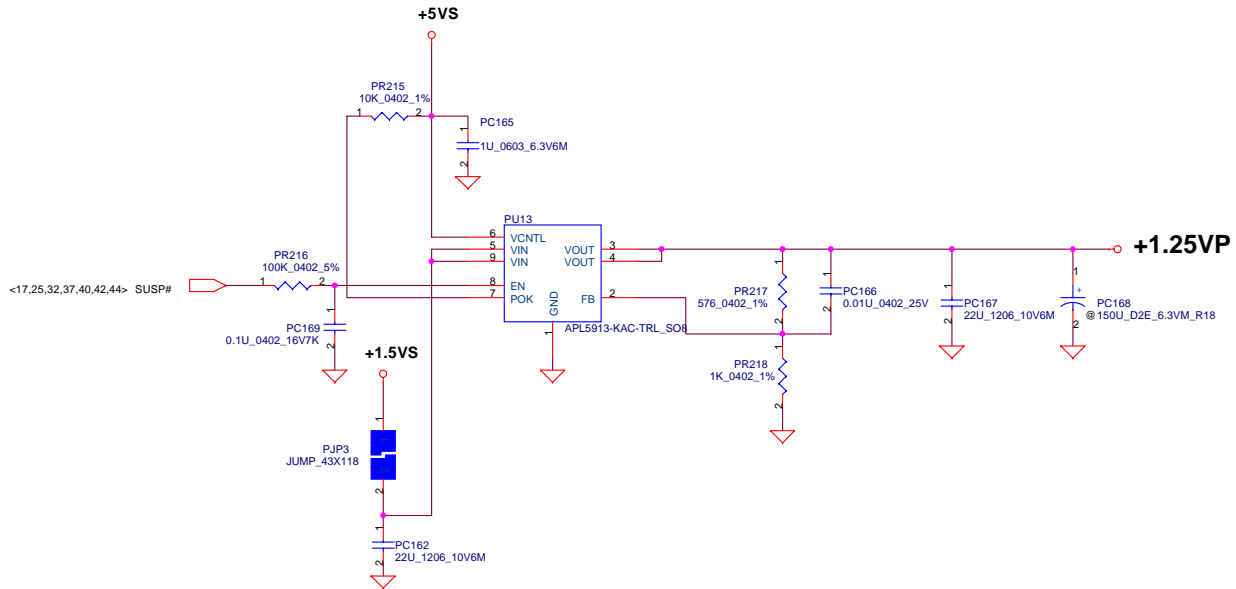
$I_{valleymin} = 9 * 10E-6 * (PR145 / Rds(ON))_{max} * 1.5$
= 9 * 10E-6 * (26.1K / (0.0115 * 1.5)) = 13.617A

$I_{valleymax} = 11 * 10E-6 * (PR145 / Rds(ON))_{min} * 1.2$
 $= 11 * 10E-6 * (26.1K / (0.009 * 1.3)) = 20.076A$

$I_{ripple} = (vin - vout) * (Ton / L) = 4.292A, 1/2 I_{ripple} = 2.146A$

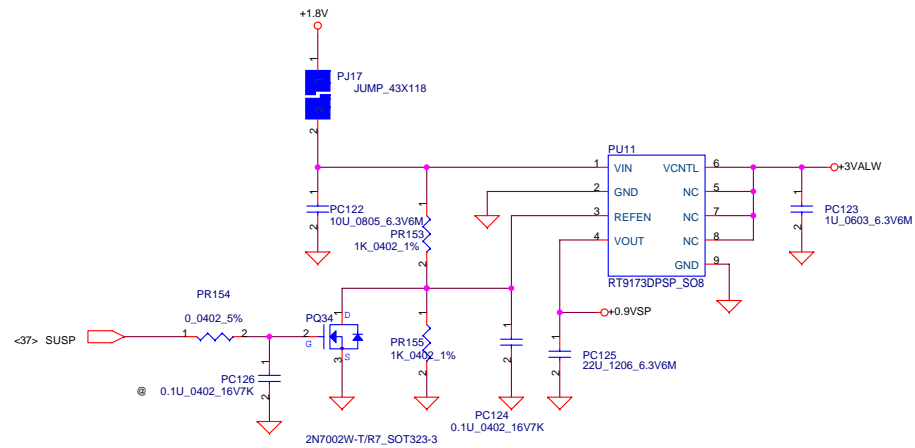
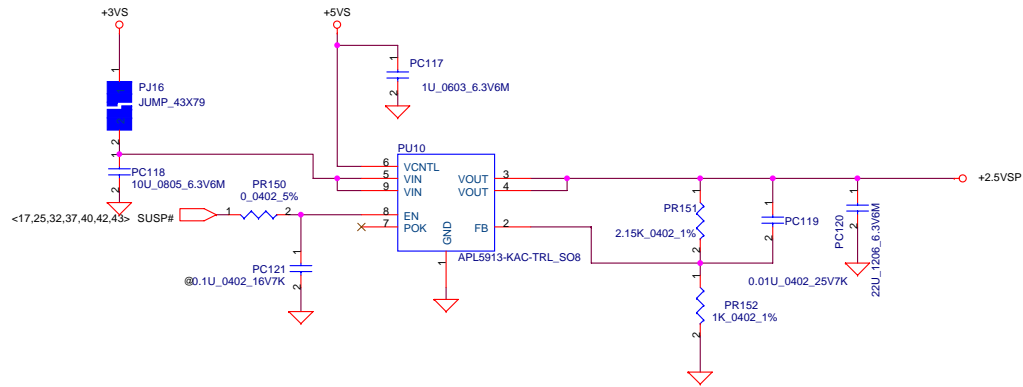
$I_{ocp} = I_{valley} + I_{ripple} / 2$
OCP => 15.763A ~ 22.222A

Ipeak=2.91A, Imax=2A.
Vo=0.8 * (1 + PR190 / PR191) = 1.2608V



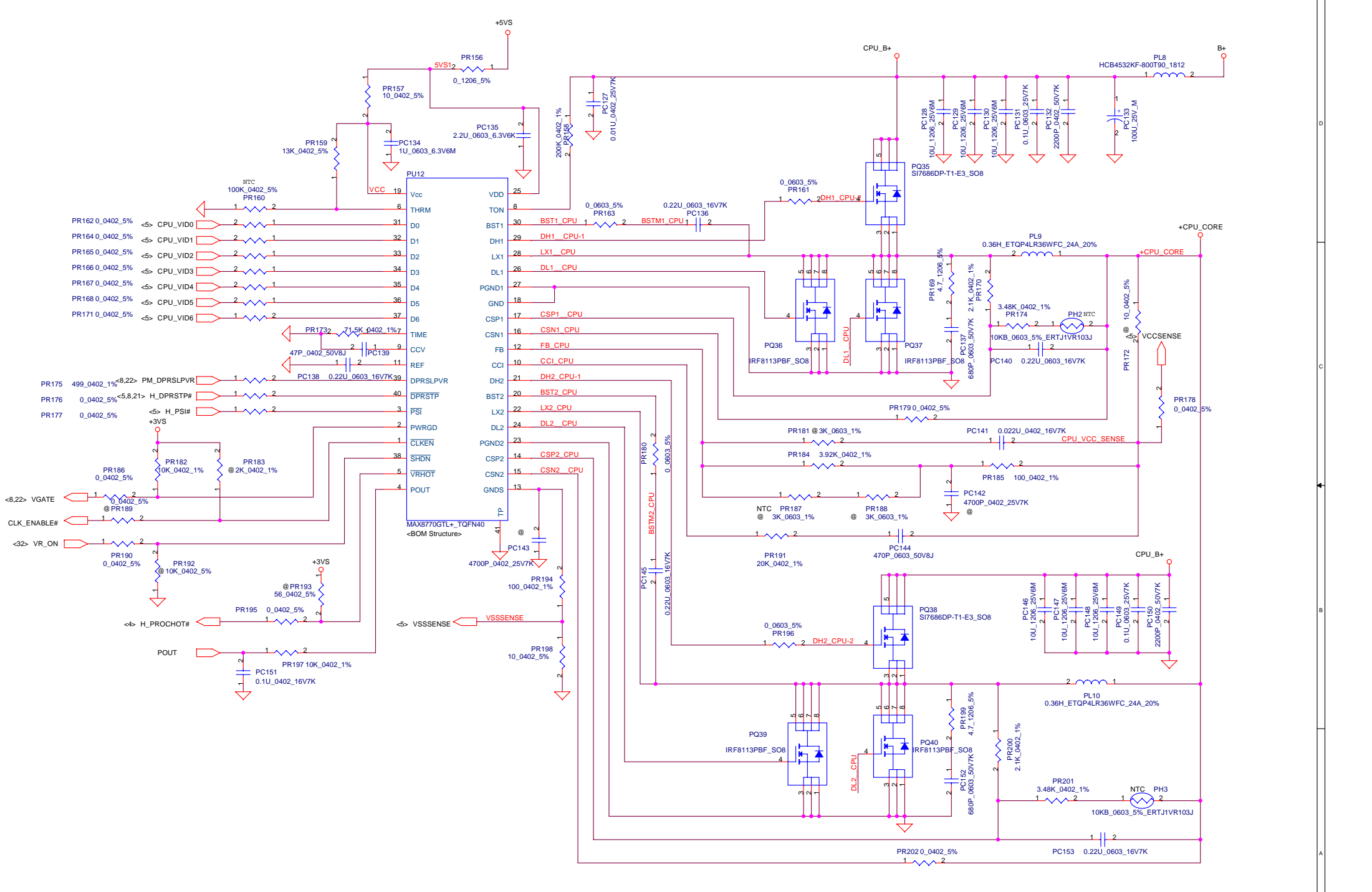
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Issued Date	2005/10/17	Deciphered Date	2006/10/17	1.5VSP/1.25VP
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Compal Electronics, Inc. +2.5VSP/0.9VSP		
Title		
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+CPU_CORE
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Version change list (P.I.R. List)

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1						
2						
3						
4						
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Version change list (P.I.R. List)

Item	Fixed Issue	Rev.	PG#	Modify List	B. Ver#	Phase
1	XDP_BPM#0~4 test point short as EMI request	B	4	Modify Layout		
2	ADD J6 for +VCC_AXG UMA VGA power shape	B	11	Modify Layout		
3	Fixed Speaker no function	A2	37	Change Q91 form SI2301BDS to MMBT3906, Del R895		
4	Fixed SWDJ function can't work	A2	36	Add R904		
5	Fixed Audio Codec can't work	A2	29	Add R905,Q96		
6	Fixed USB Port4 can't work	A2	27	Swap USB_N4 & USB_P4		
7	Fixed EMI issue	A2	32 37	Add R908,C878,C879		
8	Fixed SWDJ mode EC_MUTE# ISSUE	B	30	Add D39,Q99,R914		
9	Fixed CMOS noise	B	36	Add R912,C880		
10	Fixed EMI	B	25	Add C881,C882		
11	Add chipset id	B	33	Add R915,R916		
12	Fix SWDJ Subwoofer issue	B	31	Add R917		
13	Fix DFX issue	C	22,33	Change Y3,X1,Y2 footprint		
14	FOR E-STAR V4 wake on lan	C	22,33	Add R918,R919		
15	For ESD issue	C	36	Add C883~C887 D40,D41		
16	For AUDIO team design	C	30	Add R920 R921		
17	Change LAN led function	C	25	Swap JP73 PIN12 & PIN14		

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