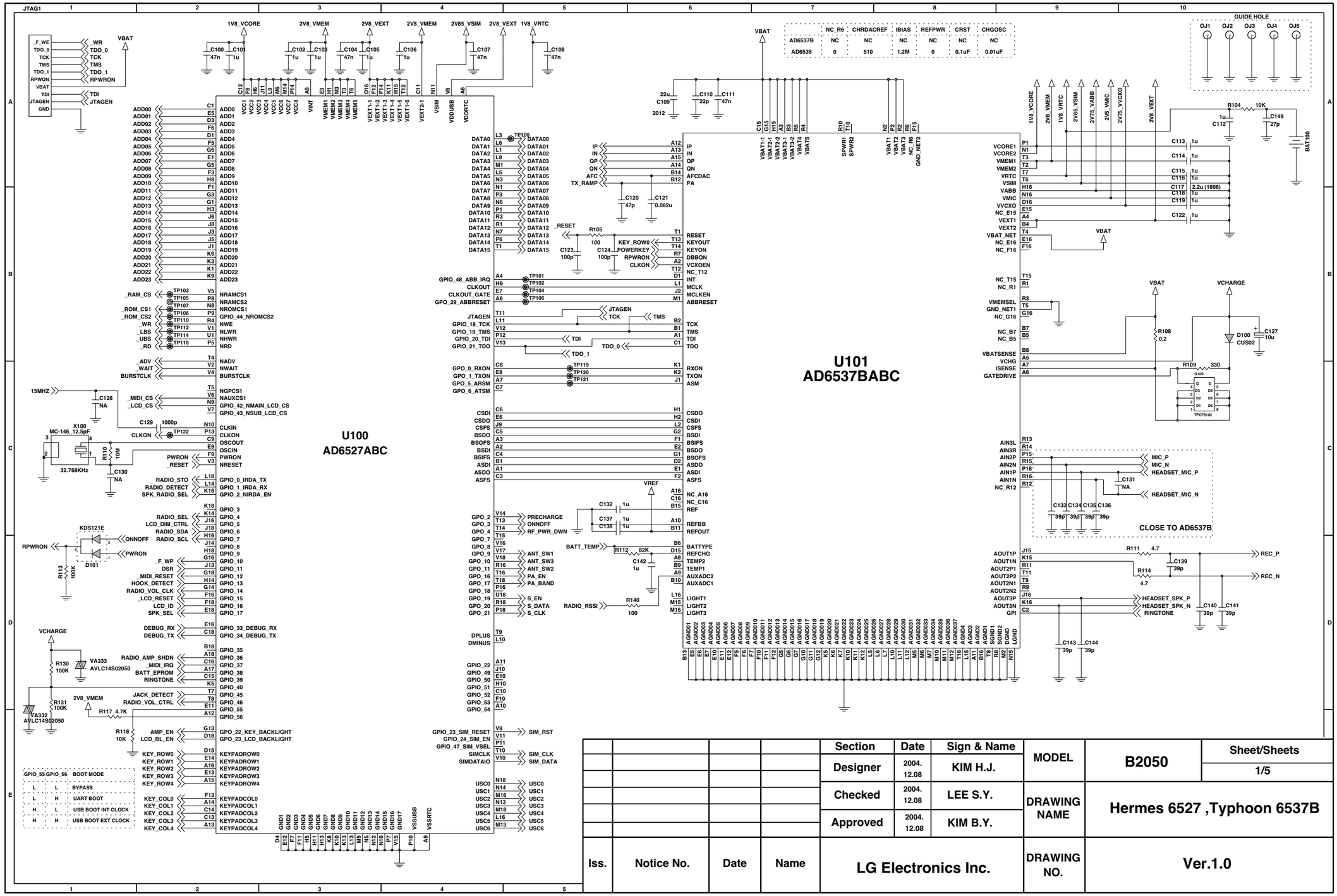
7. CIRCUIT DIAGRAM



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				Designer	2004. 12.08	KIM H.J.			1/5
				Checked	2004. 12.08	LEE S.Y.	DRAWING NAME	Hermes 6527 ,Typhoon 6537B	
				Approved	2004. 12.08	KIM B.Y.			
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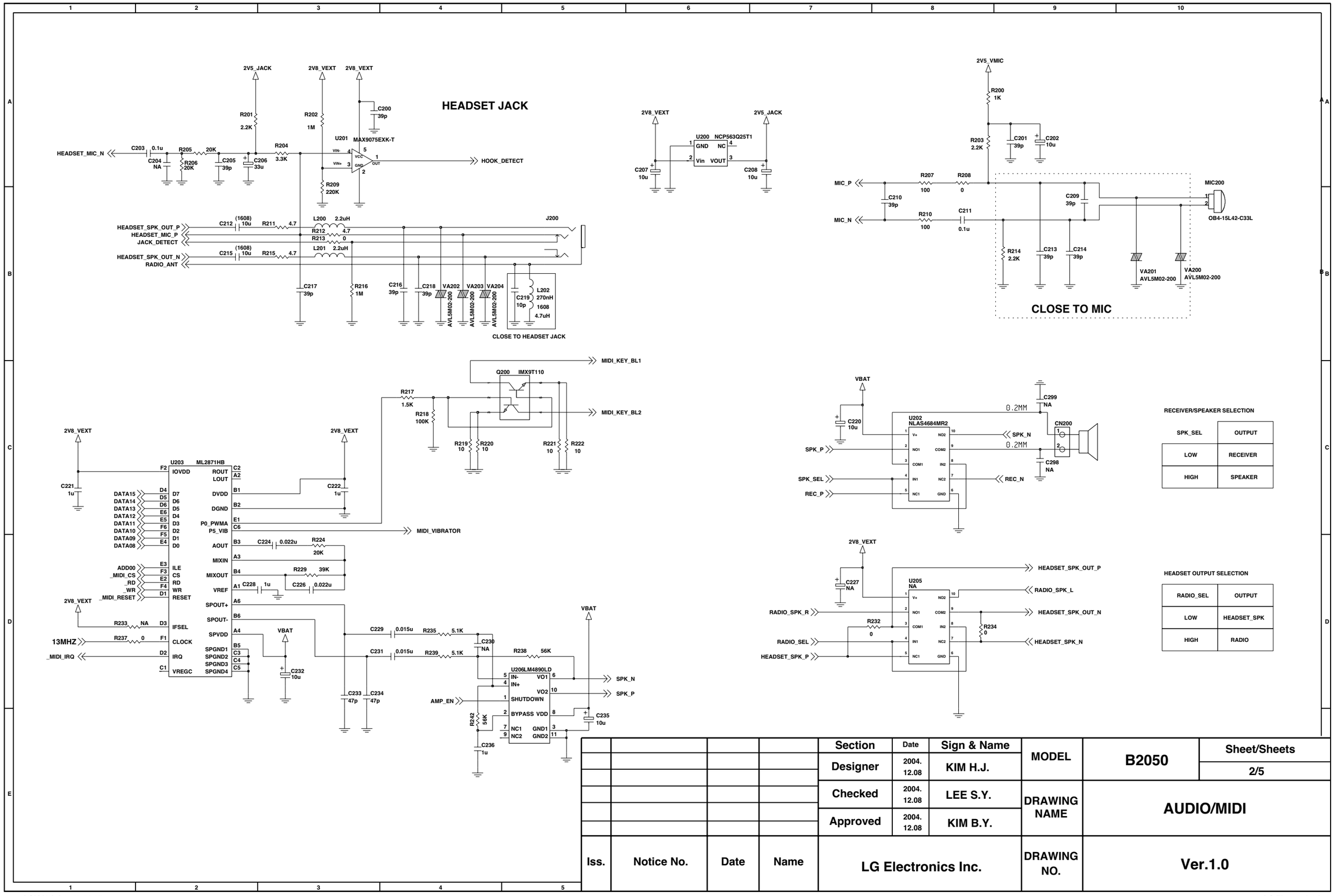
7. CIRCUIT DIAGRAM



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				Designer	2004.12.08	KIM H.J.			2/5
				Checked	2004.12.08	LEE S.Y.	DRAWING NAME	AUDIO/MIDI	
				Approved	2004.12.08	KIM B.Y.			
				LG Electronics Inc.			DRAWING NO.	Ver.1.0	

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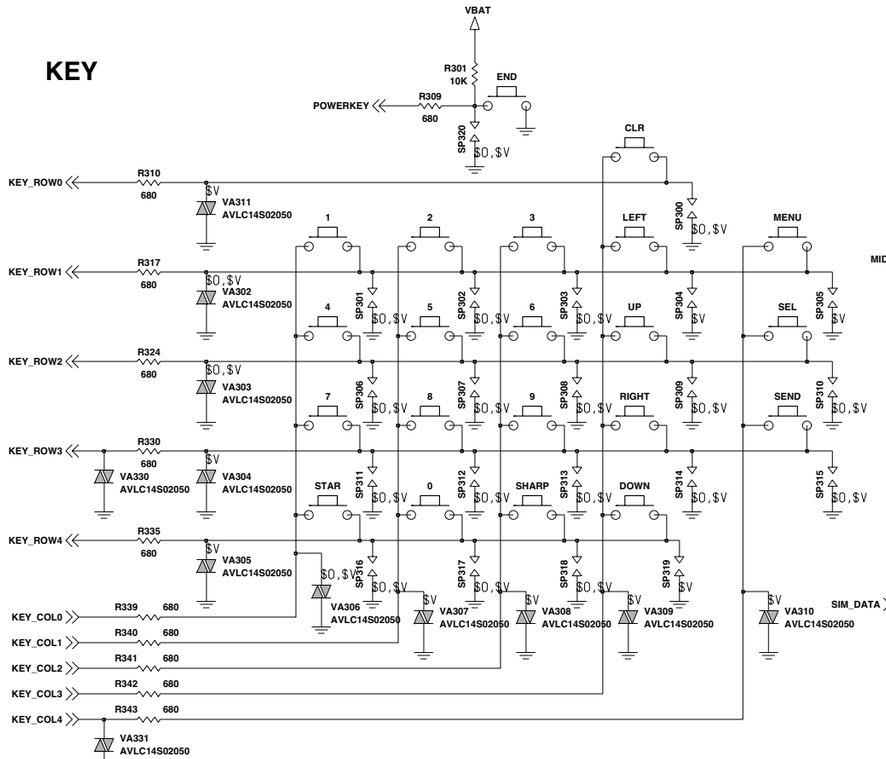
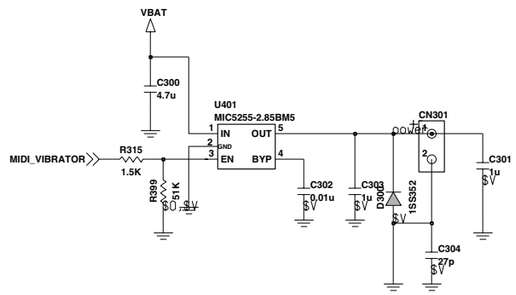
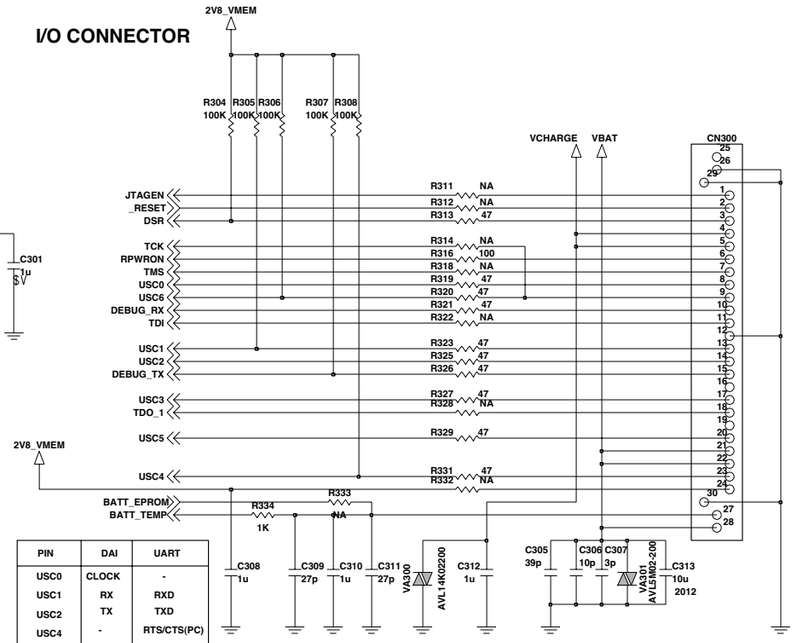
KEY



VIBRATOR

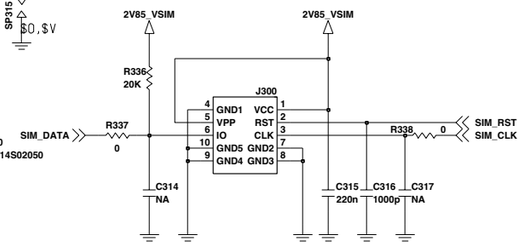


I/O CONNECTOR



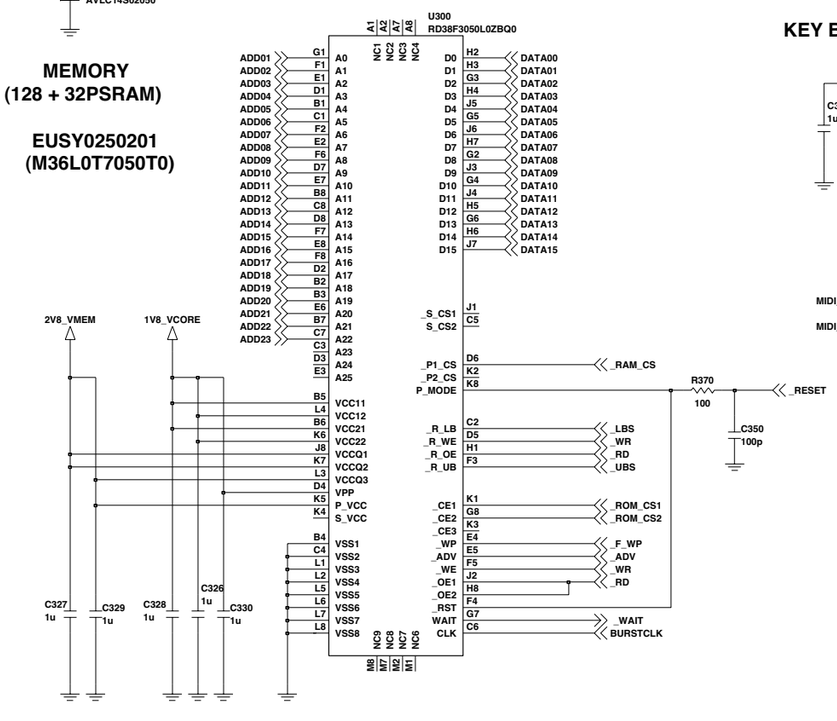
PIN	DAI	UART
USC0	CLOCK	-
USC1	RX	RXD
USC2	TX	TXD
USC4	-	RTS/CTS(PC)
USC5	-	CTS/RTS(PC)
USC6	RESET	-

SIM CONNECTOR

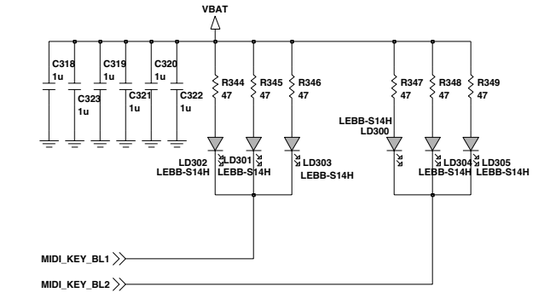


MEMORY (128 + 32PSRAM)

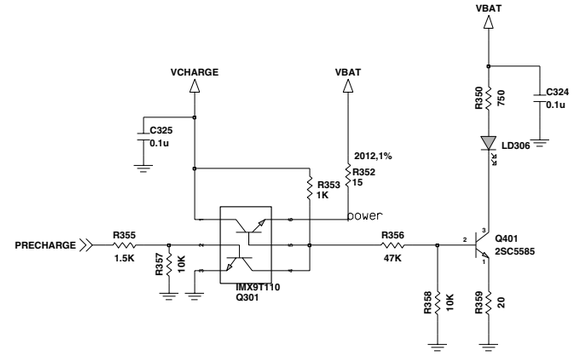
EUSY0250201
(M36L0T7050T0)



KEY BACKLIGHT



PRECHARGE CIRCUIT



Iss.	Notice No.	Date	Name

Section	Date	Sign & Name
Designer	2004. 12.08	KIM H.J.
Checked	2004. 12.08	LEE S.Y.
Approved	2004. 12.08	KIM B.Y.

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MODEL	B2050
Sheet/Sheets	3/5
DRAWING NAME	MEMORY, MMI, I/O
DRAWING NO.	Ver.1.0

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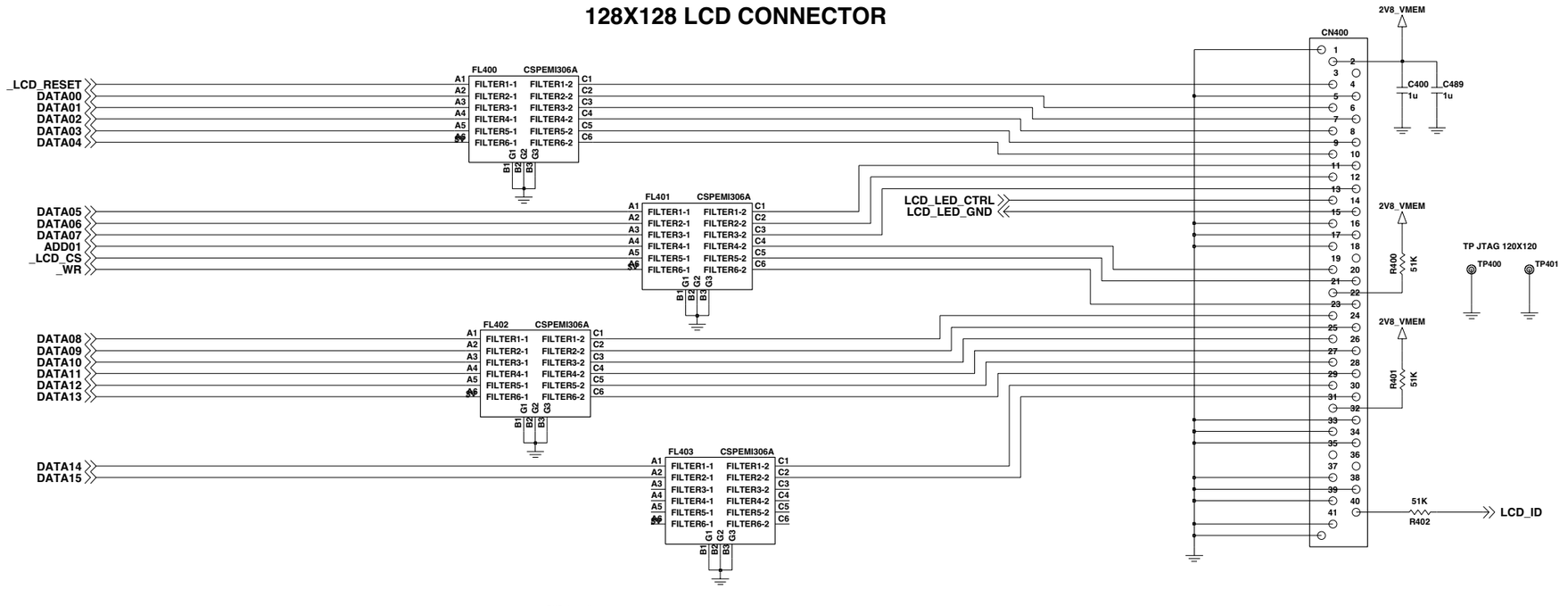
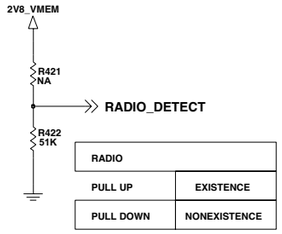
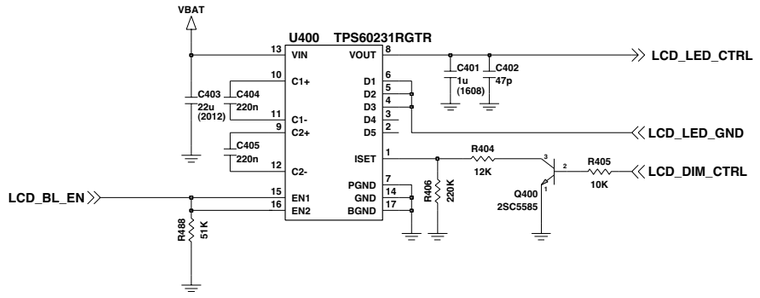
128X128 LCD CONNECTOR





LCD BACKLIGHT



				Section	Date	Sign & Name	MODEL	B2050	Sheet/ Sheets
				Designer	2004. 12.08	KIM H.J.			4/5
				Checked	2004. 12.08	LEE S.Y.	DRAWING NAME	LCD CONN,RADIO	
				Approved	2004. 12.08	KIM B.Y.			
Iss.	Notice No.	Date	Name	LG Electronics Inc.			DRAWING NO.	Ver.1.0	

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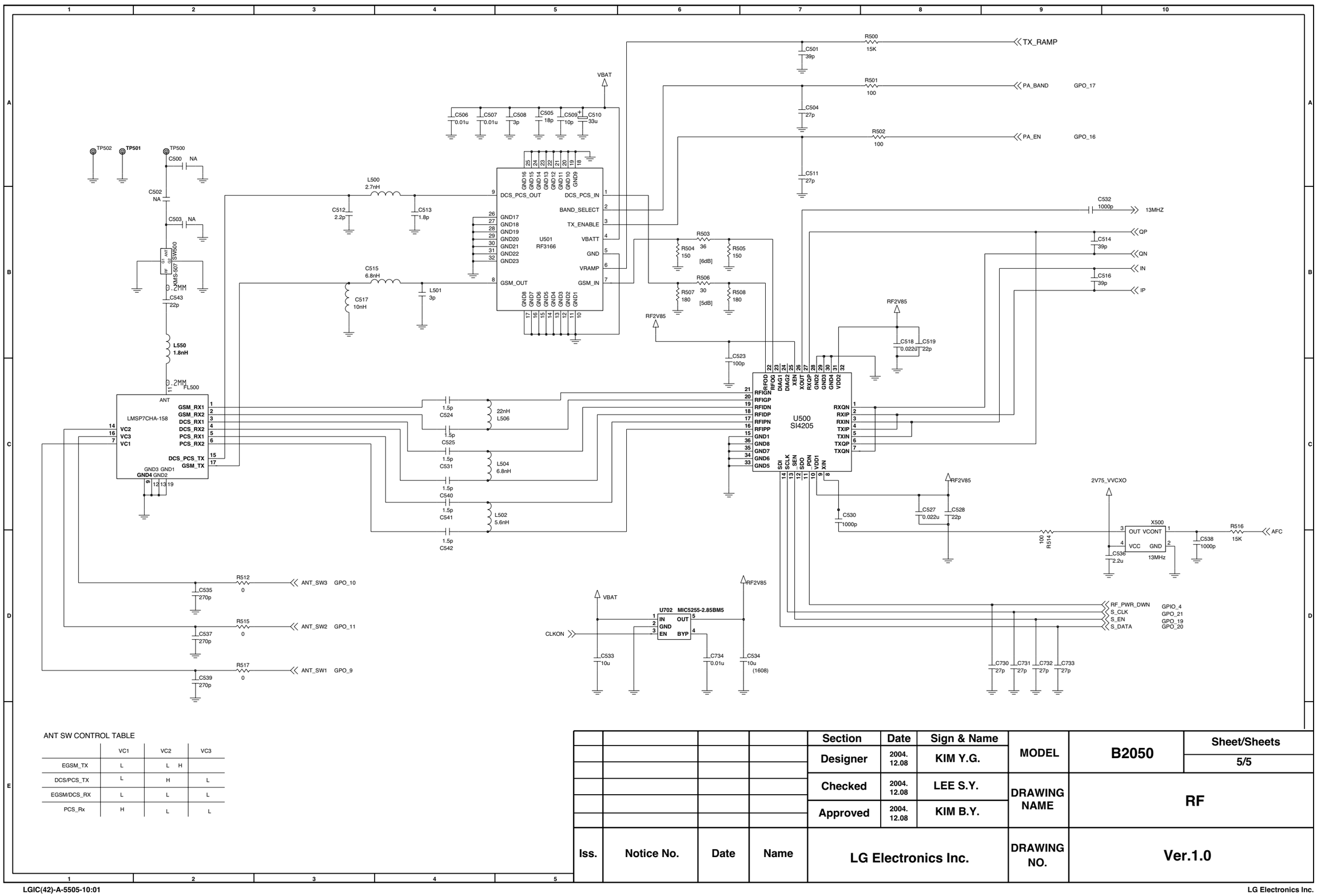
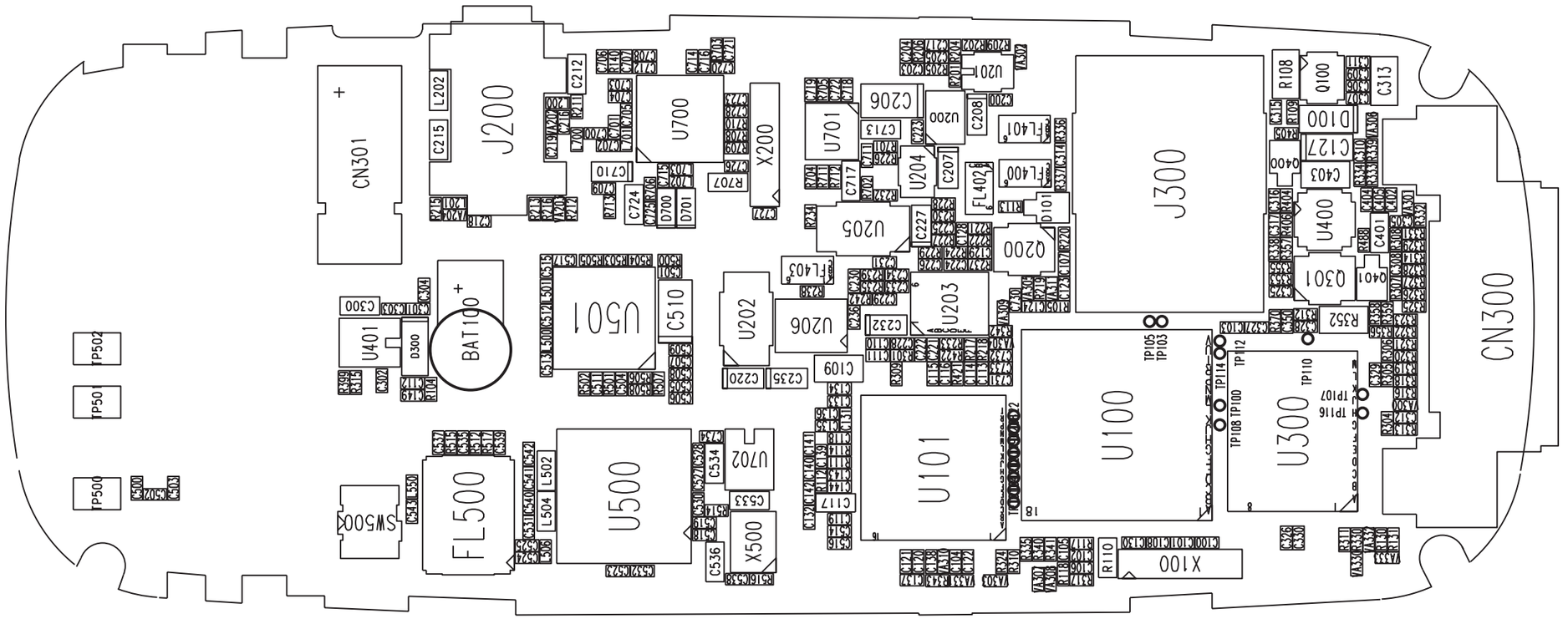


ANT SW CONTROL TABLE

	VC1	VC2	VC3
EGSM_TX	L	L H	
DCS/PCS_TX	L	H	L
EGSM/DCS_RX	L	L	L
PCS_Rx	H	L	L

Iss.	Notice No.	Date	Name	Section	Date	Sign & Name	MODEL	B2050	Sheet/Sheets
				Designer	2004.12.08	KIM Y.G.			5/5
				Checked	2004.12.08	LEE S.Y.	DRAWING NAME	RF	
				Approved	2004.12.08	KIM B.Y.			
				LG Electronics Inc.			DRAWING NO.	Ver.1.0	

8. PCB LAYOUT


CN301
J200
U700
X200
U701
C206
U204
U205
Q200
J300
U201
U200
C208
FL401
FL402
FL400
D101
C227
R108
Q100
D100
C127
C403
C313
U400
Q401
Q301
R352
C401
U203
U202
U206
C232
C109
C220
C235
U401
D300
BAT100
U501
C510
TP502
TP501
TP500
C500
C502
C503
C300
C301
C304
C302
C303
R398
R315
R316
R317
R318
R319
C512
C513
C514
C515
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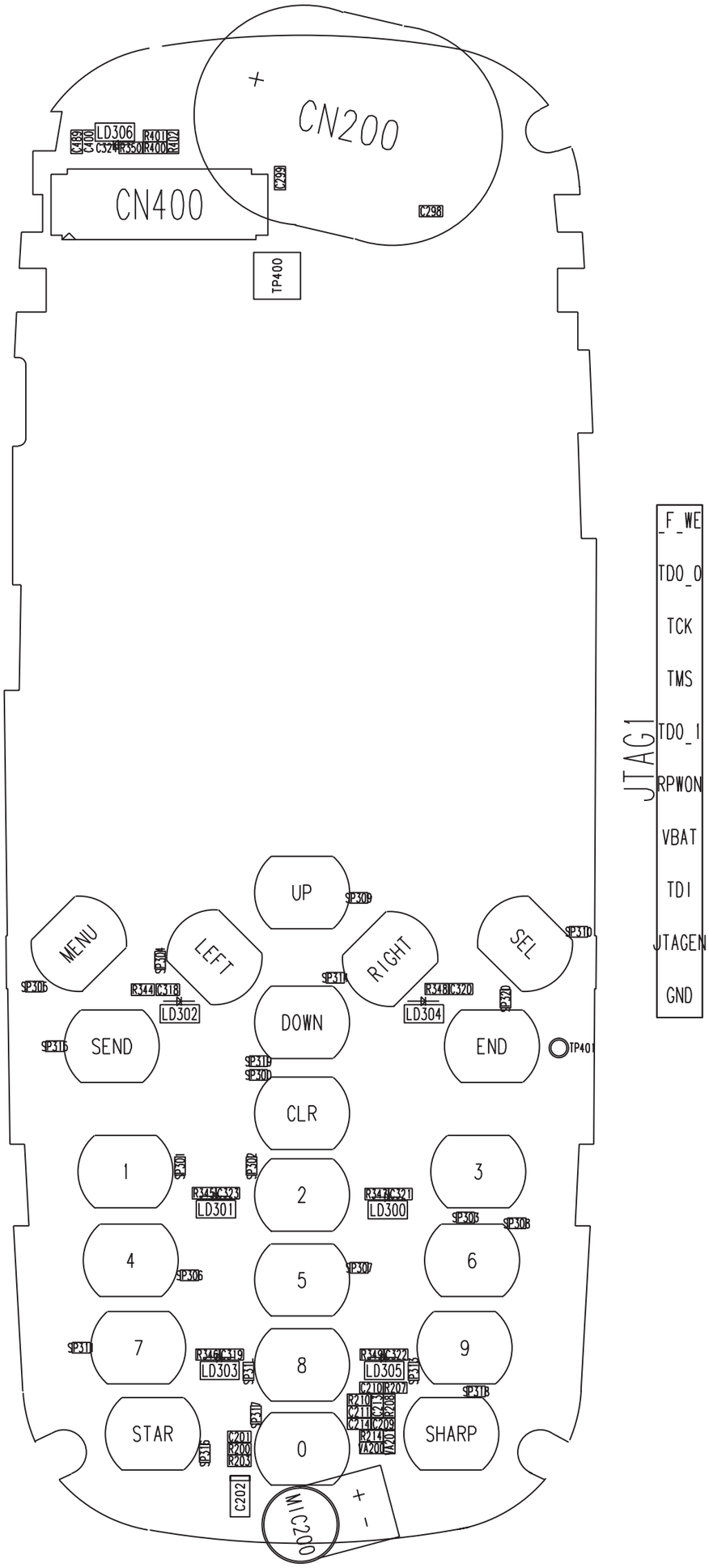

Figure 8-1 B2050 TOP SIDE PCB LAYOUT