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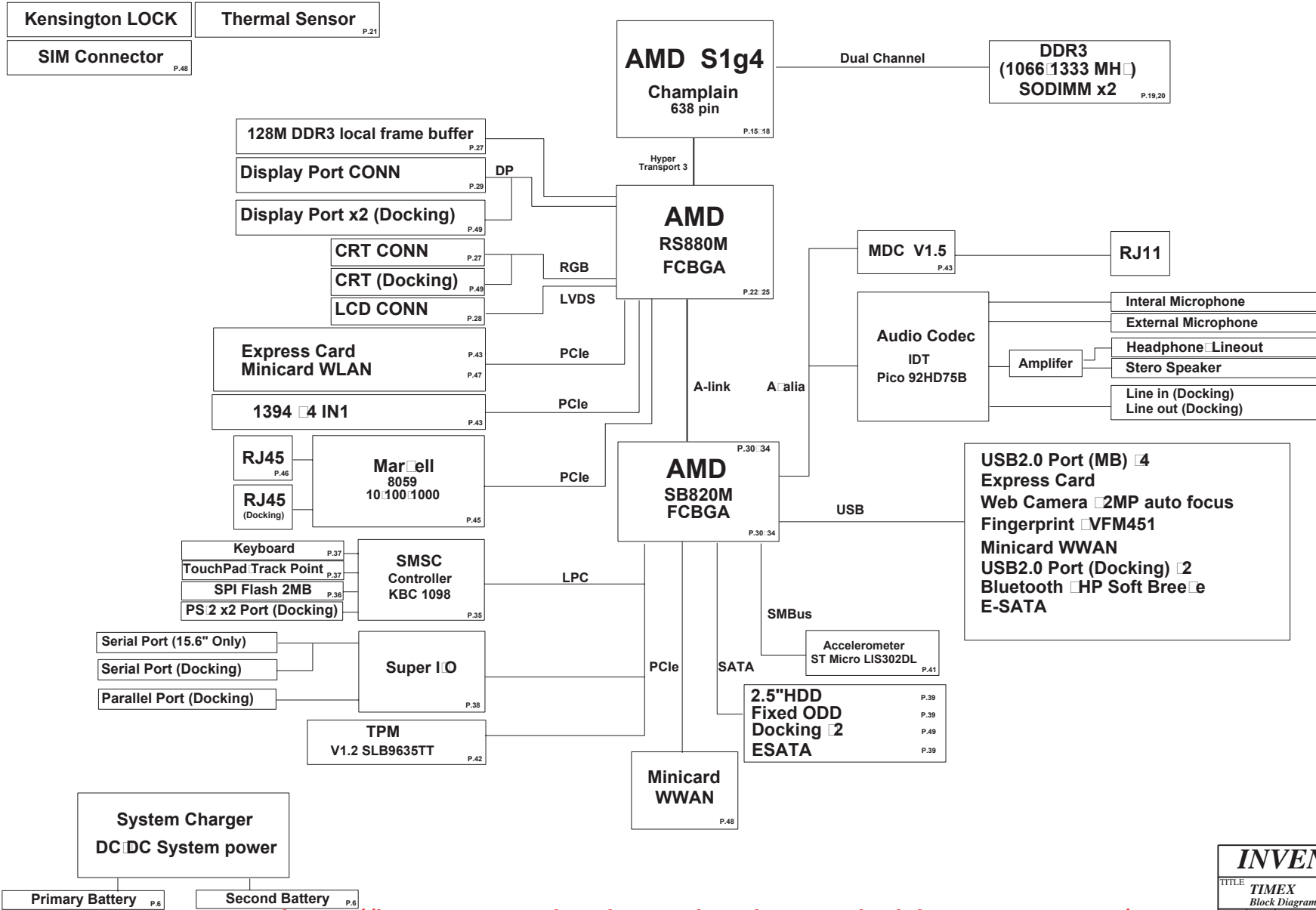
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**INVENTEC**

TITLE <b>TIMEX</b> Index			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	1310A2326802	A02
SHEET		2	OF 65

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# In entec Confidential

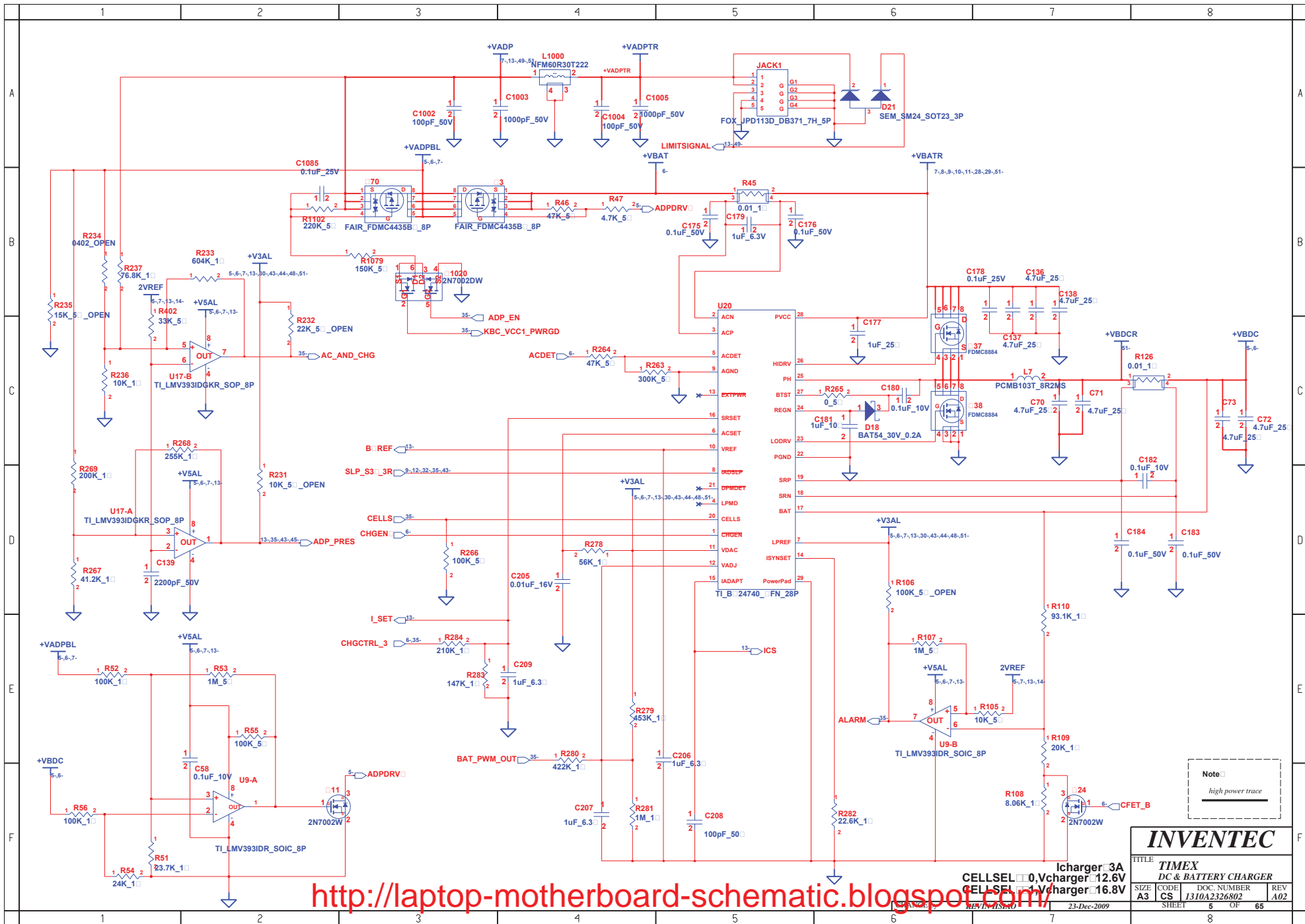


<http://laptop-motherboard-schematic.blogspot.com/>

<b>INVENTEC</b>			
TITLE: <b>TIMEX</b>			
Block Diagram			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	1310A2326802	A02
SHEET		3	OF 65

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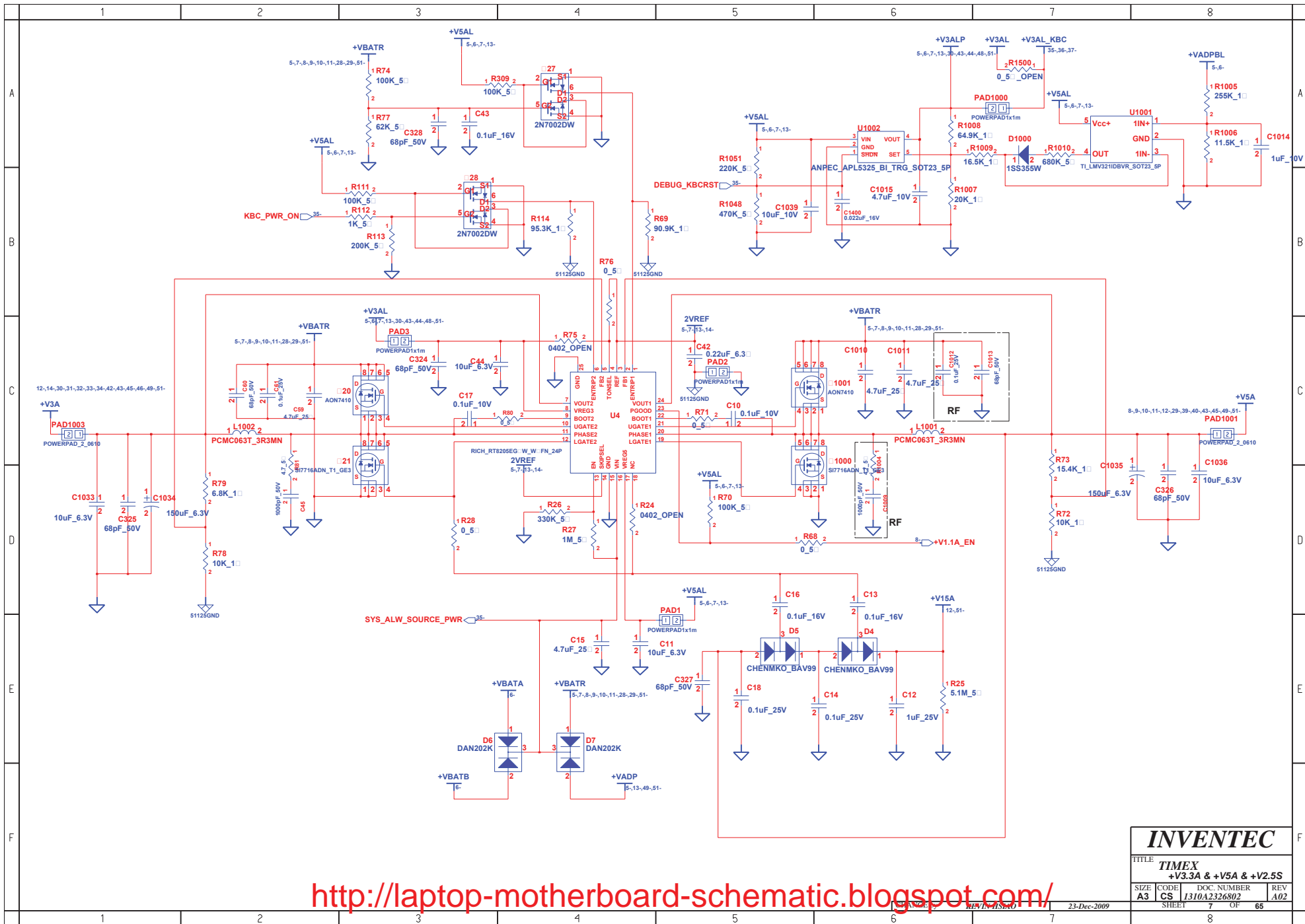
<http://laptop-motherboard-schematic.blogspot.com/>

Charger: 3A  
 CELLSEL 0, Vcharger 12.6V  
 CELLSEL 1, Vcharger 16.8V

Note:  
 high power trace

<b>INVENTEC</b>			
TITLE: <b>TIMEX DC &amp; BATTERY CHARGER</b>			
SIZE: <b>A3</b>	CODE: <b>CS</b>	DOC NUMBER: <b>I310A2326802</b>	REV: <b>A02</b>
SHEET: <b>5</b>		OF: <b>65</b>	

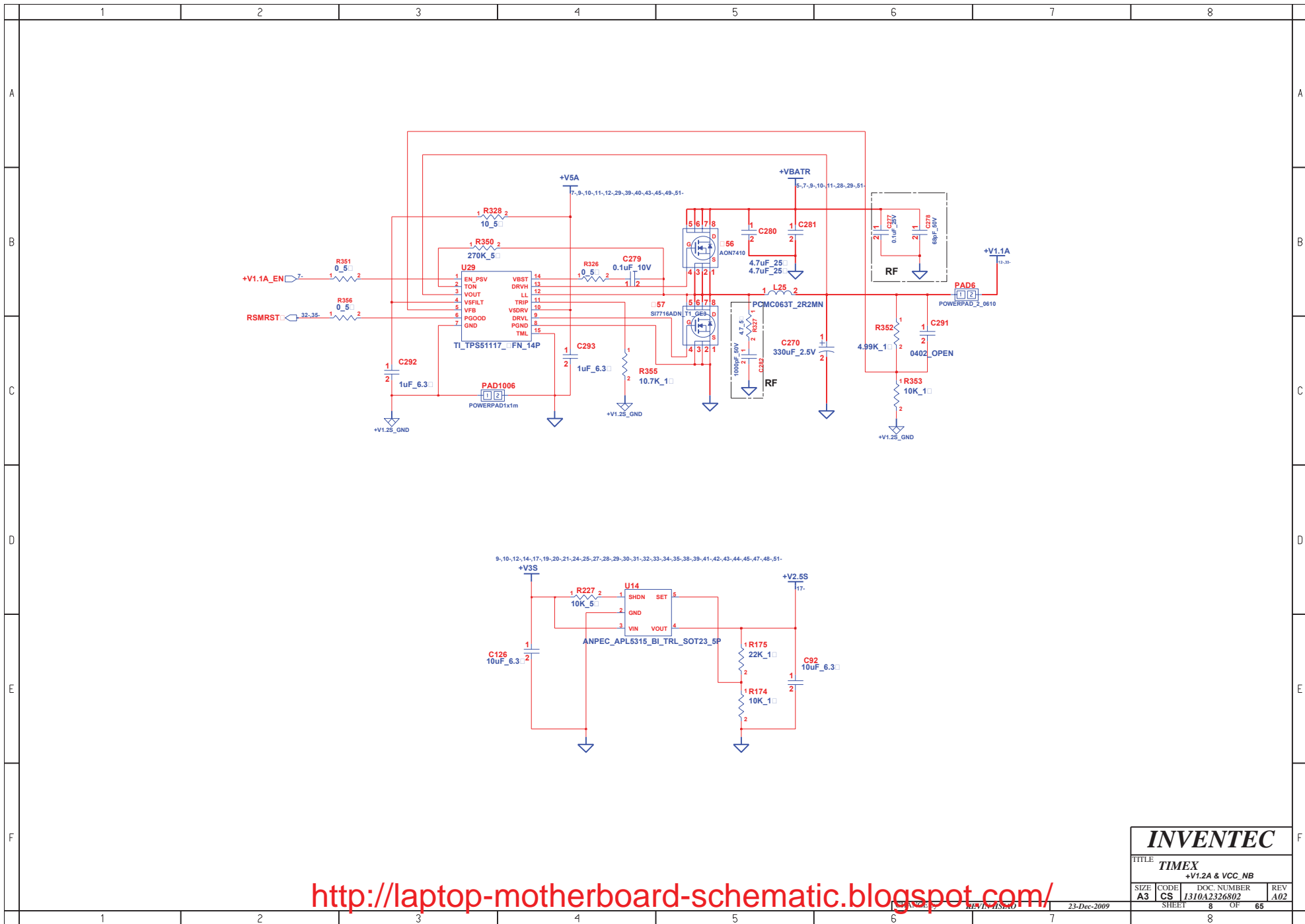




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<b>INVENTEC</b>			
TITLE: <b>TIMEX</b>			
<b>+V3.3A &amp; +V5A &amp; +V2.5S</b>			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	I310A2326802	A02
SHEET		7	OF 65

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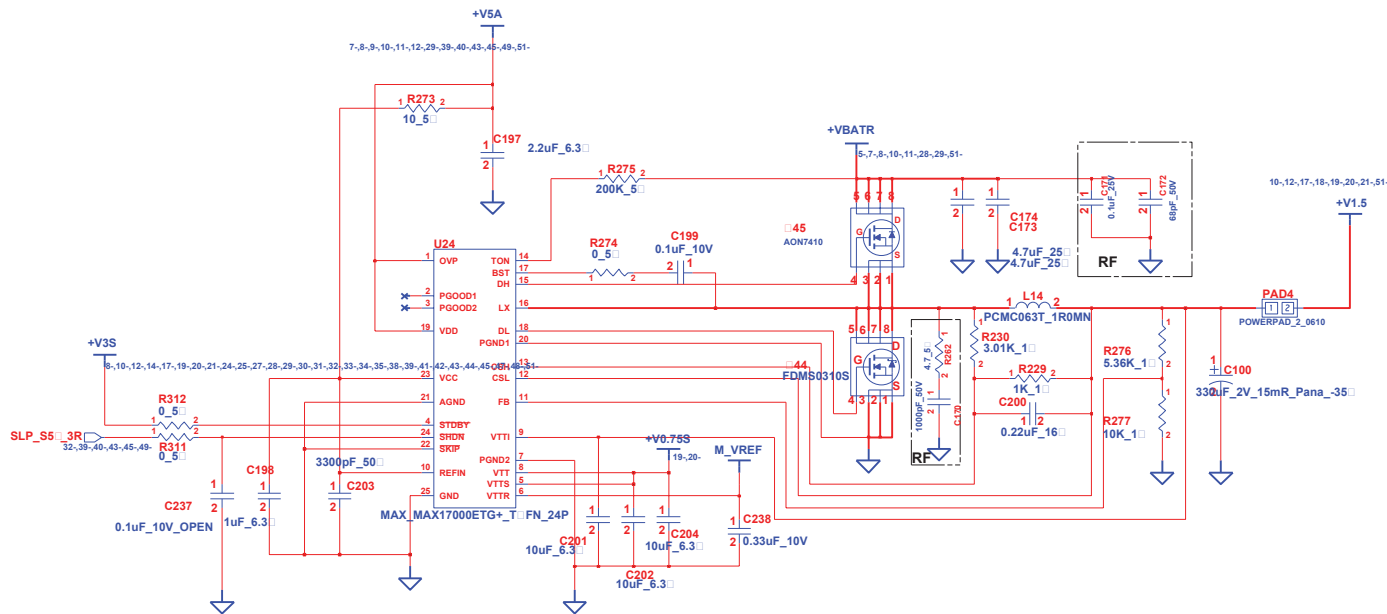
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**INVENTEC**

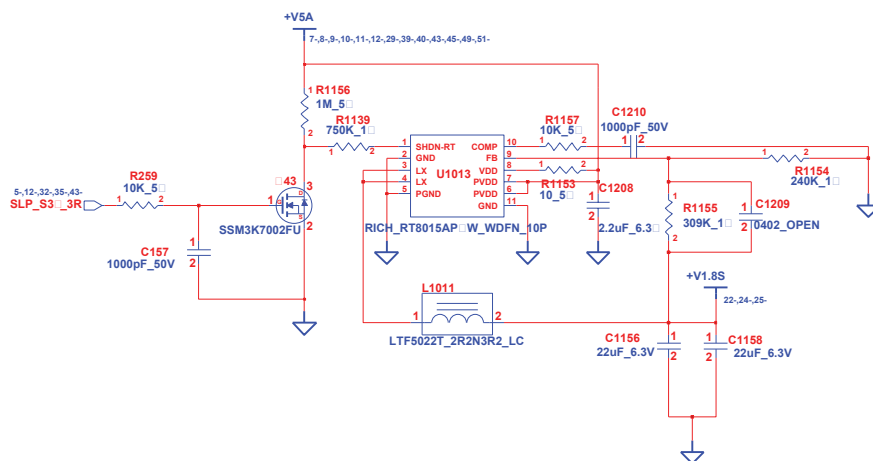
TITLE			
TIMEX			
+V1.2A & VCC_NB			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	1310A2326802	A02
SHEET		8	OF 65

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NOTE DDR3 REGULATOR



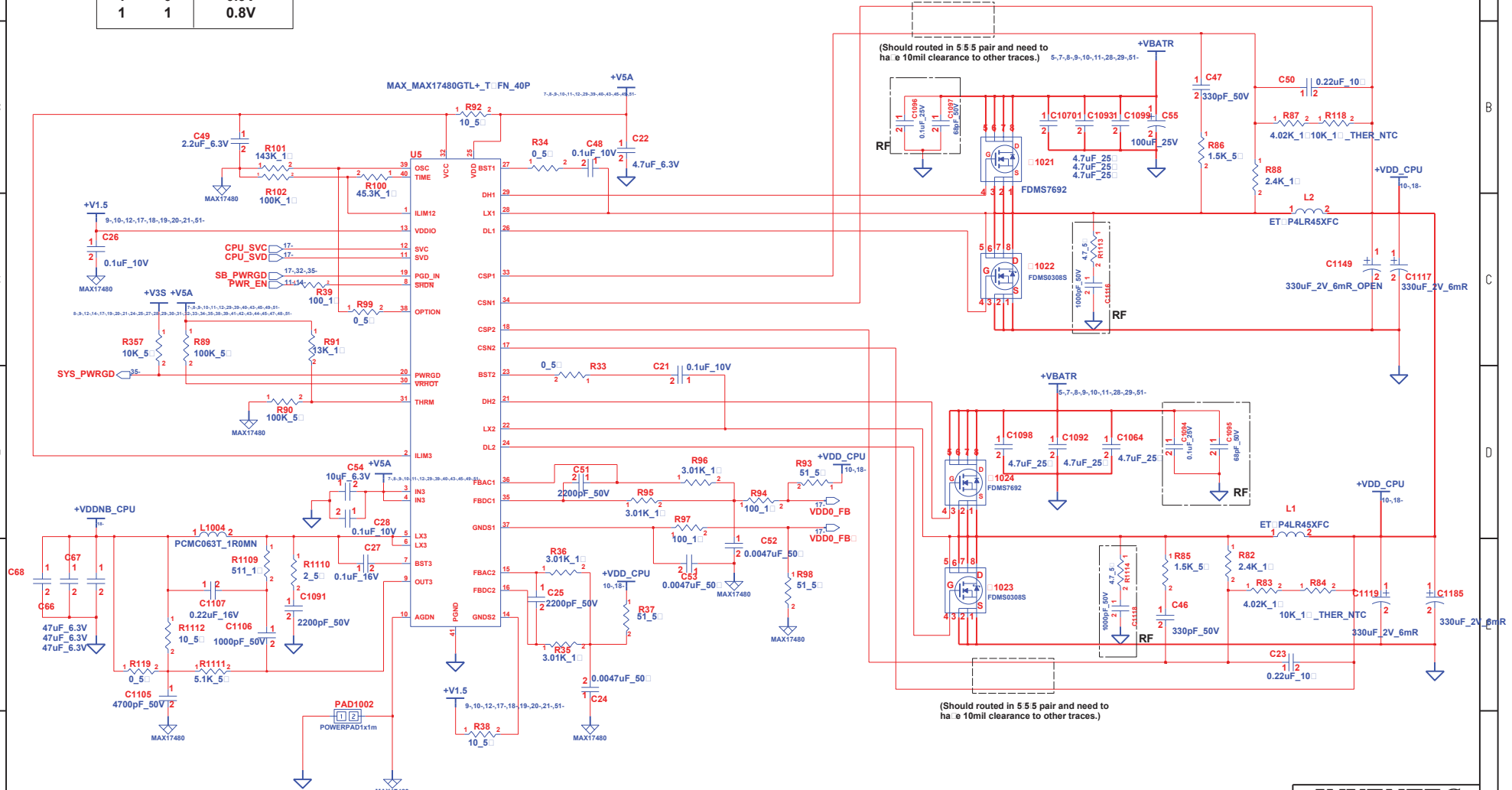
<http://laptop-motherboard-schematic.blogspot.com/>

**INVENTEC**

TITLE <b>TIMEX DDR3 POWER</b>			
SIZE <b>A3</b>	CODE <b>CS</b>	DOC. NUMBER <b>I310A2326802</b>	REV <b>A02</b>
SHEET <b>9</b>		OF <b>65</b>	

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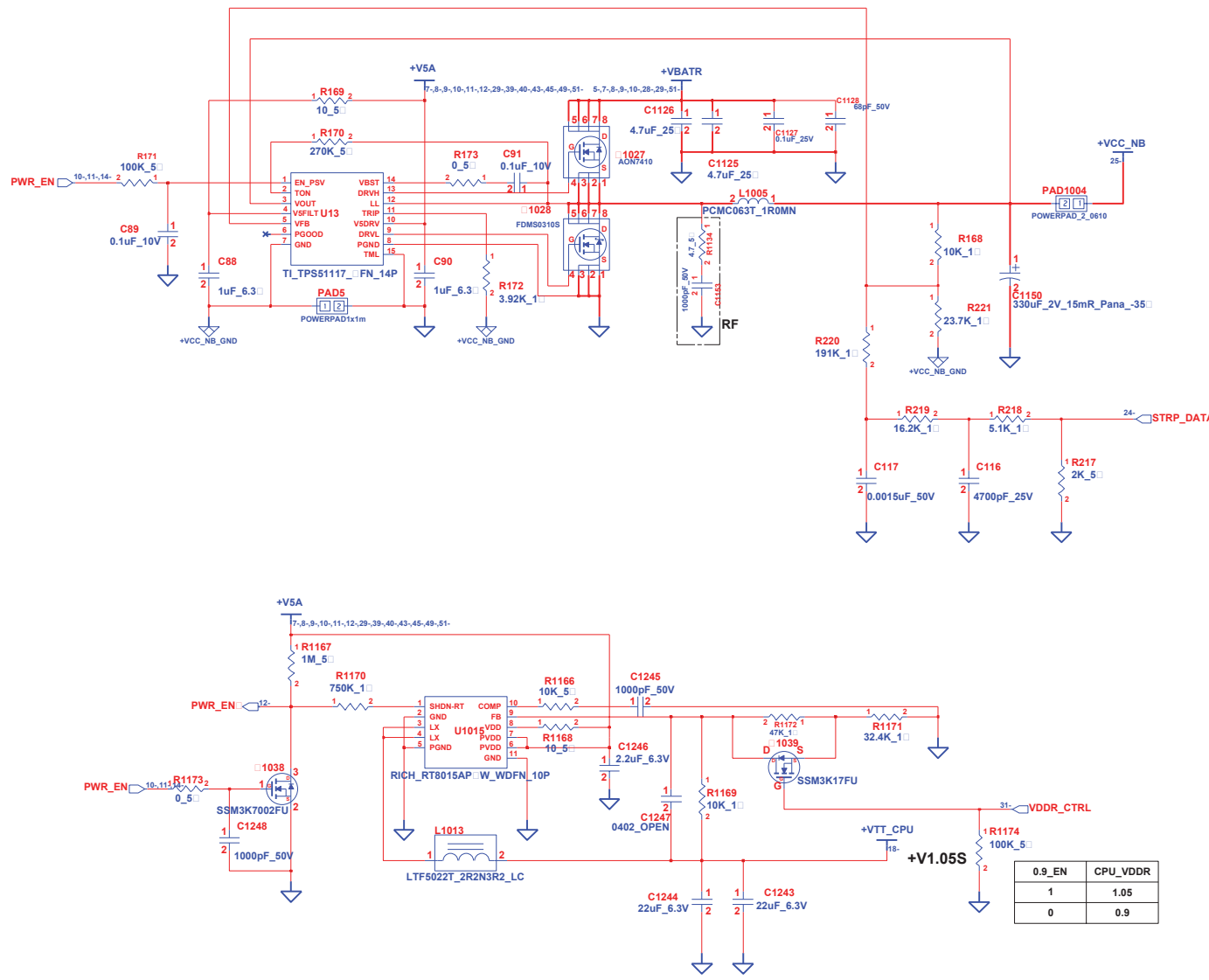
SVC	SVD	VDD_CORE
0	0	1.1V
0	1	1.0V
1	0	0.9V
1	1	0.8V



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<b>INVENTEC</b>			
TITLE: <b>TIMEX</b>			
+VCC_CORE & +VCC_CORE_NP			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	I310A2326802	A02
SHEET		10 OF 65	

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STRP_DATA	VCC_NB
0	1.1
1	0.95

0.9_EN	CPU_VDDR
1	1.05
0	0.9

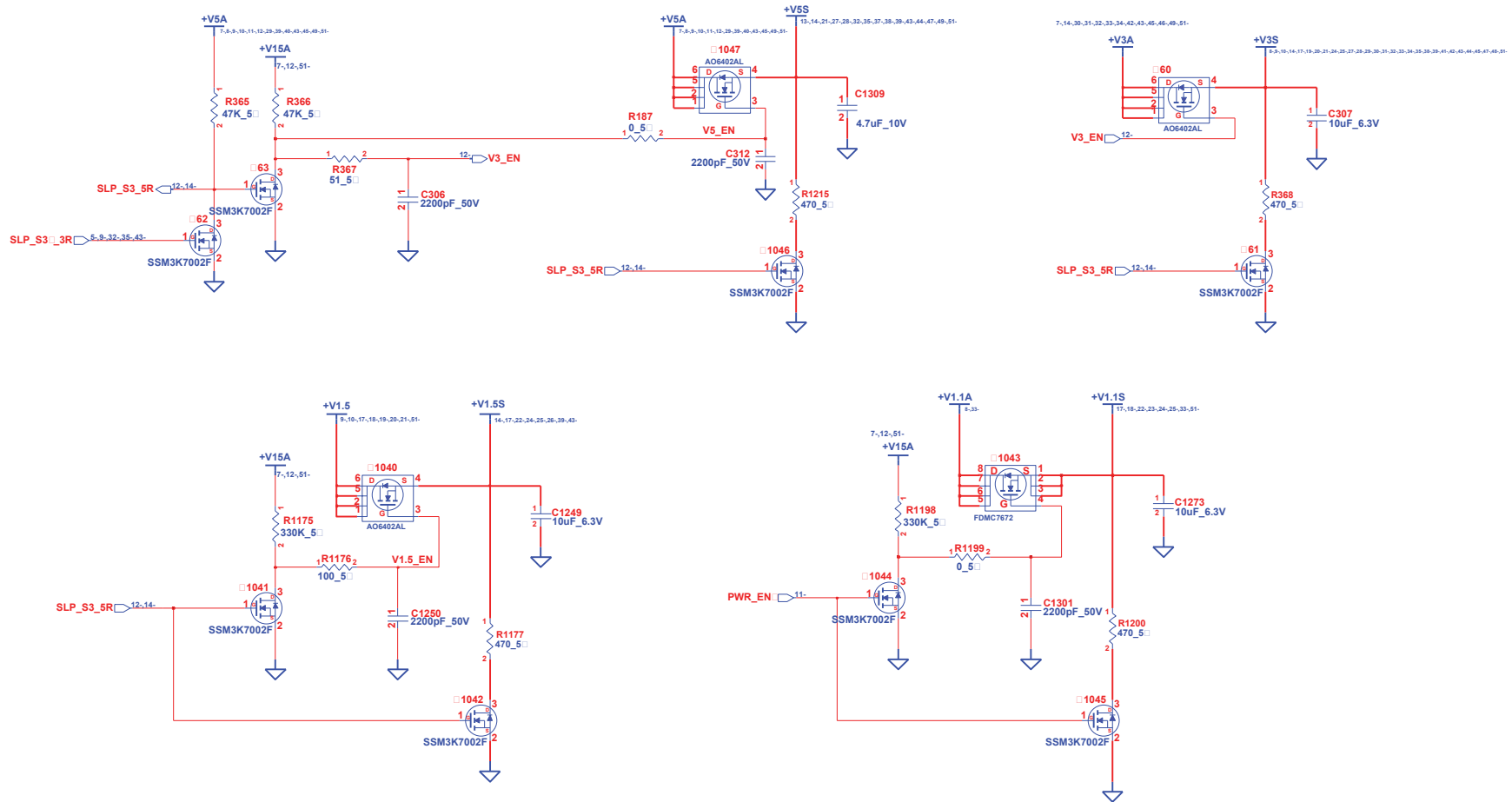
**INVENTEC**

TITLE: **TIMEX**

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A3	CS	1310A2326802	A02

SHEET 11 OF 65

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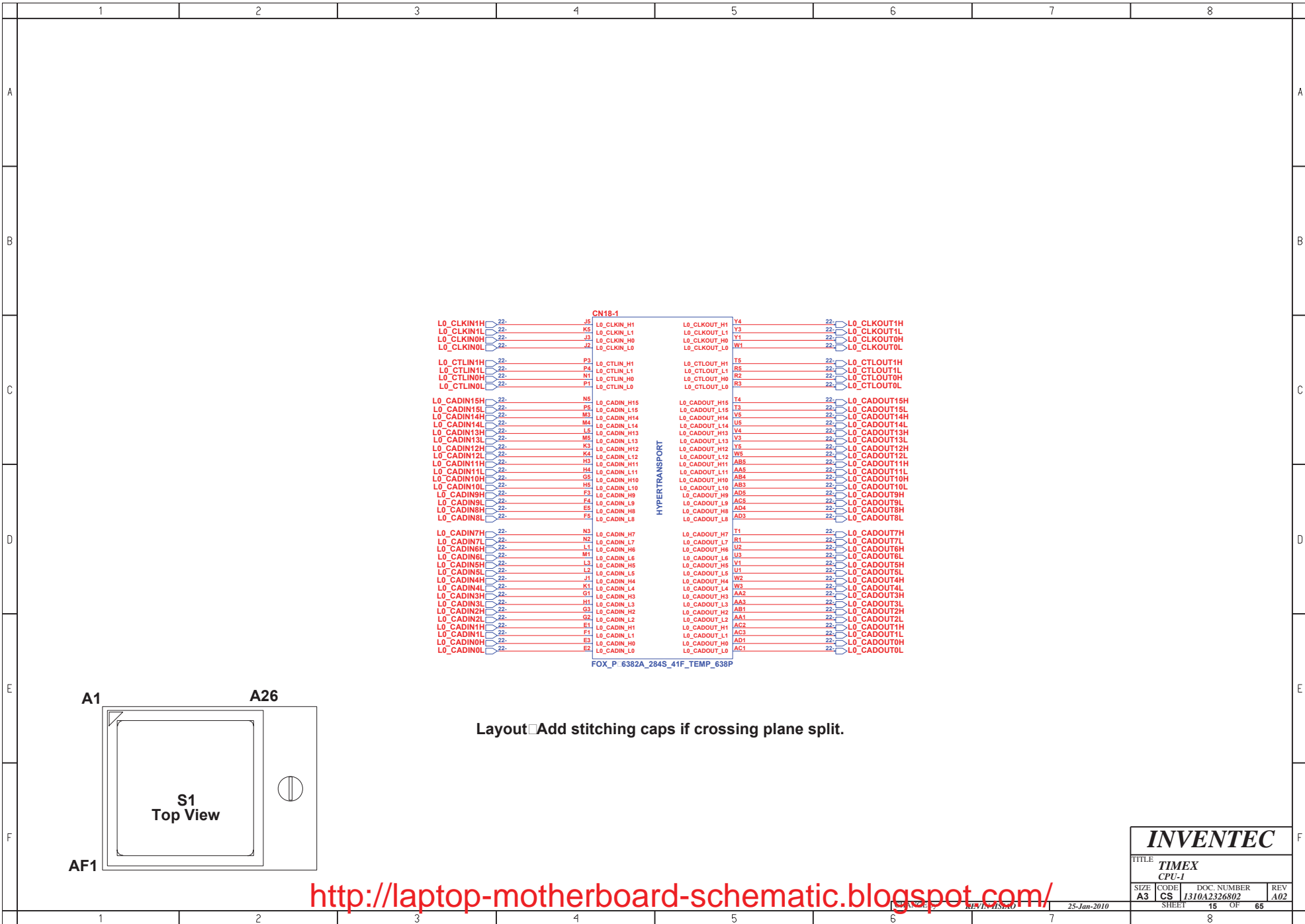
<http://laptop-motherboard-schematic.blogspot.com/>

<b>INVENTEC</b>			
TITLE: <b>TIMEX</b>			
+V5S & +V3.3S & +V1.5S & +V1.2S			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	I310A2326802	A02
SHEET		12	OF 65

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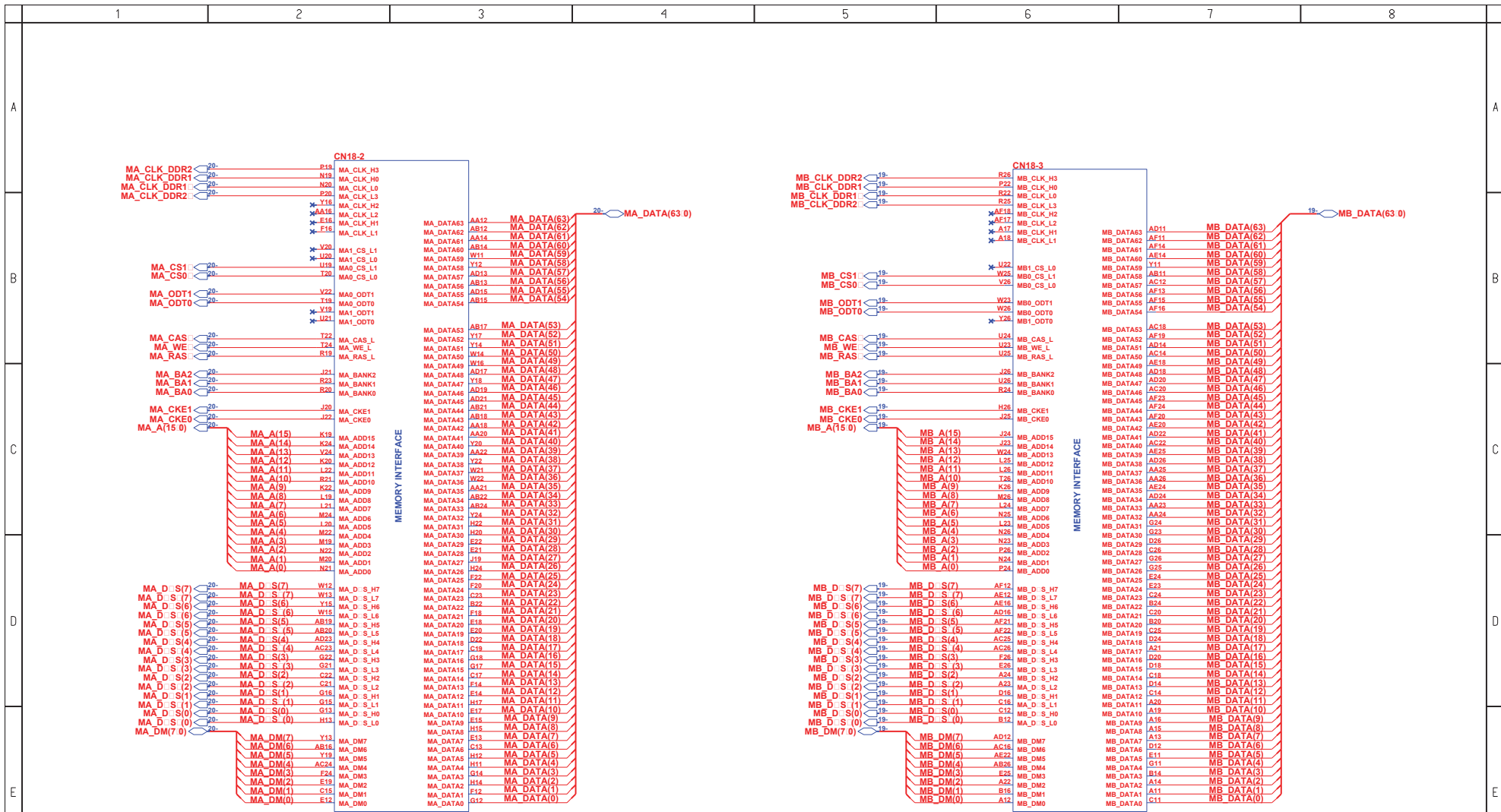
<http://laptop-motherboard-schematic.blogspot.com/>

**INVENTEC**

TITLE  
**TIMEX**  
 CPU-1

SIZE <b>A3</b>	CODE <b>CS</b>	DOC. NUMBER <b>I310A2326802</b>	REV <b>A02</b>
SHEET <b>15</b>		OF <b>65</b>	

25-Jan-2010

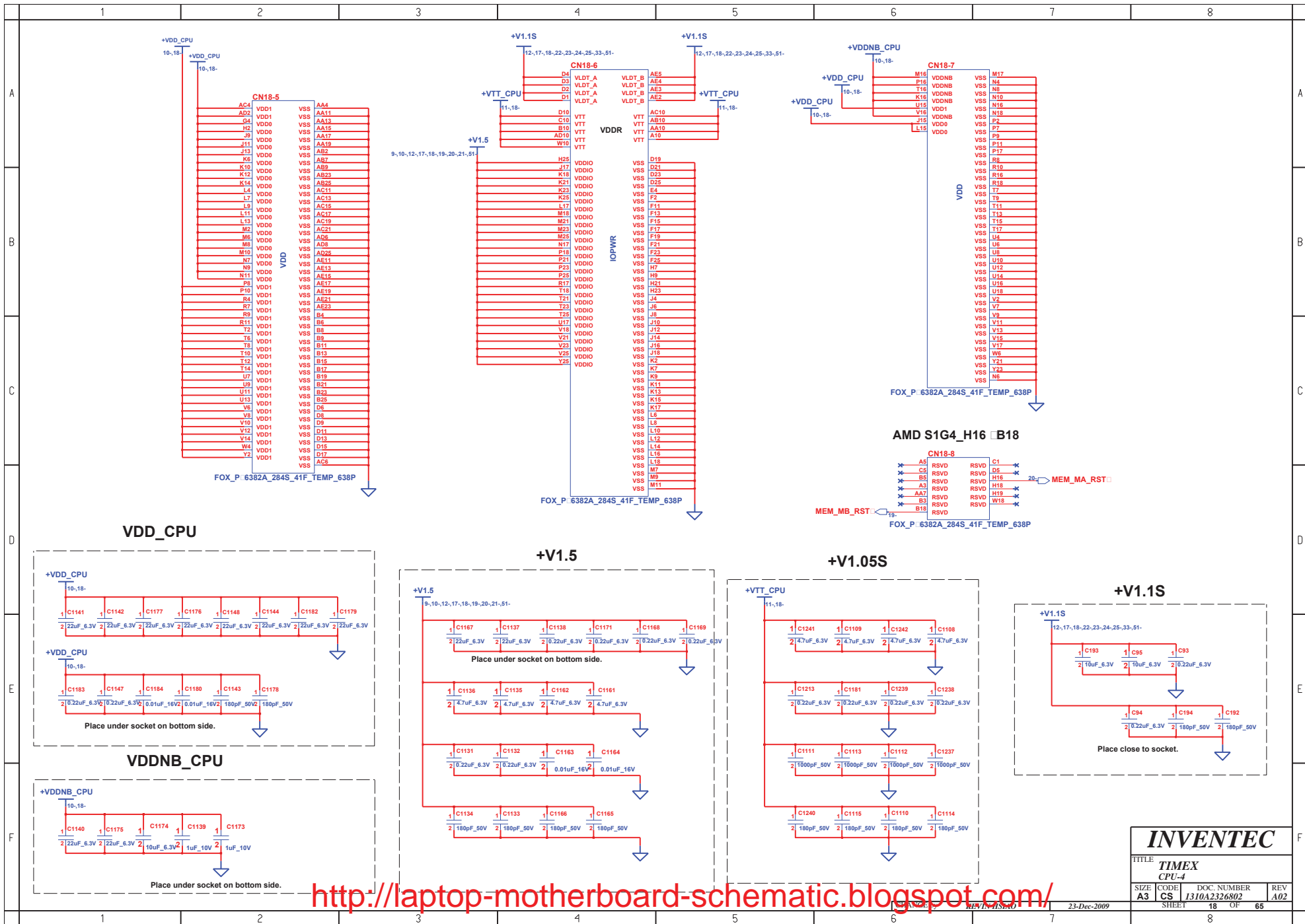


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<b>INVENTEC</b>			
TITLE: <b>TIMEX CPU-2</b>			
SIZE: <b>A3</b>	CODE: <b>CS</b>	DOC. NUMBER: <b>1310A2326802</b>	REV: <b>A02</b>
SHEET: <b>16</b>		OF: <b>65</b>	





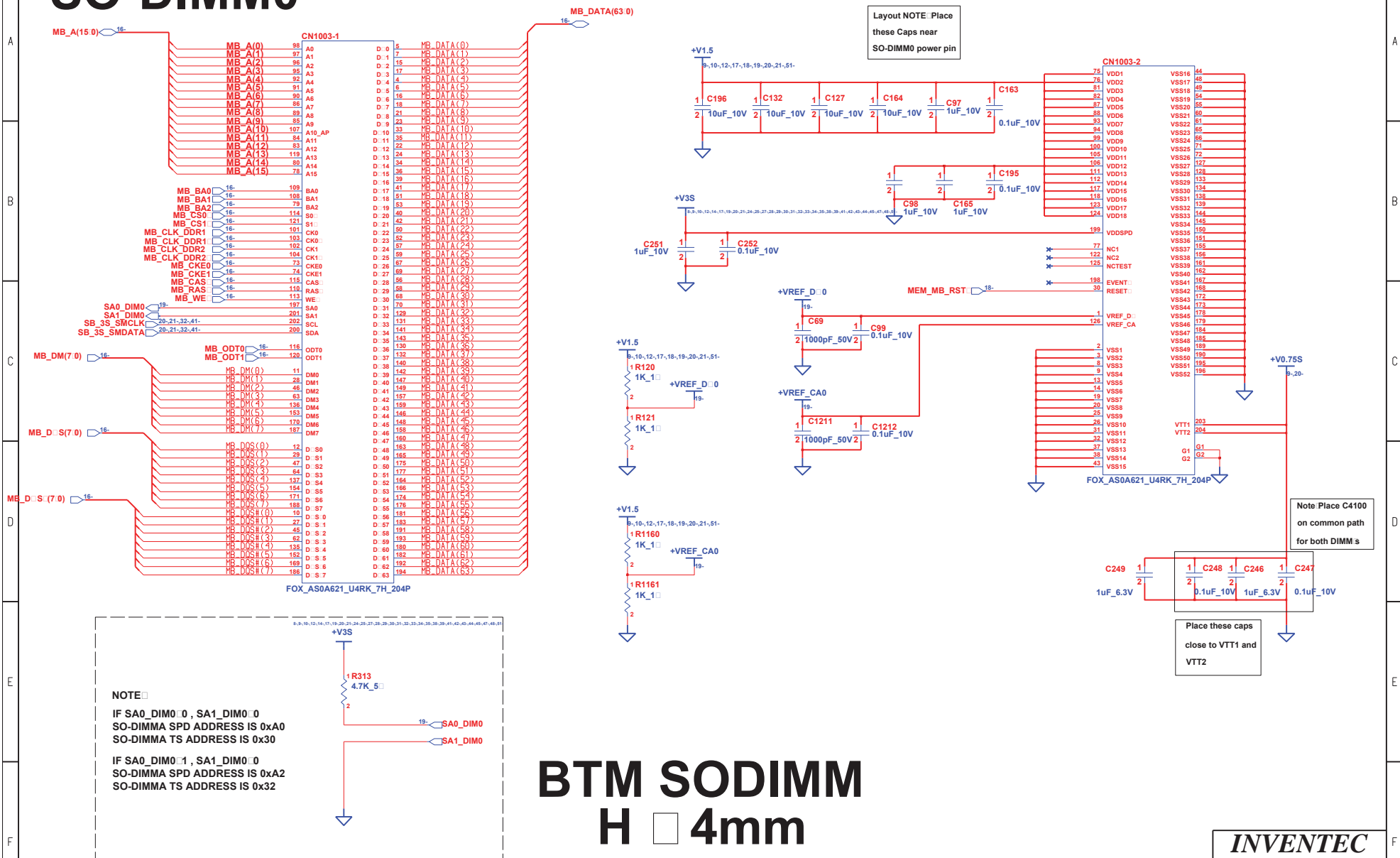


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<b>INVENTEC</b>			
TITLE: <b>TIMEX CPU-4</b>			
SIZE: <b>A3</b>	CODE: <b>CS</b>	DOC. NUMBER: <b>1310A2326802</b>	REV: <b>A02</b>
SHEET: <b>18</b>		OF: <b>65</b>	

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# SO-DIMM0



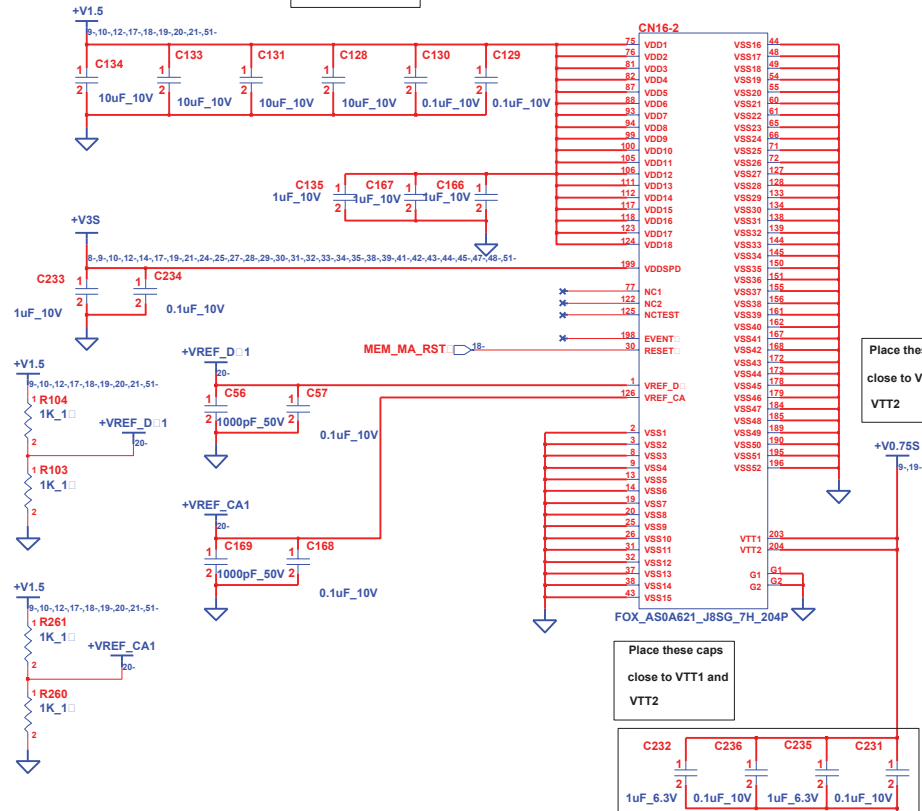
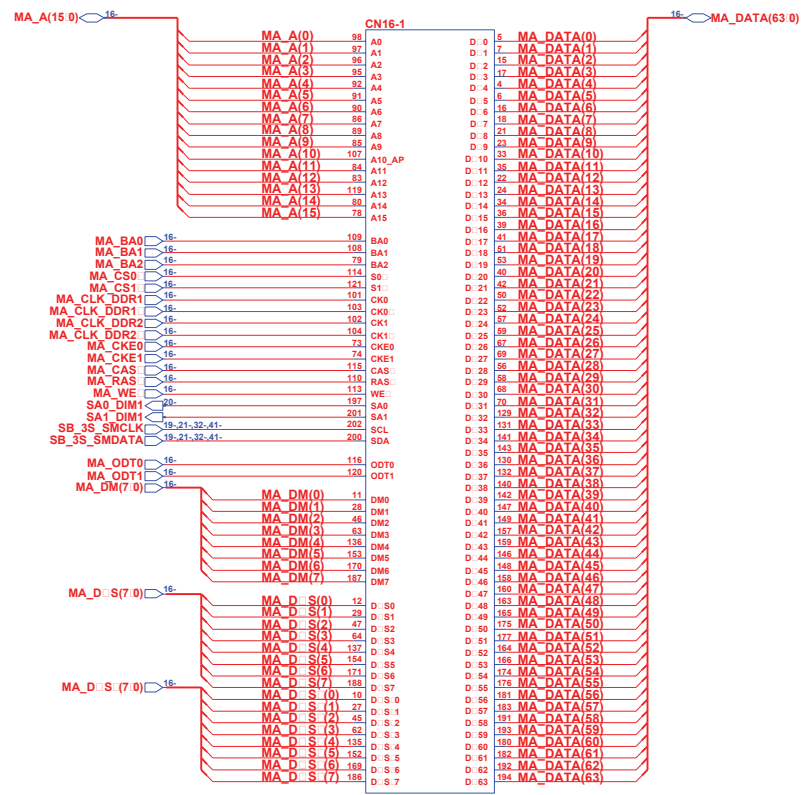
## BTM SODIMM H 4mm

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<b>INVENTEC</b>			
TITLE <b>TIMEX DDR3 DIMM0</b>			
SIZE <b>A3</b>	CODE <b>CS</b>	DOC NUMBER <b>I310A2326802</b>	REV <b>A02</b>
SHEET <b>19</b>		OF <b>65</b>	

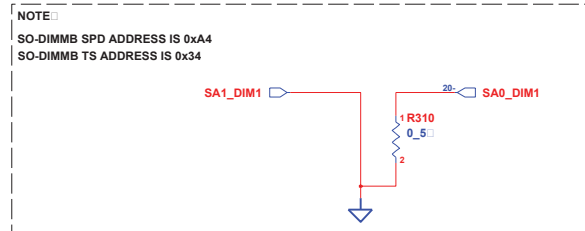
# SO-DIMM1

Layout Note Place these Caps near SO-DIMM1 power pin



Place these caps close to VTT1 and VTT2

Place these caps close to VTT1 and VTT2

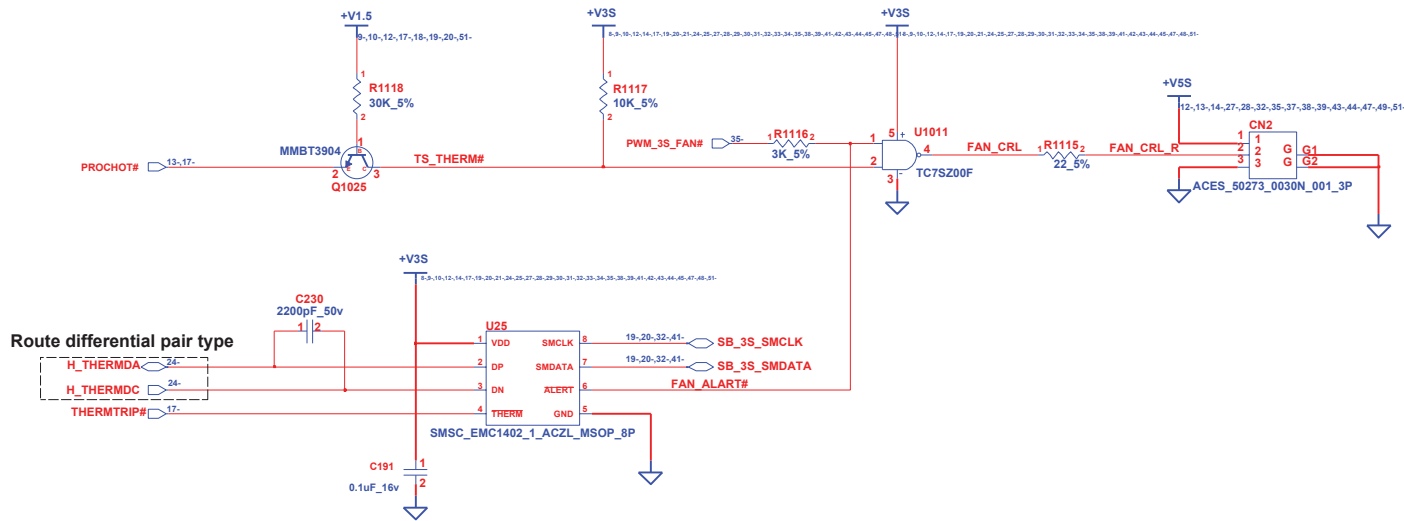


## TOP SODIMM H 8mm

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<b>INVENTEC</b>			
TITLE: <b>TIMEX DDR3 DIMM1</b>			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	I310A2326802	A02
SHEET		20	OF 65

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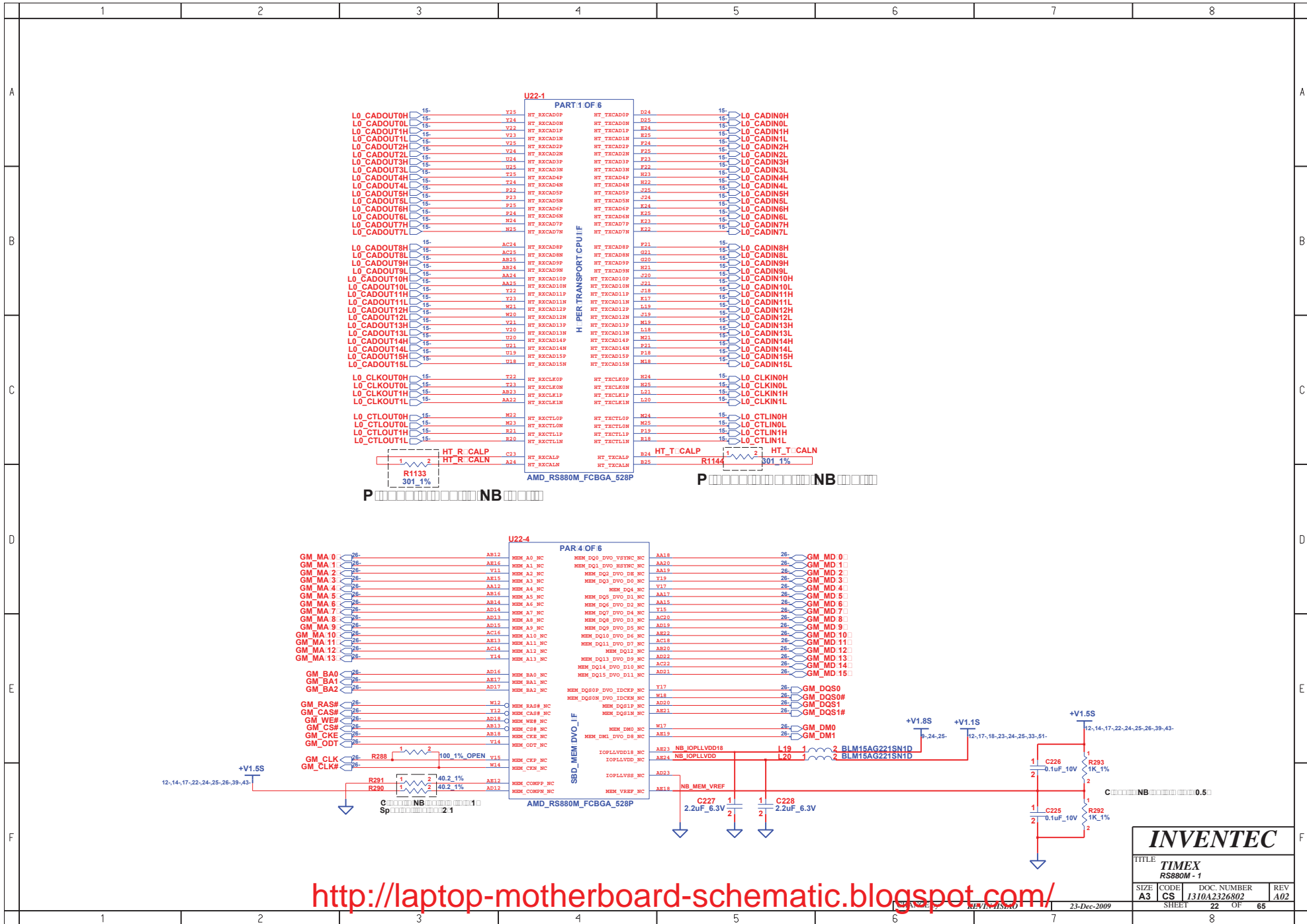


LAYOUT Note □ Put the thermal sensor close to north bridge.

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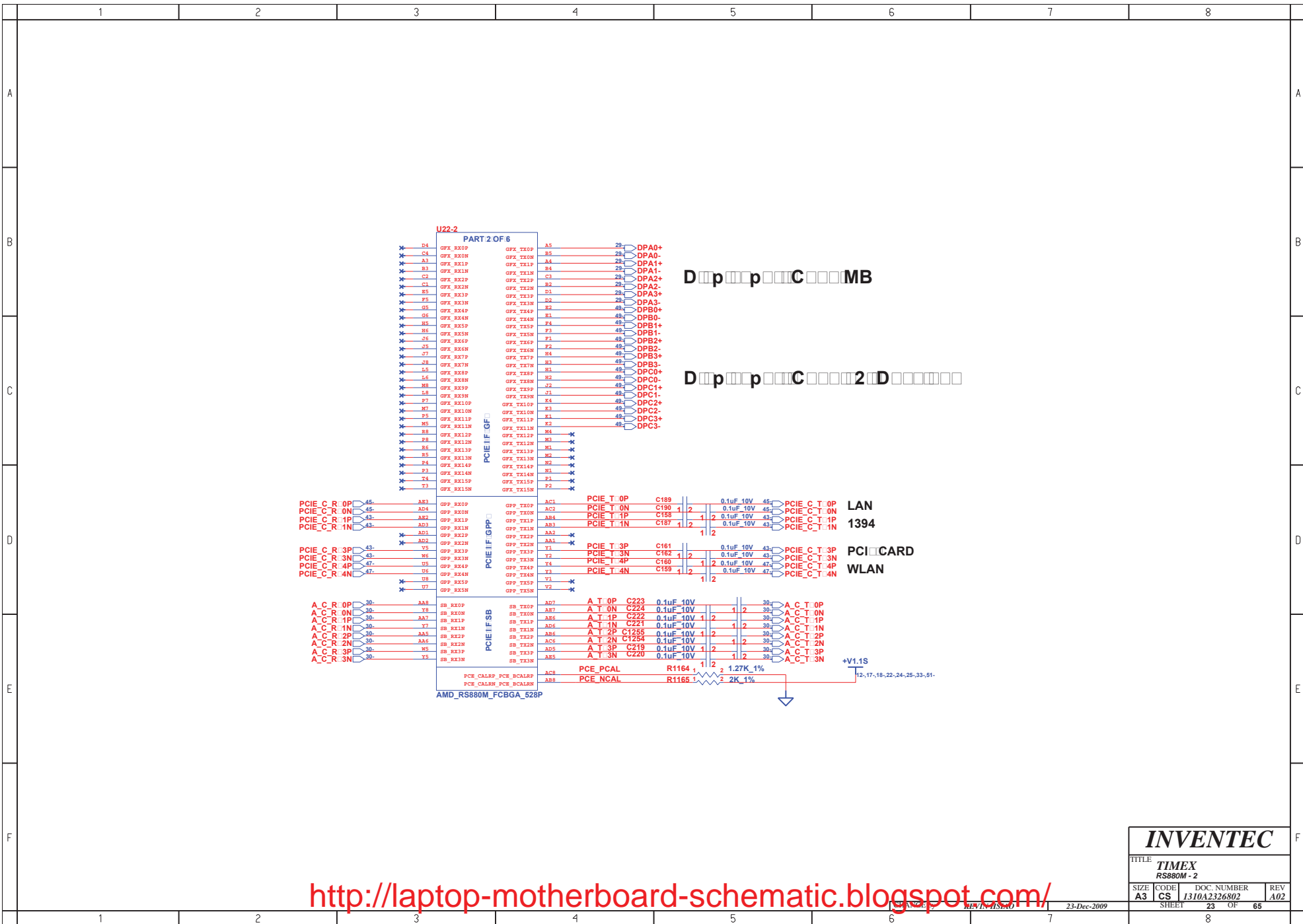
<b>INVENTEC</b>			
TITLE <b>TIMEX FAN &amp; THERMAL</b>			
SIZE <b>A3</b>	CODE <b>CS</b>	DOC. NUMBER <b>I310A2326802</b>	REV <b>A02</b>
SHEET <b>21</b>		OF <b>65</b>	

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<b>INVENTEC</b>			
TITLE <b>TIMEX</b> RS880M - 1			
SIZE <b>A3</b>	CODE <b>CS</b>	DOC. NUMBER <b>I310A2326802</b>	REV <b>A02</b>
SHEET <b>22</b> OF <b>65</b>		DATE <b>23-Dec-2009</b>	



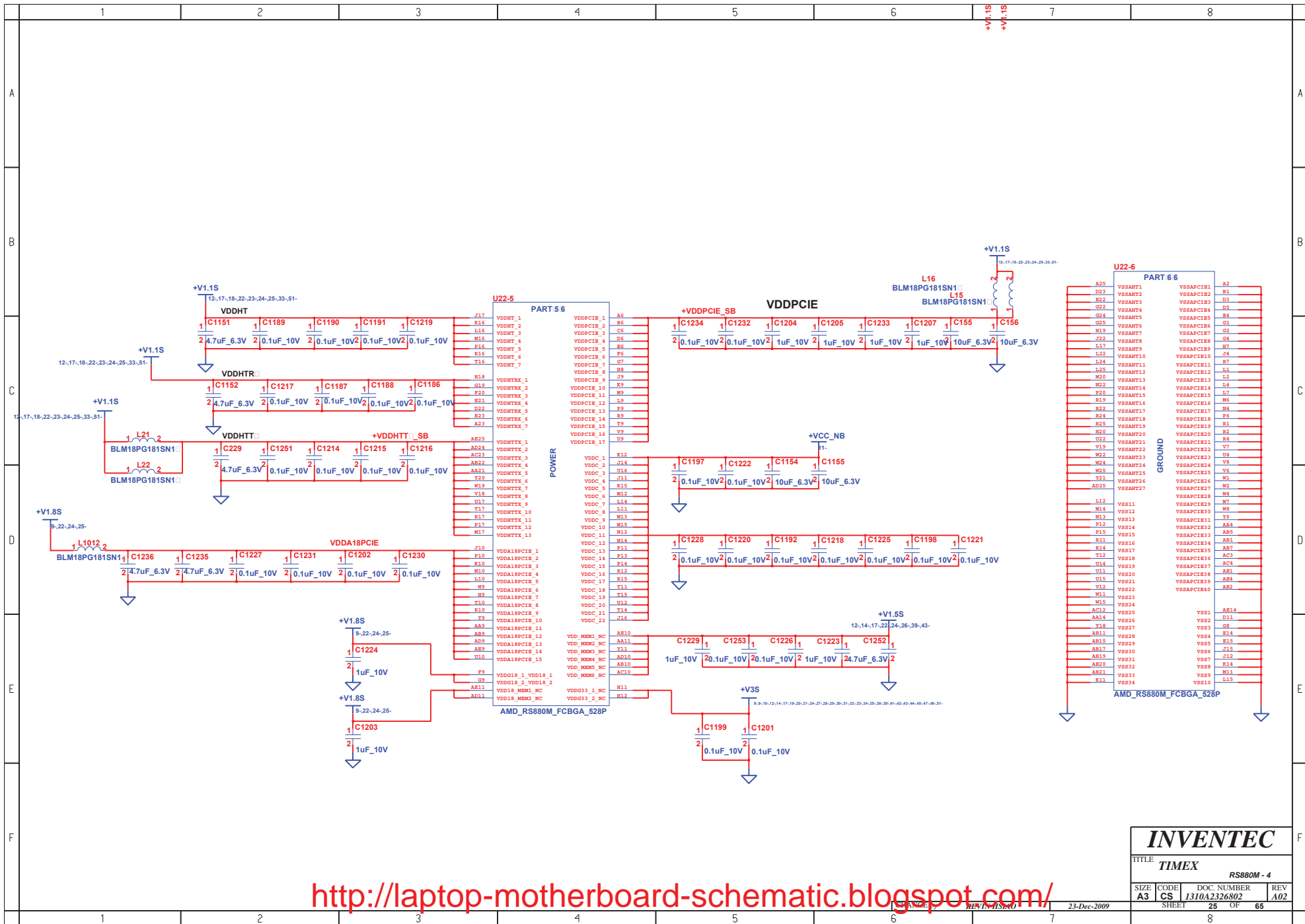
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<b>INVENTEC</b>			
TITLE <b>TIMEX RS880M - 2</b>			
SIZE <b>A3</b>	CODE <b>CS</b>	DOC. NUMBER <b>1310A2326802</b>	REV <b>A02</b>
SHEET <b>23</b>		OF <b>65</b>	

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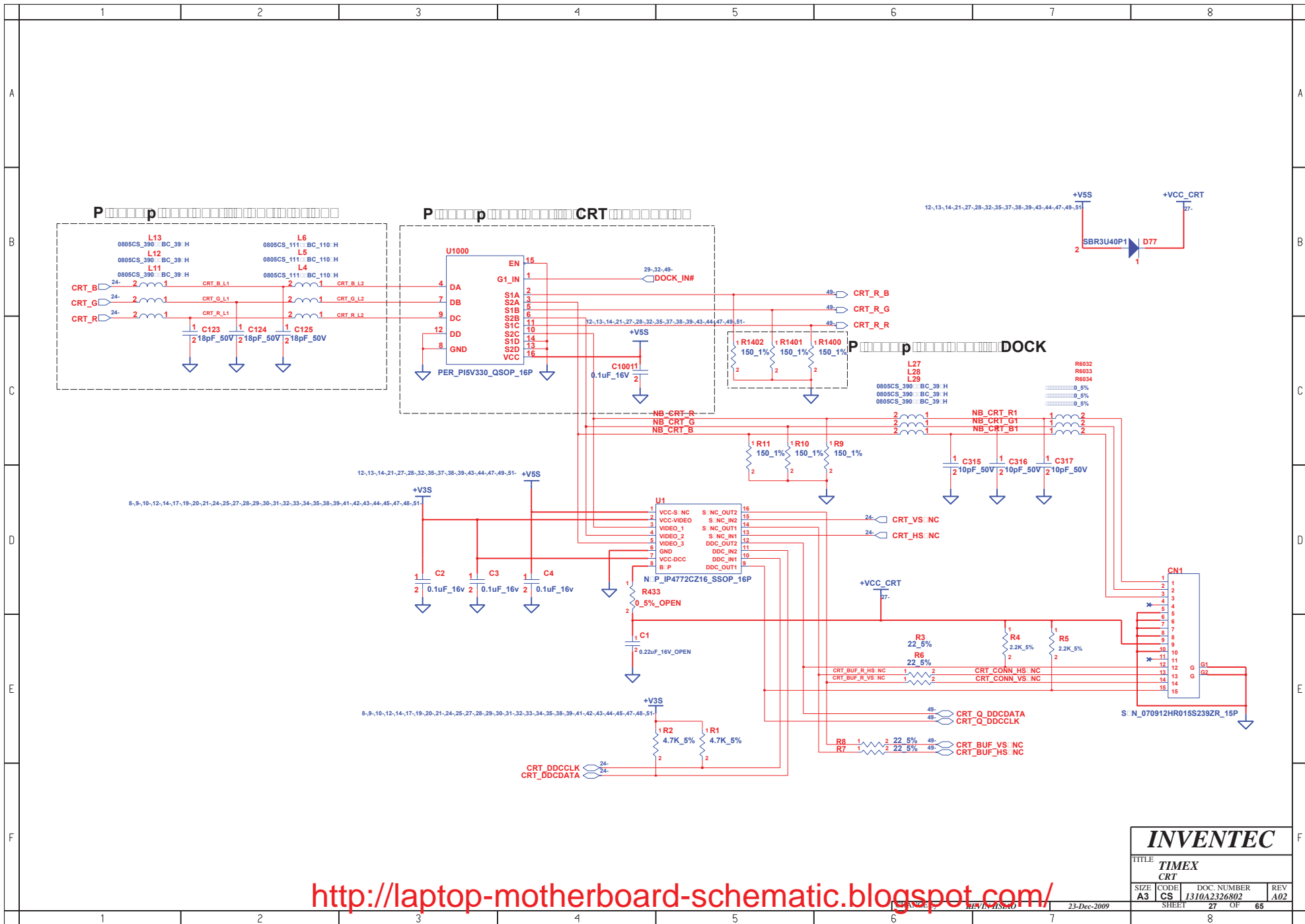


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<b>INVENTEC</b>			
TITLE <b>TIMEX</b>			
RS880M - 4			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	I310A2326802	A02
SHEET		25	OF 65

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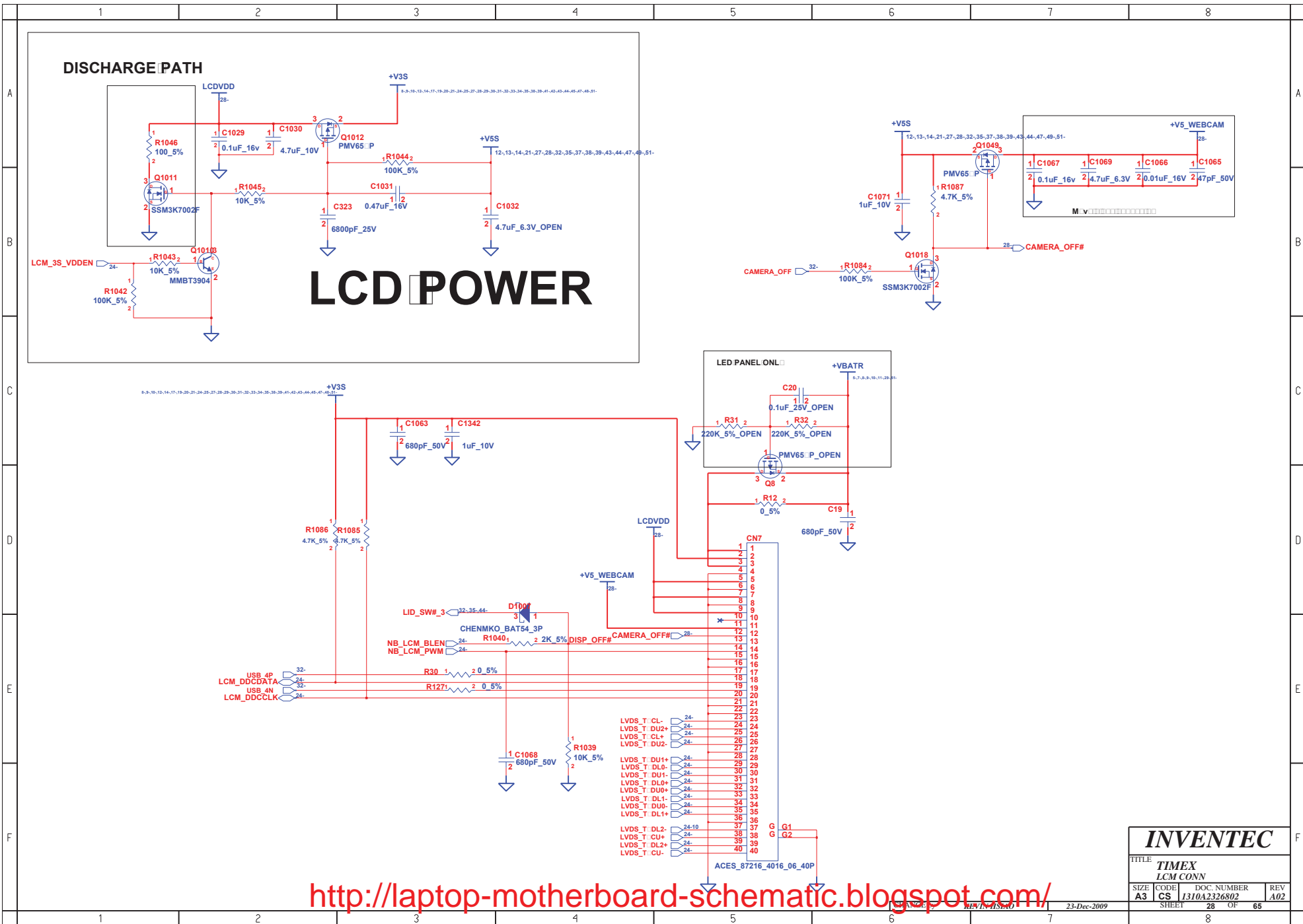




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<b>INVENTEC</b>			
TITLE <b>TIMEX CRT</b>			
SIZE <b>A3</b>	CODE <b>CS</b>	DOC. NUMBER <b>I310A2326802</b>	REV <b>A02</b>
SHEET <b>27</b>		OF <b>65</b>	

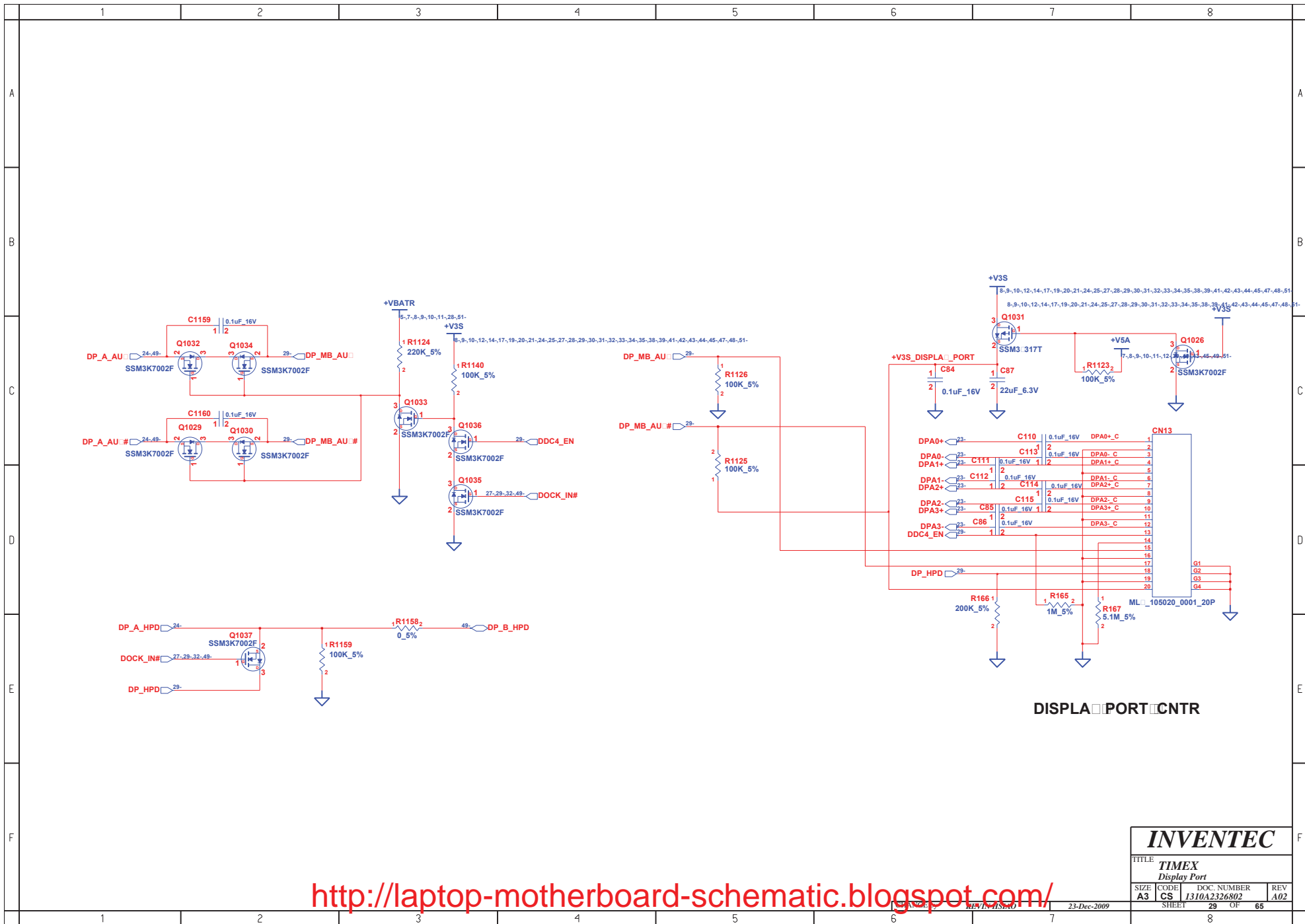
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<b>INVENTEC</b>			
TITLE <b>TIMEX LCM CONN</b>			
SIZE <b>A3</b>	CODE <b>CS</b>	DOC. NUMBER <b>I310A2326802</b>	REV <b>A02</b>
SHEET <b>28</b>		OF <b>65</b>	

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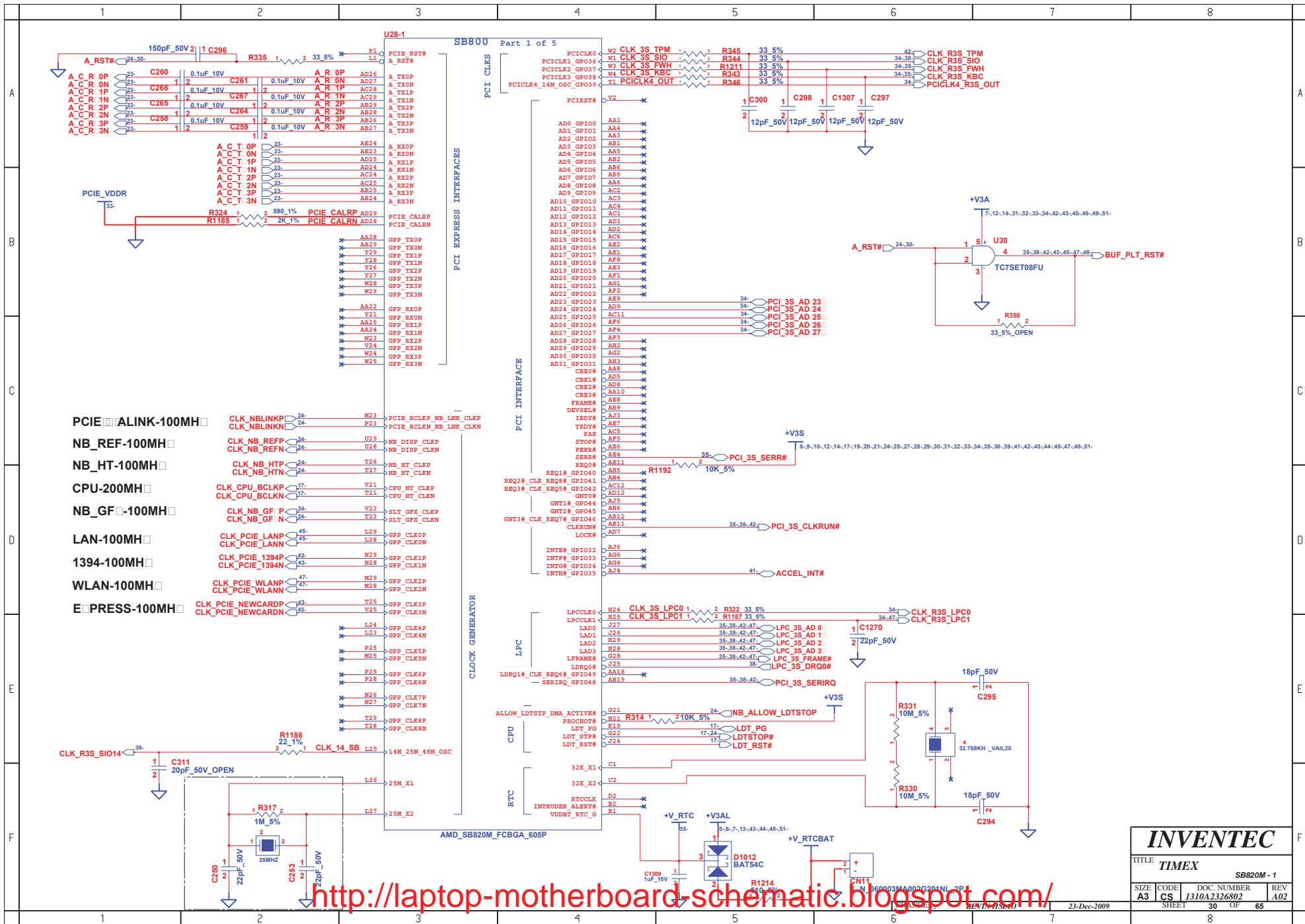


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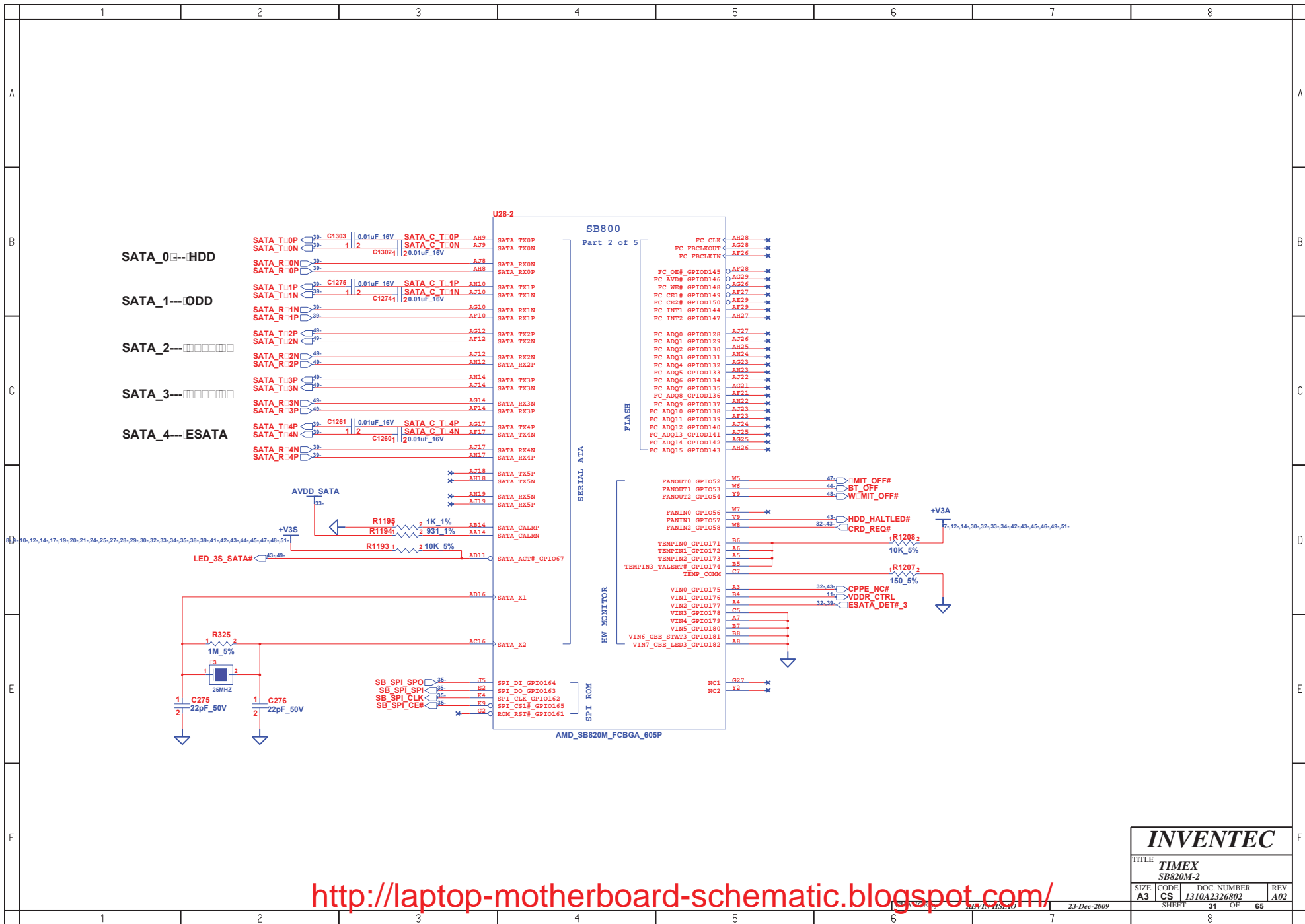
TITLE			
TIMEX			
Display Port			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	I310A2326802	A02
SHEET		29	OF 65

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<b>INVENTEC</b>			
TITLE <b>TIMEX</b>			
		SB820M - 1	
SIZE	CODE	DOC. NUMBER	REV
A3	CS	1310A2326802	A02
SHEET 30		OF 65	

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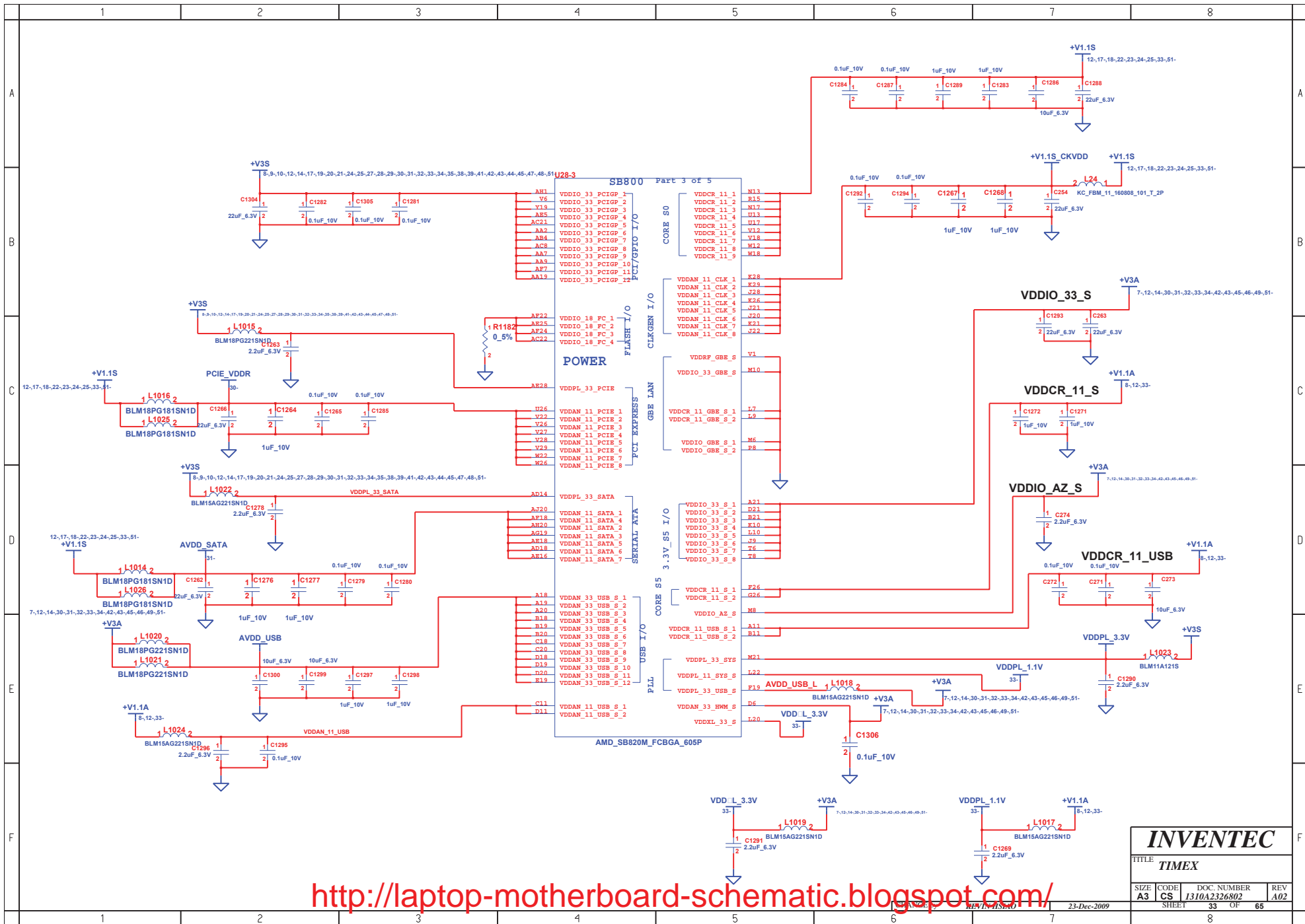
**INVENTEC**

TITLE			
TIMEX			
SB820M-2			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	I310A2326802	A02
SHEET		31	OF 65

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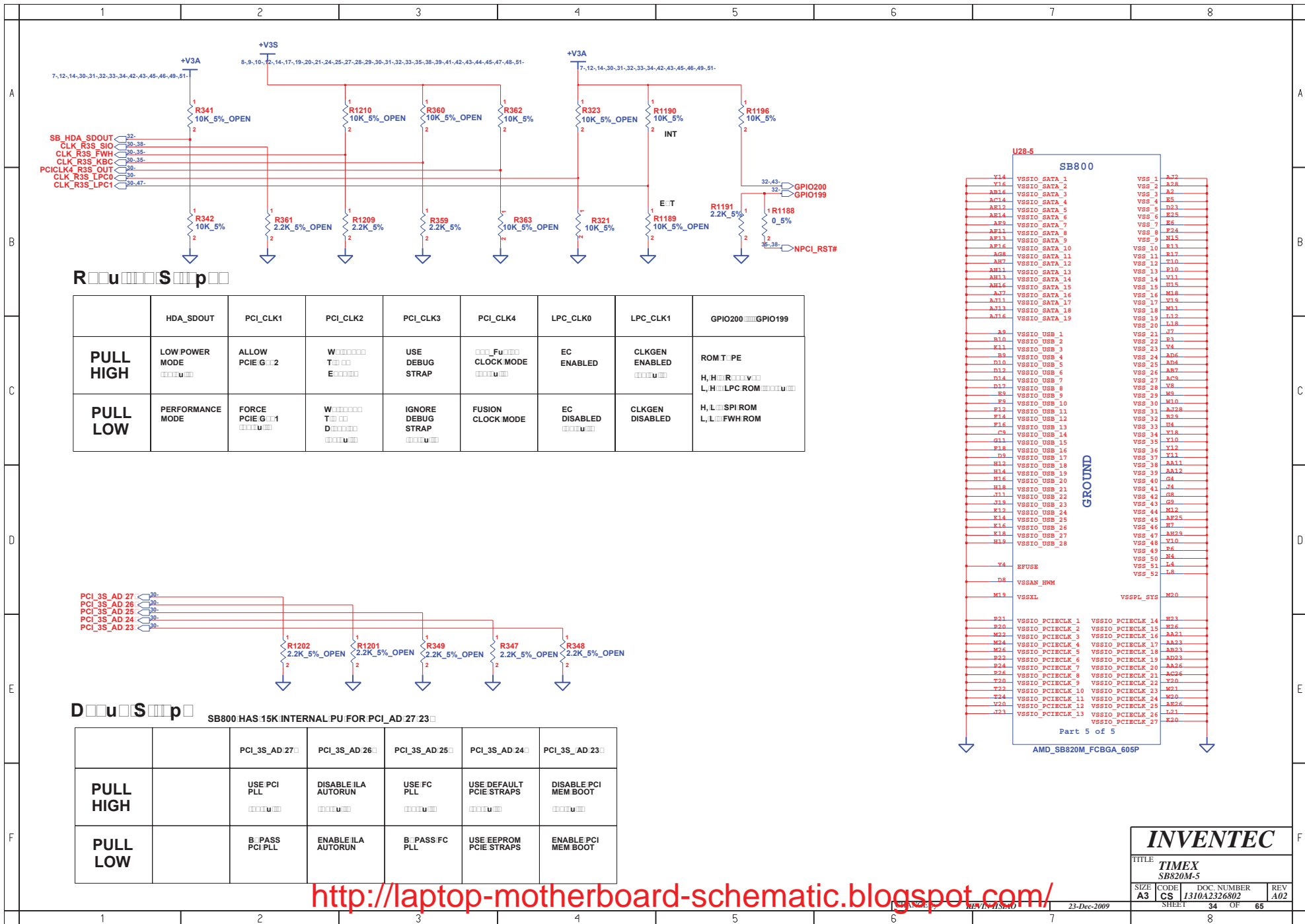






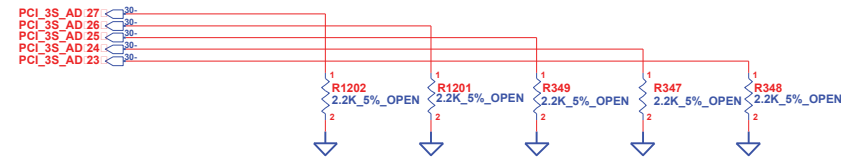
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<b>INVENTEC</b>			
TITLE <b>TIMEX</b>			
SIZE <b>A3</b>	CODE <b>CS</b>	DOC. NUMBER <b>I310A2326802</b>	REV <b>A02</b>
SHEET <b>33</b>		OF <b>65</b>	



R    u    S    p    

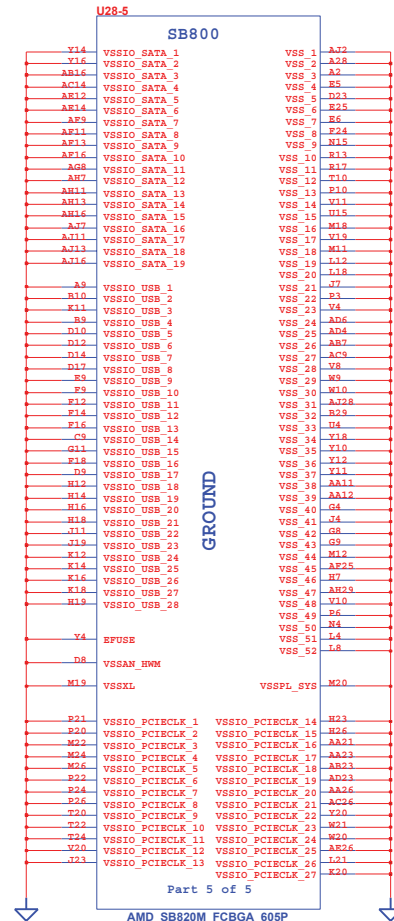
	HDA_SDOUT	PCI_CLK1	PCI_CLK2	PCI_CLK3	PCI_CLK4	LPC_CLK0	LPC_CLK1	GPIO200 GPIO199
<b>PULL HIGH</b>	LOW POWER MODE u	ALLOW PCIE G2	W T E	USE DEBUG STRAP	Fu CLOCK MODE u	EC ENABLED	CLKGEN ENABLED	ROM.T.PE H,H[R,v] L,H[LPC ROM]u
<b>PULL LOW</b>	PERFORMANCE MODE	FORCE PCIE G1	W T D	IGNORE DEBUG STRAP	FUSION CLOCK MODE	EC DISABLED	CLKGEN DISABLED	H,L[SPI ROM] L,L[FWH.ROM]



D    u    S    p    

SB800 HAS 15K INTERNAL PU FOR PCI\_AD 27 23

	PCI_3S_AD 27	PCI_3S_AD 26	PCI_3S_AD 25	PCI_3S_AD 24	PCI_3S_AD 23
<b>PULL HIGH</b>	USE PCI PLL	DISABLE ILA AUTORUN	USE FC PLL	USE DEFAULT PCIE STRAPS	DISABLE PCI MEM BOOT
<b>PULL LOW</b>	B_PASS PCI.PLL	ENABLE ILA AUTORUN	B_PASS FC PLL	USE EEPROM PCIE STRAPS	ENABLE PCI MEM BOOT

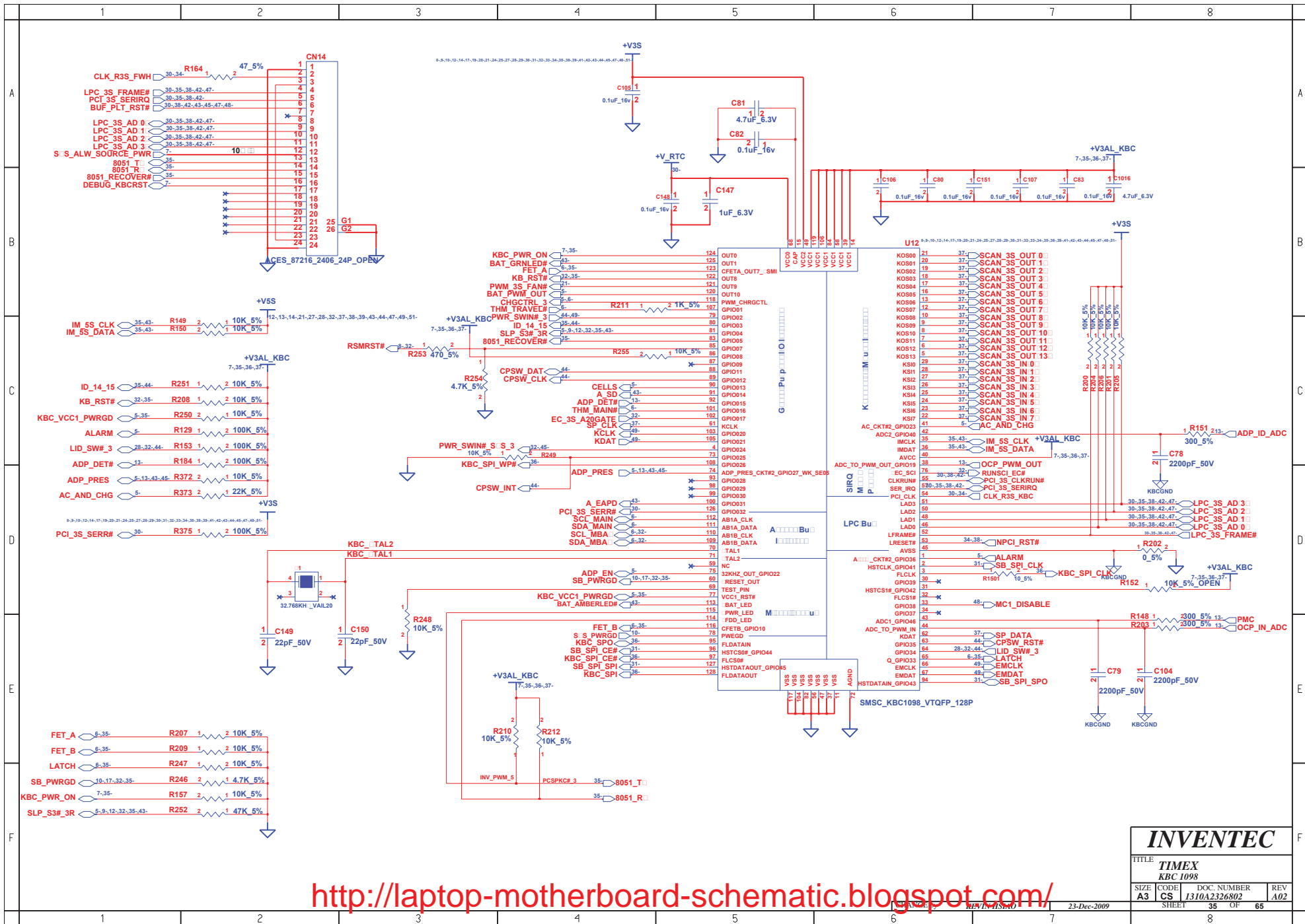


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**INVENTEC**

TITLE: **TIMEX SB820M-5**

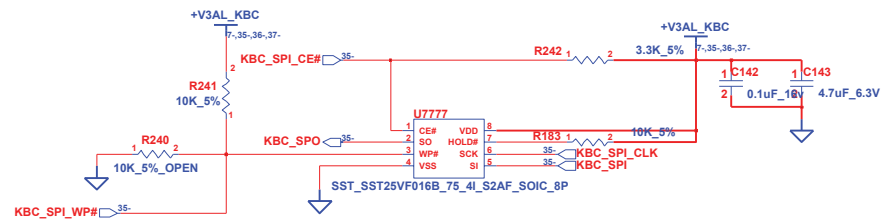
SIZE: <b>A3</b>	CODE: <b>CS</b>	DOC. NUMBER: <b>I310A2326802</b>	REV: <b>A02</b>
SHEET: <b>34</b>		OF: <b>65</b>	



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<b>INVENTEC</b>			
TITLE <b>TIMEX KBC 1098</b>			
SIZE <b>A3</b>	CODE <b>CS</b>	DOC. NUMBER <b>1310A2326802</b>	REV <b>A02</b>
SHEET <b>35</b>		OF <b>65</b>	

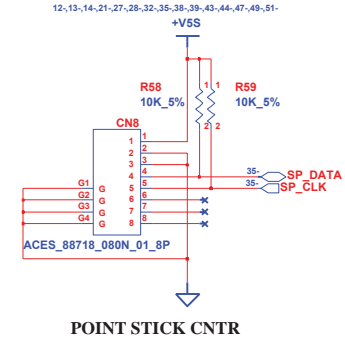
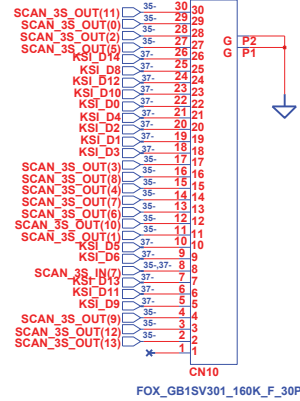
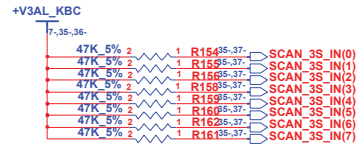
23-Dec-2009



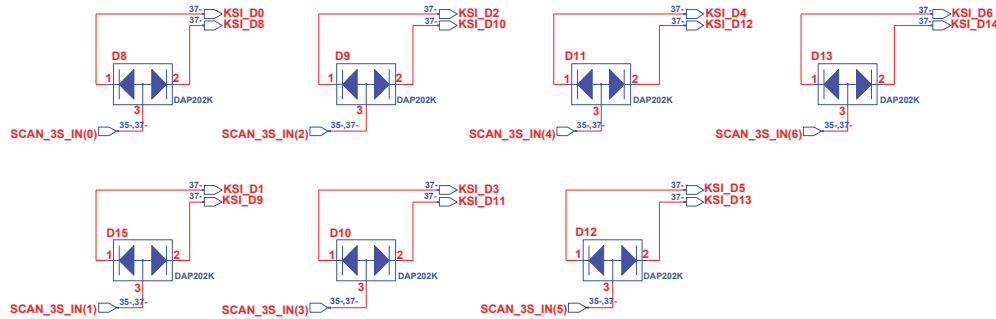
SPI □ □ □ □

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<b>INVENTEC</b>			
TITLE <b>TIMEX</b> <b>SPI</b>			
SIZE <b>A3</b>	CODE <b>CS</b>	DOC. NUMBER <b>1310A2326802</b>	REV <b>A02</b>
SHEET <b>36</b>		OF <b>65</b>	

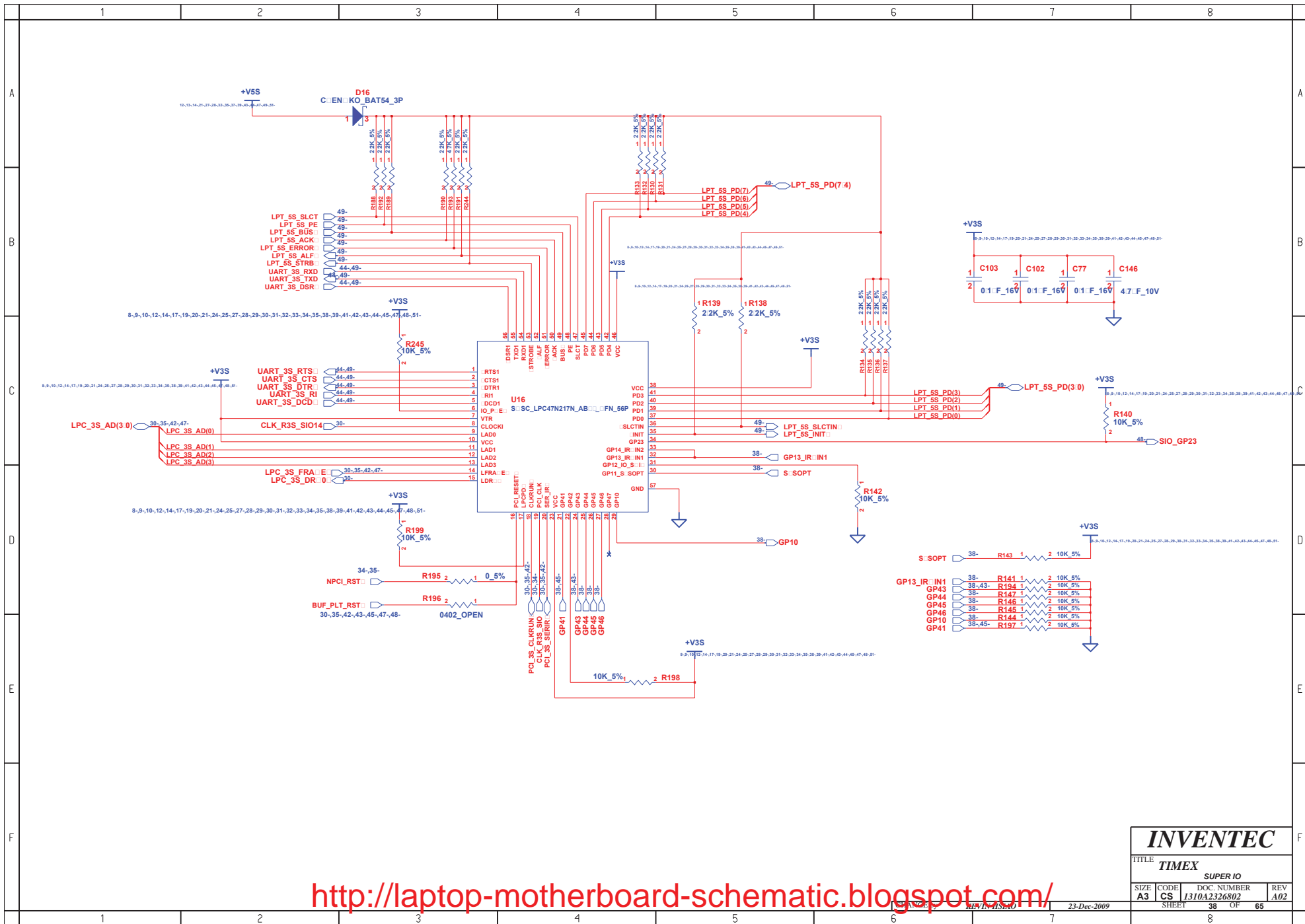


## KeyBoard CONN (30 pin)



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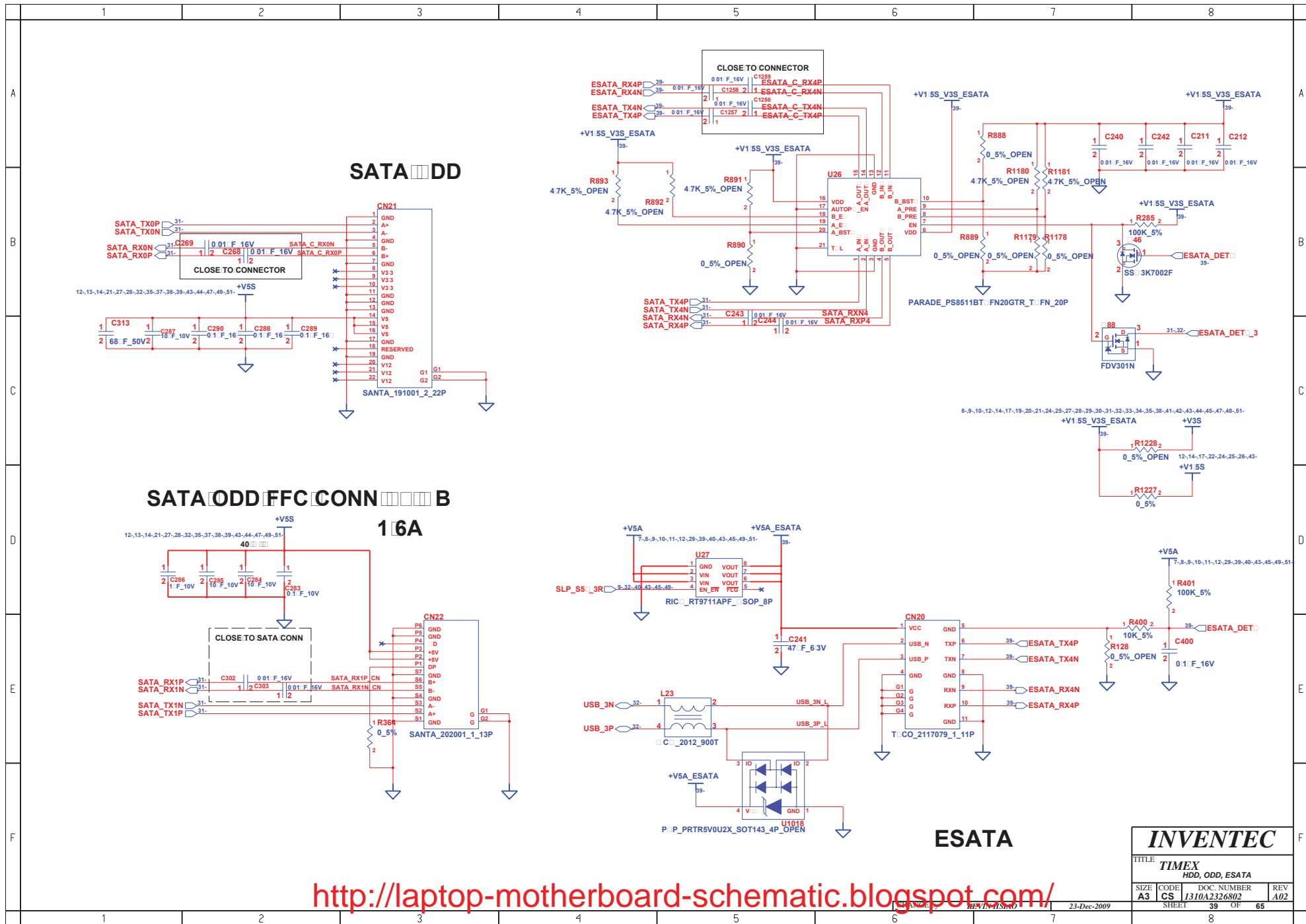
<b>INVENTEC</b>			
TITLE <b>TIMEX</b> KEYBOARD , Pointstick			
SIZE <b>A3</b>	CODE <b>CS</b>	DOC. NUMBER <b>I310A2326802</b>	REV <b>A02</b>
SHEET <b>37</b>		OF <b>65</b>	



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<b>INVENTEC</b>			
TITLE: <b>TIMEX</b>			
SUPER IO			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	I310A2326802	A02
SHEET		OF	
38		65	

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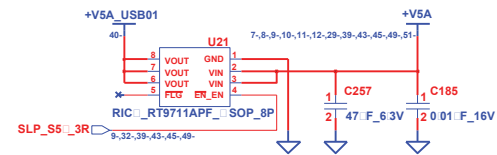
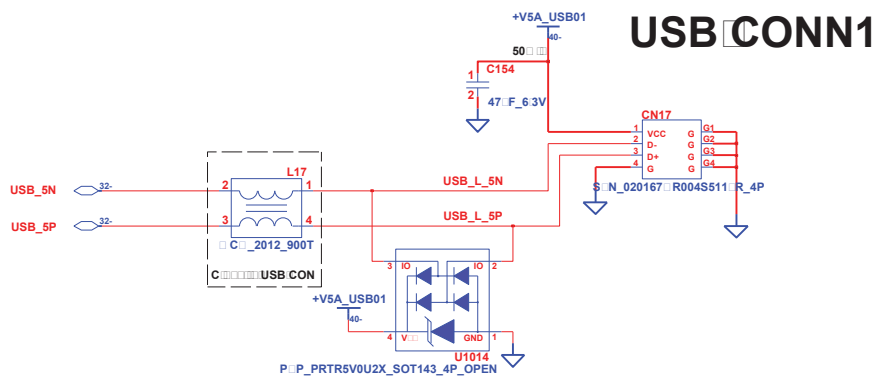
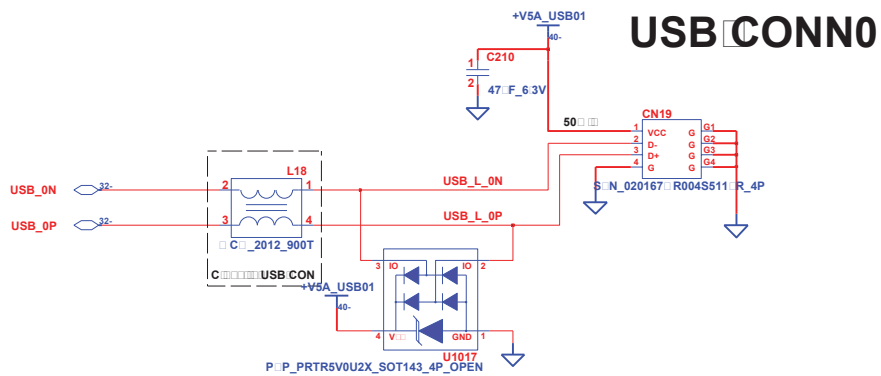


<http://laptop-motherboard-schematic.blogspot.com/>

**ESATA**

<b>INVENTEC</b>			
TITLE <b>TIMEX</b> HDD, ODD, ESATA			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	I310A2326802	A02
SHEET		39	OF 65

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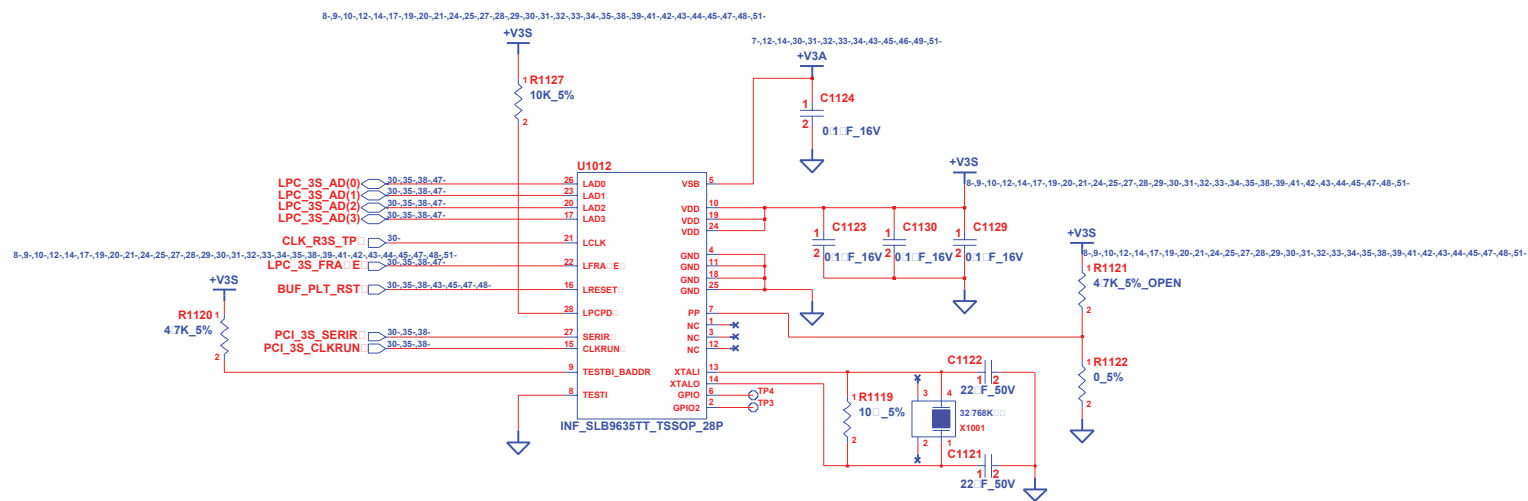
**INVENTEC**

TITLE <b>TIMEX</b> USB CONN			
SIZE <b>A3</b>	CODE <b>CS</b>	DOC. NUMBER <b>I310A2326802</b>	REV <b>A02</b>
SHEET <b>40</b>		OF <b>65</b>	

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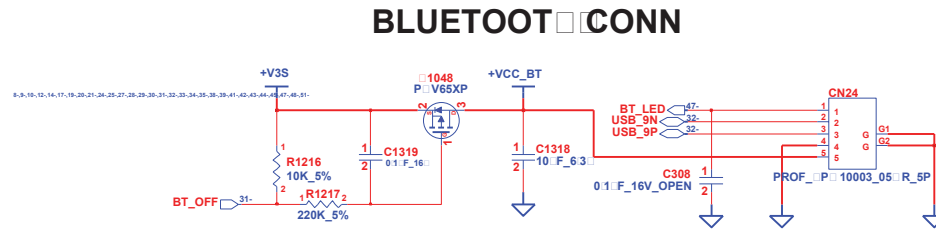
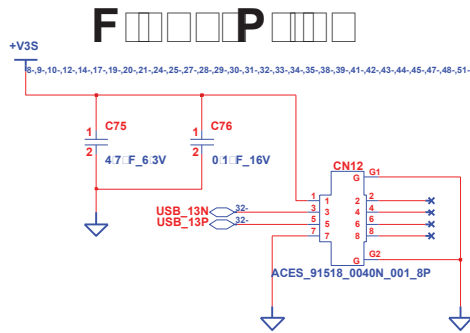
TPM [V12]

<http://laptop-motherboard-schematic.blogspot.com/>

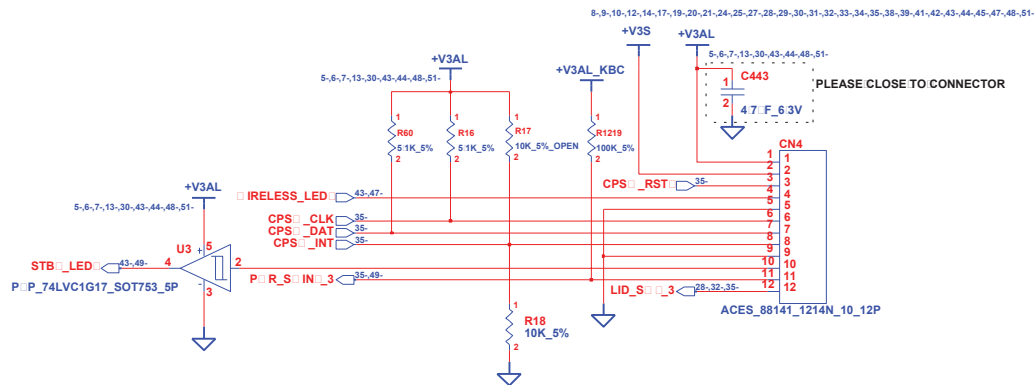
<b>INVENTEC</b>			
TITLE <b>TIMEX</b>			
TPM			
SIZE <b>A3</b>	CODE <b>CS</b>	DOC. NUMBER <b>I310A2326802</b>	REV <b>A02</b>
SHEET <b>42</b>		OF <b>65</b>	

23-Dec-2009

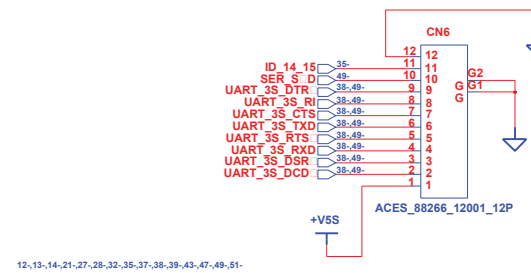




### S ITC BOARD



### SERIAL PORT CONN



**2B C**

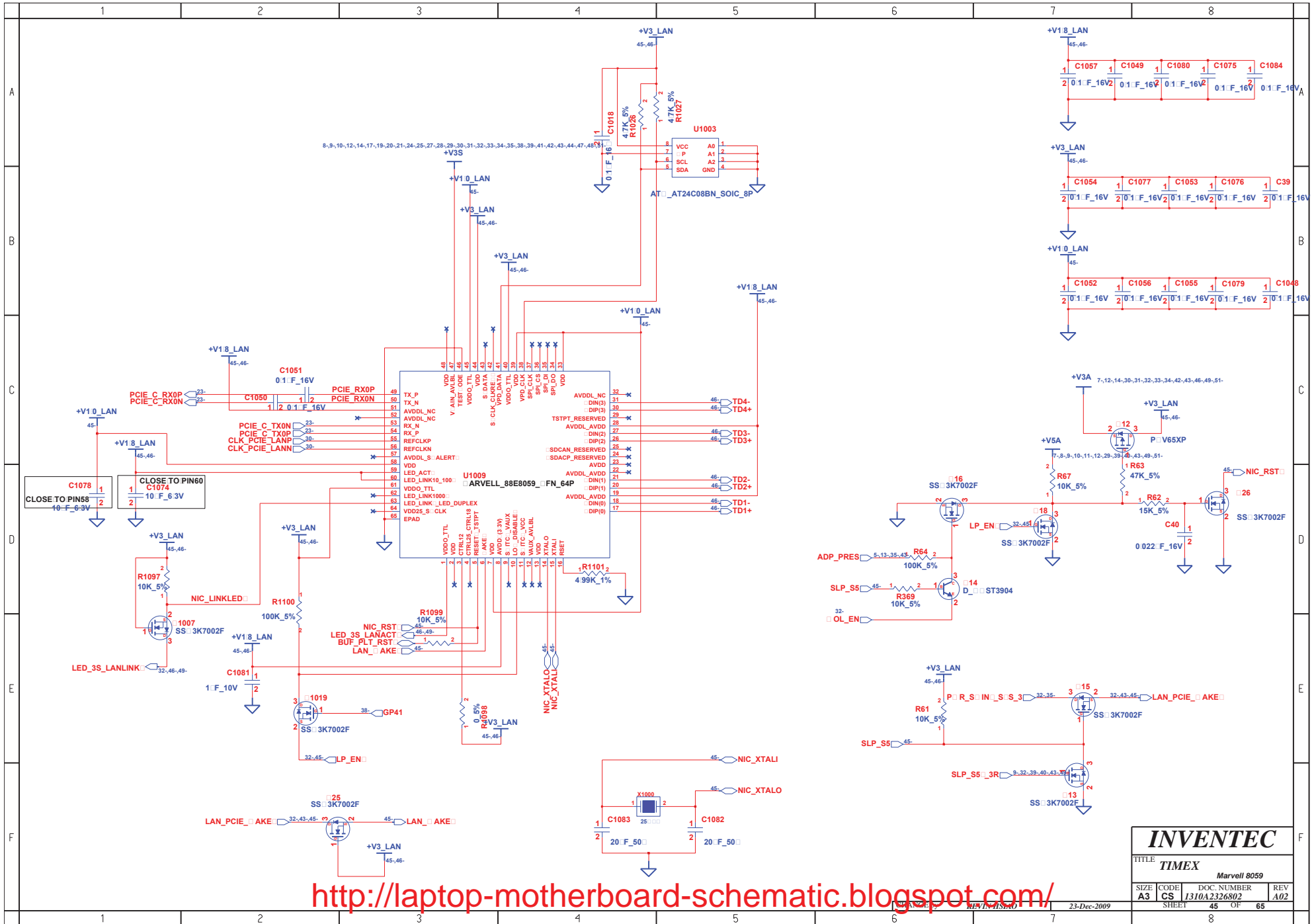
<http://laptop-motherboard-schematic.blogspot.com/>

**INVENTEC**

TITLE			
TIMEX			
W2B Connectors			
SIZE	CODE	DOC NUMBER	REV
A3	CS	I310A2326802	A02
SHEET		44	OF 65

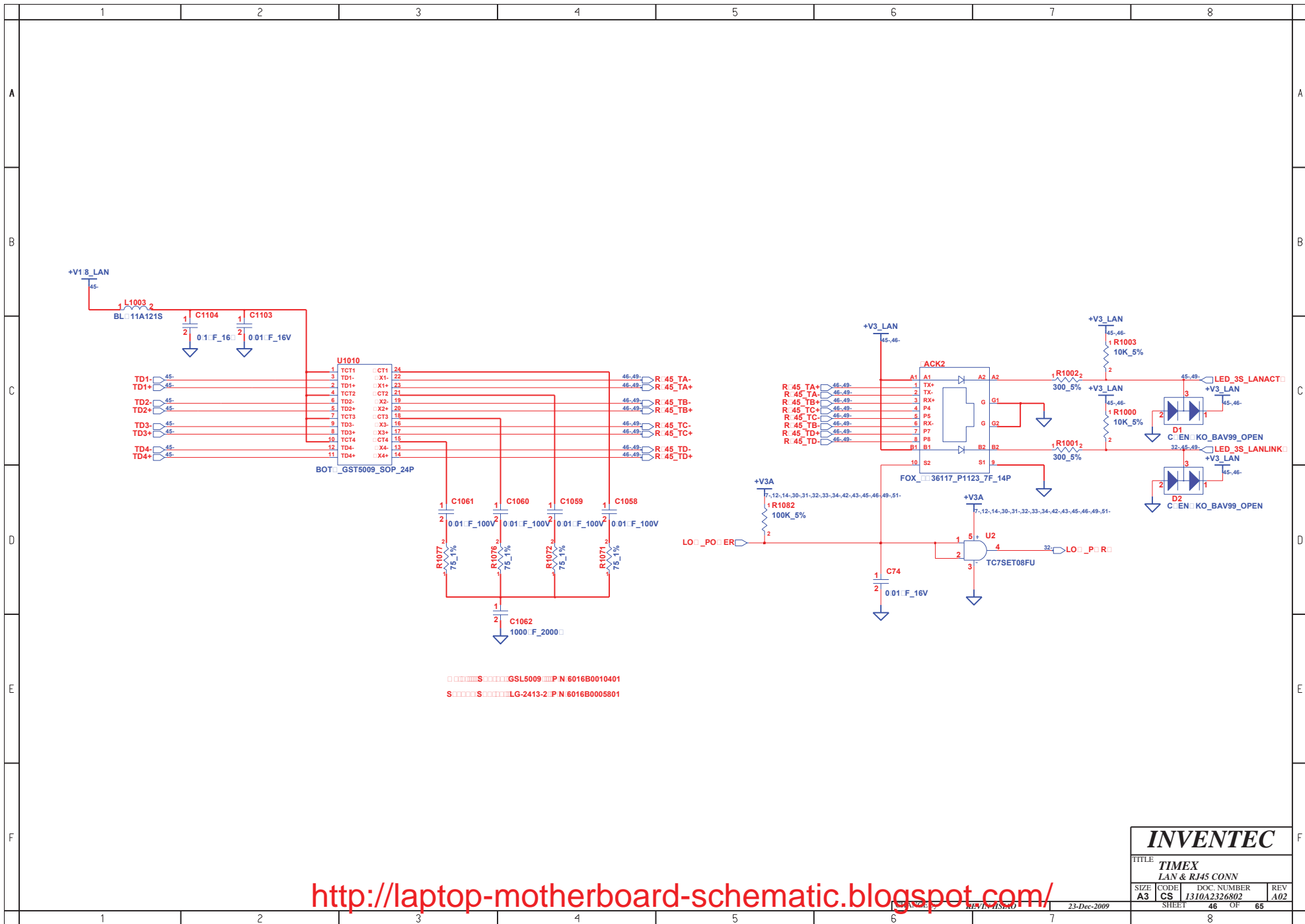
23-Dec-2009

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<b>INVENTEC</b>				
TITLE: <b>TIMEX</b>				
Marvell 8059				
SIZE	CODE	DOC. NUMBER	REV	
A3	CS	I310A2326802	A02	
SHEET		45 OF		65

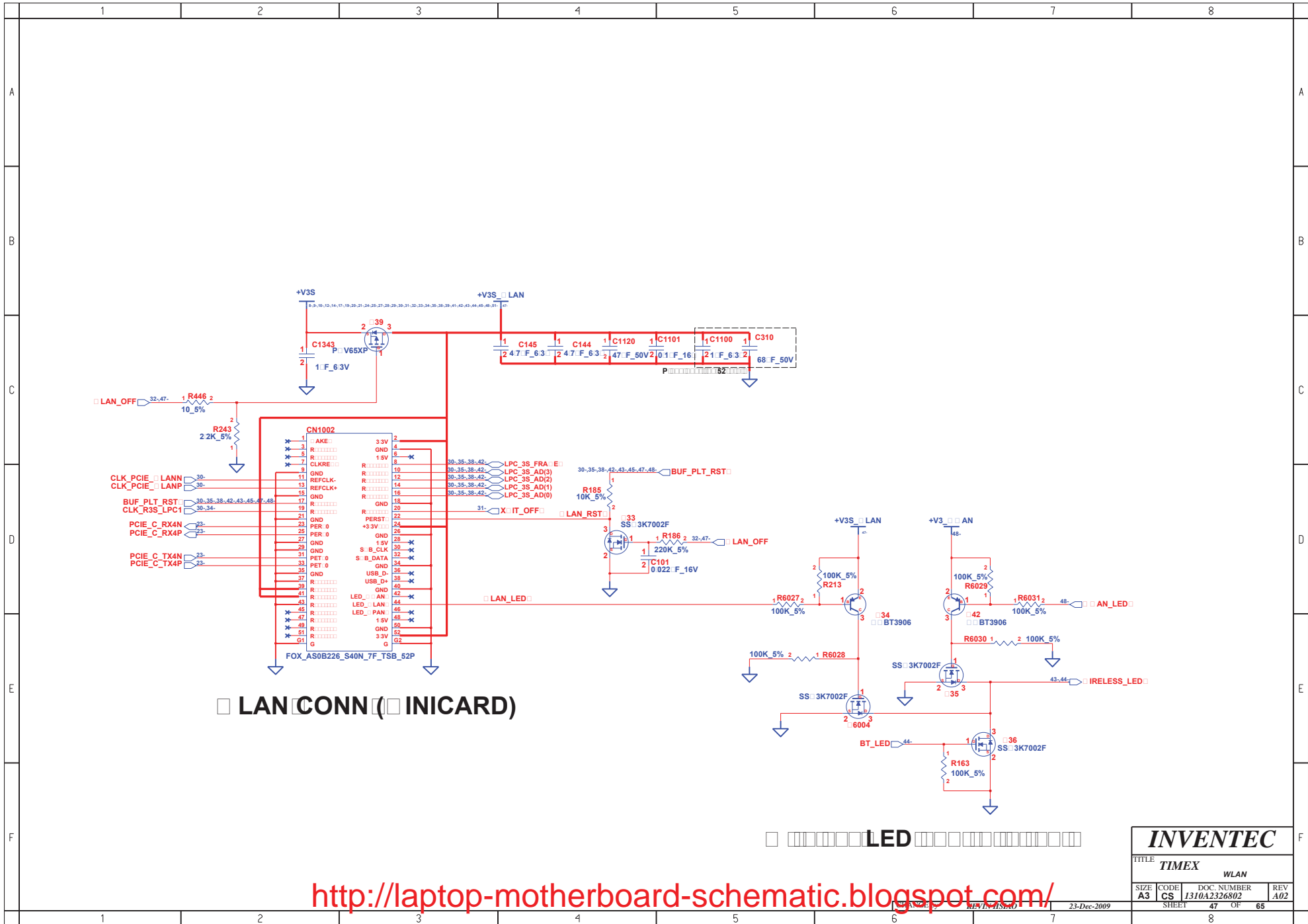


□ □ □ □ □ S □ □ □ □ □ GSL5009 □ □ P N 6016B0010401  
 S □ □ □ □ S □ □ □ □ □ LG-2413-2 P N 6016B0005801

<http://laptop-motherboard-schematic.blogspot.com/>

<b>INVENTEC</b>			
TITLE <b>TIMEX</b> LAN & RJ45 CONN			
SIZE <b>A3</b>	CODE <b>CS</b>	DOC. NUMBER <b>I310A2326802</b>	REV <b>A02</b>
SHEET		OF	
46		65	

23-Dec-2009

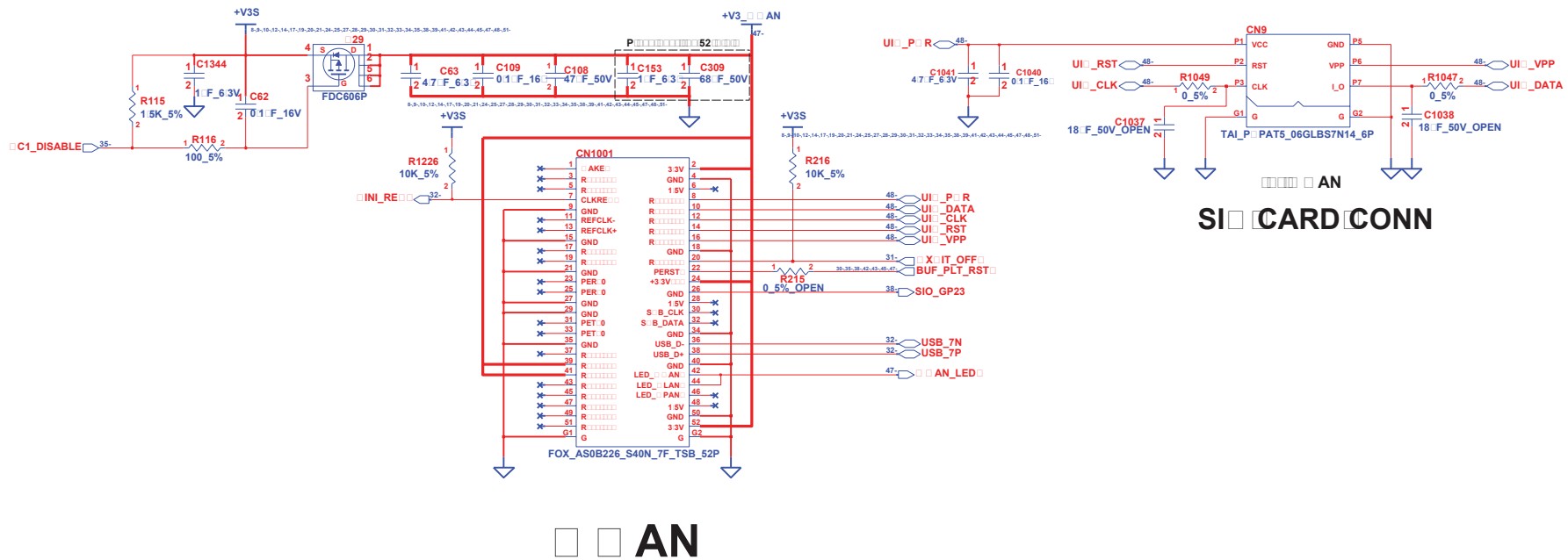


<http://laptop-motherboard-schematic.blogspot.com/>

<b>INVENTEC</b>			
TITLE <b>TIMEX WLAN</b>			
SIZE <b>A3</b>	CODE <b>CS</b>	DOC. NUMBER <b>I310A2326802</b>	REV <b>A02</b>
SHEET <b>47</b>		OF <b>65</b>	

23-Dec-2009

# OPTION PART



SI CARD CONN

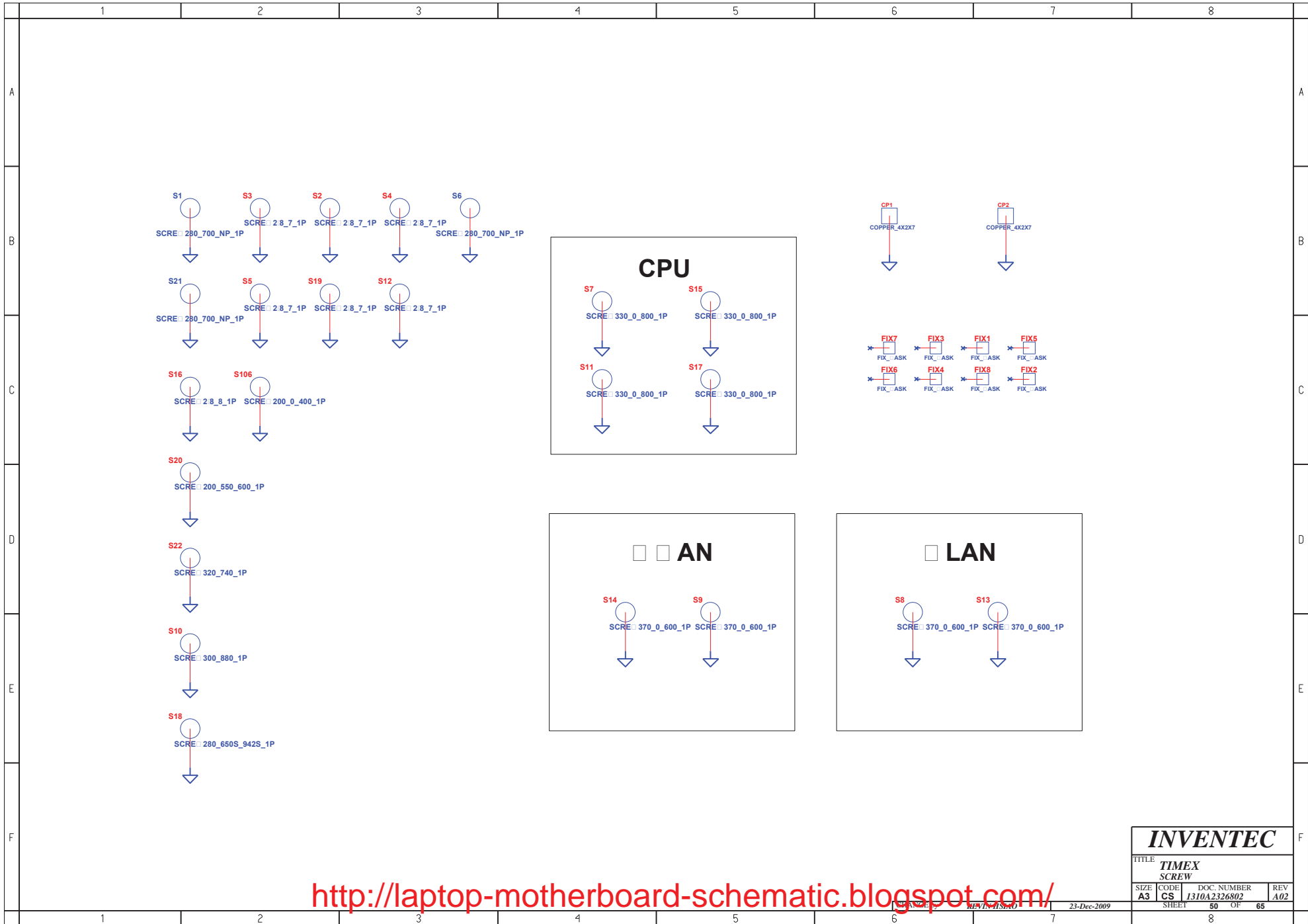
AN

<http://laptop-motherboard-schematic.blogspot.com/>

<b>INVENTEC</b>			
TITLE <b>TIMEX</b> WWAN & SIM CARD CONN			
SIZE <b>A3</b>	CODE <b>CS</b>	DOC. NUMBER <b>I310A2326802</b>	REV <b>A02</b>
SHEET <b>48</b>		OF <b>65</b>	



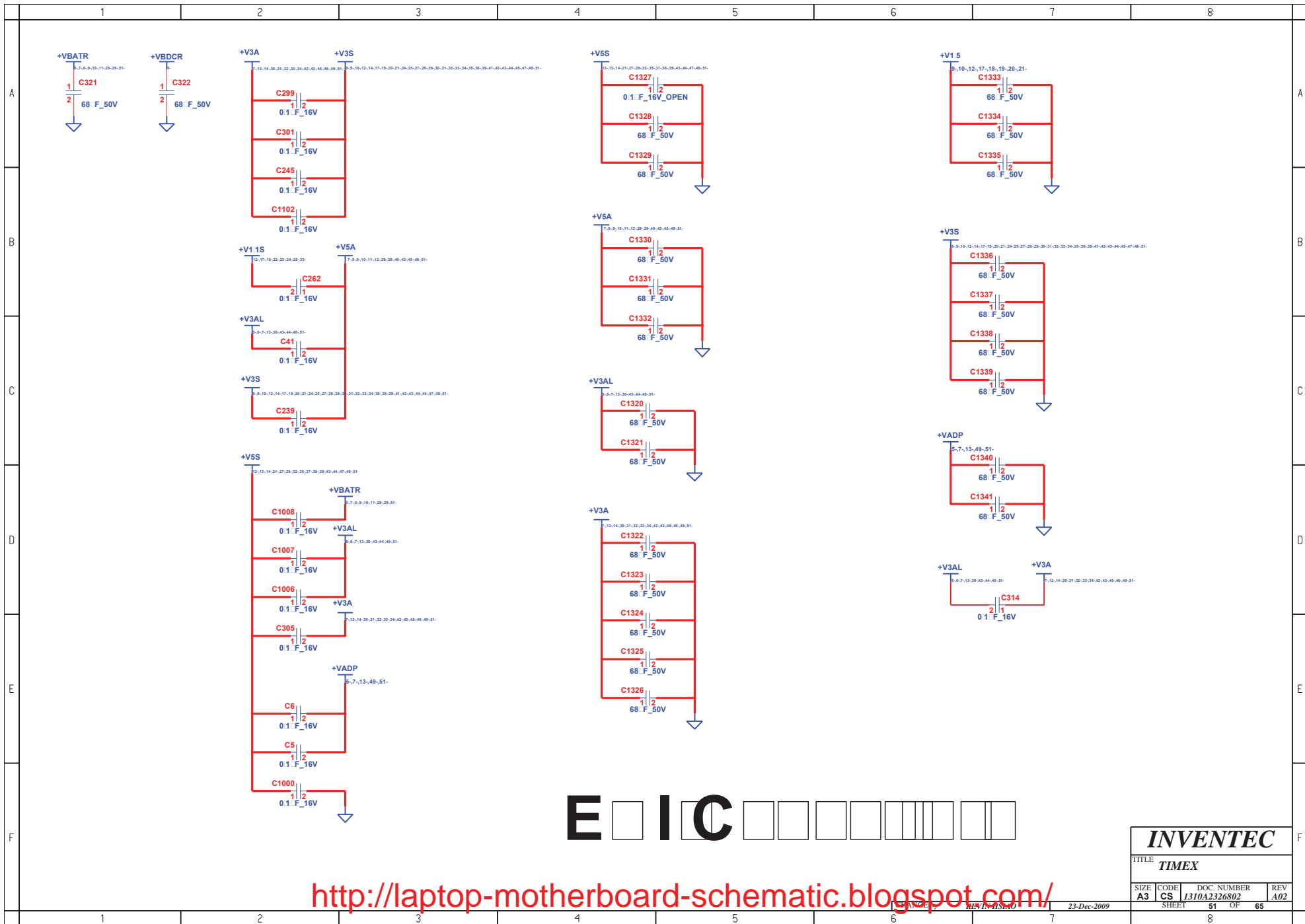




<http://laptop-motherboard-schematic.blogspot.com/>

<b>INVENTEC</b>			
TITLE: <b>TIMEX SCREW</b>			
SIZE: <b>A3</b>	CODE: <b>CS</b>	DOC. NUMBER: <b>1310A2326802</b>	REV: <b>A02</b>
SHEET: <b>50</b>		OF: <b>65</b>	

23-Dec-2009



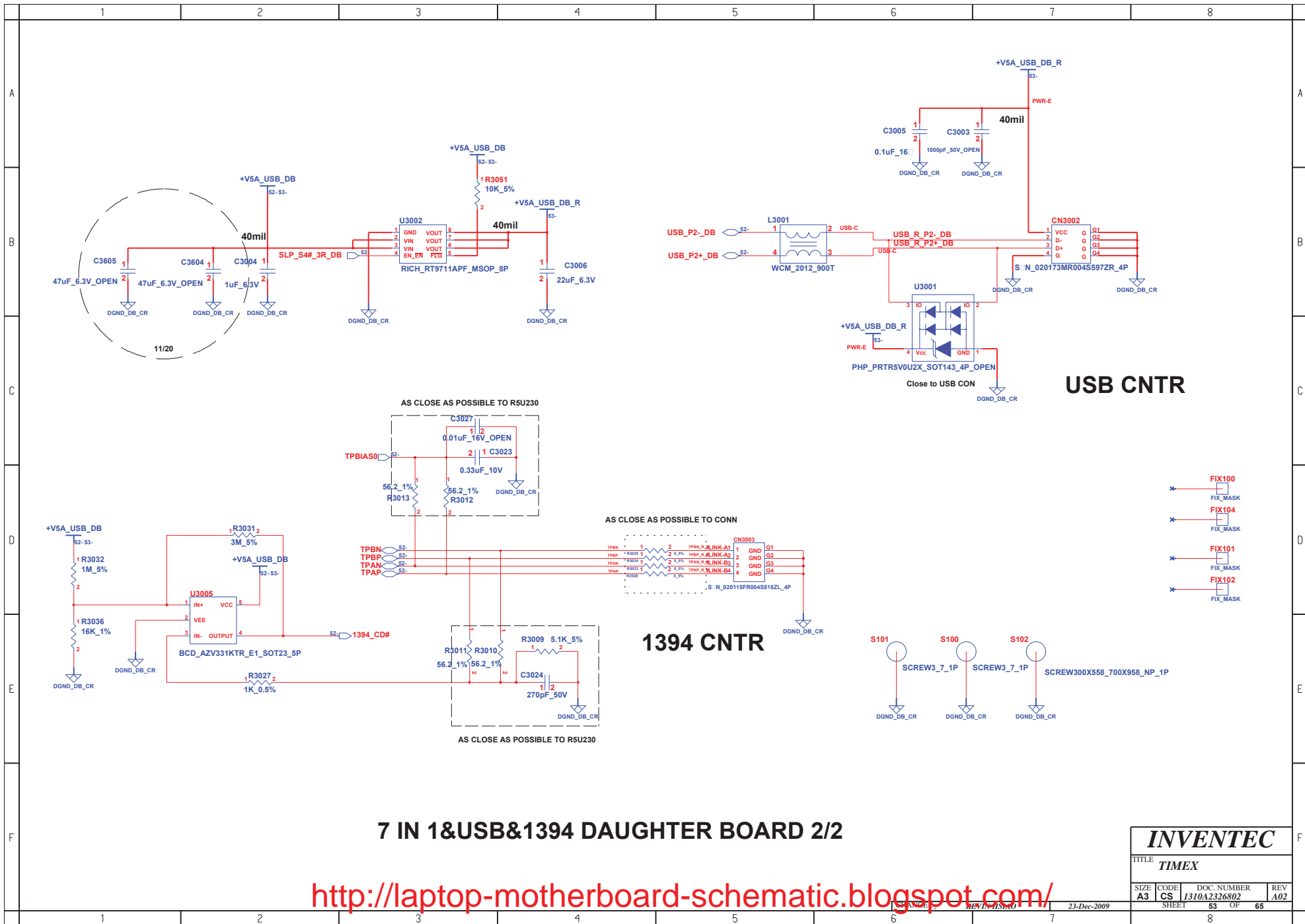
E I C

<http://laptop-motherboard-schematic.blogspot.com/>

<b>INVENTEC</b>			
TITLE TIMEX			
SIZE A3	CODE CS	DOC. NUMBER I310A2326802	REV A02
SHEET 51		OF 65	

23-Dec-2009



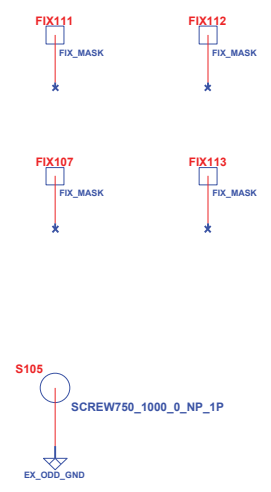
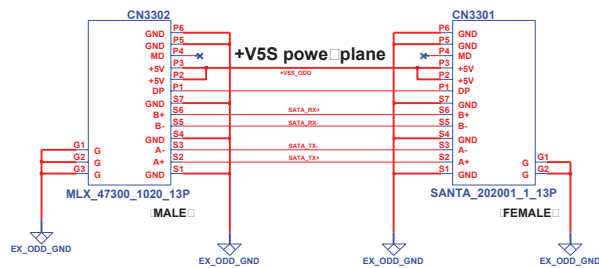


**7 IN 1&USB&1394 DAUGHTER BOARD 2/2**

<http://laptop-motherboard-schematic.blogspot.com/>

<b>INVENTEC</b>			
TITLE <b>TIMEX</b>			
SIZE <b>A3</b>	CODE <b>CS</b>	DOC. NUMBER <b>1310A2326802</b>	REV <b>A02</b>
SHEET <b>53</b>		OF <b>65</b>	

23-Dec-2009

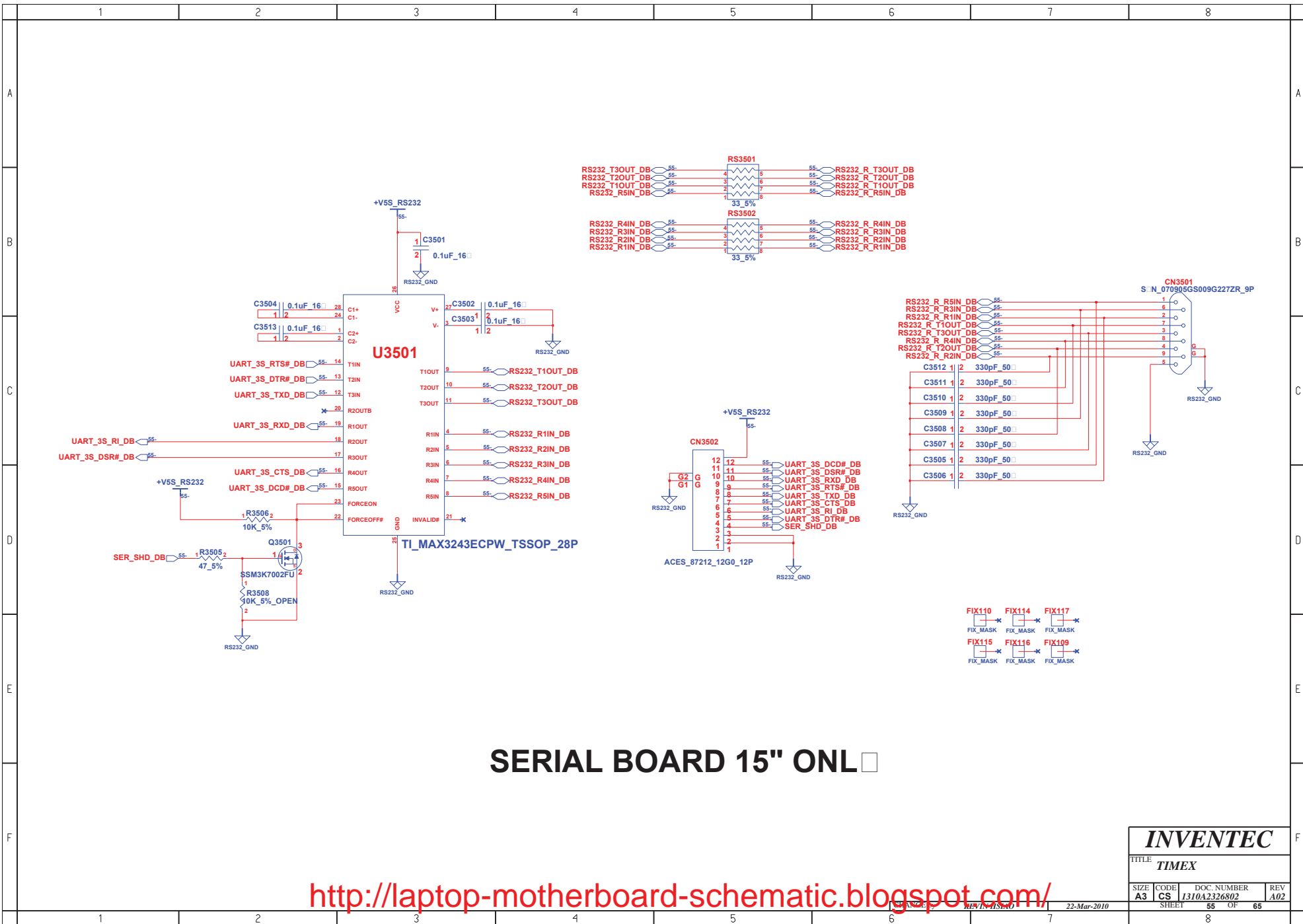


15" only

# ODD Extension Board

<http://laptop-motherboard-schematic.blogspot.com/>

<b>INVENTEC</b>			
TITLE <b>TIMEX</b>			
SIZE <b>A3</b>	CODE <b>CS</b>	DOC. NUMBER <b>1310A2326802</b>	REV <b>A02</b>
SHEET		54	OF 65

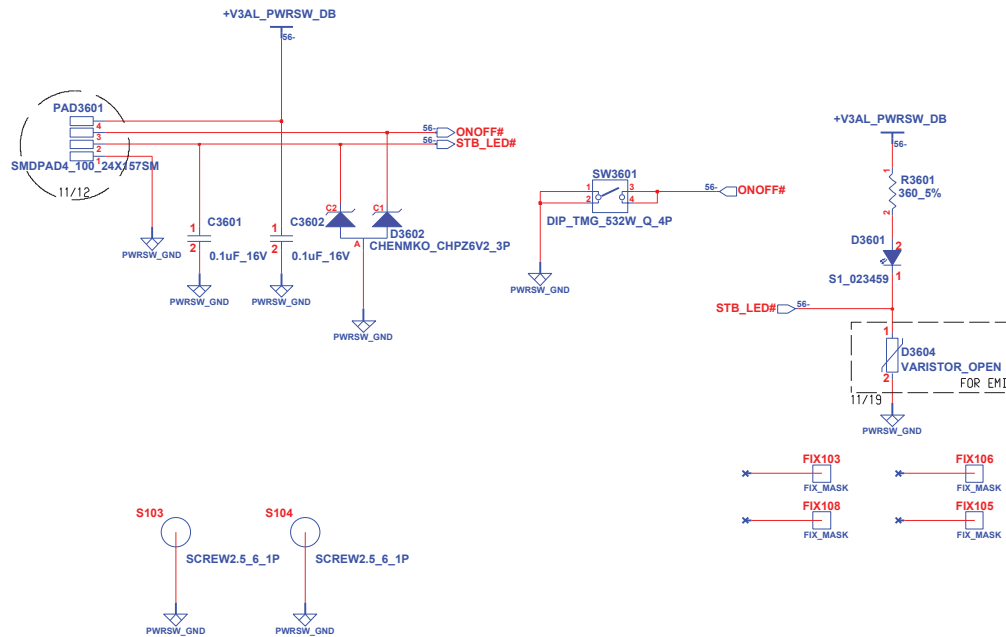


# SERIAL BOARD 15" ONL

<http://laptop-motherboard-schematic.blogspot.com/>

<b>INVENTEC</b>			
TITLE <b>TIMEX</b>			
SIZE <b>A3</b>	CODE <b>CS</b>	DOC. NUMBER <b>I310A2326802</b>	REV <b>A02</b>
SHEET <b>55</b>		OF <b>65</b>	

22-Mar-2010



### POWER SWITCH DAUGHTER BOARD

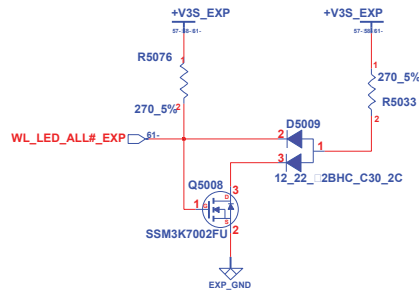
<http://laptop-motherboard-schematic.blogspot.com/>

<b>INVENTEC</b>			
TITLE <b>TIMEX</b>			
SIZE <b>A3</b>	CODE <b>CS</b>	DOC. NUMBER <b>I310A2326802</b>	REV <b>A02</b>
SHEET <b>56</b>		OF <b>65</b>	

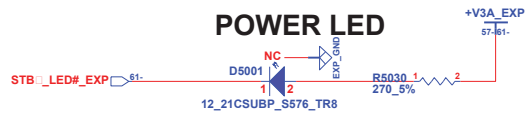
23-Mar-2010



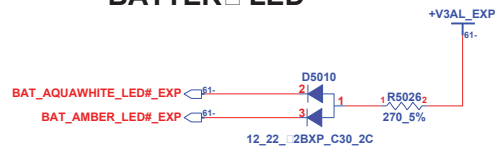
### WIRELESS LED



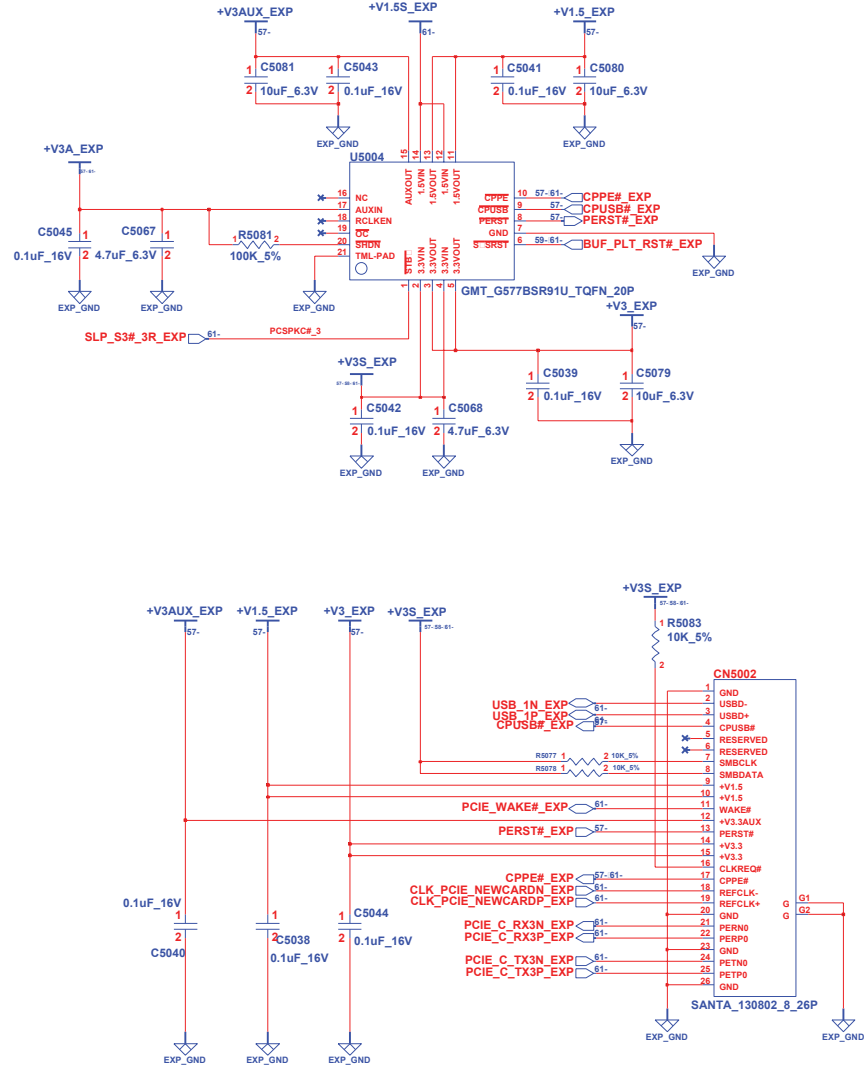
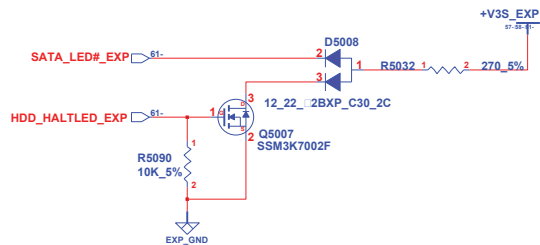
### POWER LED



### BATTERY LED



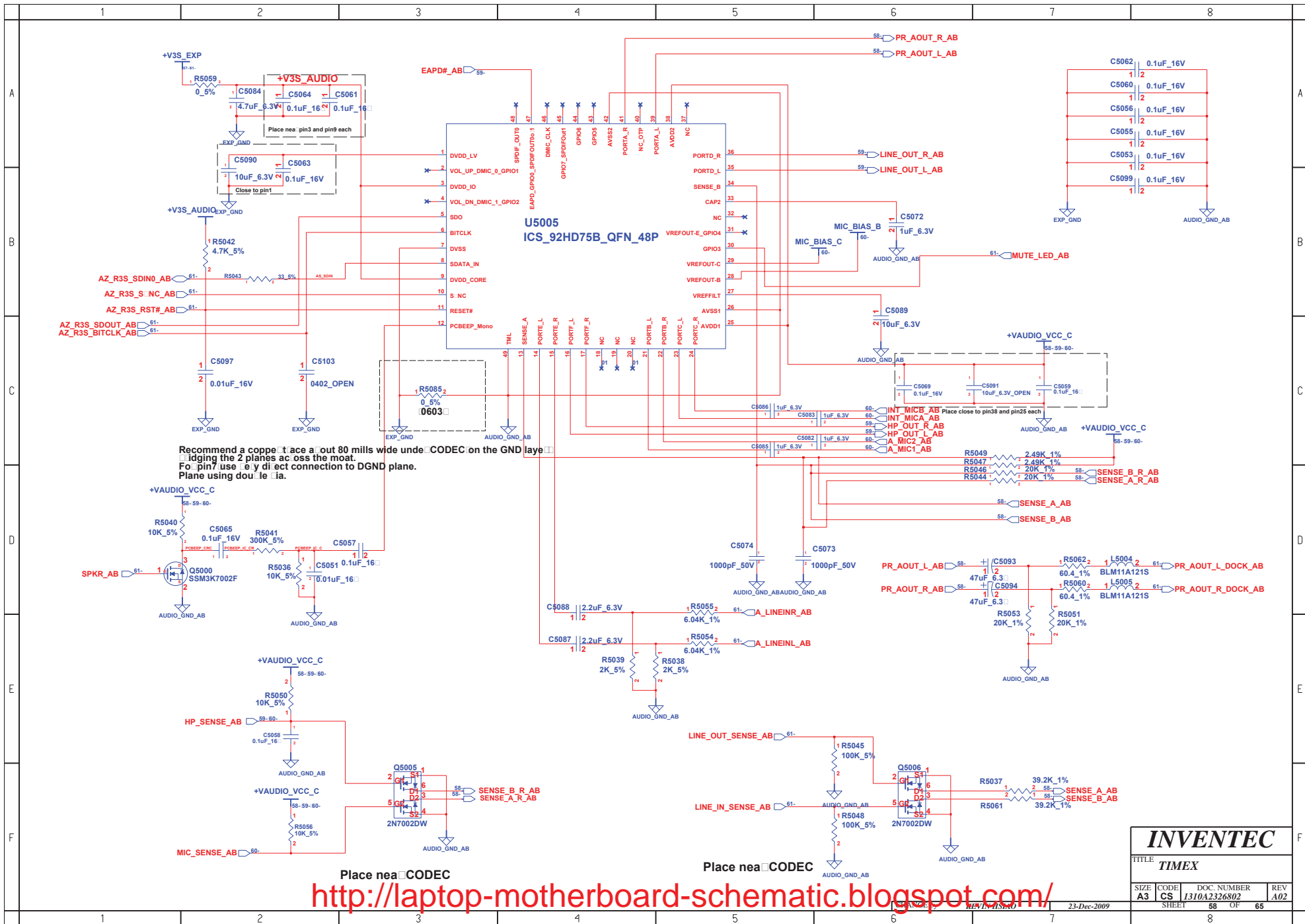
### SATA LED & HDD-HALTED LED



# EXPRESS CARD

<b>INVENTEC</b>			
TITLE <b>TIMEX</b>			
SIZE <b>A3</b>	CODE <b>CS</b>	DOC. NUMBER <b>I310A2326802</b>	REV <b>A02</b>
SHEET <b>57</b>		OF <b>65</b>	

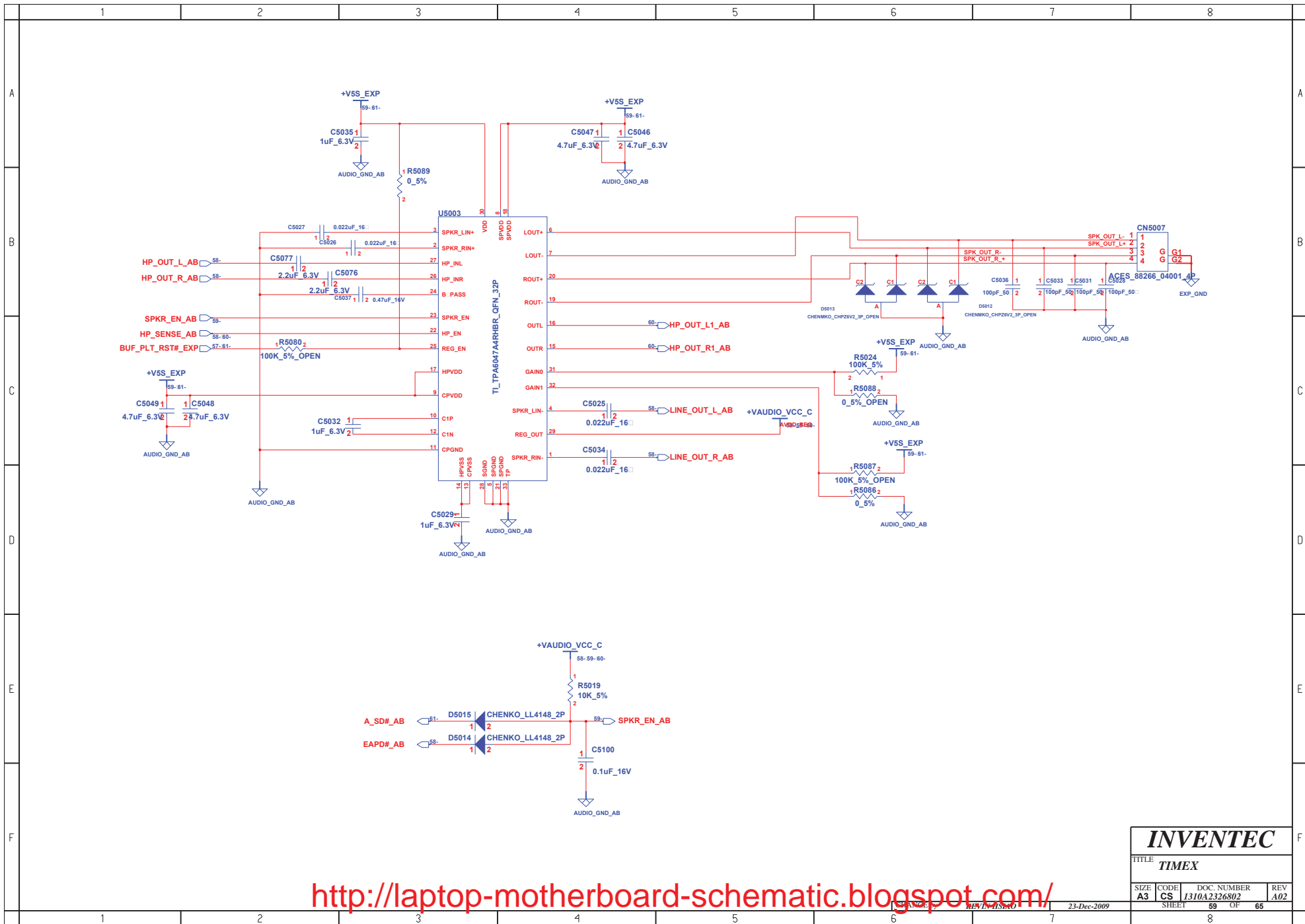
<http://laptop-motherboard-schematic.blogspot.com/>



Recommend a copper trace at least 80 mils wide under CODEC on the GND layer. Bridging the 2 planes across the board. For pin7 use a direct connection to DGND plane. Plane using double via.

Place near CODEC  
<http://laptop-motherboard-schematic.blogspot.com/>

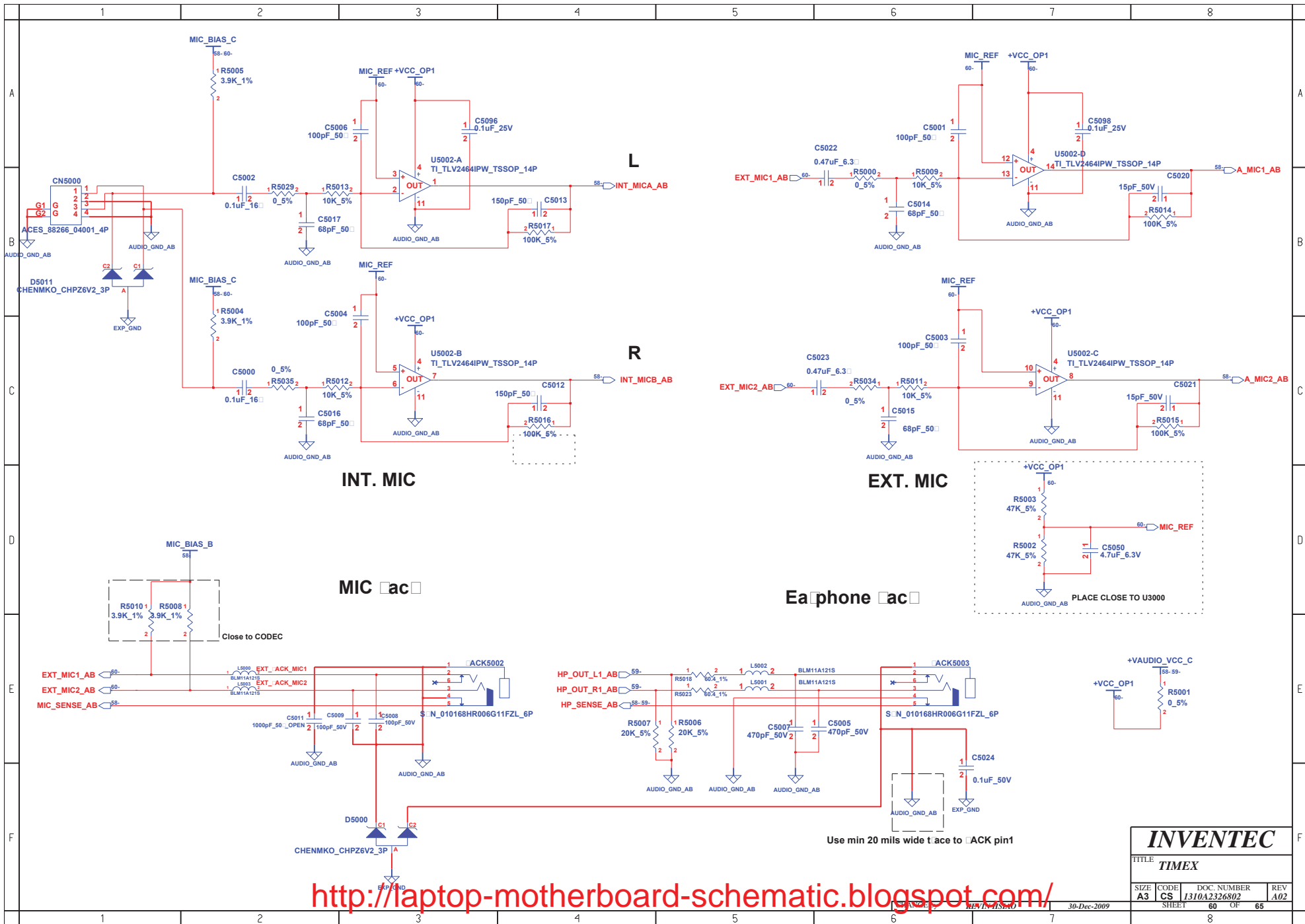
<b>INVENTEC</b>			
TITLE: <b>TIMEX</b>			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	I310A2326802	A02
SHEET		58	OF 65



<http://laptop-motherboard-schematic.blogspot.com/>

<b>INVENTEC</b>			
TITLE <b>TIMEX</b>			
SIZE <b>A3</b>	CODE <b>CS</b>	DOC. NUMBER <b>I310A2326802</b>	REV <b>A02</b>
SHEET <b>59</b>		OF <b>65</b>	

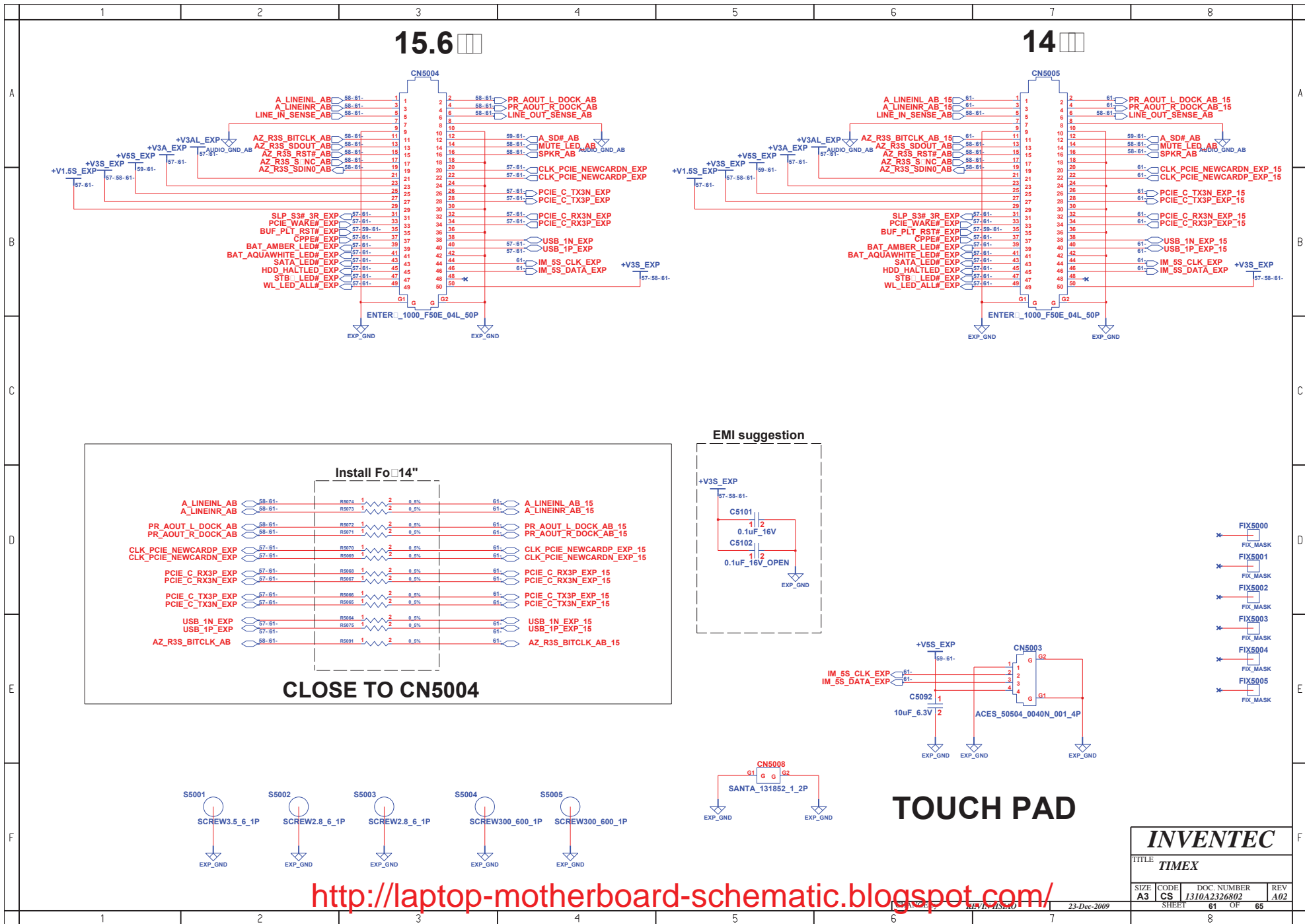
23-Dec-2009



<http://laptop-motherboard-schematic.blogspot.com/>

<b>INVENTEC</b>			
TITLE <b>TIMEX</b>			
SIZE <b>A3</b>	CODE <b>CS</b>	DOC. NUMBER <b>I310A2326802</b>	REV <b>A02</b>
SHEET <b>60</b>		OF <b>65</b>	

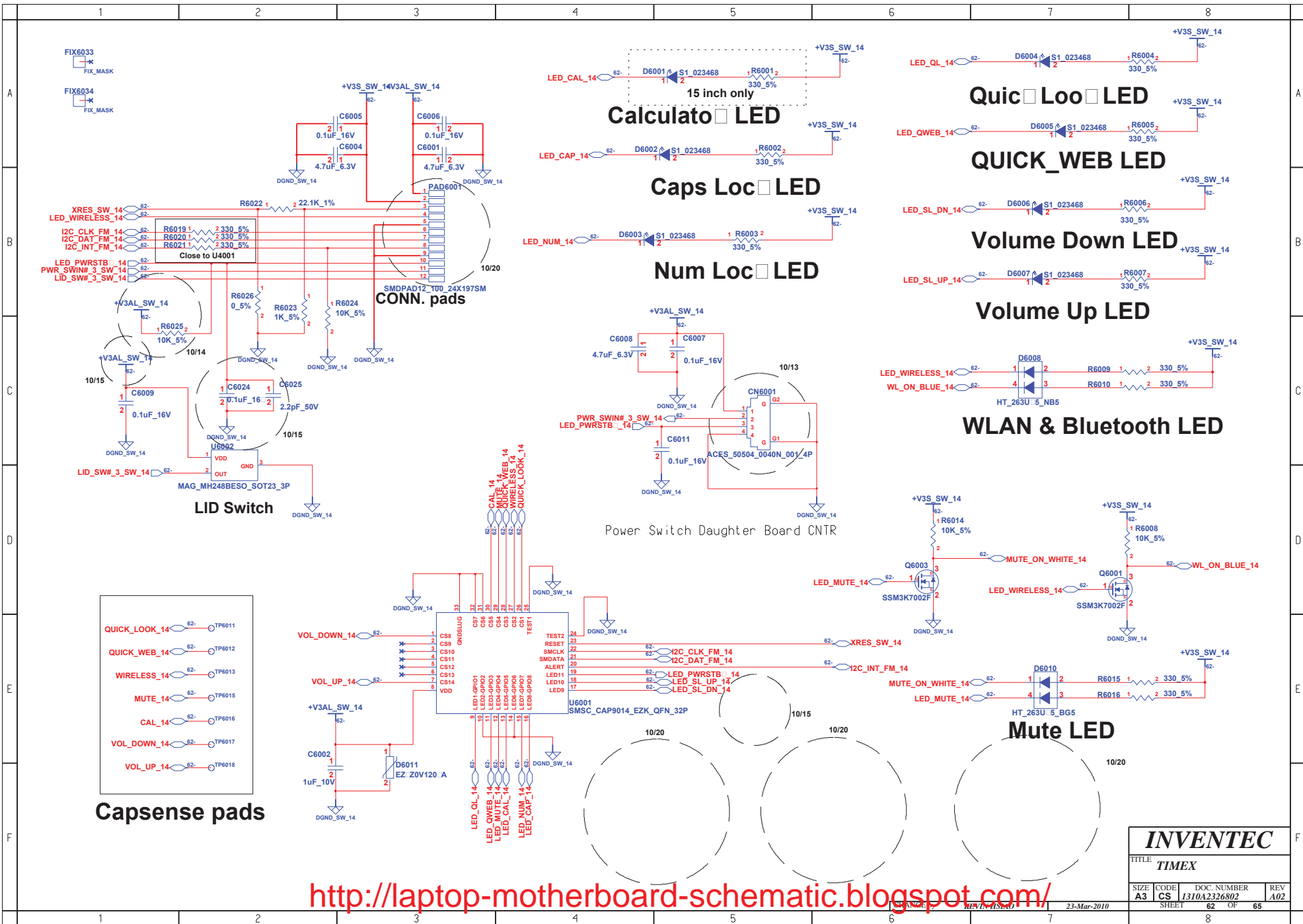
30-Dec-2009



<http://laptop-motherboard-schematic.blogspot.com/>

<b>INVENTEC</b>			
TITLE: <b>TIMEX</b>			
SIZE: <b>A3</b>	CODE: <b>CS</b>	DOC. NUMBER: <b>1310A2326802</b>	REV: <b>A02</b>
SHEET: <b>61</b>		OF: <b>65</b>	

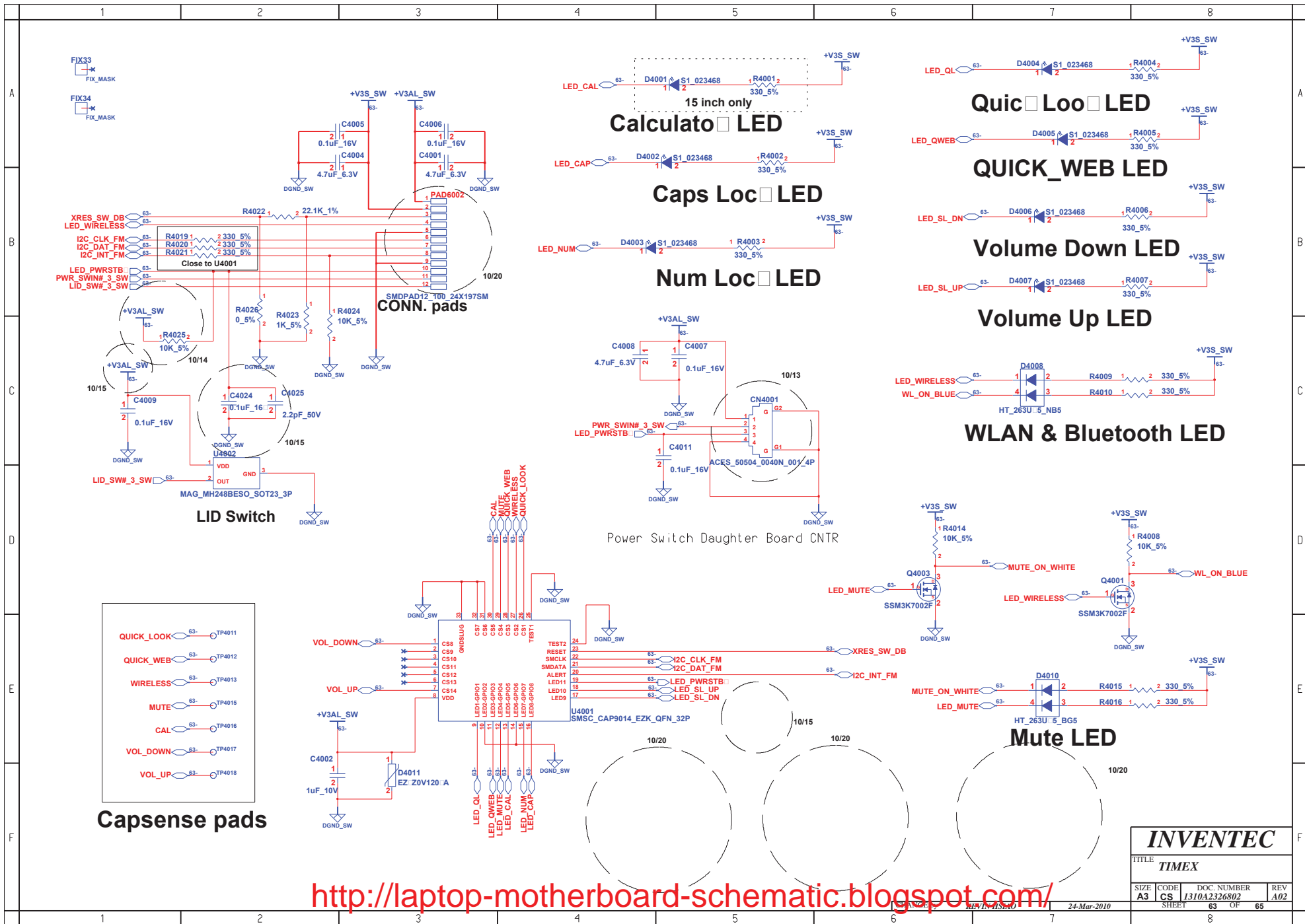
23-Dec-2009



<http://laptop-motherboard-schematic.blogspot.com/>

<b>INVENTEC</b>			
TITLE <b>TIMEX</b>			
SIZE <b>A3</b>	CODE <b>CS</b>	DOC. NUMBER <b>I310A2326802</b>	REV <b>A02</b>
SHEET <b>62</b>		OF <b>65</b>	

23-Mar-2010



<http://laptop-motherboard-schematic.blogspot.com/>

<b>INVENTEC</b>			
TITLE <b>TIMEX</b>			
SIZE <b>A3</b>	CODE <b>CS</b>	DOC. NUMBER <b>I310A2326802</b>	REV <b>A02</b>
SHEET <b>63</b>		OF <b>65</b>	

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<http://laptop-motherboard-schematic.blogspot.com/>

**INVENTEC**

TITLE  
**TIMEX**

SIZE <b>A3</b>	CODE <b>CS</b>	DOC. NUMBER <b>1310A2326802</b>	REV <b>A02</b>
SHEET		<b>64</b>	OF <b>65</b>

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<http://laptop-motherboard-schematic.blogspot.com/>

**INVENTEC**

TITLE  
**TIMEX**

SIZE <b>A3</b>	CODE <b>CS</b>	DOC. NUMBER <b>1310A2326802</b>	REV <b>A02</b>
SHEET		<b>65</b>	OF <b>65</b>

REVISED BY  
**REVISED BY**

23-Dec-2009

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