

INDICATOR BOARD CON 24

EXTERNAL CLOCK GEN. RTM865T-300 2

INTEL YONAH CPU Celeron M uFCPGA 479 Pin 4, 5

PCI BUS	GNT#/REQ#	INTR	IDSEL
MINI PCI	0	C/D	AD25

4X DATA
2X ADDRESS

AGTL+
533MHz

ATI NB - RC415MD
AGTL+ YONAH CPU I/F
SINGLE DDR2 CHANNEL
INTEGRATED GRAPHICS M22
TVOUT
1 X2 PCIE I/F FOR SB
4 X1 PCIE I/F
7, 8, 9, 10, 11

VGA CON 15

LVDS CON 15

UNBUFFERED DDR2 DIMM0 12

UNBUFFERED DDR2 DIMM1 13

NEW CARD CON
PCIE0
USB6
26

LAN Marvell 8038 23

ATI SB - SB600/SB460
USB2.0---8
SATA---4
HD AUDIO 1.0
ATA 66/100/133
ACPI 1.1
LPC I/F
INTERNAL RTC
PCI/PCI BRIDGE
16, 17, 18, 19, 20

CARD READER USB4 25

USB3 21

USB2 21

USB1 21

AZALIA CODEC 28, 29

MDC CON 27

HDD 22

ODD 22

MINI PCI 27

CHARGER 32

VCORE 36

3.3VSTBY / 5VS / 1.8VSTBY 33

1.5VS / 1.05VS / 1.2VS-NB 35

1.8V / 0.9V / 1.2VS 34

3V / 3VS / 1.8VS / 3VLAN / POWERGOOD 32

KBC IT8510G 30

BIOS SST39VF080-70-4C 30

TOUCH PAD 24

SCAN KB 24

FAN 3

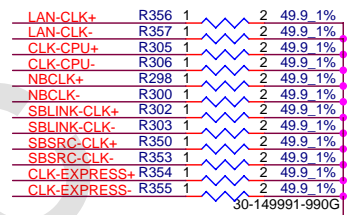
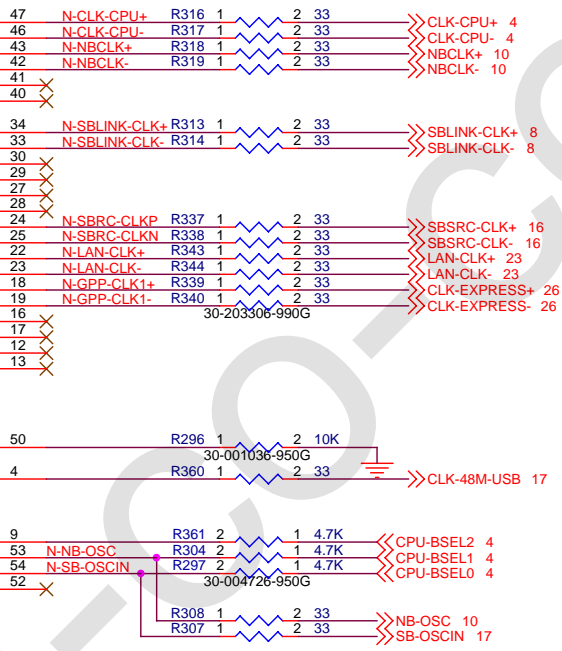
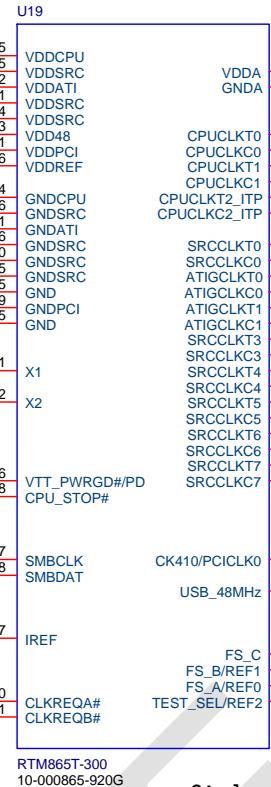
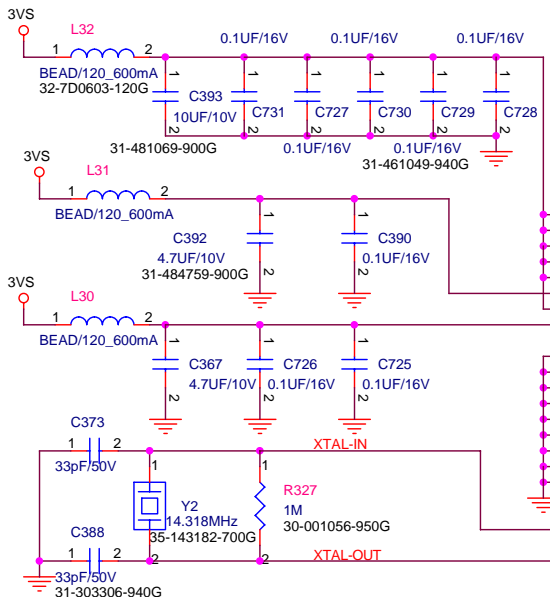
YONAH Celeron M Processor + RC415MD + SB460

Arima ARIMA COMPUTER CORP.

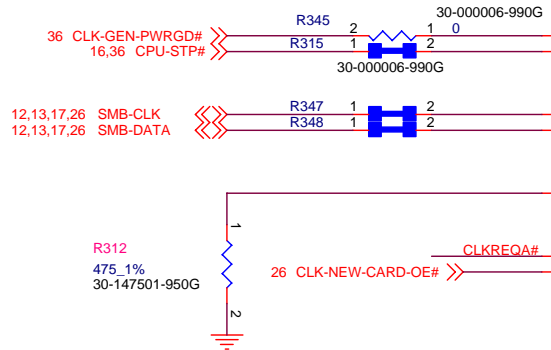
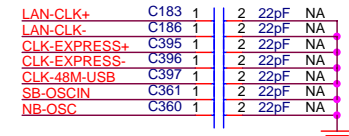
Project Name: **W340UI** Title: **BLOCK**

Size: Custom Document Number: **40GAB1230-B100** Rev: **B**

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EMI



2'nd source ICS951413 10-395141-920G

FSC	FSB	FSA	CPU	SRC	PCI	REF
1	0	1	100	100	33	14.31
0	0	1	133	100	33	14.31
0	1	1	166	100	33	14.31
0	1	0	200	100	33	14.31
0	0	0	266	100	33	14.31
1	0	0	333	100	33	14.31
1	1	0	400	100	33	14.31
1	1	1	RESV.	100	33	14.31

EXT CLK Frequency Select Table

LAYOUT NOTE:

1. Place Series termination resistor close to CLK GEN.
2. Route CPUCLK+/-, NBCLK+/-, ITPCLK+/-, SCR+/- as different pair rule.
3. Put decoupling CAPS close to CLK GEN power pin.

02_CLOCK GENERATOR

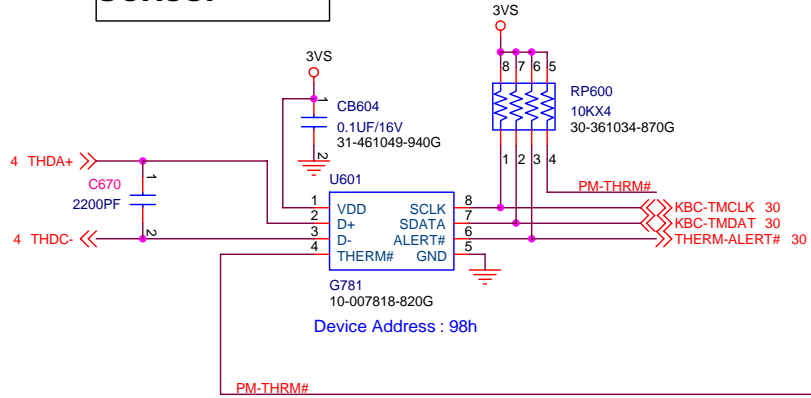
Arima ARIMA COMPUTER CORP.

Project Name : **W340UI** Title : **CLK GEN-ICS951461**

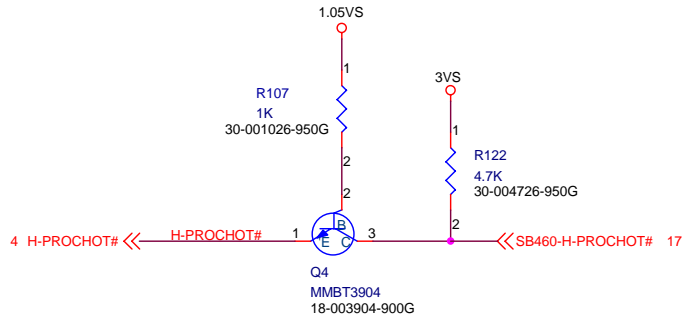
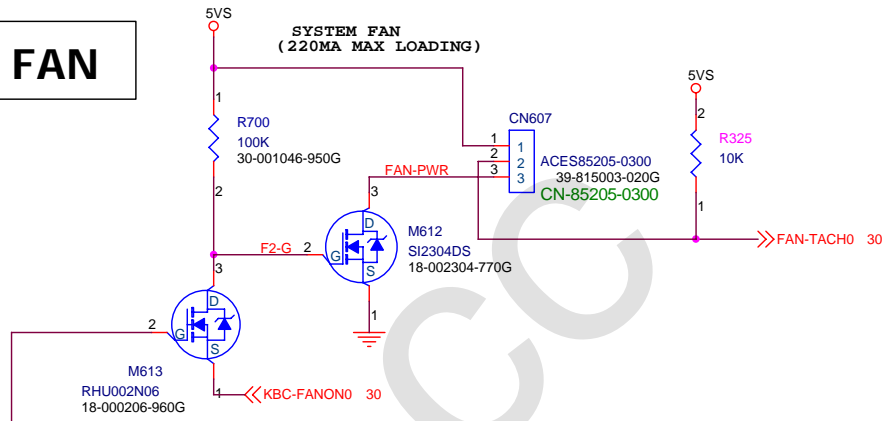
Size : Document Number : **40GAB1230-B100** Rev : **B**

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CPU Thermal Sensor

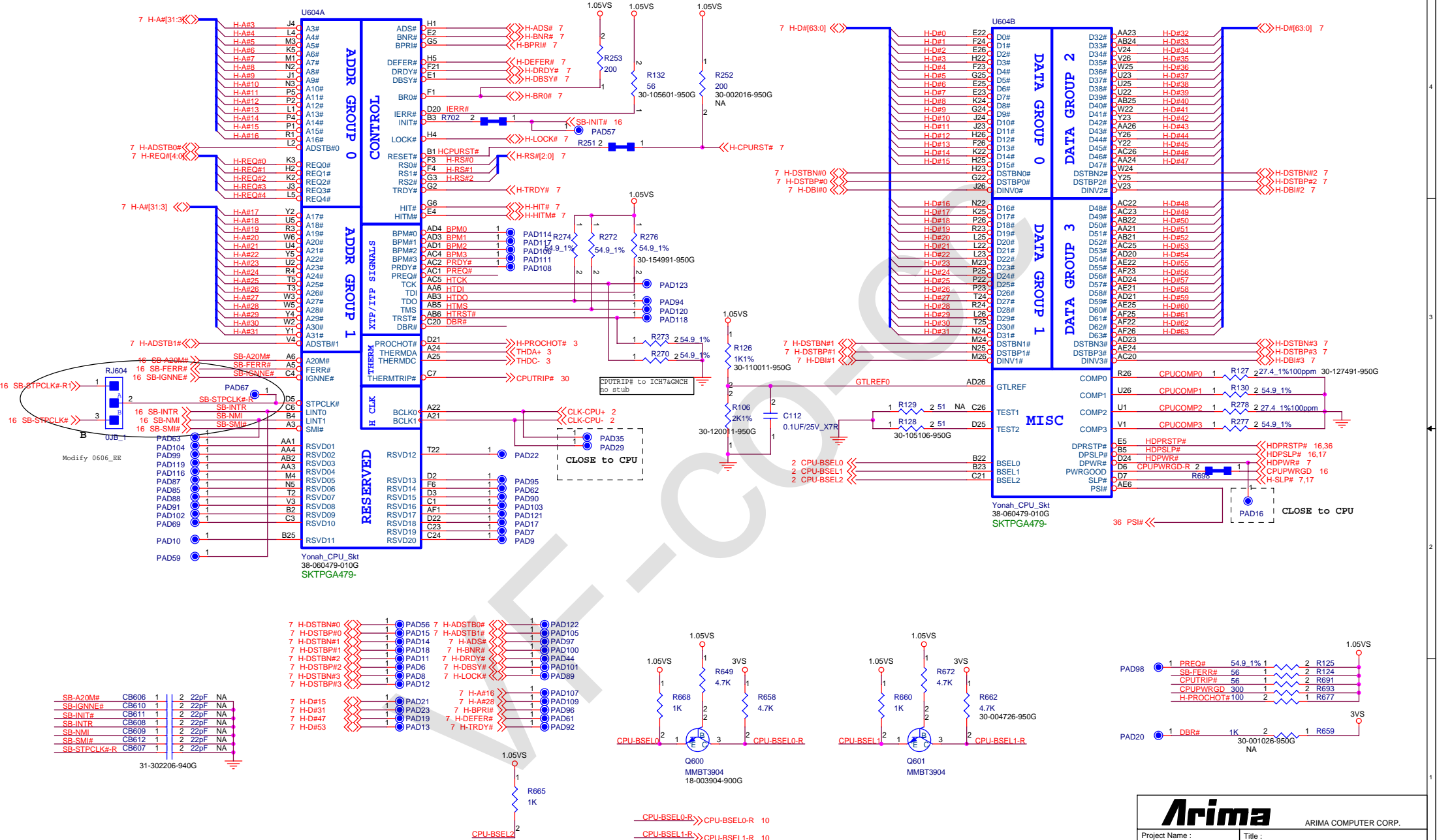


FAN



03_CPU THERMAL SENSIOR_FAN

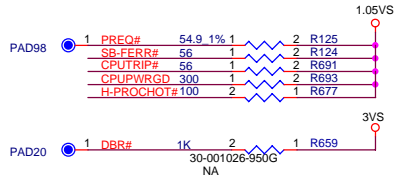
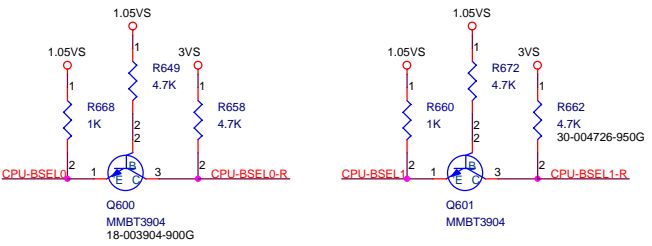
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Project Name : W340UI		Title : CPU THERM SENSOR/FAN	
Size :	Document Number :	40GAB1230-B100	Rev : B
Date : Wednesday, June 21, 2006		Sheet : 3 of 39	



04_YONAH CPU SOCKET_AGTL+_MISC

SB-A20M#	CB606	1	2	22pF	NA
SB-IGNNE#	CB610	1	2	22pF	NA
SB-INIT#	CB611	1	2	22pF	NA
SB-INTR	CB608	1	2	22pF	NA
SB-NMI	CB609	1	2	22pF	NA
SB-SMI#	CB612	1	2	22pF	NA
SB-STPCLK#-R	CB607	1	2	22pF	NA

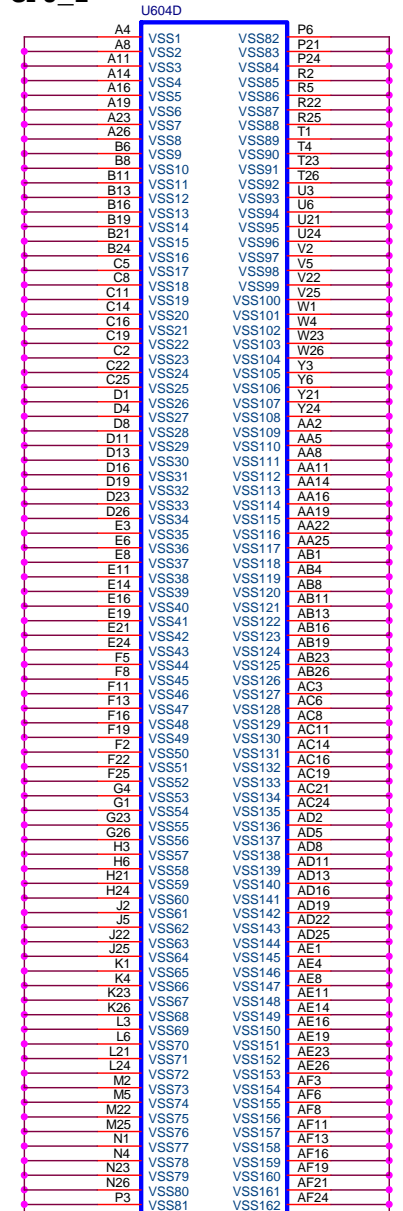
7 H-DSTBN#0	1	PAD56	7 H-ADSTB#0	1	PAD122
7 H-DSTBN#1	1	PAD15	7 H-ADSTB#1	1	PAD105
7 H-DSTBN#2	1	PAD18	7 H-ADS#	1	PAD97
7 H-DSTBN#3	1	PAD11	7 H-BNR#	1	PAD100
7 H-DSTBP#0	1	PAD6	7 H-DEFER#	1	PAD44
7 H-DSTBP#1	1	PAD8	7 H-DRDY#	1	PAD101
7 H-DSTBP#2	1	PAD12	7 H-DBSY#	1	PAD101
7 H-DSTBP#3	1	PAD12	7 H-LOCK#	1	PAD89
7 H-D#15	1	PAD21	7 H-A#16	1	PAD107
7 H-D#31	1	PAD23	7 H-A#28	1	PAD109
7 H-D#47	1	PAD19	7 H-BPRI#	1	PAD96
7 H-D#53	1	PAD13	7 H-DEFER#	1	PAD61
			7 H-TRDY#	1	PAD92



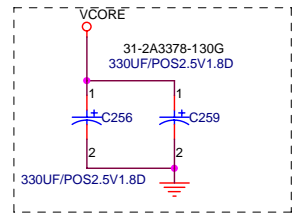
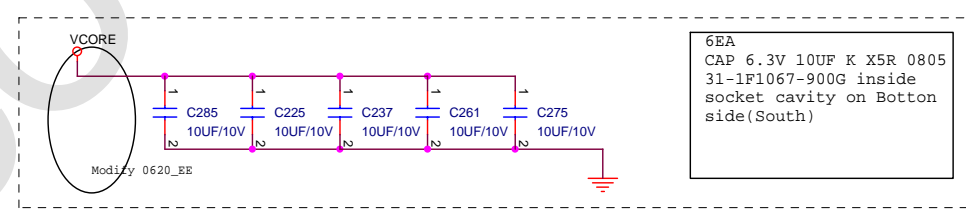
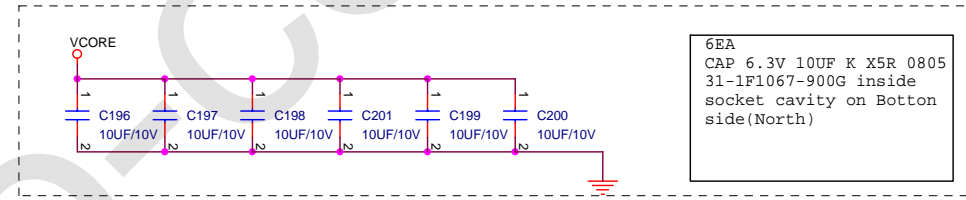
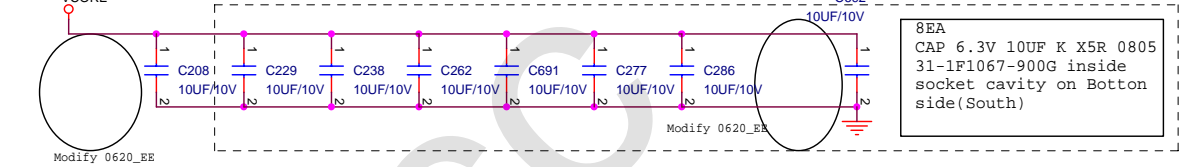
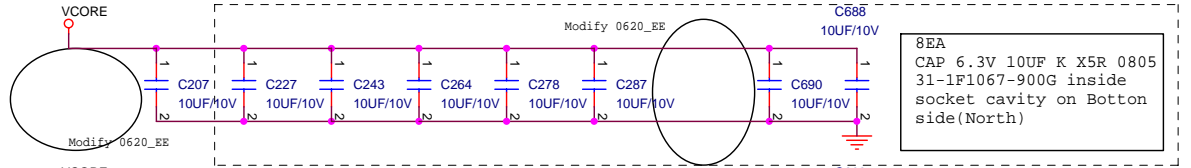
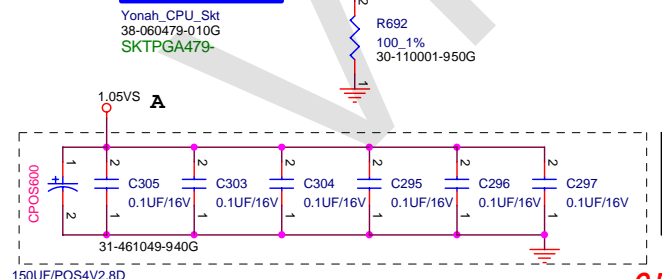
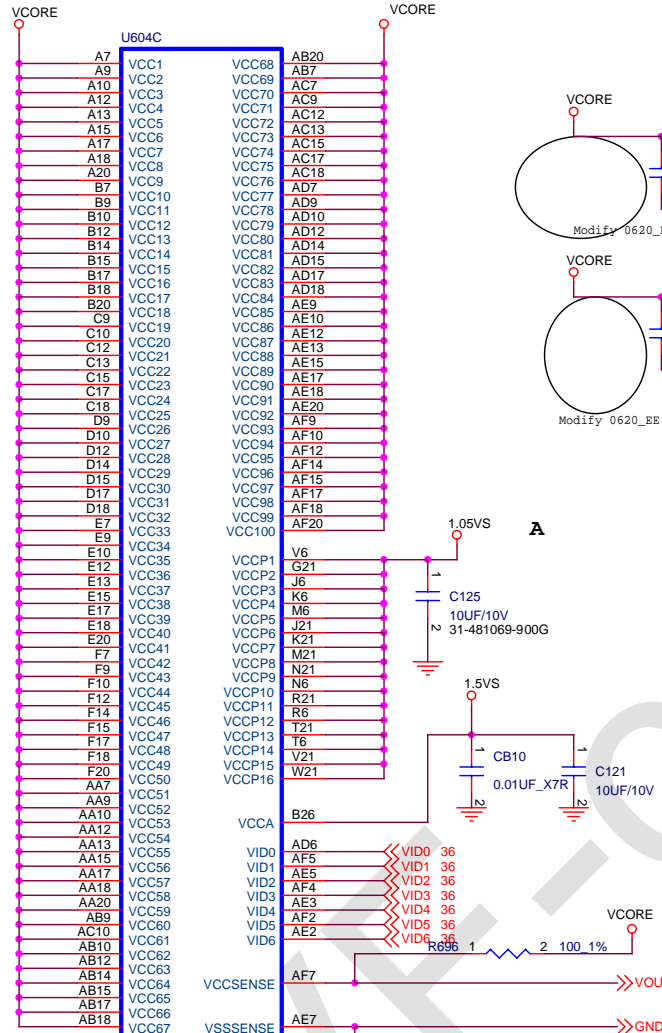
Arima		ARIMA COMPUTER CORP.	
Project Name :	W340UI	Title :	YONAH CPU SOCKET[1/2]
Size :	Document Number :	40GAB1230-B100	Rev : B
Custom :	Date : Wednesday, June 21, 2006	Sheet :	4 of 39

VF-CO-CC

CPU_2



Yonah_CPU_Skt
38-060479-010G
SKTPGA479-



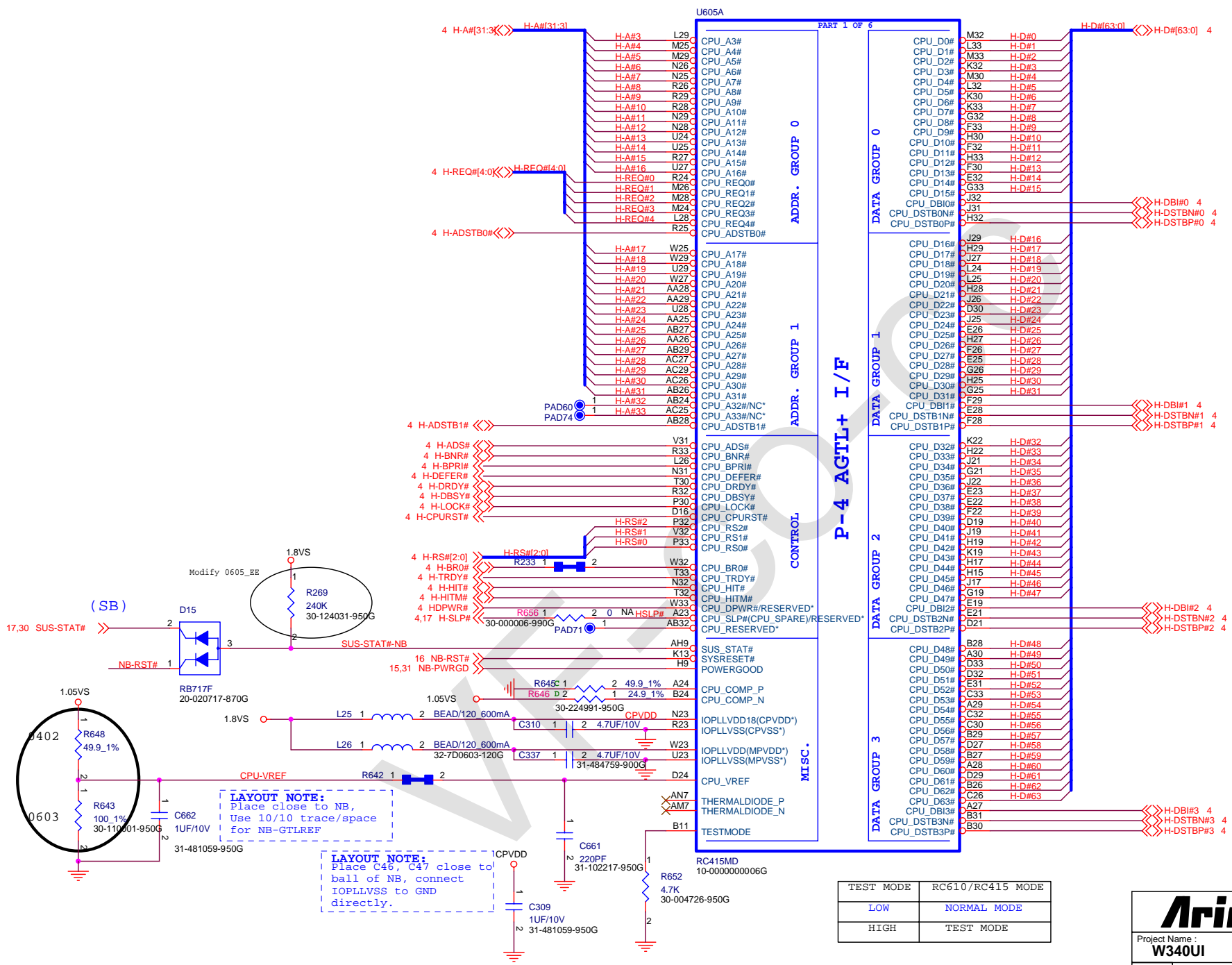
Place these inside socket cavity on L8 (North side Secondary)

05_YONAH_CPU_SOCKET_PWR_GND

Arima		ARIMA COMPUTER CORP.	
Project Name: W340UI		Title: YONAH CPU SOCKET[2/2]	
Size: B	Document Number: 40GAB1230-B100	Rev: B	
Date: Wednesday, June 21, 2006		Sheet: 5 of 39	

VF-CO-CC

Arima		ARIMA COMPUTER CORP.			
Project Name:	W340UI	Title:	BLANK		
Size:	C	Document Number:	40GAB1230-B100	Rev:	B
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07_RC415MD_AGTL+

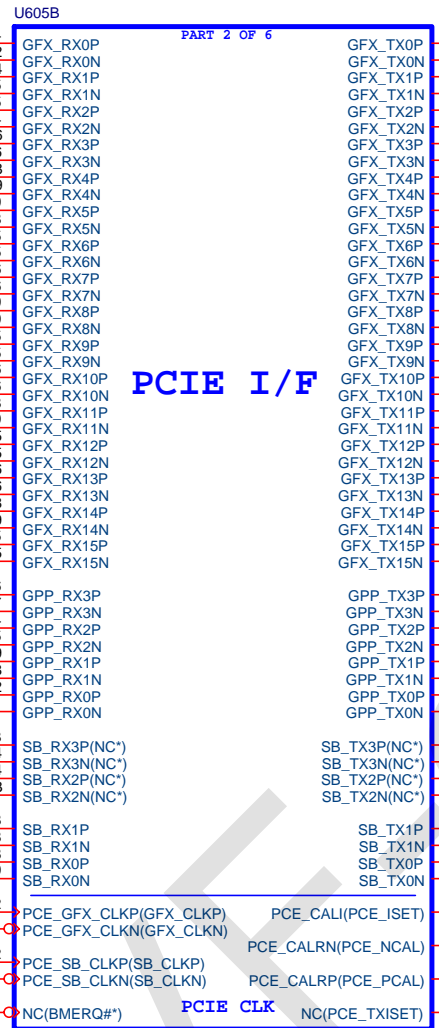
TEST MODE	RC610/RC415 MODE
LOW	NORMAL MODE
HIGH	TEST MODE

Arima ARIMA COMPUTER CORP.

Project Name : **W340UI** Title : **RC415ME(1/5)_AGTL+**

Size : Document Number : **40GAB1230-B100** Rev : **B**

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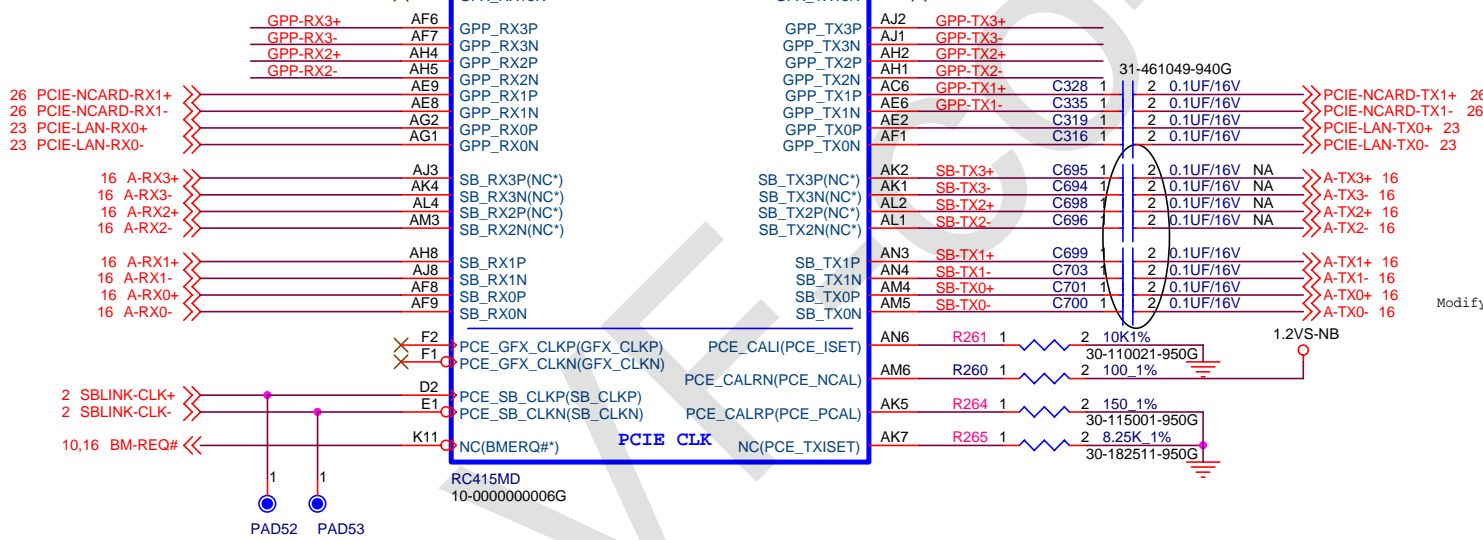


PCIE I/F

LAYOUT NOTE:
Close to NB (<0.5 Inch)

PAD68	1	GPP-RX3+
PAD65	1	GPP-RX3-
PAD79	1	GPP-RX2+
PAD78	1	GPP-RX2-
PAD72	1	A-RX0+
PAD73	1	A-RX0-
PAD81	1	A-RX1+
PAD83	1	A-RX1-
PAD607	1	A-RX2+
PAD606	1	A-RX2-
PAD82	1	A-RX3+
PAD75	1	A-RX3-
PAD601	1	GPP-TX3+
PAD600	1	GPP-TX3-
PAD77	1	GPP-TX2+
PAD78	1	GPP-TX2-
PAD604	1	SB-TX3+
PAD602	1	SB-TX3-
PAD608	1	SB-TX2+
PAD605	1	SB-TX2-
PAD609	1	SB-TX1+
PAD611	1	SB-TX1-
PAD610	1	SB-TX0+
PAD612	1	SB-TX0-

PCIE0	LAN
PCIE1	NEW CARD

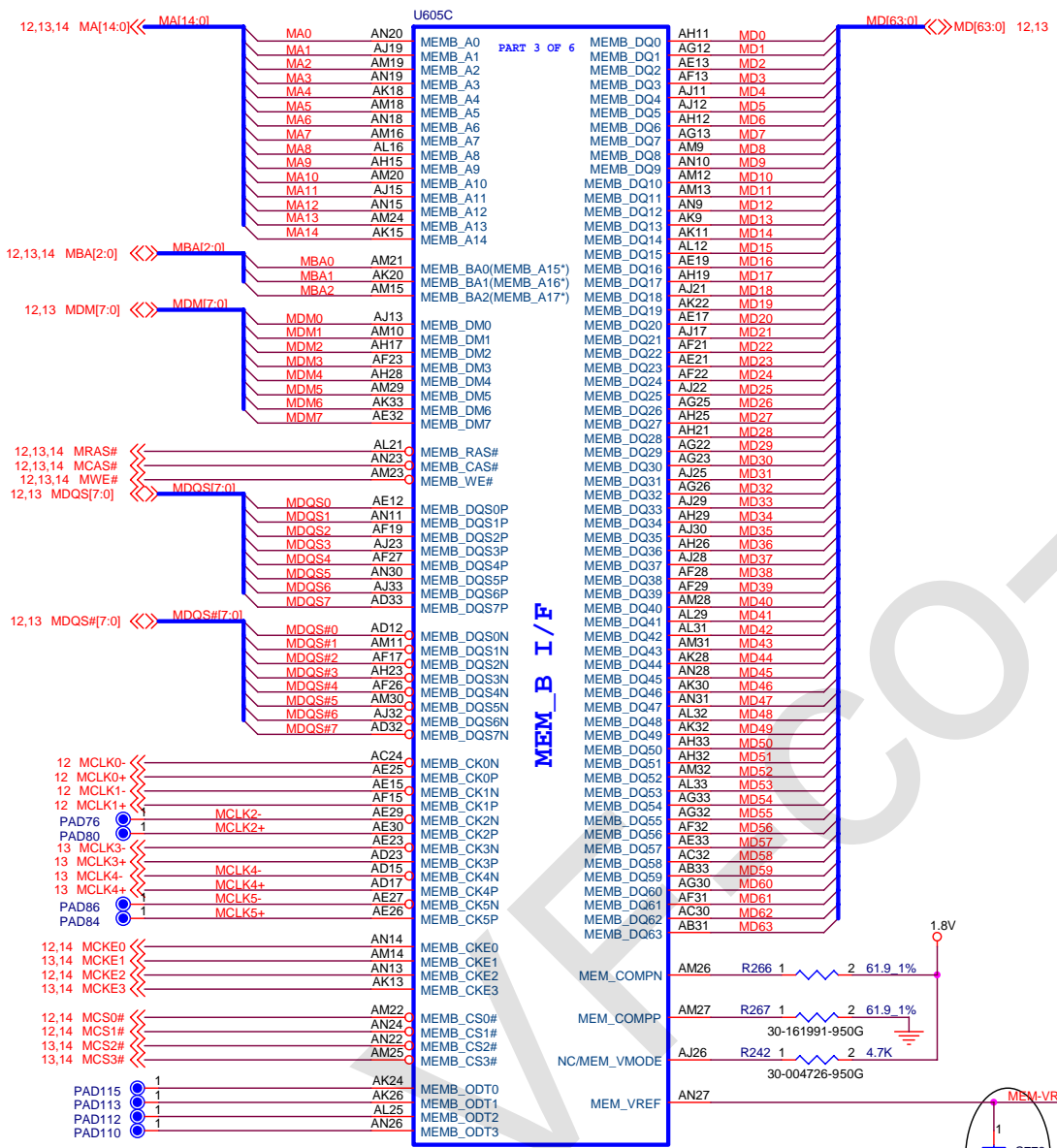


Modify 0608_EE

08_RC415MD_PCIE

Arima ARIMA COMPUTER CORP.

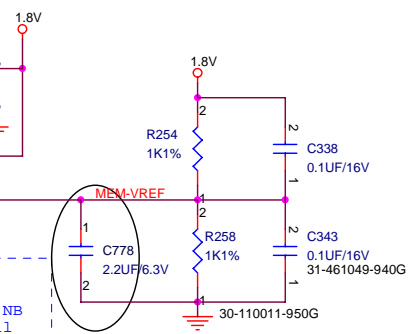
Project Name : W340UI		Title : CALISTOGA-PM (2/5)	
Size : Custom	Document Number : 40GAB1230-B100	Rev : B	
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MEM_B I/F

RC415MD
10-0000000006G

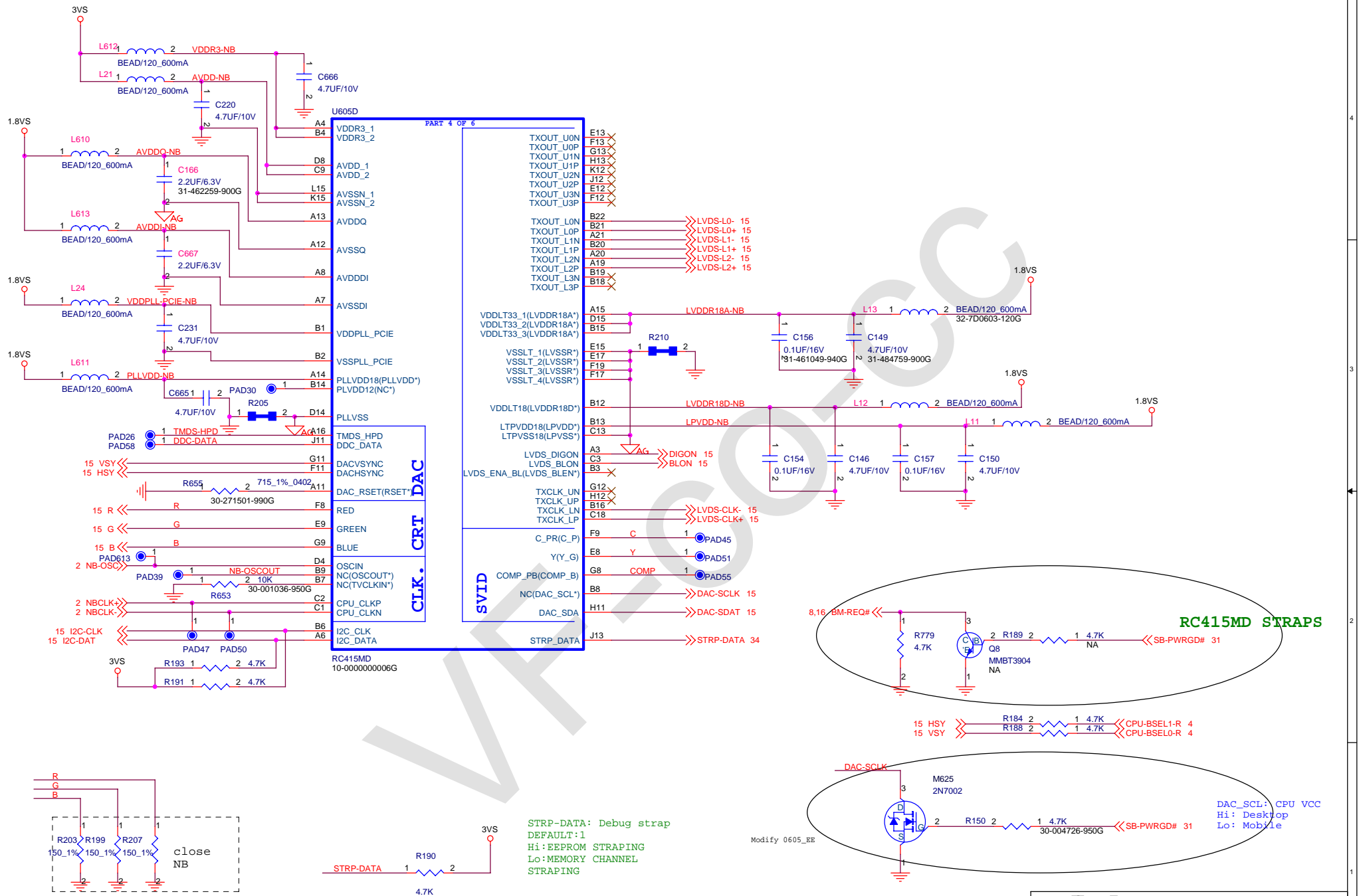
LAYOUT NOTE:
Resistors divider close to NB and MEM-VREF should be 20mil



5/10 Add for ATI request.

09_RC415MD_MEMORY

Arima		ARIMA COMPUTER CORP.			
Project Name :	W340UI		Title :	RC415ME(3/5)_MEMORY	
Size :	Document Number :	40GAB1230-B100		Rev :	B
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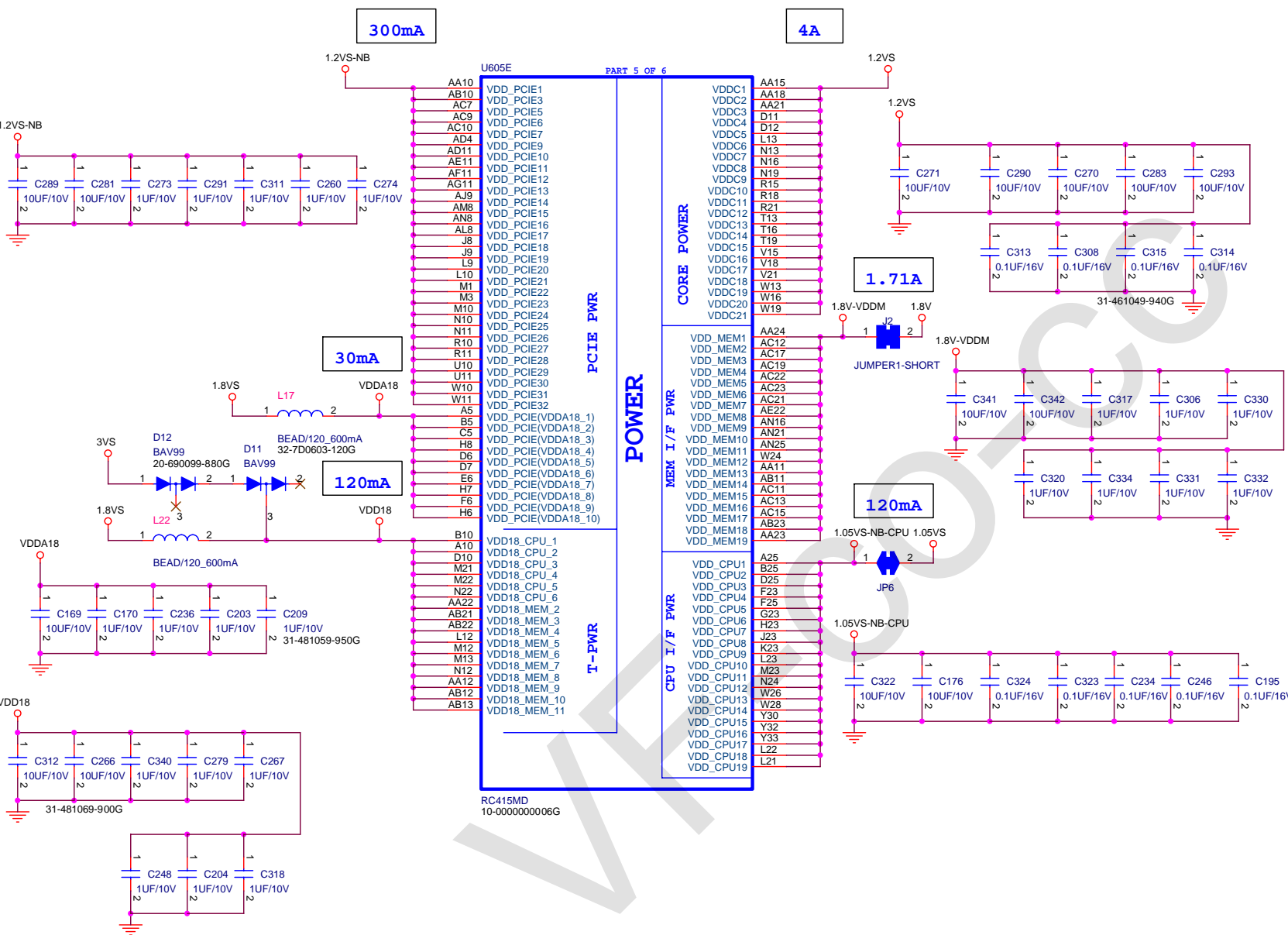


10_RC415MD_VGA_TV_LVDS

STRP-DATA: Debug strap
 DEFAULT: 1
 Hi: EEPROM STRAPING
 Lo: MEMORY CHANNEL STRAPING

Modify 0605_EE

Arima		ARIMA COMPUTER CORP.	
Project Name:	W340UI	Title:	RC415ME(3/5)_VGA_TV_LVDS
Size:	Document Number:	40GAB1230-B100	Rev: B
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PART 6 OF 6

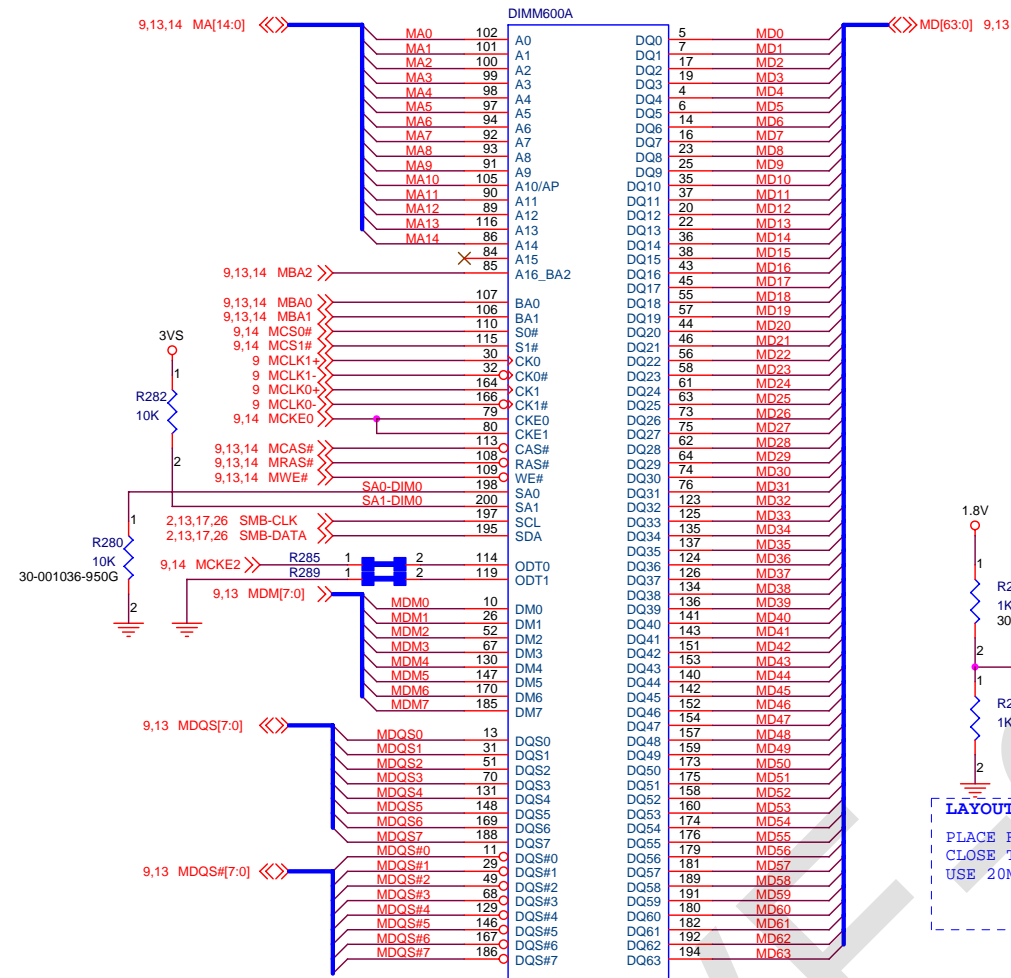
A2	VSS1	VSS75	N15
A3	VSS2	VSS76	N16
A18	VSS3	VSS77	N27
A22	VSS4	VSS78	N33
A26	VSS5	VSS79	R13
A31	VSS6	VSS80	R16
A32	VSS7	VSS81	R19
AA13	VSS8	VSS82	R30
AA16	VSS9	VSS83	V16
AA19	VSS10	VSS84	T15
AA27	VSS11	VSS85	T18
AA30	VSS12	VSS86	T21
AA32	VSS13	VSS87	U26
AA33	VSS14	VSS88	V13
AB25	VSS15	VSS89	V19
AB28	VSS16	VSS90	V33
AC28	VSS17	VSS91	W15
AC33	VSS18	VSS92	W18
AD19	VSS19	VSS93	W21
AD21	VSS20	VSS94	W30
AD22	VSS21	VSS95	AA1
AD30	VSS22	VSS_P01E1	AA3
AE28	VSS23	VSS_P01E2	AA7
AF12	VSS24	VSS_P01E3	AA7
AF25	VSS25	VSS_P01E4	AA9
AF33	VSS26	VSS_P01E5	AA9
AG15	VSS27	VSS_P01E6	AB4
AG17	VSS28	VSS_P01E7	AC4
AG19	VSS29	VSS_P01E8	AC8
AG21	VSS30	VSS_P01E9	AE1
AK27	VSS31	VSS_P01E10	AE3
AH13	VSS32	VSS_P01E11	AE7
AH22	VSS33	VSS_P01E12	AF2
AH30	VSS34	VSS_P01E13	AF4
AK10	VSS35	VSS_P01E14	AF5
AK14	VSS36	VSS_P01E15	AG4
AK19	VSS37	VSS_P01E16	AG8
AK23	VSS38	VSS_P01E17	AG9
AK31	VSS39	VSS_P01E18	AJ6
AM33	VSS40	VSS_P01E19	AJ6
AN12	VSS41	VSS_P01E20	AH6
AN29	VSS42	VSS_P01E21	AK6
AN32	VSS43	VSS_P01E22	AL3
B23	VSS44	VSS_P01E23	AM1
B32	VSS45	VSS_P01E24	AM2
B33	VSS46	VSS_P01E25	AN2
C22	VSS47	VSS_P01E26	AN5
C31	VSS48	VSS_P01E27	D1
D20	VSS49	VSS_P01E28	H1
D23	VSS50	VSS_P01E29	H3
D28	VSS51	VSS_P01E30	J2
E29	VSS52	VSS_P01E31	K4
E33	VSS53	VSS_P01E32	L8
E11	VSS54	VSS_P01E33	L7
F15	VSS55	VSS_P01E34	L5
F21	VSS56	VSS_P01E35	L4
G15	VSS57	VSS_P01E36	M5
G22	VSS58	VSS_P01E37	M7
G30	VSS59	VSS_P01E38	N2
G33	VSS60	VSS_P01E39	N4
H21	VSS61	VSS_P01E40	R8
H26	VSS62	VSS_P01E41	P4
J15	VSS63	VSS_P01E42	R4
J28	VSS64	VSS_P01E43	T1
J33	VSS65	VSS_P01E44	T1
K17	VSS66	VSS_P01E45	T3
K21	VSS67	VSS_P01E46	U7
L11	VSS68	VSS_P01E47	U8
L17	VSS69	VSS_P01E48	V2
L19	VSS70	VSS_P01E49	V4
L27	VSS71	VSS_P01E50	W4
L30	VSS72	VSS_P01E51	W7
M11	VSS73	VSS_P01E52	Y4
M27	VSS74	VSS_P01E53	Y4
		VSS_P01E54	AA8
		VSS_P01E55	N7

RC415MD
10-000000006G

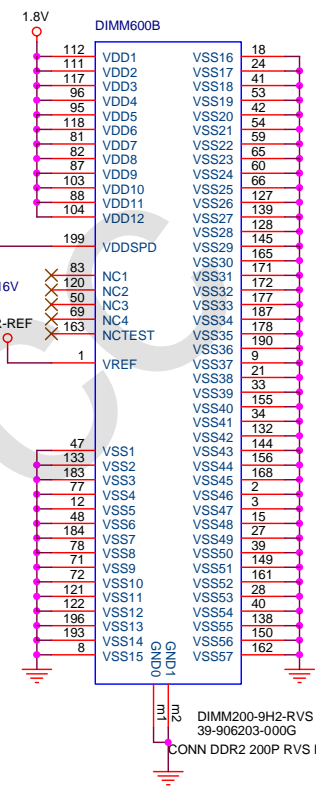
11_RC415MD_PWR_GND

Arima ARIMA COMPUTER CORP.

Project Name : W340UI	Title : RC415ME(5/5)_PWR&GND
Size : B	Document Number : 40GAB1230-B100
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LAYOUT NOTE:
 PLACE RESISTOR DIVIDER
 CLOSE TO MEMORY SLOTS.
 USE 20MILS FOR DDR-REF



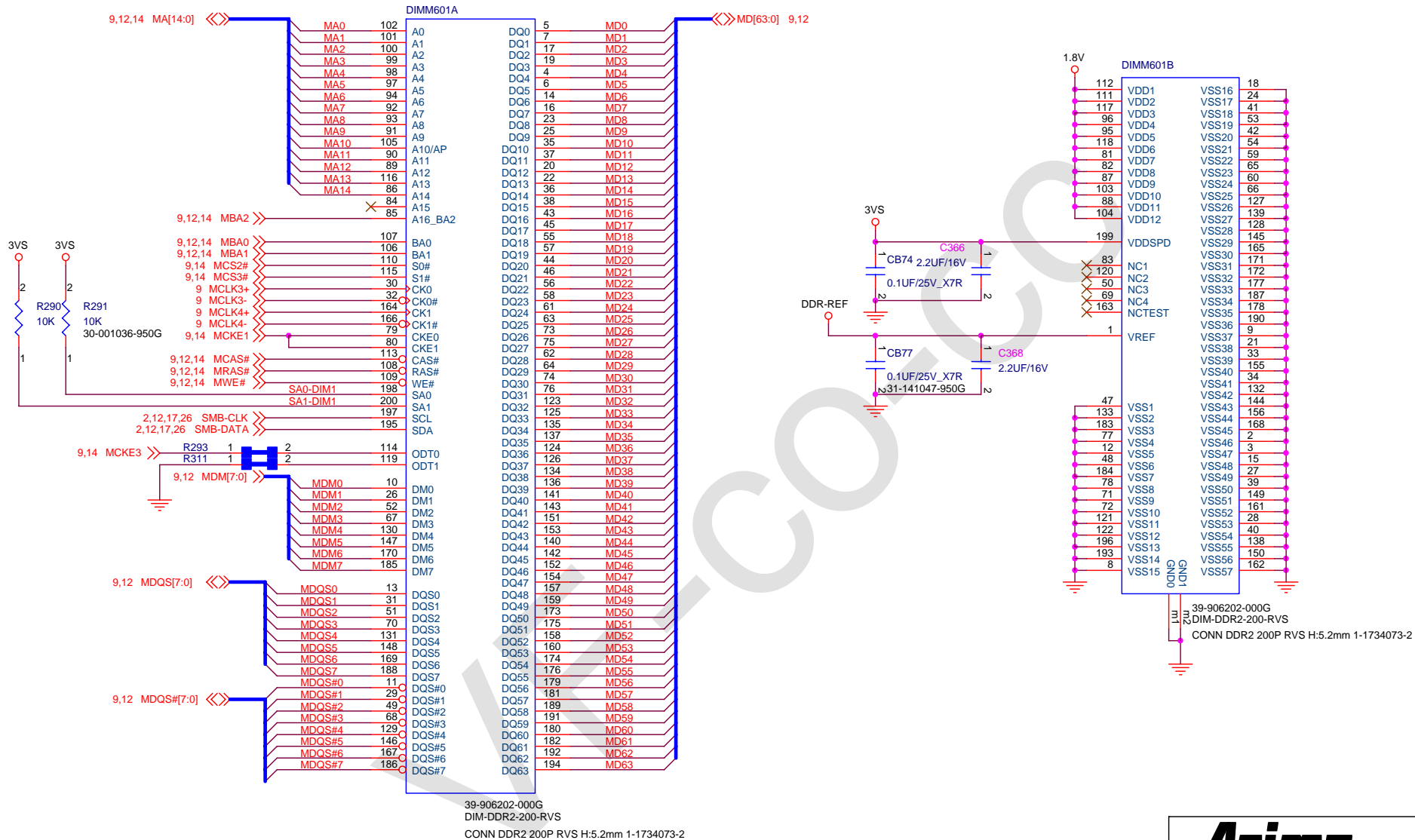
DIMM200-9H2-RVS
 39-906203-000G
 CONN DDR2 200P RVS H:9.2mm 1473150-4

DIMM200-9H2-RVS
 39-906203-000G
 CONN DDR2 200P RVS H:9.2mm 1473150-4

SO-DIMM1 IS PLACED
 FARTHER FROM THE
 SO-DIMM0

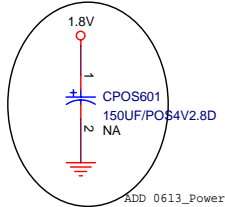
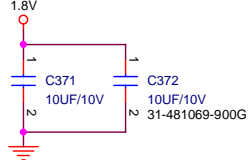
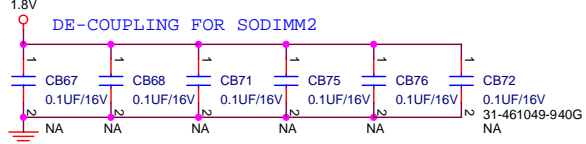
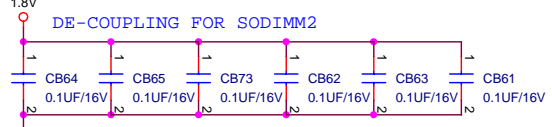
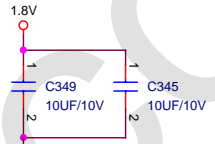
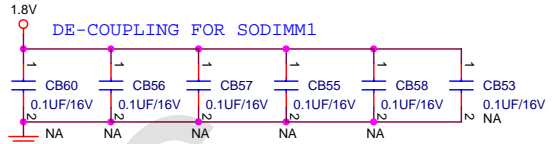
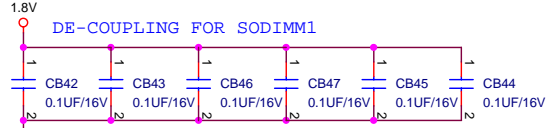
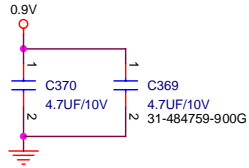
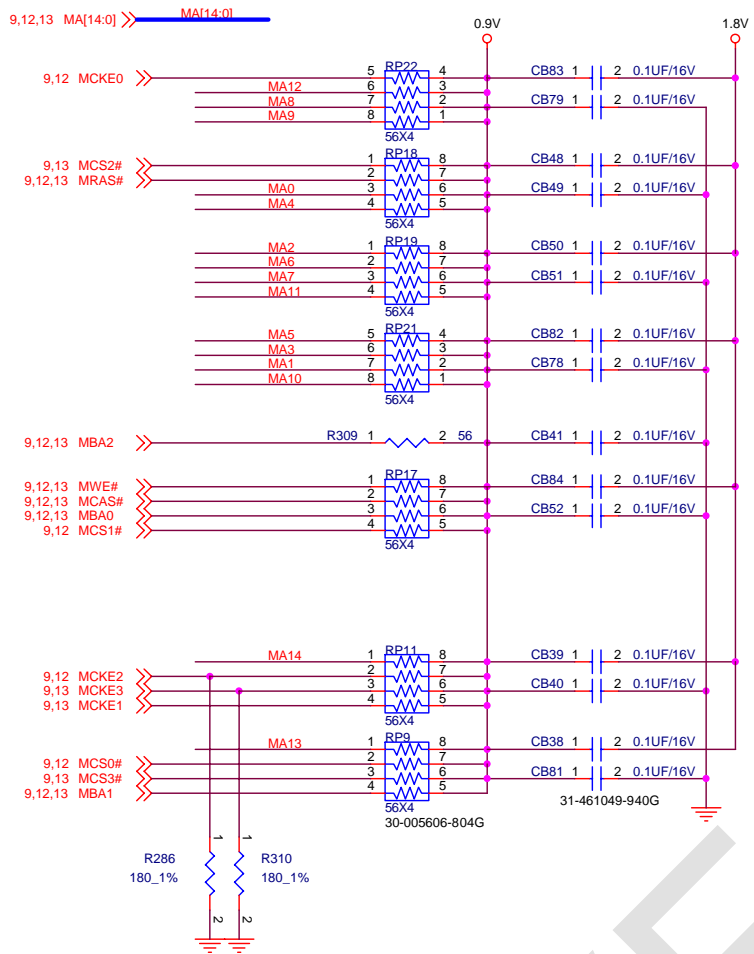
12_DDR2 SO-DIMM1

Arima		ARIMA COMPUTER CORP.	
Project Name : W340UI		Title : DDR2 SO-DIMM0	
Size : B	Document Number : 40GAB1230-B100	Rev : B	
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13_DDR2_SO-DIMM2

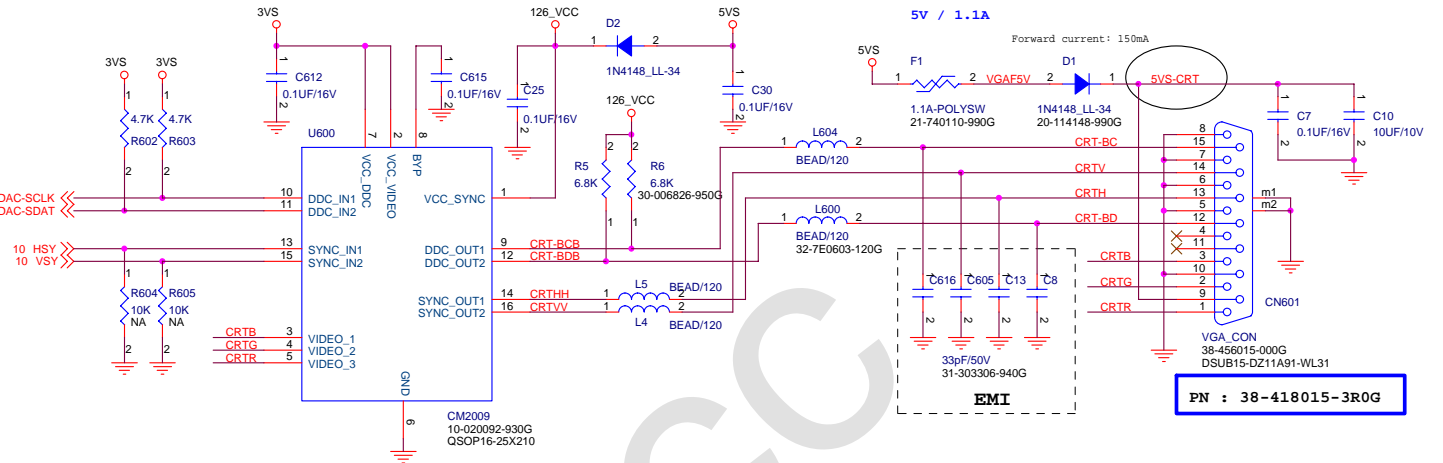
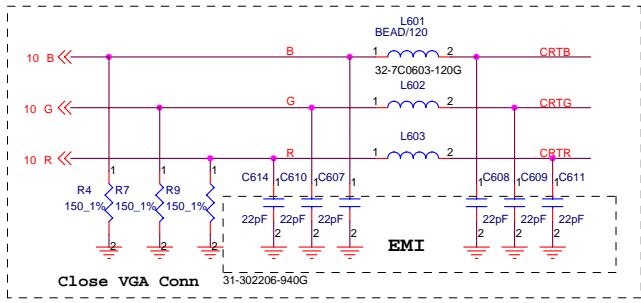
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Project Name : W340UI		Title : DDR2 SO-DIMM1	
Size :	Document Number :	Rev :	
Custom	40GAB1230-B100	B	
Date: Wednesday, June 21, 2006		Sheet : 13 of 39	



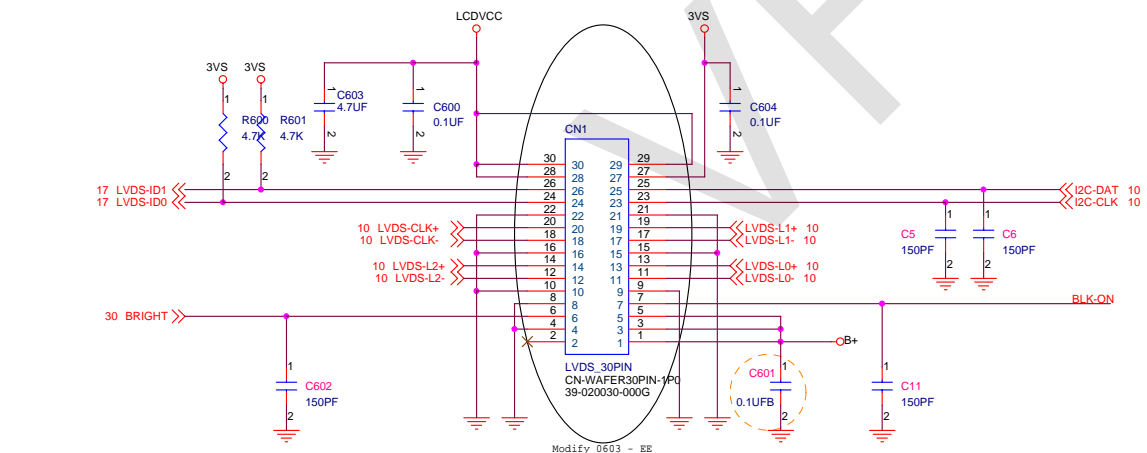
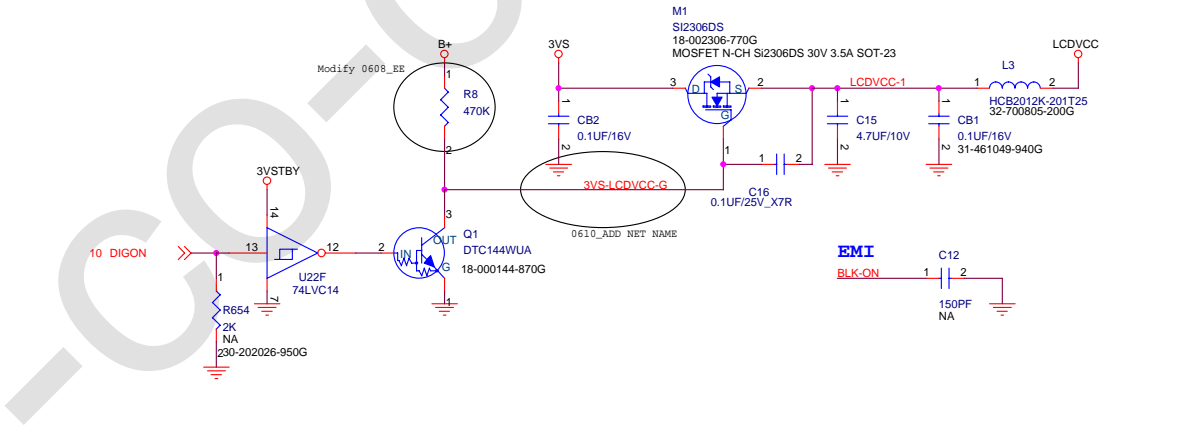
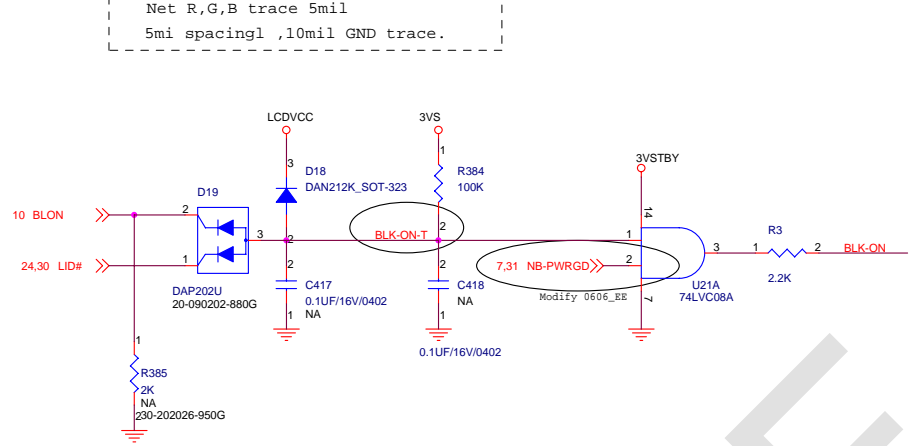
14_DDR2 TERMINATION

Arima		ARIMA COMPUTER CORP.	
Project Name : W340UI	Title : DDR2 TERMINATION		
Size : B	Document Number : 40GAB1230-B100	Rev : B	
Date: Wednesday, June 21, 2006		Sheet: 14 of 39	

VGA CONNECTOR LCD CONNECTOR INVERTER CONN.

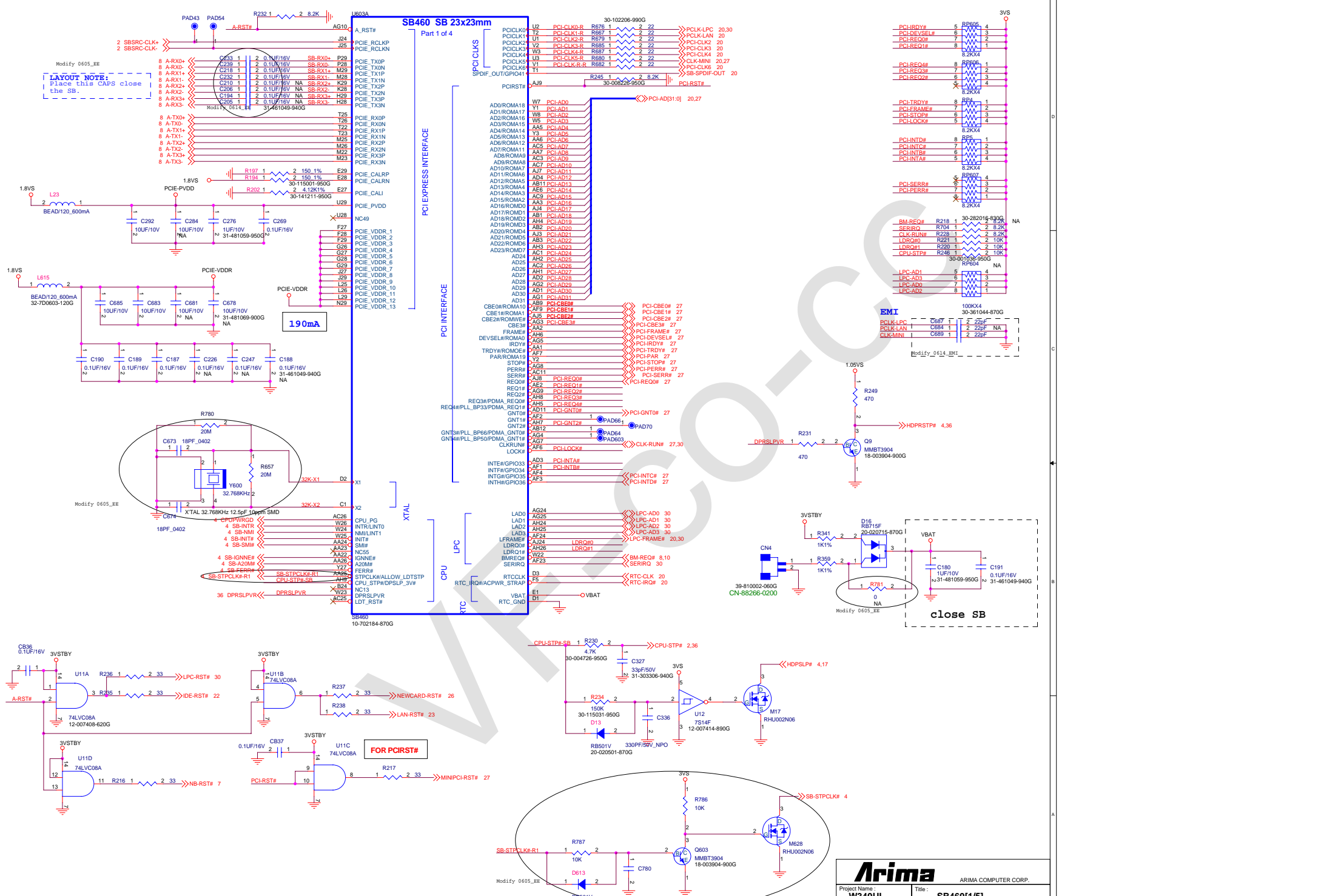


Layout Notice
 Net R,G,B trace 5mil
 5mi spacing, 1.0mil GND trace.



15_LVDS_CRT

Arima		ARIMA COMPUTER CORP.	
Project Name :	W340UI	Title :	LVDS_CRT
Size :	Document Number :	Customer :	40GAB1230-B100
Date :	Wednesday, June 21, 2006	Rev :	B
		Sheet :	15 of 39



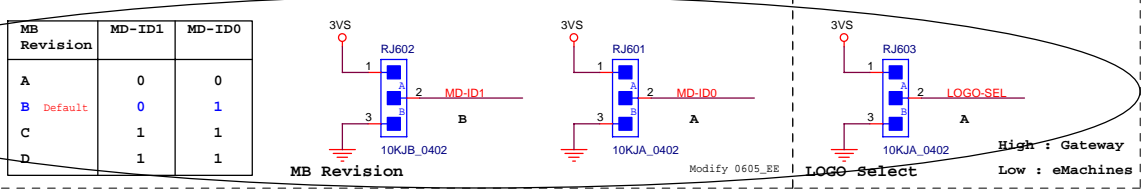
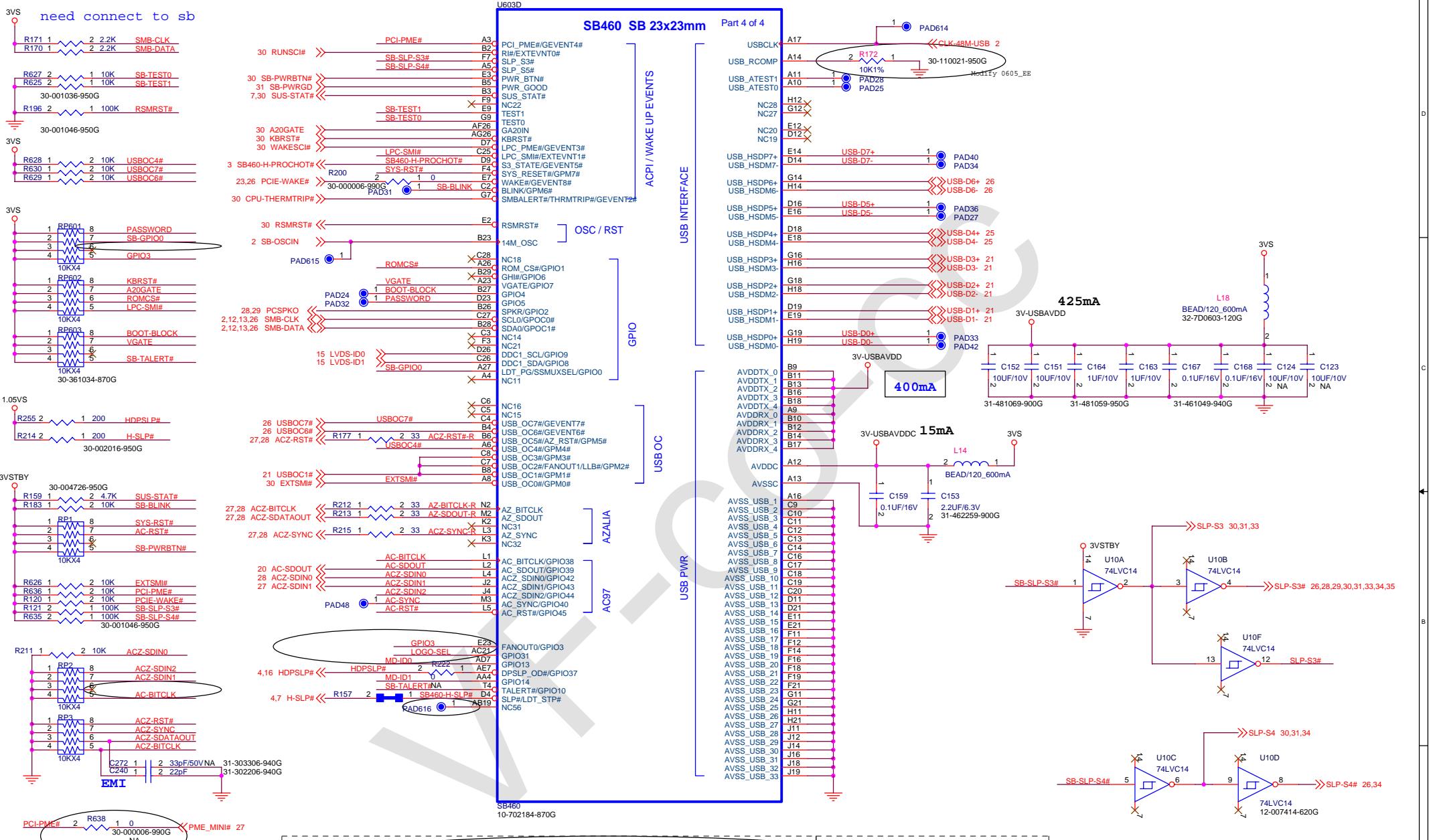
16_SB460_PCIE_PCI_LPC_RTC_CPU I/F

Arima
ARIMA COMPUTER CORP.

Project Name: **W340U1** Title: **SB460[1/5]**

Size: Document Number: **40GAB1230-B100** Rev: **B**

Customer: Date: Wednesday, June 21, 2006 Sheet: 16 of 39



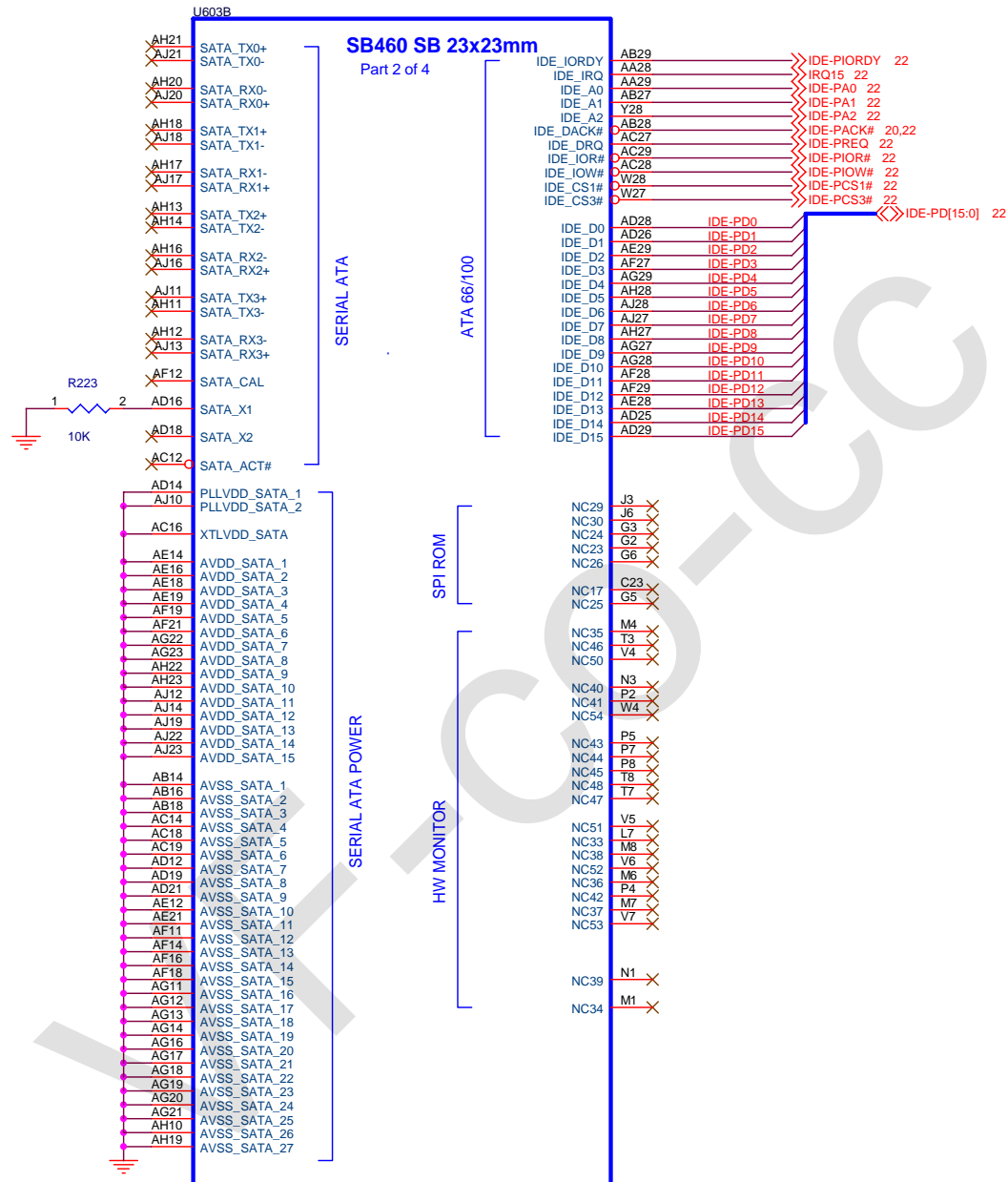
Arima
ARIMA COMPUTER CORP.

Project Name: **W340UI** Title: **SB460[2/5]**

Size: **40GAB1230-B100** Document Number: **40GAB1230-B100** Rev: **B**

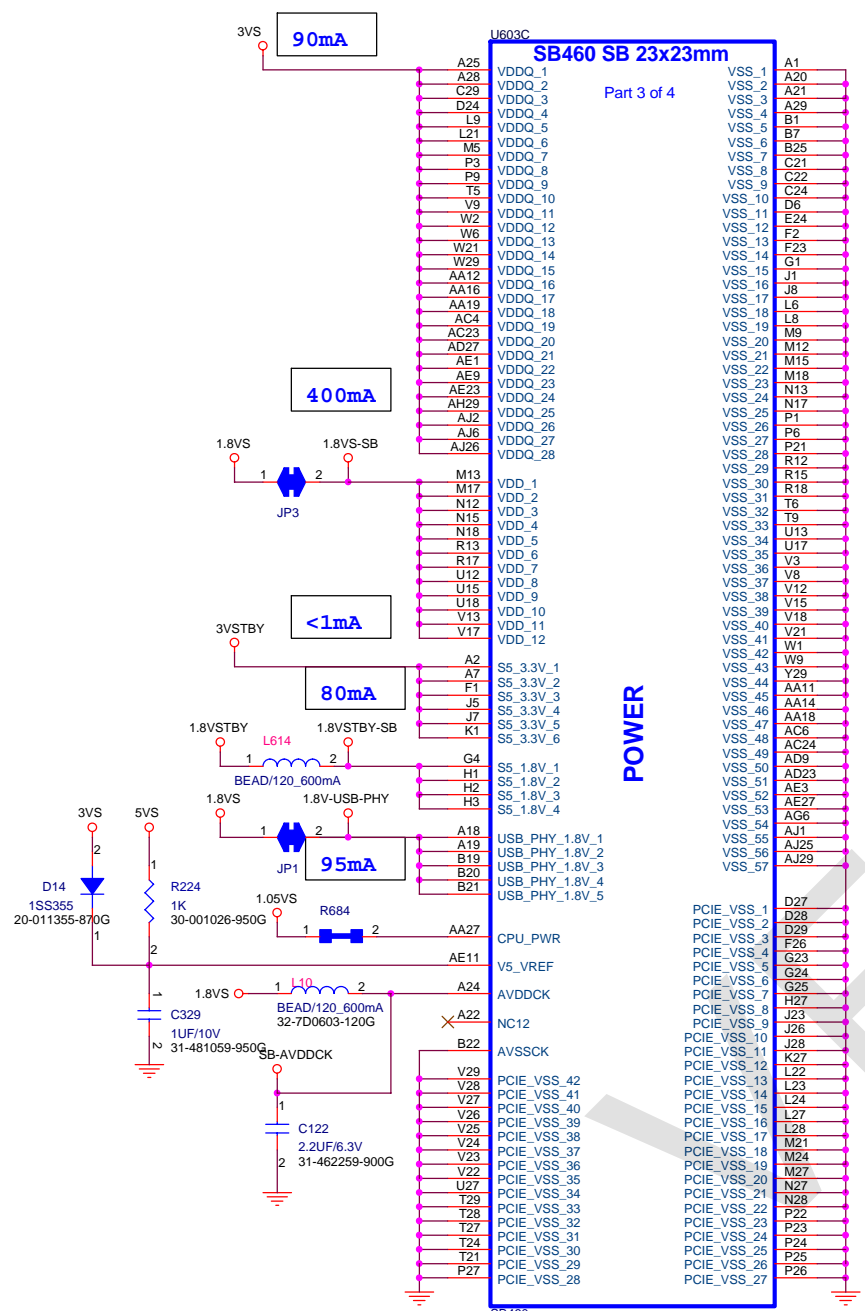
Date: Wednesday, June 21, 2006 Sheet: 17 of 39

17_SB460_SATA ACPI I/F & AC'97 & USB I/F

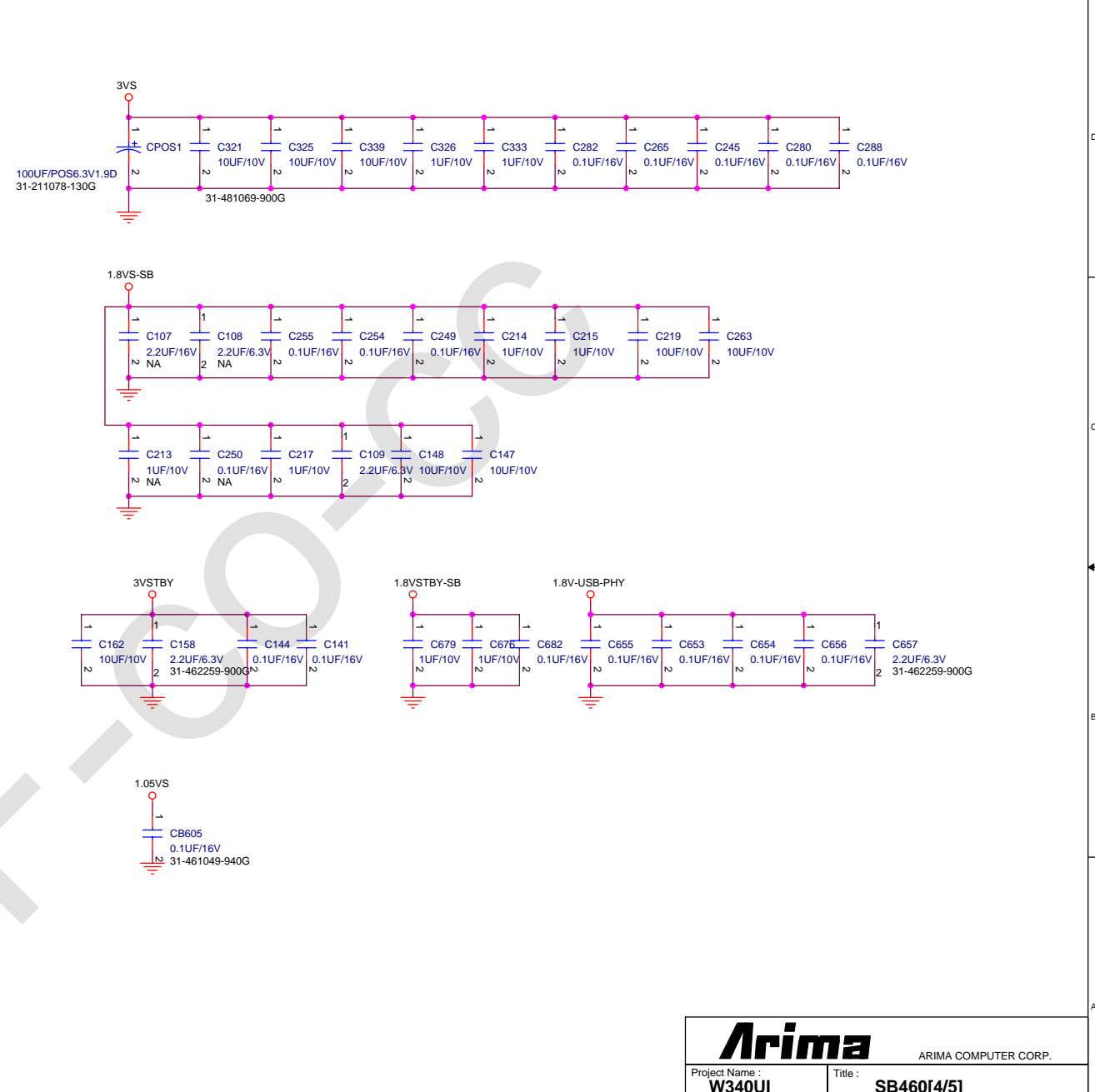


18_SB460_SATA_IDE I/F

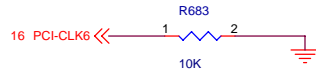
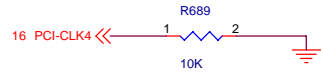
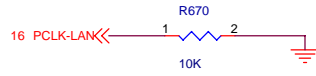
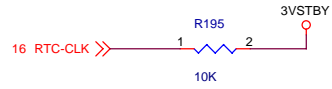
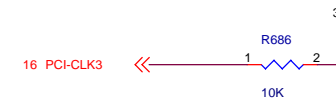
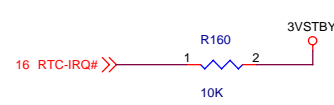
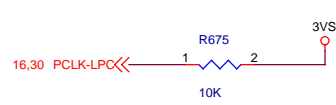
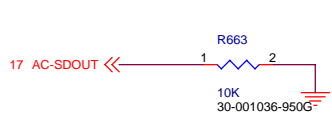
Arima		ARIMA COMPUTER CORP.	
Project Name : W340U1		Title : SB460[3/5]	
Size :	Document Number :	40GAB1230-B100	Rev : B
Date : Wednesday, June 21, 2006		Sheet : 18	of 39



19_SB460_POWER & GND



Arima		ARIMA COMPUTER CORP.	
Project Name : W340UI		Title : SB460[4/5]	
Size :	Document Number :	Rev :	
Custom	40GAB1230-B100	B	
Date : Wednesday, June 21, 2006		Sheet :	19 of 39

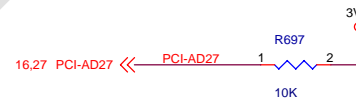
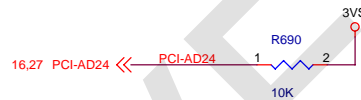
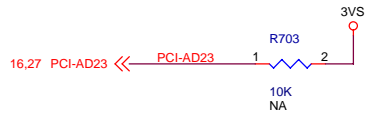
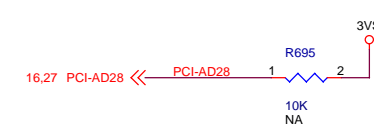
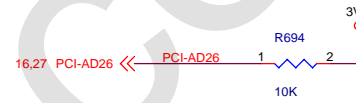
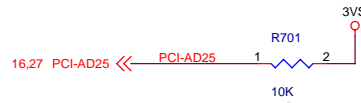


REQUIRED STRAPS

	AC-SDOUT	RTC_CLK	PCI_CLK4	PCI_CLK6	PCLK-LPC PCI_CLK0	PCLK-LAN PCI_CLK1
PULL HIGH	USE DEBUG STRAPS DEFAULT	INTERNAL RTC DEFAULT	USE INT. PLL48	CPU I/F = K8	ROM TYPE H,H = PCI ROM H,L = LPC I ROM	DEFAULT
PULL LOW	IGNORE DEBUG STRAPS DEFAULT	EXTERNAL RTC	USE EXT. 48MHz DEFAULT	CPU I/F = P4 DEFAULT	L,H = LPC II ROM L,L = FWH ROM	

PCI_CLK0 NOT USED
PCI_CLK1 NOT USED
PCI_CLK2 NOT USED
PCI_CLK3 NOT USED
PCI_CLK4 NOT USED
PCI_CLK5 CLK-MINI
PCI_CLK6 NOT USED
PCI_CLK7 NOT USED
PCI_CLK8 NOT USED
PCLK-LPC NOT USED
PCLK-LAN NOT USED

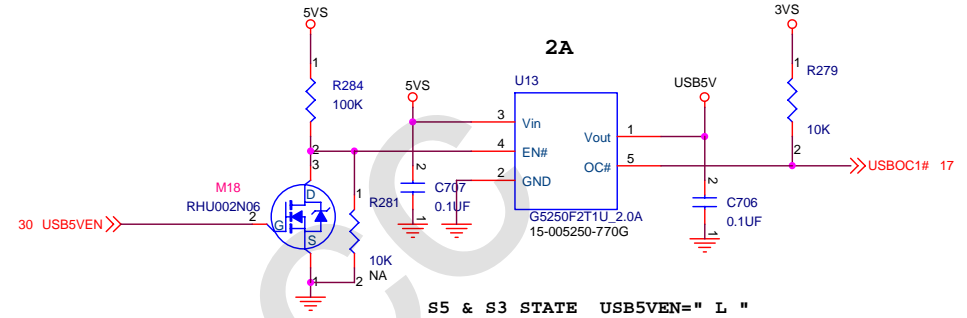
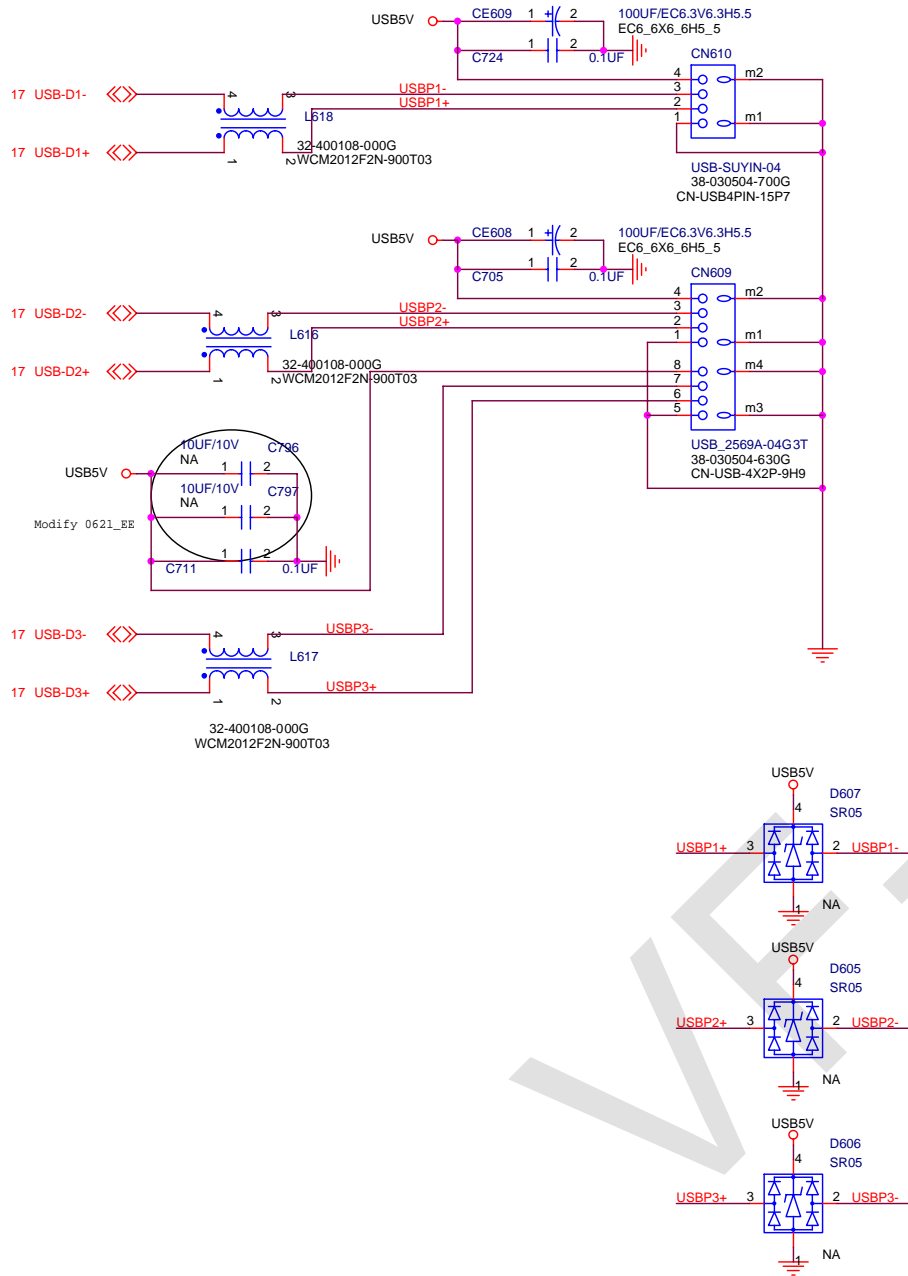
	RTC-IRQ# ACPWON	SPDIF_OUT	PCI_CLK2	PCI_CLK3	CLK-MINI PCI_CLK5	LPC-FRAME#
PULL HIGH	MANUAL PWR ON DEFAULT	SIO 24MHz	XTAL MODE NOT SUPPORTED	USB PHY PWRDOWN DISABLE DEFAULT	PCIE_CM_SET LOW DEFAULT	ENABLE THERMALTRIP# DEFAULT
PULL LOW	AUTO PWR ON	SIO 48MHz DEFAULT	48MHz OSC MODE DEFAULT	USB PHY PWRDOWN ENABLE	PCIE_CM_SET HIGH	DISABLE THERMALTRIP#



DEBUG STRAPS

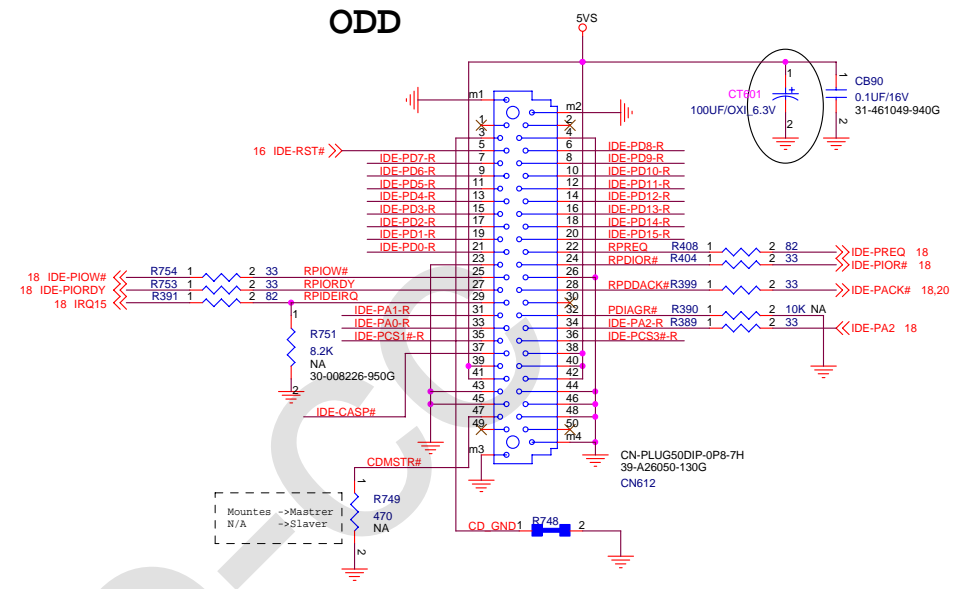
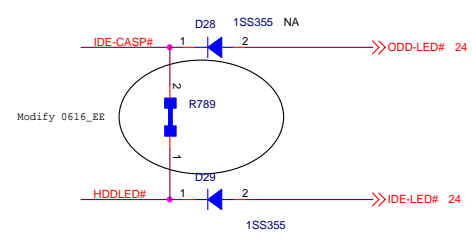
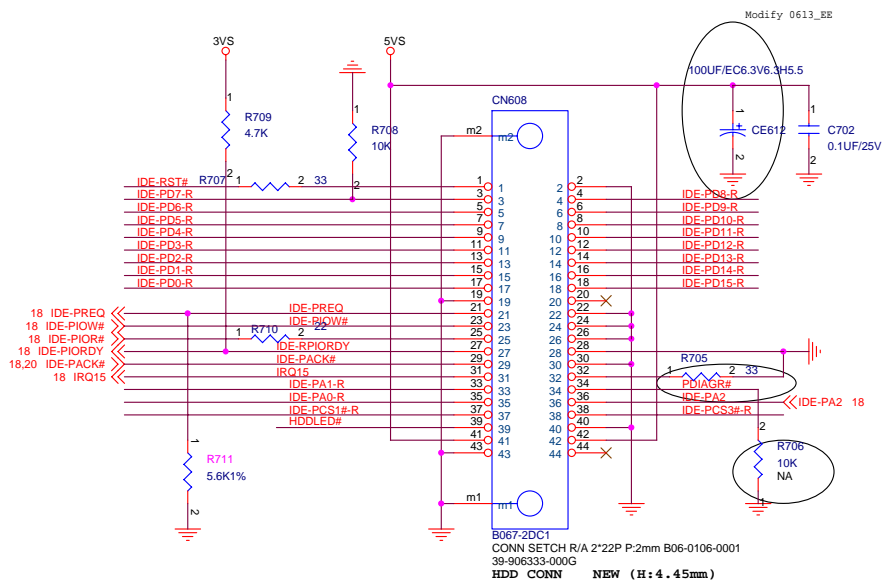
	IDE-PACK#	PCI-AD28	PCI-AD27	PCI-AD26	PCI-AD25	PCI-AD24	PCI-AD23
PULL HIGH	USE LONG RESET DEFAULT	USE LONG RESET DEFAULT	USE PCI PLL DEFAULT	USE ACPI BCLK DEFAULT	USE IDE PLL DEFAULT	USE DEFAULT PCIE STRAPS DEFAULT	BOOTFAIL TIMER DISABLED DEFAULT
PULL LOW	USE SHORT RESET	USE SHORT RESET	BYPASS PCI PLL	BYPASS ACPI BCLK	BYPASS IDE PLL	USE EEPROM PCIE STRAPS	BOOTFAIL TIMER ENABLE

USB PORT

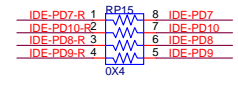
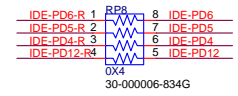
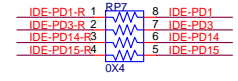
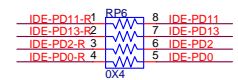


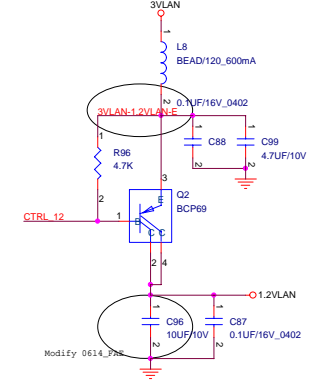
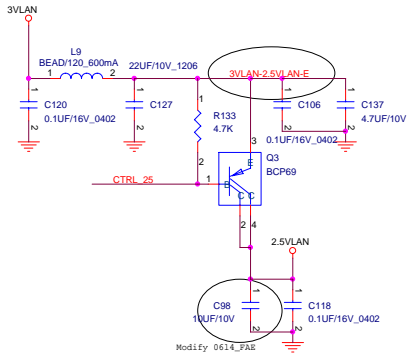
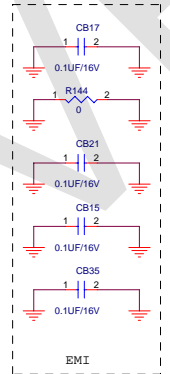
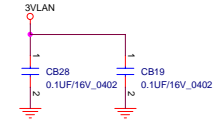
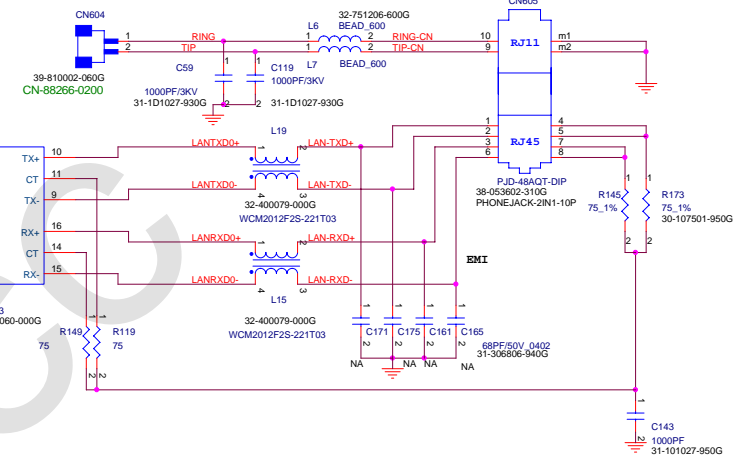
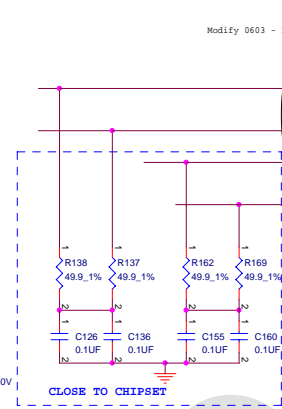
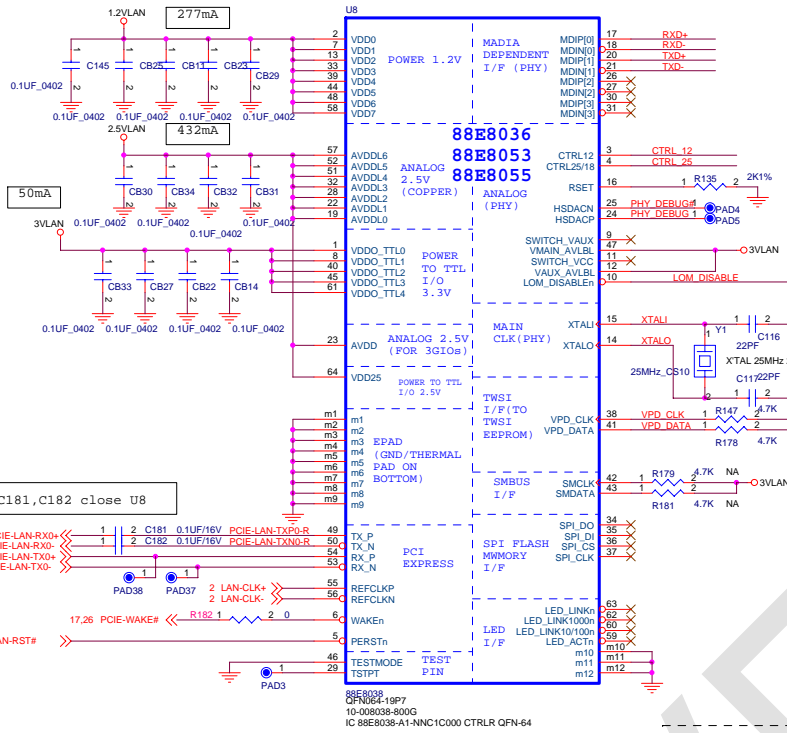
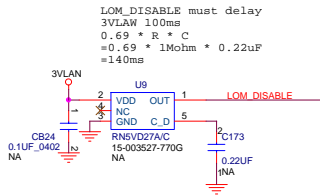
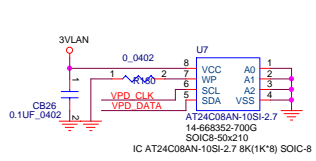
21_USB 2.0

Arima		ARIMA COMPUTER CORP.	
Project Name : W340U1	Title : USB2.0		
Size : Custom	Document Number : 40GAB1230-B100	Rev : B	
Date: Wednesday, June 21, 2006	Sheet: 21	of	39



18 IDE-PD15[0] <<>

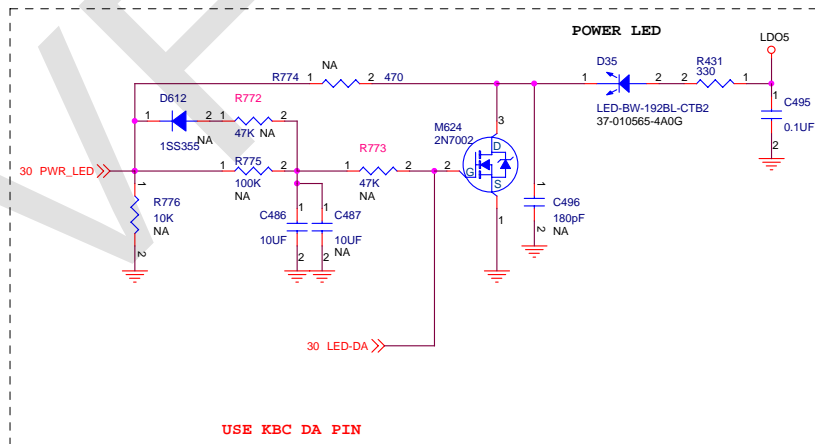
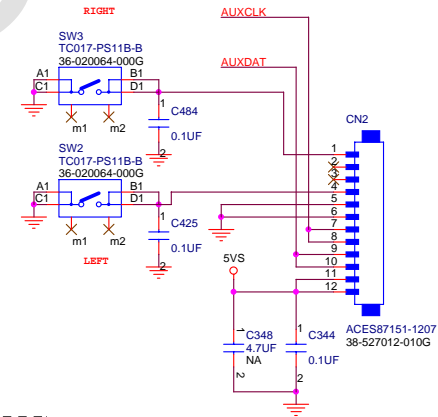
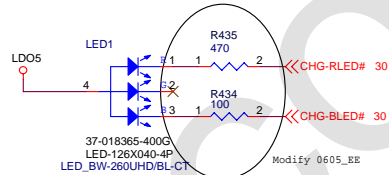
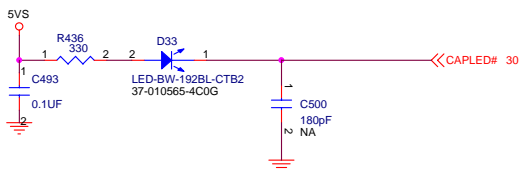
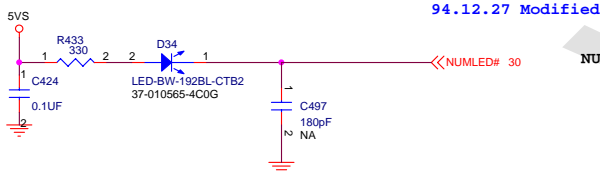
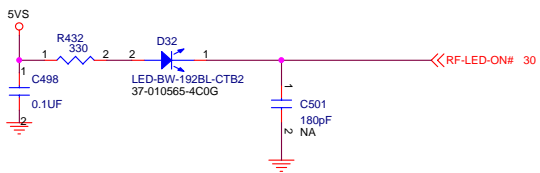
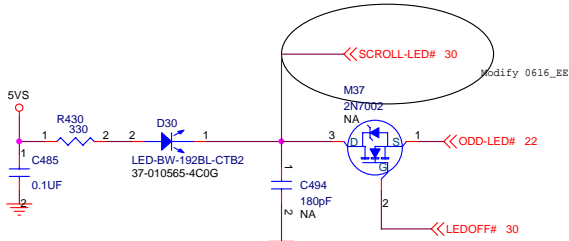
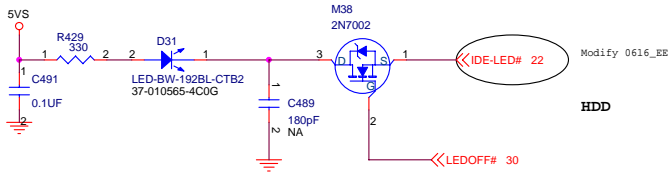
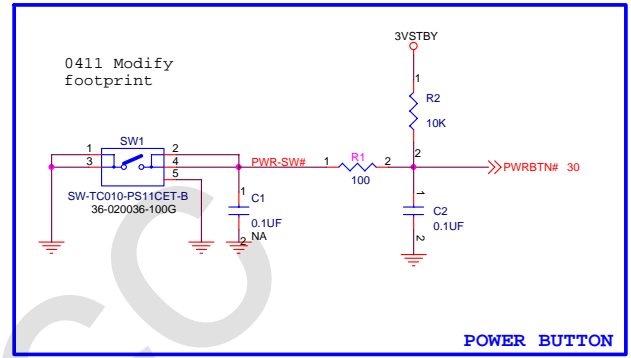
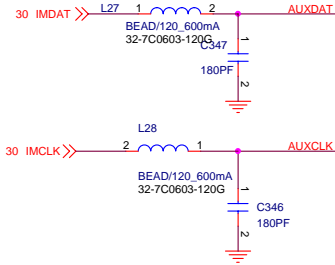
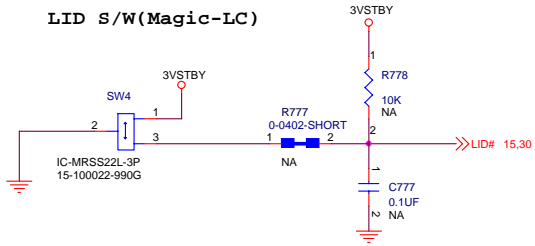




23_LAN_Marvell 8038

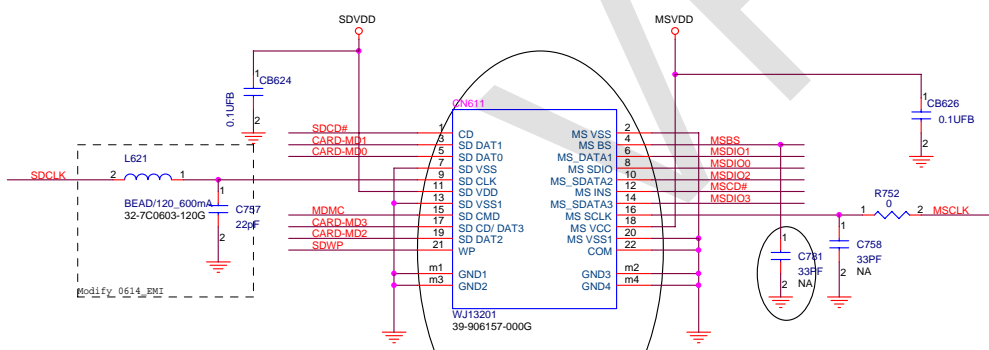
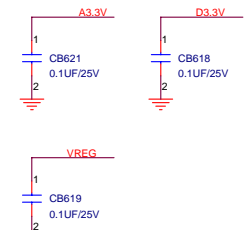
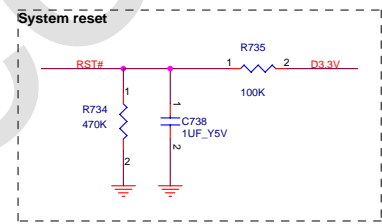
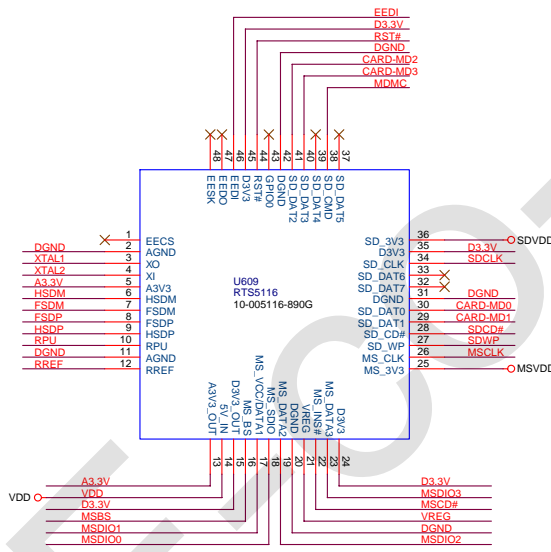
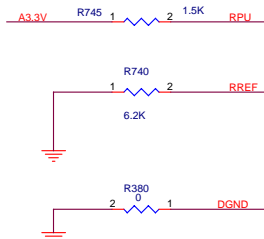
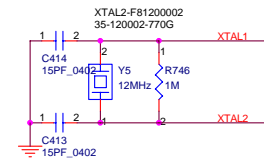
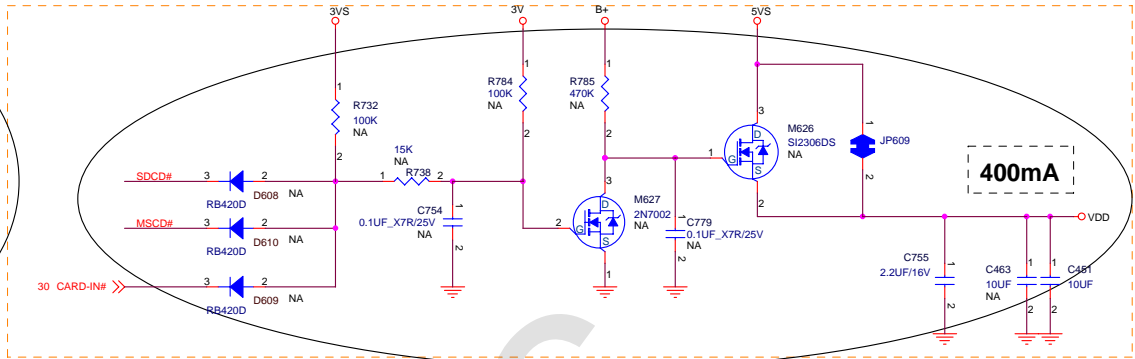
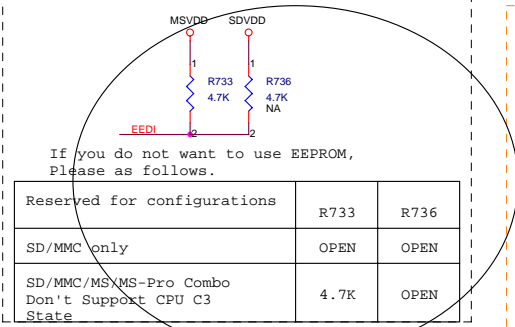
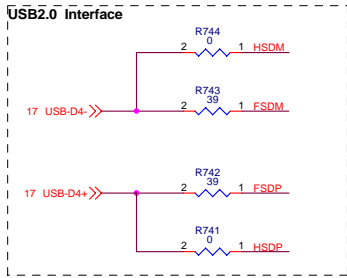
Arima		ARIMA COMPUTER CORP.	
Project Name :	W340UI	Title :	LAN-Marvell 8038
Size :	Document Number :	Customer :	40GAB1230-B100
Rev :		Rev :	B
Date: Wednesday, June 21, 2006			Sheet: 23 of 39

LID S/W(Magic-LC)



24_INDICATOR / LID / LED / TP

Arima		ARIMA COMPUTER CORP.	
Project Name:	W340UI	Title:	INDICATOR / LID / LED / TP
Size:	Document Number:	40GAB1230-B100	Rev: B
Custom	Date: Wednesday, June 21, 2006	Sheet:	24 of 39

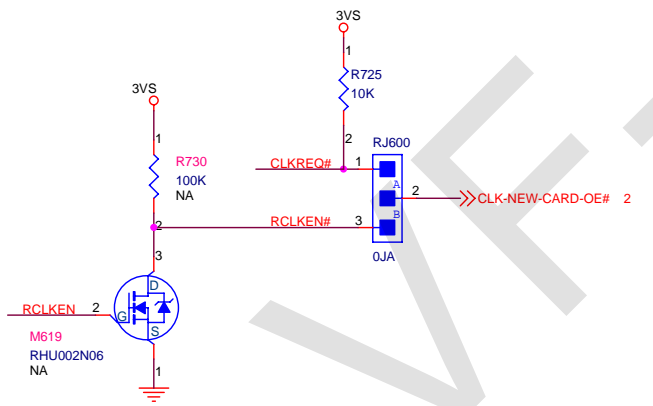
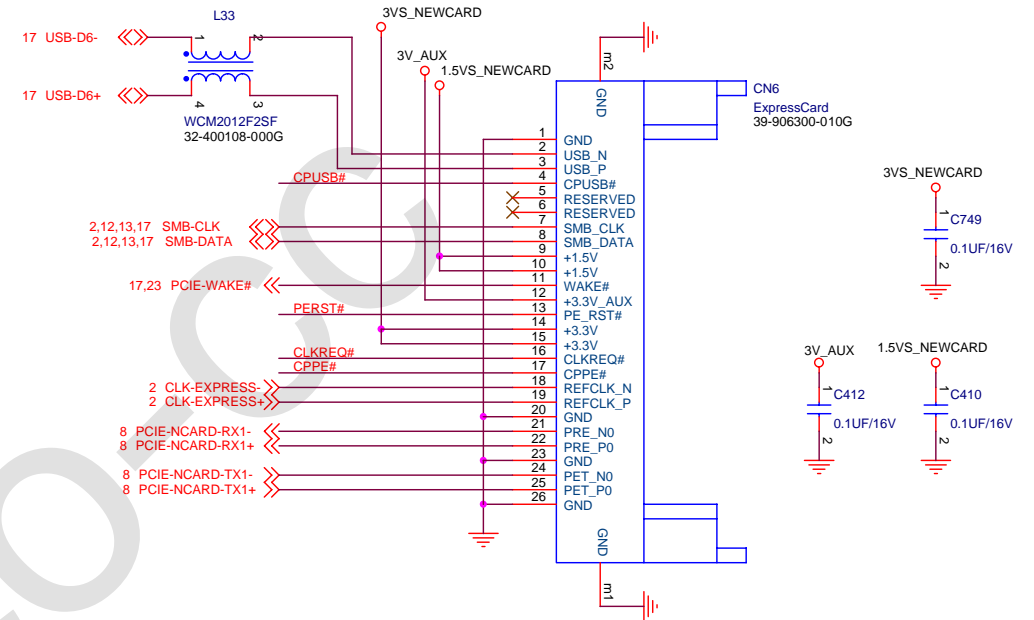
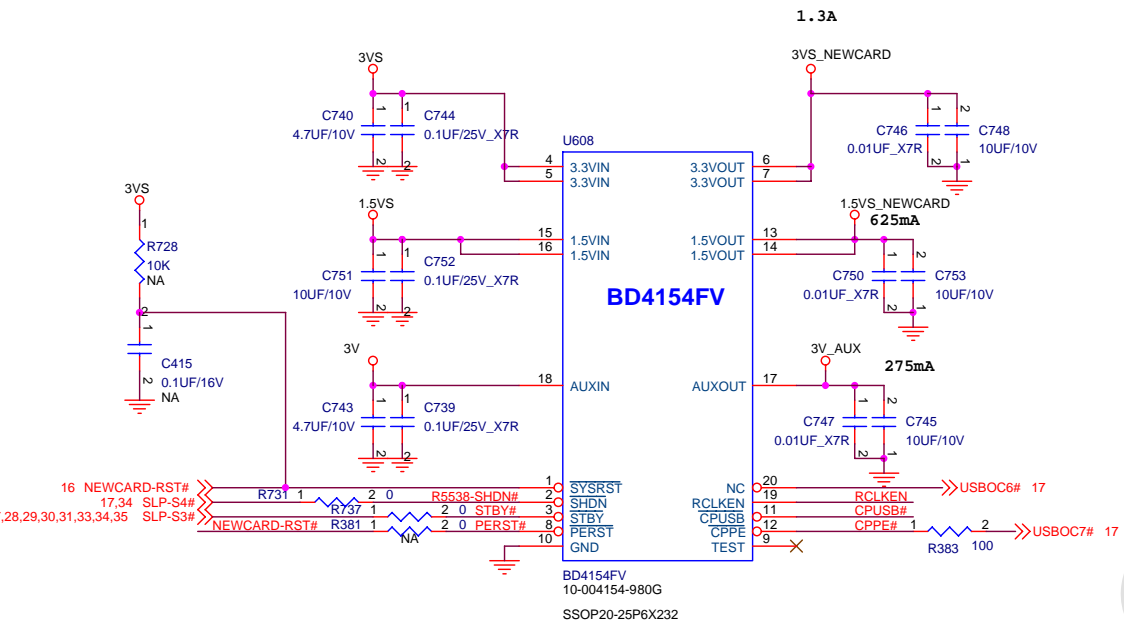


25_CARD READER CONTROL

NewCardSlot

Express Slot(New Card)

New Card Body : 39-906300-010G



EMI

SMB-CLK	C404	1	2	180pF	NA
SMB-DATA	C408	1	2	180pF	NA
PCI-E-WAKE#	C411	1	2	180pF	NA
CLK-NEW-CARD-OE#	C394	1	2	180pF	NA

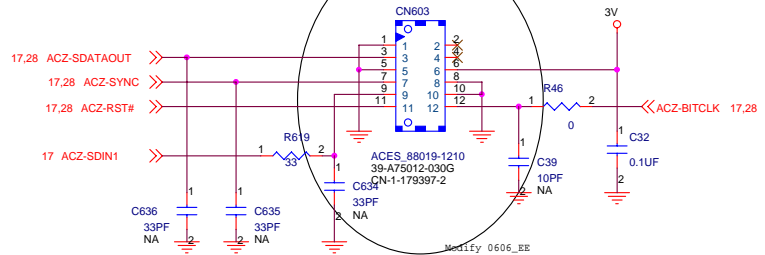
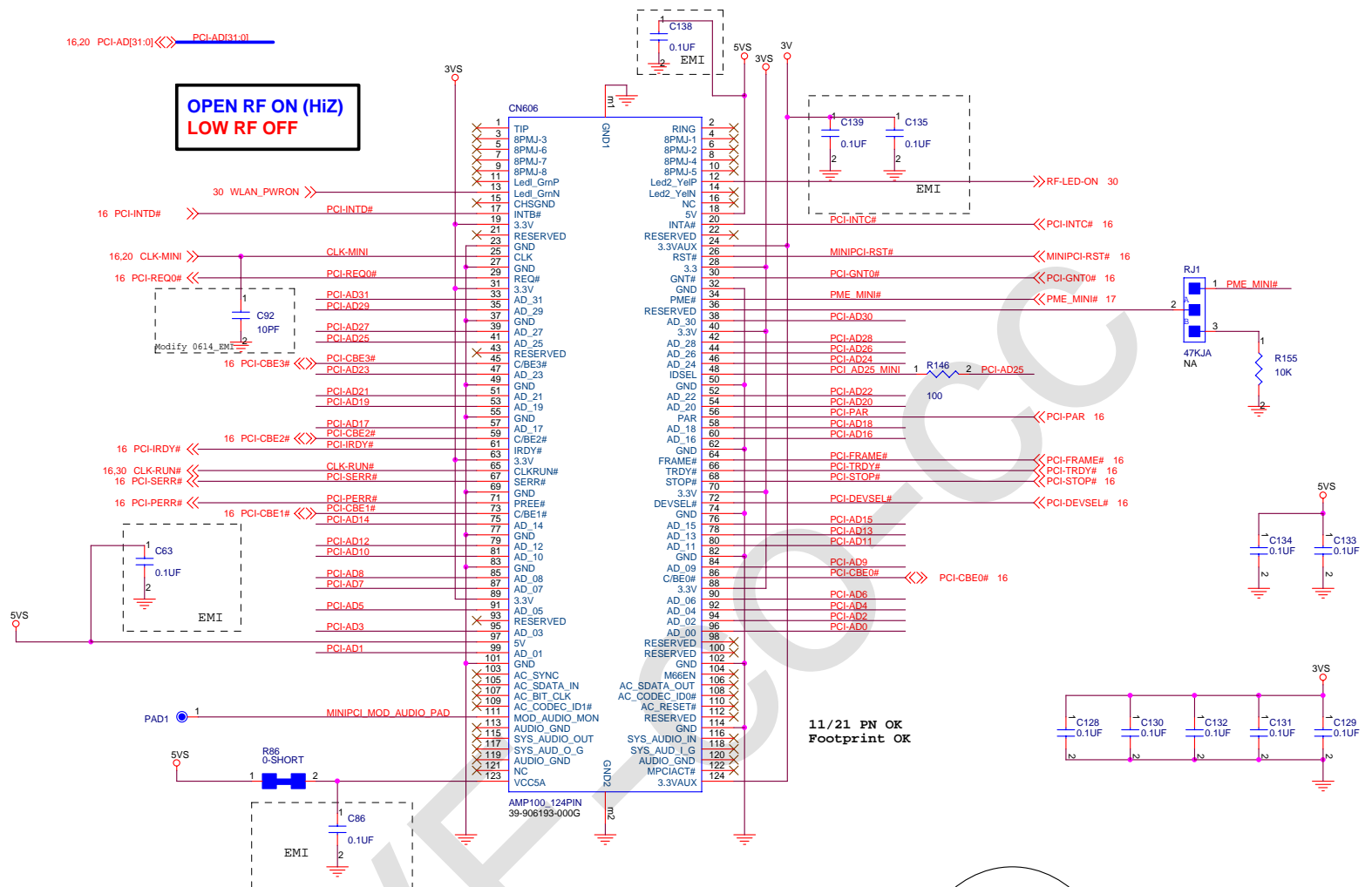
31-101817-940G

26_NewCard Slot

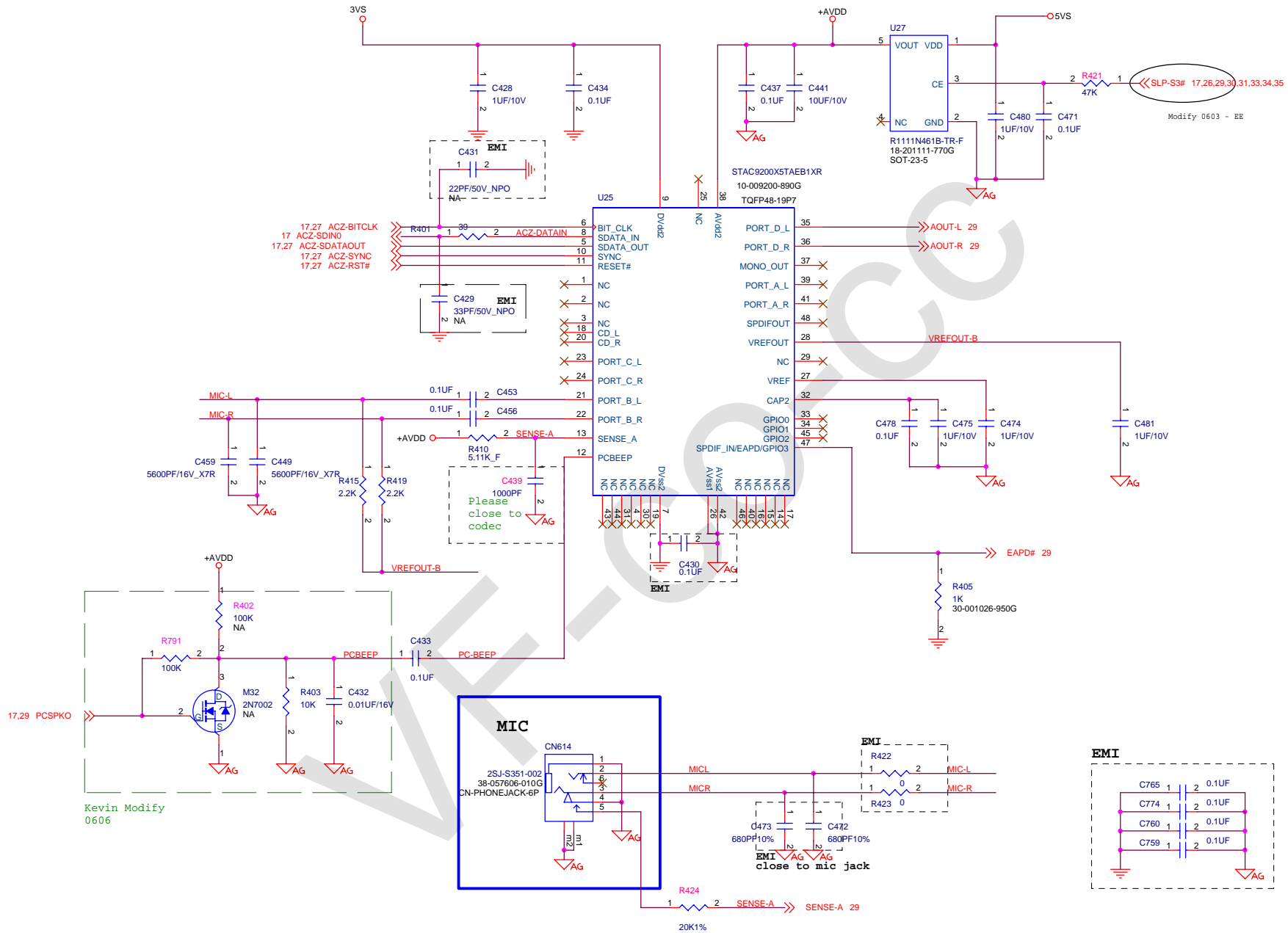
Arima		ARIMA COMPUTER CORP.	
Project Name :	W340UI	Title :	NEW CARD SLOT
Size :	Document Number :	Rev :	B
B	40GAB1230-B100	Sheet :	26 of 39
Date : Wednesday, June 21, 2006			

16.20 PCI-AD[31:0] <<> PCI-AD[31:0]

OPEN RF ON (HiZ)
LOW RF OFF

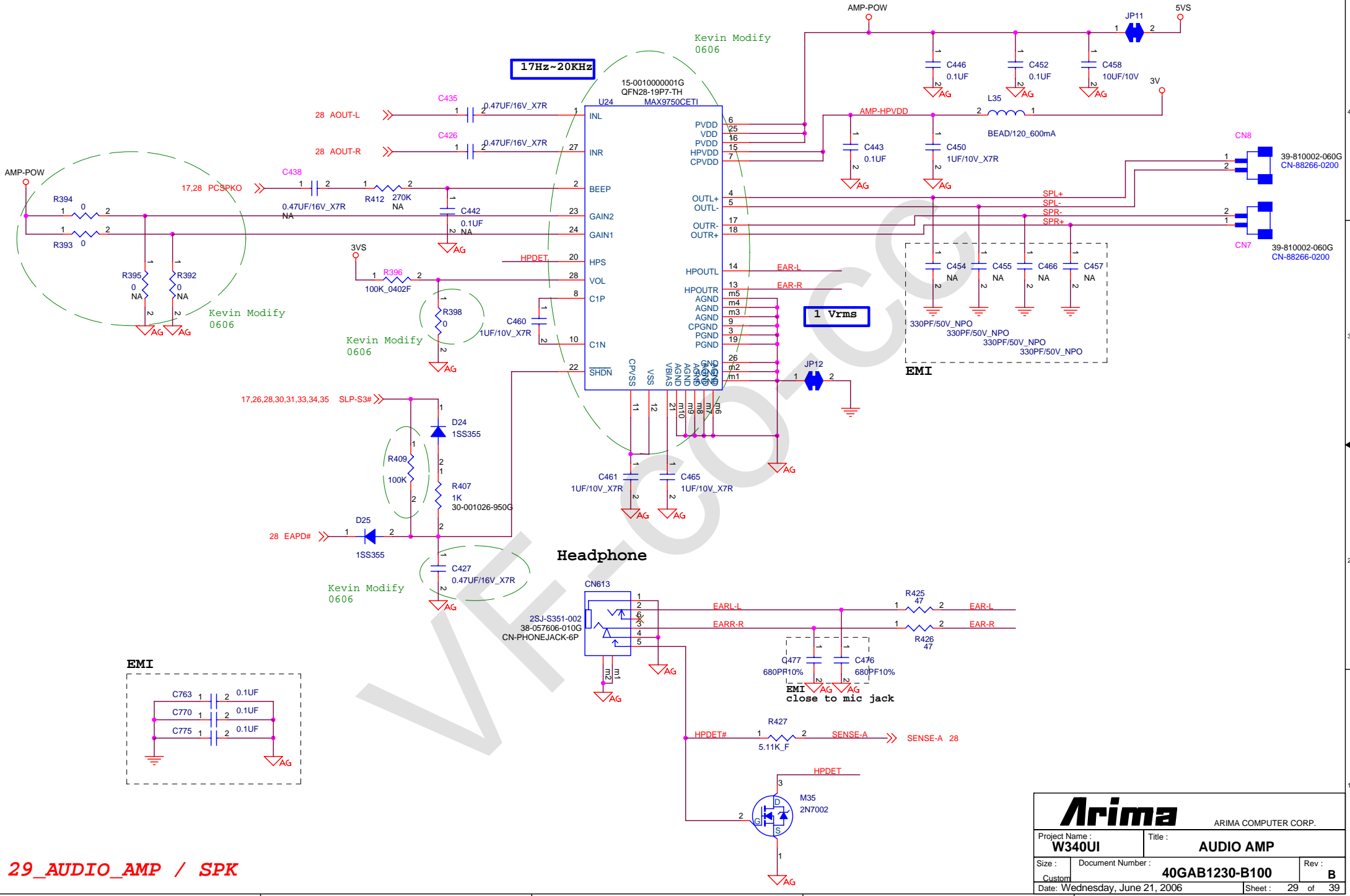


Arima		ARIMA COMPUTER CORP.	
Project Name :	W340UI	Title :	MINI PCI / MDC
Size :	Document Number :	40GAB1230-B100	Rev : B
Custom :	Date : Wednesday, June 21, 2006	Sheet :	27 of 39



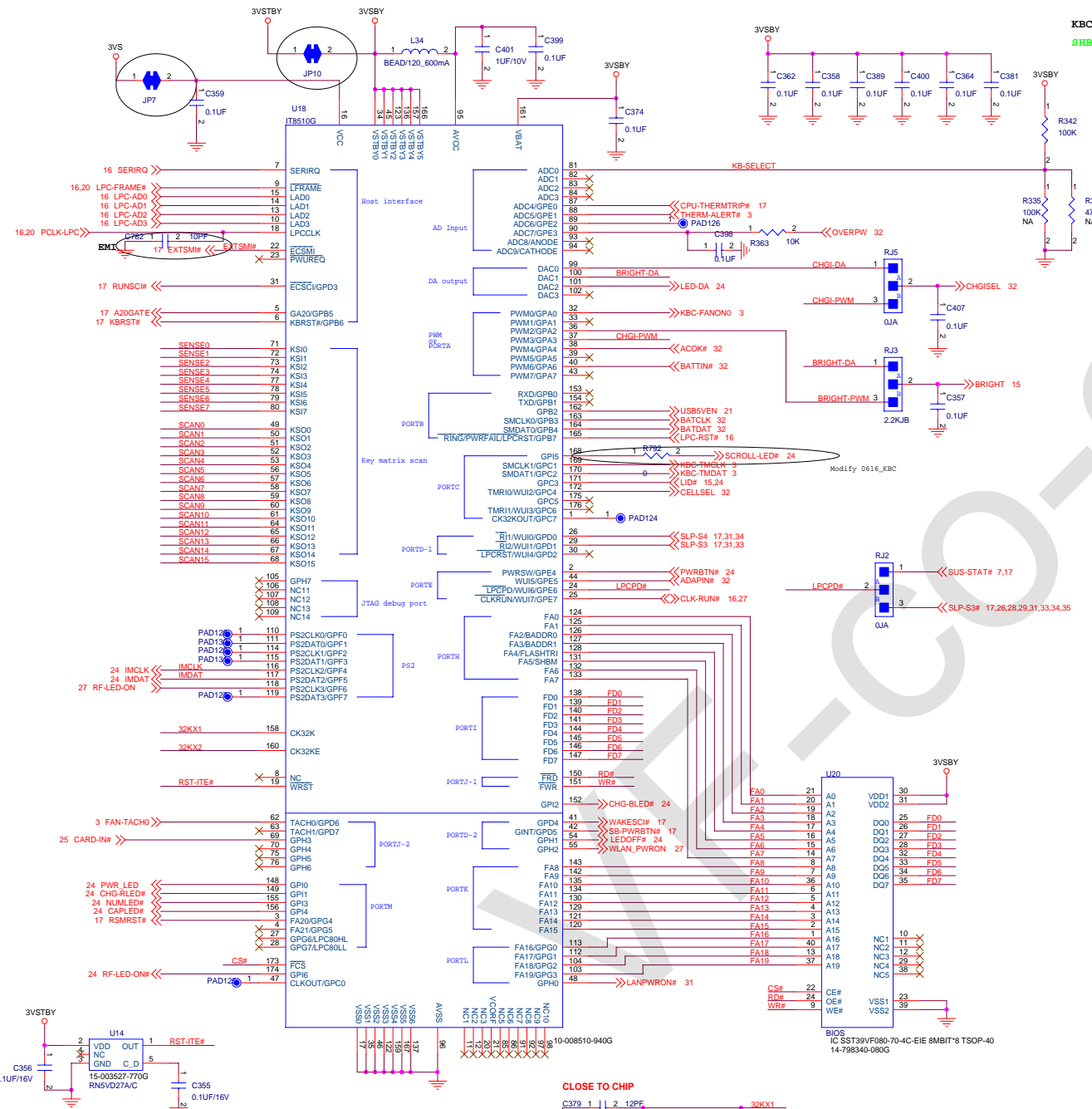
Kevin Modify
0606

Arima		ARIMA COMPUTER CORP.	
Project Name :	W340UI	Title :	AUDIO CODEC
Size :	Custom	Document Number :	40GAB1230-B100
Date :	Wednesday, June 21, 2006	Rev :	B
		Sheet :	28 of 39

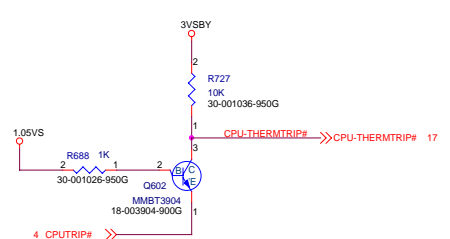
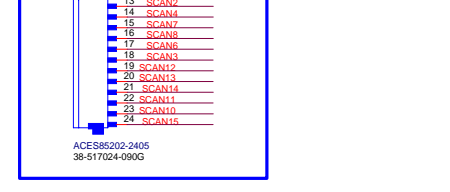
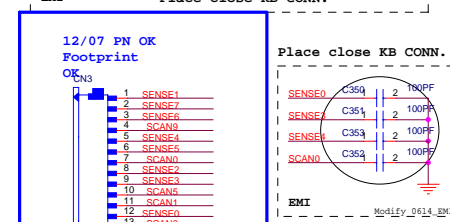
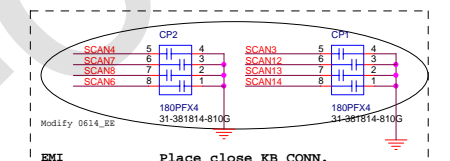
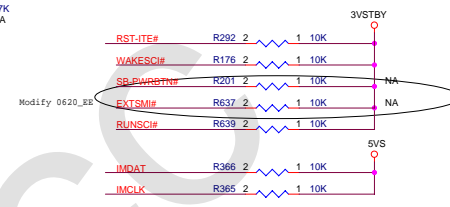
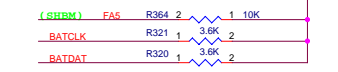


29_AUDIO_AMP / SPK

Arima		ARIMA COMPUTER CORP.	
Project Name: W340UI		Title: AUDIO AMP	
Size:	Document Number: 40GAB1230-B100	Rev:	B
Custom:	Date: Wednesday, June 21, 2006	Sheet:	29 of 39



KBC I/O Address (FA3=0, FA2=0) : Index 2E, Data 2F
 SHBM=1: Enable shared memory with host BIOS



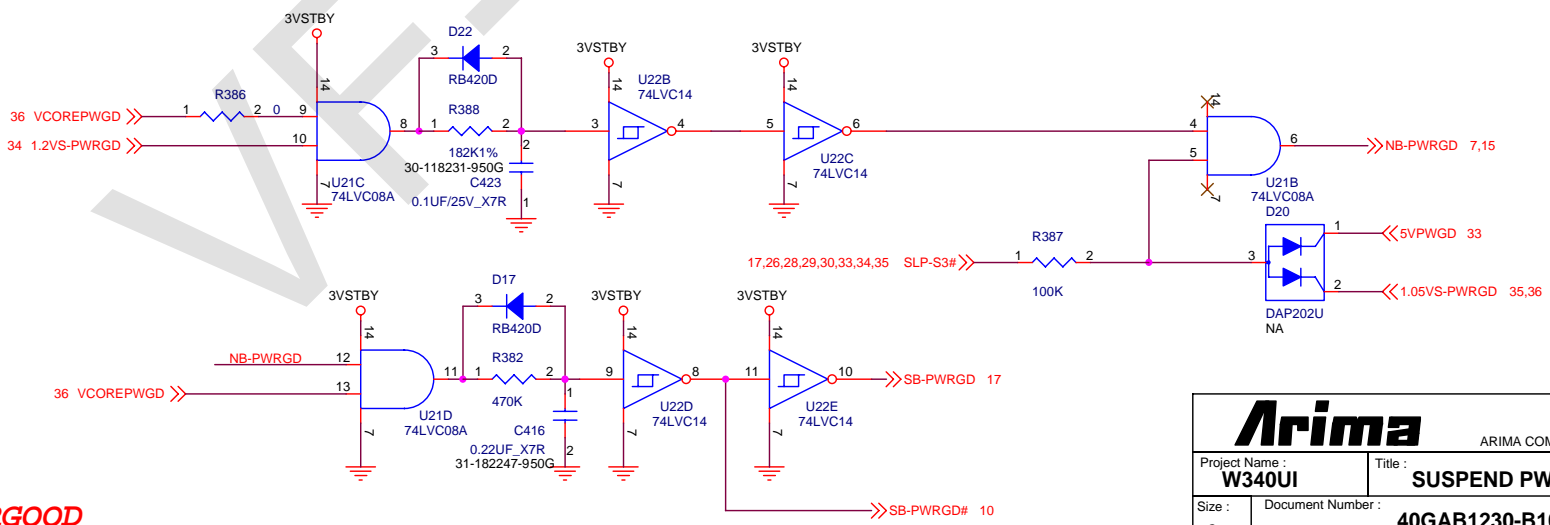
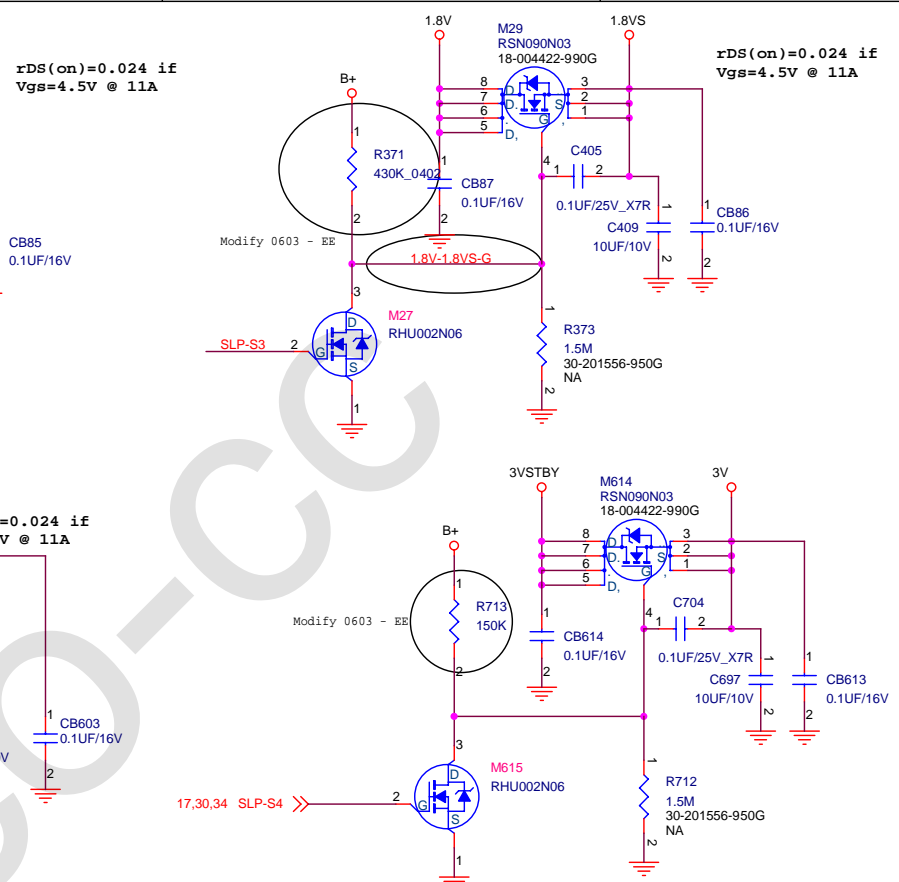
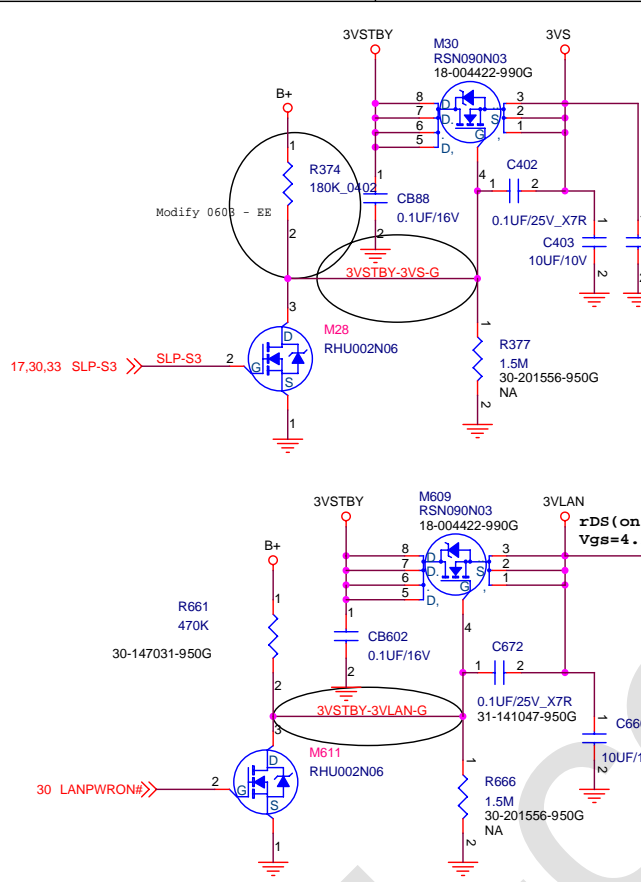
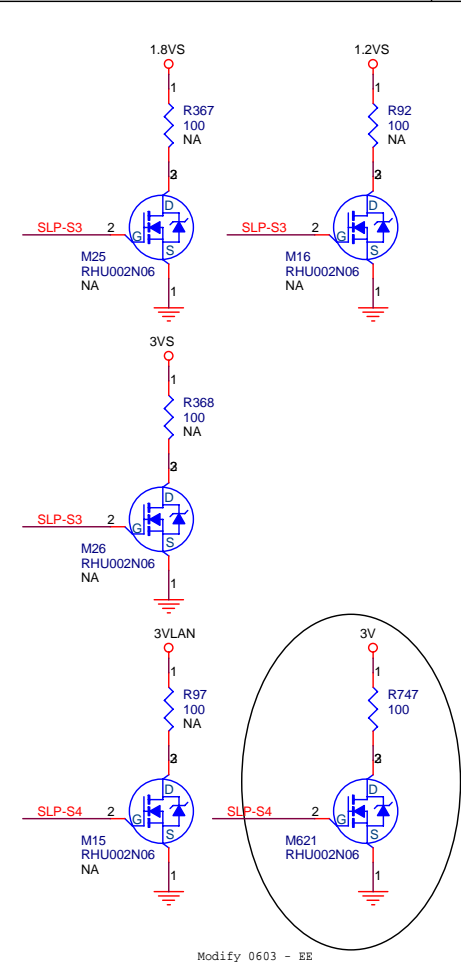
30_KBC / BIOS

Arima ARIMA COMPUTER CORP.

Project Name: **W340UI** Title: **KBC&BIOS**

Size: **40GAB1230-B100** Document Number: **40GAB1230-B100** Rev: **B**

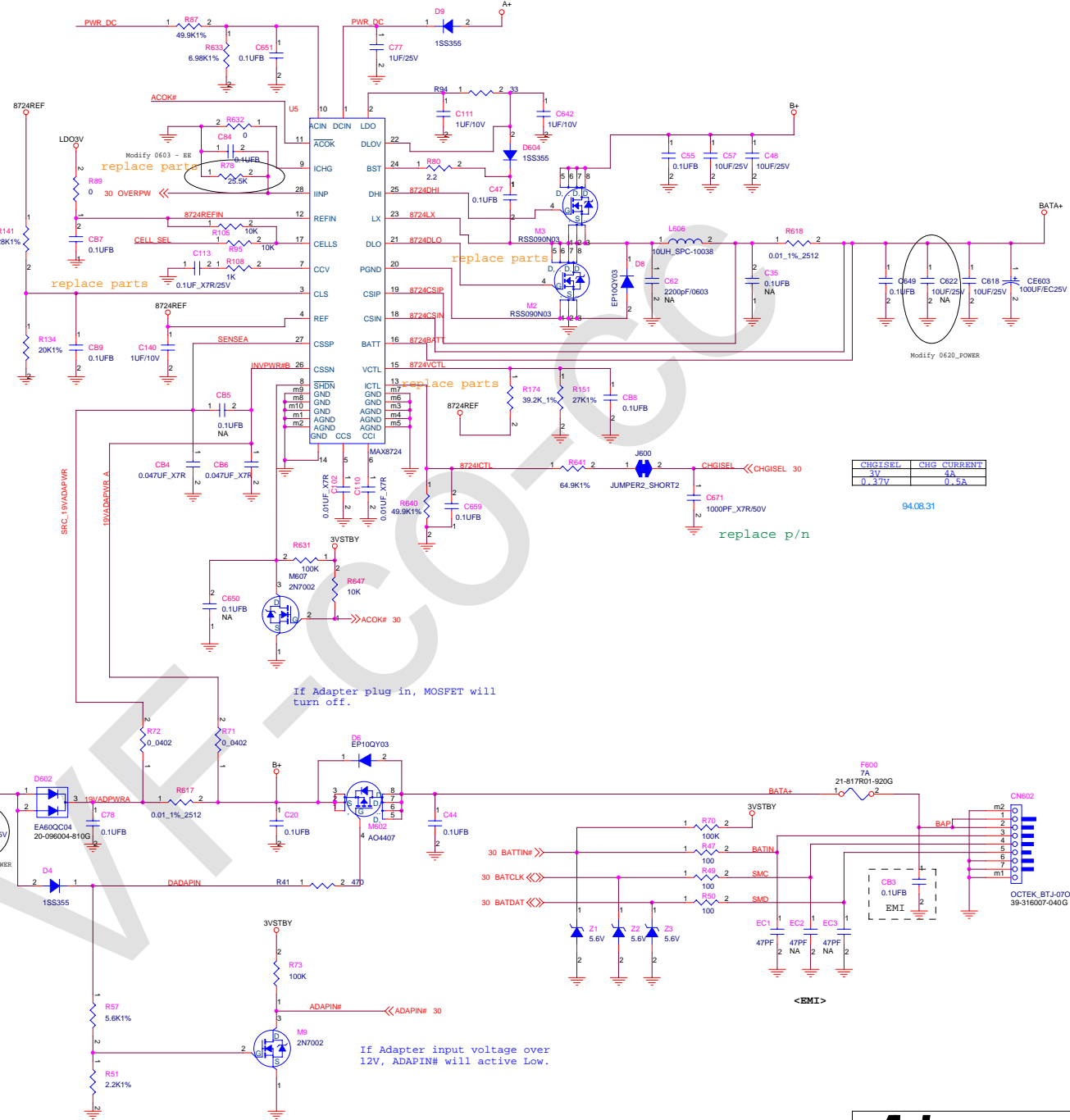
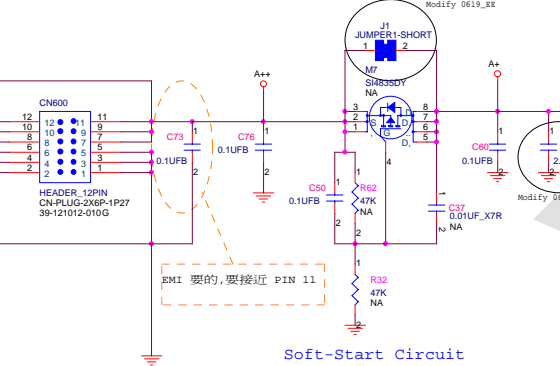
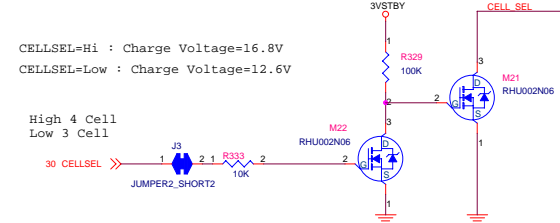
Date: Wednesday, June 21, 2006 Sheet: 30 of 39



31_SUSPEND POWER & POWERGOOD

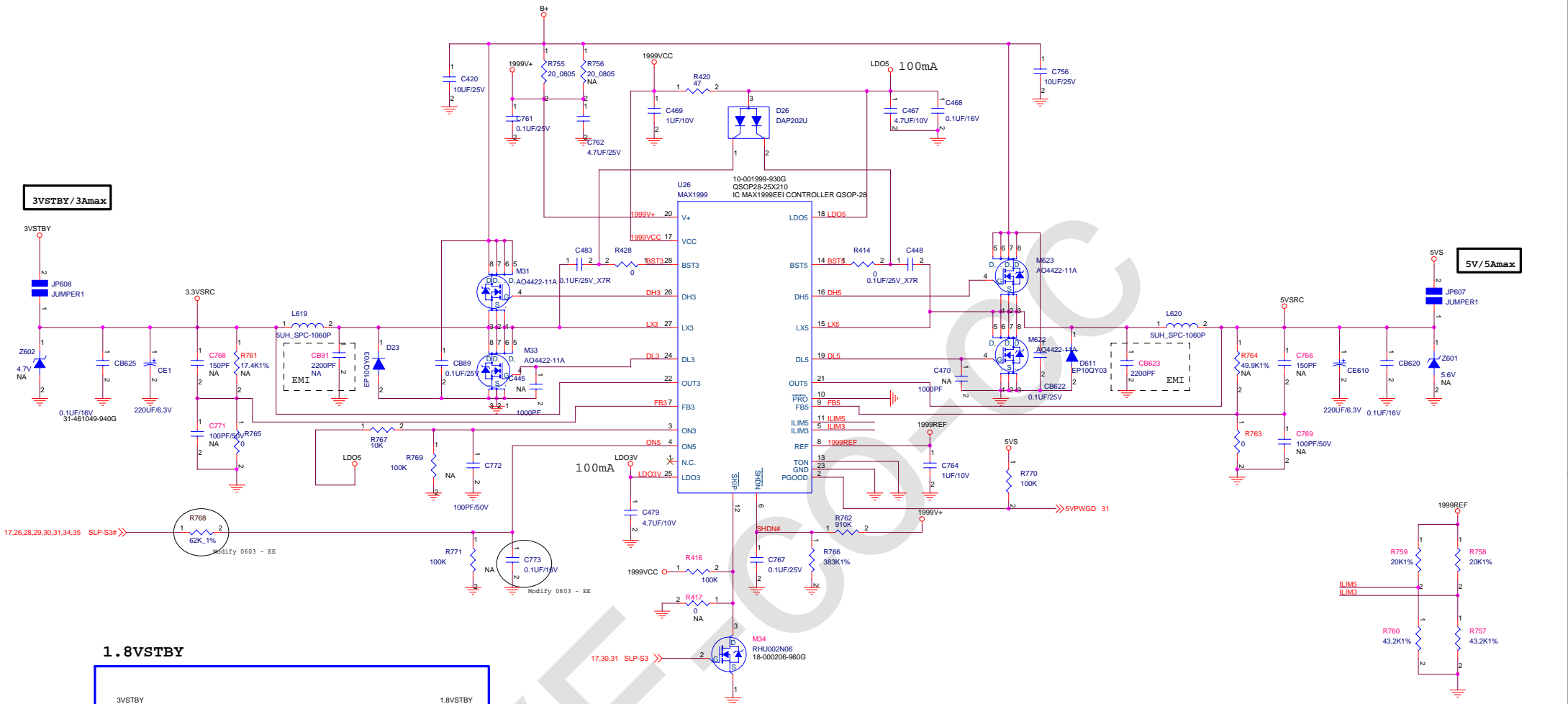
Arima		ARIMA COMPUTER CORP.	
Project Name :	W340UI	Title :	SUSPEND PWR_PWRGD
Size :	Document Number :	40GAB1230-B100	Rev : B
Custom :	Date :	Wednesday, June 21, 2006	Sheet : 31 of 39

If Adapter input power over 105%,
OVERPW# will active Low.

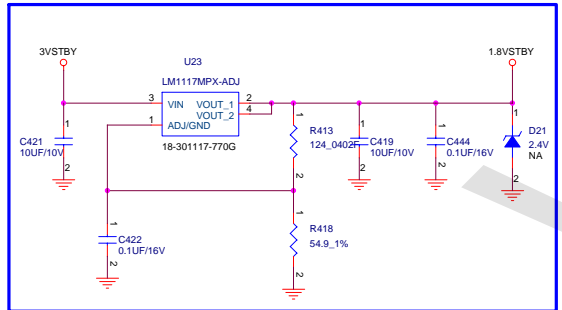


3VSTBY / 3Amax

5V / 5Amax



1.8VSTBY



When the 3.3V main output voltage is above the 2.91V, an internal 1.5ohm P-channel MOSFET switch connects OUT3 to LDO3, while simultaneously shutting down the LDO3 linear regulator.

-SKIP connect to:
Floating for ultrasonic mode
VCC for PWM mode
GND for normal idle-mode

33_3VSTBY_5VS_1.8VS

Arima		ARIMA COMPUTER CORP.	
Project Name :	W340UI	Title :	3VSTBY_5V
Size :	Document Number :	Rev :	B
Custom :	40GAB1230-B100	Sheet :	33 of 39
Date: Wednesday, June 21, 2006			

Modify 0609_POWER

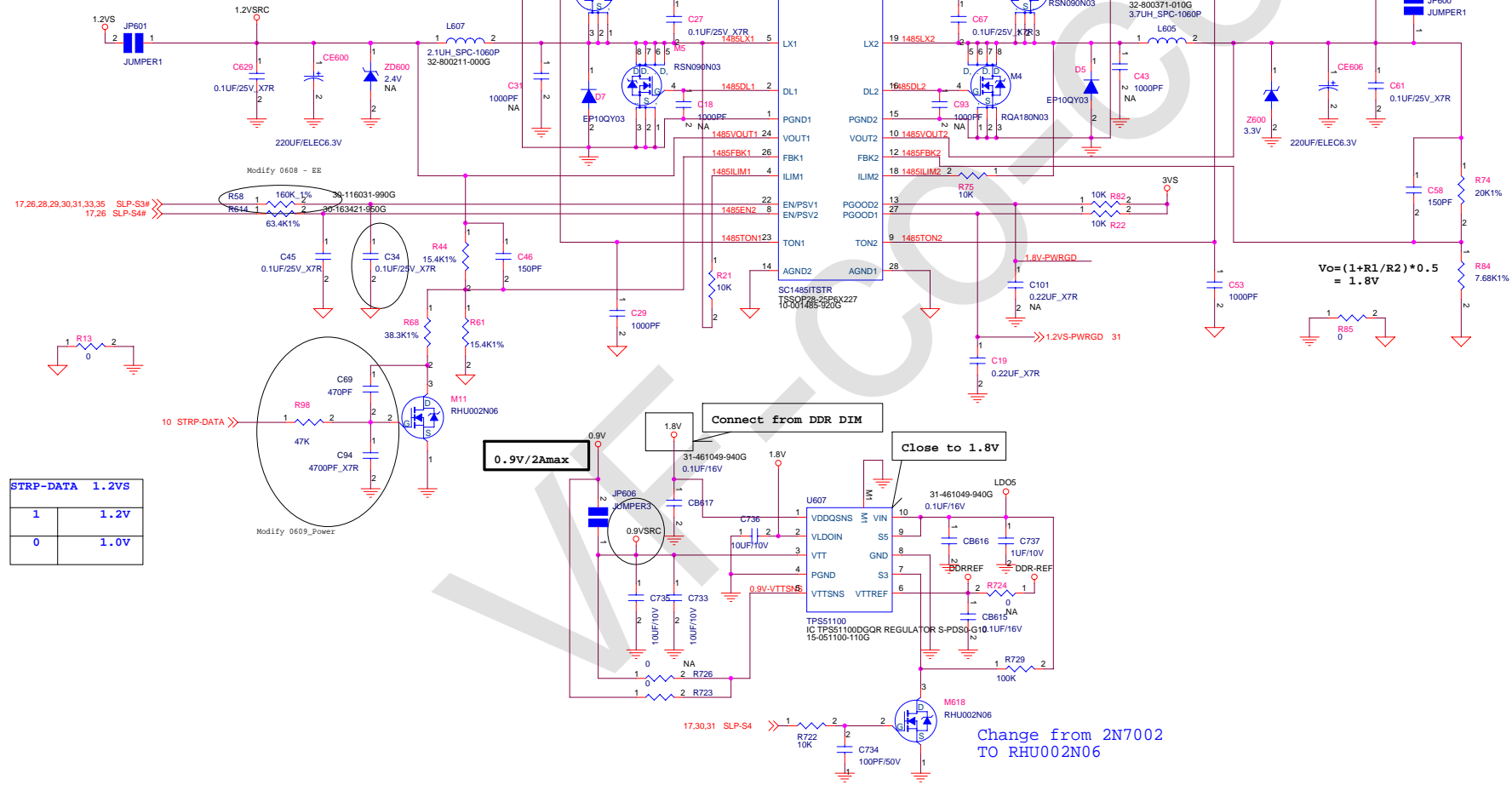
1.2V/5A max

1.8V/8Amax

0.9V/2Amax

Close to 1.8V

Change from 2N7002 TO RHU002N06



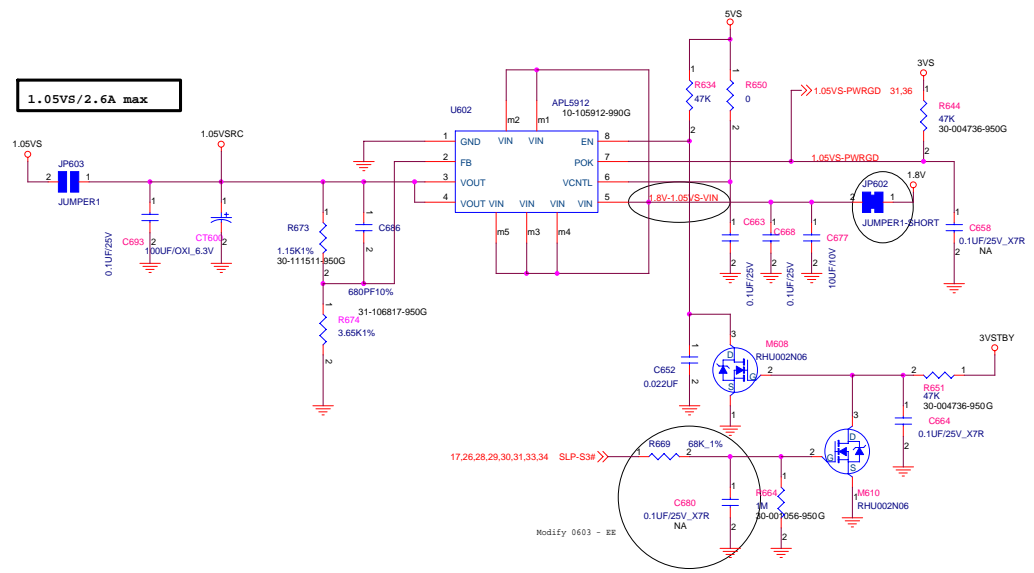
STRP-DATA 1.2VS	
1	1.2V
0	1.0V

34_1.2VS_1.8V_0.9V

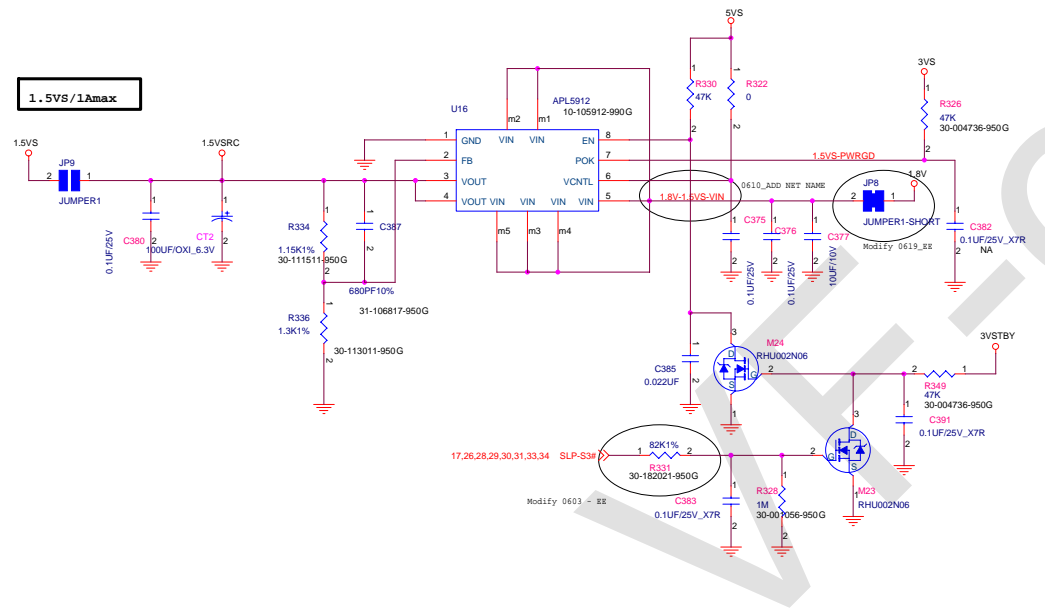
Arima ARIMA COMPUTER CORP.

Project Name: W340UI	Title: 1.8V_0.9V_1.2V
Size: Custom	Document Number: 40GAB1230-B100
Date: Wednesday, June 21, 2006	Rev: B
Sheet: 34 of 39	

1.05VS/2.6A max

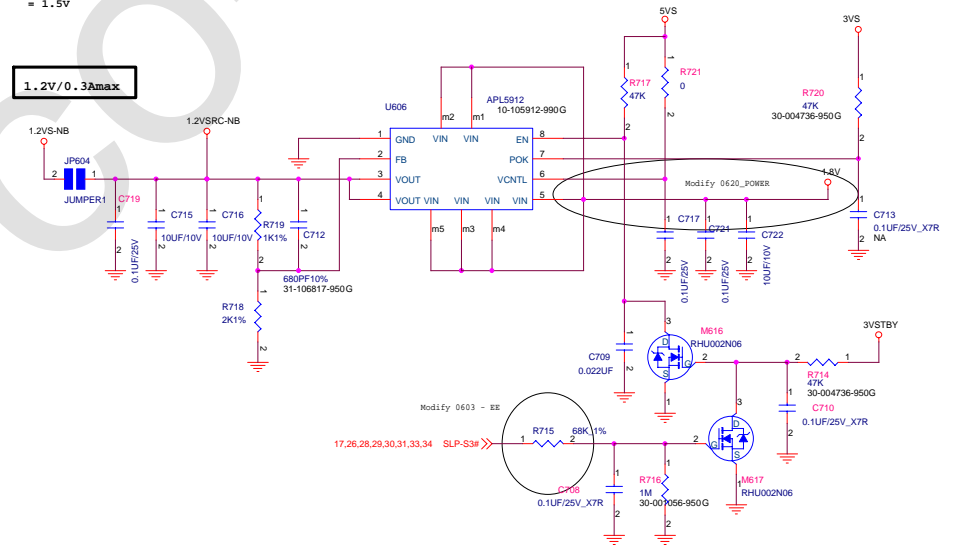


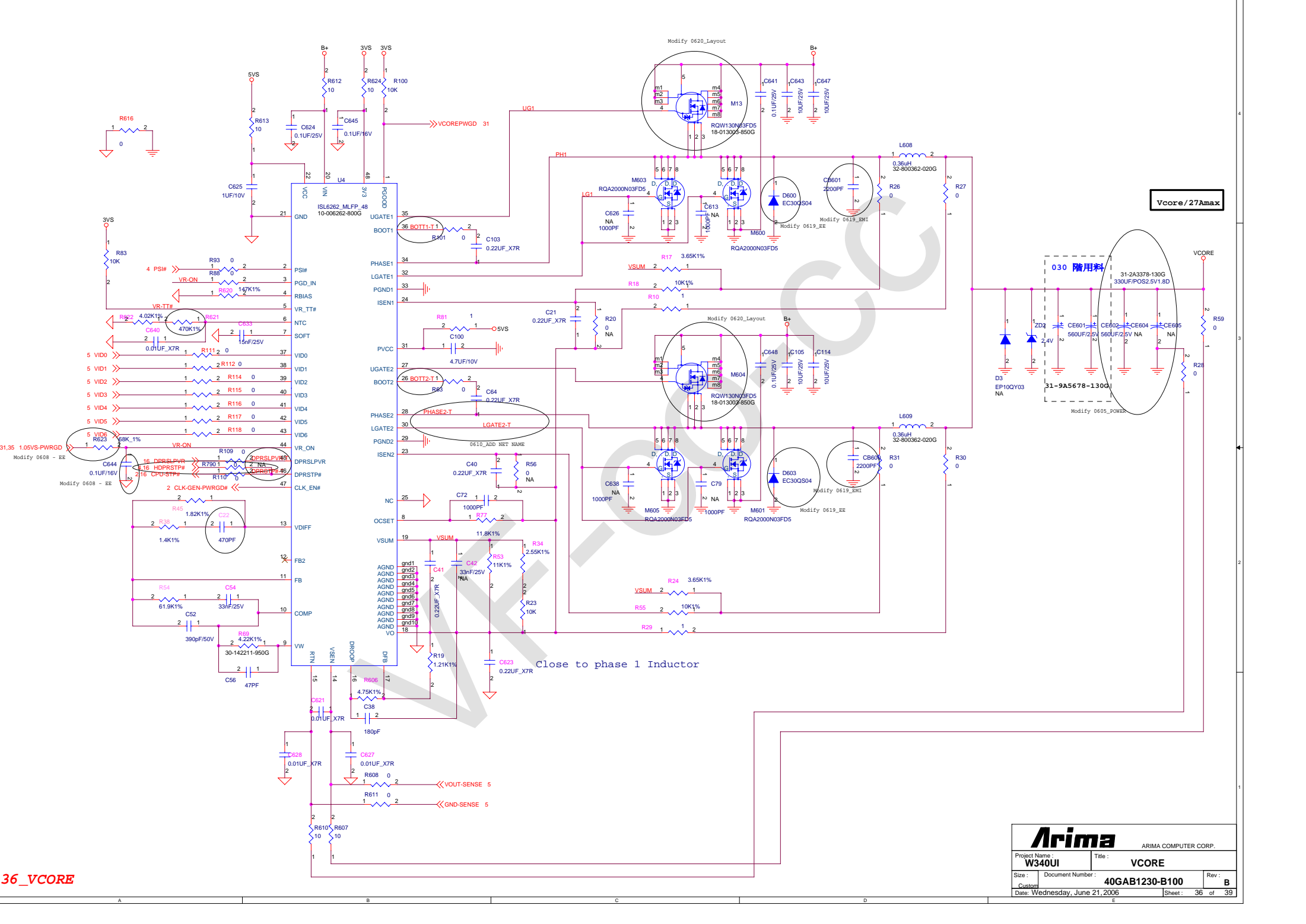
1.5VS/1Amax



$$V_o = (1 + R1/R2) * 0.5 = 1.5V$$

1.2V/0.3Amax



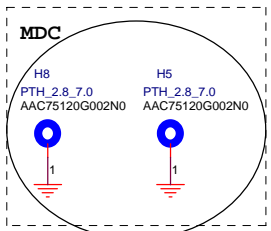
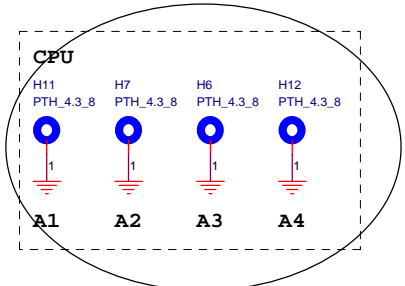
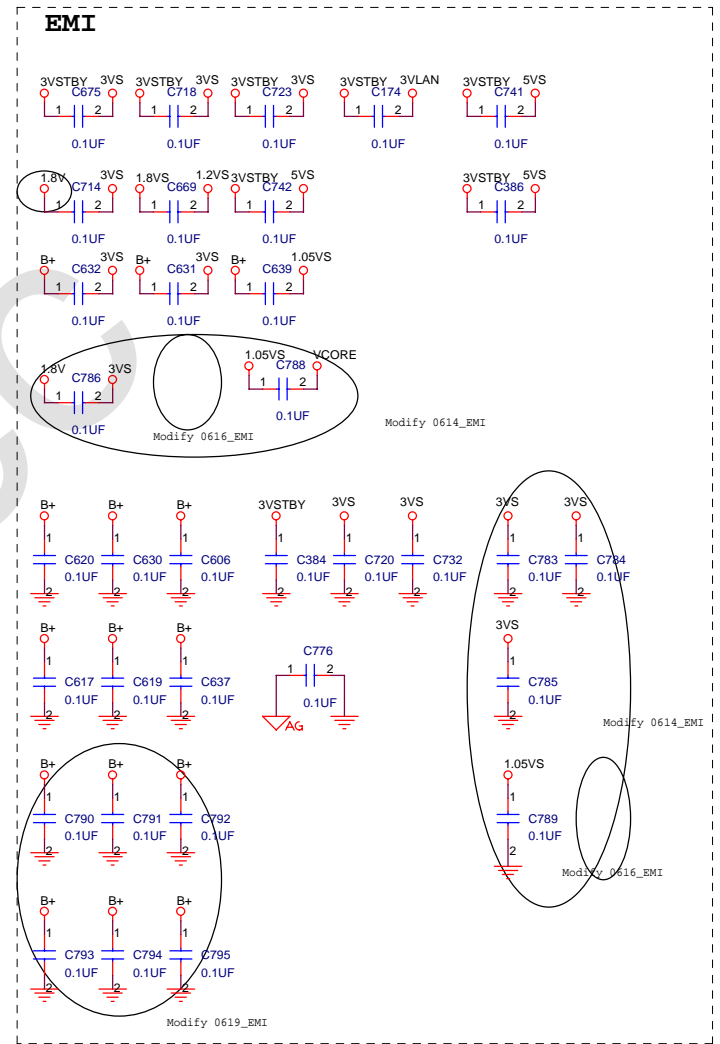
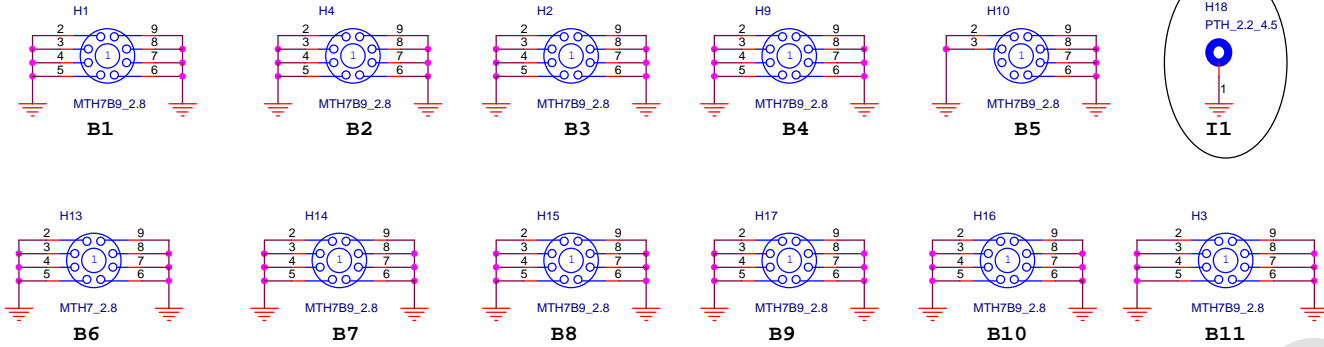


Vcore/27Amax

030 階用料

Close to phase 1 Inductor

Arima		ARIMA COMPUTER CORP.	
Project Name :	W340UI	Title :	VCORE
Size :	Custom	Document Number :	40GAB1230-B100
Date :	Wednesday, June 21, 2006	Rev :	B
			Sheet : 36 of 39



FID7 FIDUCIAL 本PIN不可接任何信號或電源 CAD-016 FIDUCIAL MARK 光學定位用	FID603 FIDUCIAL 本PIN不可接任何信號或電源 CAD-016 FIDUCIAL MARK 光學定位用	FID1 FIDUCIAL 本PIN不可接任何信號或電源 CAD-016 FIDUCIAL MARK 光學定位用	FID600 FIDUCIAL 本PIN不可接任何信號或電源 CAD-016 FIDUCIAL MARK 光學定位用	Top Layer
FID3 FIDUCIAL 本PIN不可接任何信號或電源 CAD-016 FIDUCIAL MARK 光學定位用	FID601 FIDUCIAL 本PIN不可接任何信號或電源 CAD-016 FIDUCIAL MARK 光學定位用	FID5 FIDUCIAL 本PIN不可接任何信號或電源 CAD-016 FIDUCIAL MARK 光學定位用	FID602 FIDUCIAL 本PIN不可接任何信號或電源 CAD-016 FIDUCIAL MARK 光學定位用	

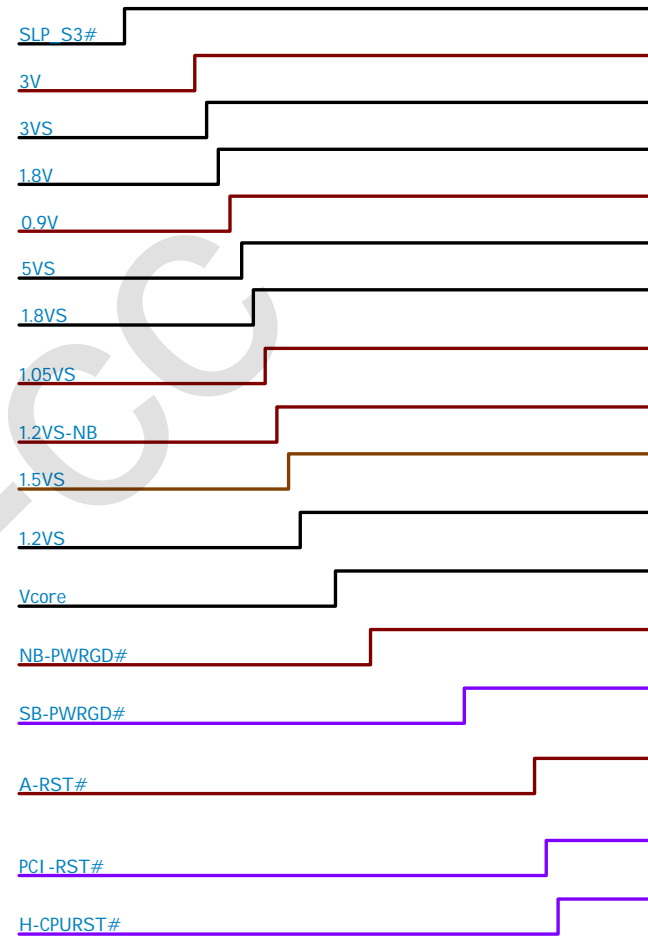
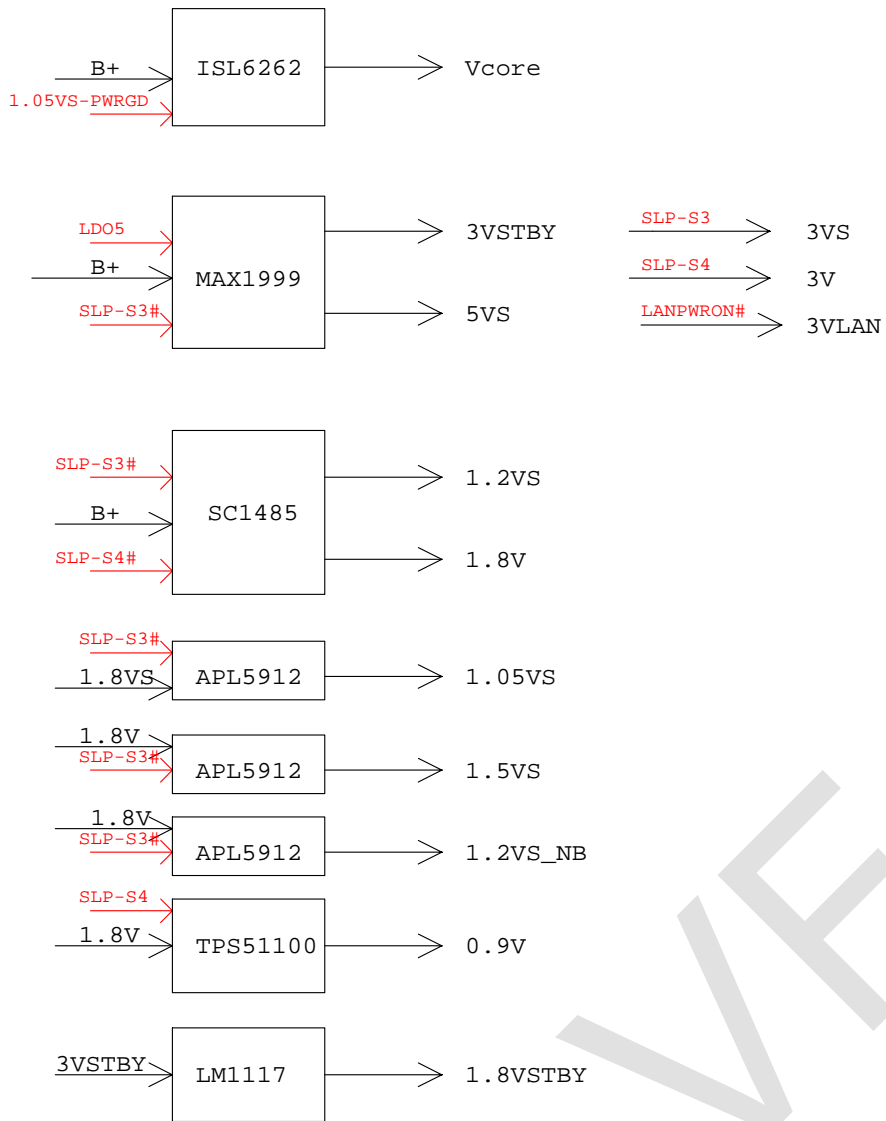
94.12.28 Modified

Arima ARIMA COMPUTER CORP.

Project Name : **W340UI** Title : **MOUNTING HOLE**

Size : Custom Document Number : **40GAB1230-B100** Rev : **B**

Date : Wednesday, June 21, 2006 Sheet : 37 of 39



Rev.A to Rev.B MODIFY LIST A: ADD D:DEL M:Modify #:DEL ITEM

2006/05/18 01.Page 07 M R648 0603 change to 0402
 02.Page 07 M R643 0402 change to 0603
 03.Page 37 M H11, H7, H6, H12 PTH9_5 change to PTH_4.3_8
 04.Page 37 M H8,H5 PTH_6_1.2 change to PTH_2.8_7.0
 05.Page 37 A H18 PTH_2.2_4.5

2006/06/05 02.Page 07 M R269 300Kohm change to 240Kohm

05.Page 09 A C778 2.2uF
 06.Page 10 A R779 4.7Kohm Pull Low
 07.Page 10 D Q8, R189 NA
 08.Page 10 M Q7 (BJT) change to M625 (2N7002)
 09.Page 15 M CN1 Modify direction for Conn

12.Page 16 A R780 20Mohm
 13.Page 16 M Y600 35-327682-722G change to 35-327682-700G

15.Page 16 A R781 0ohm
 16.Page 16 A R787 10Kohm - Delay SB-STPCLK#
 17.Page 16 A D613 DIODE 1SS355 - Delay SB-STPCLK#
 18.Page 16 A C780 330PF/50V_NPO - Delay SB-STPCLK#
 19.Page 16 A Q603 MMBT3904 - Delay SB-STPCLK#
 20.Page 16 A R786 10Kohm Pull 3VS - Delay SB-STPCLK#
 21.Page 16 A M628 RHU002N06 - Delay SB-STPCLK#
 22.Page 17 D RP601 Pin6 (MD-ID1)
 23.Page 17 D RP2 Pin6 (MD-ID0)

25.Page 17 A RJ601 10Kohm_A - MB Revision
 26.Page 17 A RJ602 10Kohm_B - MB Revision
 27.Page 17 A RJ603 10Kohm_A - LOGO Select
 28.Page 17 A PAD616

30.Page 22 M CN608 Pin34 connect to CN612 Pin32 (PDIAGR#)
 31.Page 22 M R706 10Kohm NA

33.Page 23 D CB13 & CB16
 34.Page 23 M TT600 Pin6, Pin3 connect to 2.5VLAN
 35.Page 24 M R435 560ohm change to 470ohm
 36.Page 24 M R434 560ohm change to 100ohm
 37.Page 25 M D608, D609, D610 DIODE NA
 38.Page 25 M R732 NA
 39.Page 25 A R764 100Kohm NA
 40.Page 25 A M627 2N7002 NA
 41.Page 25 A R765 470Kohm NA
 42.Page 25 A C779 0.1uF NA
 43.Page 25 A M626 MOSFET N-CH S12306DS NA
 44.Page 25 A JP609
 45.Page 28 M R421 Pin1 connect to SLP-S#3
 46.Page 31 M M621 Pin2 connect to SLP-S4
 47.Page 31 M R747 Pin1 connect to 3V
 48.Page 31 M R374 300Kohm change to 180Kohm
 49.Page 31 M R371 300Kohm change to 430Kohm
 50.Page 31 M R713 300Kohm change to 150Kohm
 51.Page 32 M R78 10Kohm change to 25.5Kohm
 52.Page 33 M R768 20Kohm change to 62Kohm
 53.Page 33 M C773 100PF change to 0.1uF
 54.Page 34 A RJ525 0ohm_A (5VS)
 55.Page 34 M D601 Pin2, C17 Pin1, C104 Pin1 connect to RJ525 Pin2

57.Page 34 M C34 0.1uF Mount

61.Page 34 M VTSRC modify to 0.9VSR
 62.Page 35 M R669 150Kohm change to 68Kohm
 63.Page 35 M C680 0.1uF NA
 64.Page 35 M R331 47Kohm change to 82Kohm
 65.Page 35 M R715 47Kohm change to 68Kohm

67.Page 36 M CE604, CE605 31-9A5678-130G change to 31-2A3378-130G

2006/06/06 01.Page 07 M RJ604 Pin2 connect to SB-STPCLK#-R , A: SB-STPCLK#
 24.Page 17 A U603 Pin E23 connect to RP601 Pin5 (3VS)
 01.Page 15 M U21 Pin2 connect to NB-PWRGD
 02.Page 27 M CN603 39-A75012-020G change to 39-A75012-030G

2006/06/08 03.Page 08 M C699, C703, C701, C700 0.1uF
 04.Page 08 M C695, C694, C698, C696 0.1uF
 29.Page 17 M R172 11.8Kohm change to 10Kohm
 56.Page 34 M R58 120Kohm change to 160Kohm

66.Page 36 A R790 0ohm HDRPRSTP # connect to U4 Pin46
 01.Page 36 M C644 100PF change to 0.1uF
 02.Page 28 M M32 2N7002 NA
 03.Page 28 M R402 100Kohm NA
 04.Page 28 M C432 0.1uF change to 0.01uF
 05.Page 28 A R791 100Kohm M32 Pin 2 & M32 Pin3
 06.Page 29 M U512 10-009750-800G change to 15-0010000001G (Max9750C)
 07.Page 29 M R392 0ohm NA
 08.Page 29 M R393 Mount - 30-000006-990G
 09.Page 29 M R395 NA
 10.Page 29 M R394 Mount - 30-000006-990G
 11.Page 29 M R398 22Kohm change to 0ohm
 12.Page 29 M R409 470Kohm change to 100Kohm
 13.Page 29 M C427 0.1UF (CAP0402) change to 0.47UF (CAP0805)
 14.Page 15 M R8 1Mohm change to 470Kohm
 15.Page 17 M R638 NA

2006/06/09 54.Page 34 # RJ525 0ohm_A (5VS)
 55.Page 34 # D601 Pin2, C17 Pin1, C104 Pin1 connect to RJ525 Pin2
 58.Page 34 M R98 1Kohm change to 47Kohm
 59.Page 34 M C69 2200PF change to 47PF
 60.Page 34 M C94 0.22UF change to 4700PF
 66.Page 29 M U24 10-009750-800G change to 15-0010000001G (Max9750C)

2006/06/13 01.Page 14 D CE611 220uF
 02.Page 25 M R733 4.7Kohm mount
 03.Page 25 M R736 4.7Kohm NA
 04.Page 25 A C781 33PF NA
 05.Page 25 M CN611 Footprint CN-W13201 change to CN-2WX1301-11
 06.Page 30 M JP10 Footprint JUMPER change to JUMPER2-SHORT
 07.Page 30 M JP7 Footprint JUMPER change to JUMPER2-SHORT
 08.Page 22 D CPOS601 & CPOS2
 09.Page 22 A CB612 31-611078-130G
 10.Page 22 A CT601 31-A11078-130G
 11.Page 14 A CPOS601 150uF

2006/06/14 10.Page 16 M C233, C239, C218, C232 0.1uF
 11.Page 16 M C210, C206, C194, C205 0.1uF
 01.Page 16 M C687 Mount - EMI
 02.Page 16 M C689 Mount - EMI
 03.Page 25 M R750 0ohm (DEL) change to L621 BEAD 120ohm - EMI
 04.Page 25 M C757 33pF change to 22pF - EMI
 05.Page 27 M C92 33pF change to 10pF - EMI
 06.Page 30 A C782 10pF - EMI
 07.Page 30 M CP1 & CP2 180pFX4 Mount - EMI
 08.Page 30 M C350, C351, C352, C353 100pF Mount - EMI
 09.Page 37 A C783, C784, C785 0.1uF (Pull 3VS) - EMI
 10.Page 23 M C98, C96 4.7uF change to 10uF
 11.Page 24 A R792 Pin1 connect U18 Pin70
 12.Page 24 A D30 Pin1 connect to R792 Pin2 (ODD-ELED#)
 13.Page 24 M M37 2N7002 NA
 14.Page 37 A C789 0.1uF (Pull 1.05VS) - EMI

16.Page 37 A C788 0.1uF between 1.05VS and VCore - EMI
 18.Page 37 A C786 0.1uF between 1.8V and 3VS - EMI
 19.Page 37 M C714 Pin1 net name Change to 1.8V from 1.8VS - EMI

2006/06/16 15.Page 37 # C790 0.1uF (Pull 1.8V) - EMI
 17.Page 37 # C787 0.1uF between 1.8V and 1.2VS - EMI
 32.Page 22 M R789 Footprint RES0402 change to RES0402-SHORT05
 01.Page 22 M D29 Pin 1 connect to M38 Pin1 (IDE-LED#)
 02.Page 24 M D30 Pin 1 connect to U18 Pin168 (SCROLL-LED#)

2006/06/19 01.Page 36 M CB600, CB601 Mount - EMI
 02.Page 37 A C790, C791, C792 0.1uF between B+ and GND - EMI
 03.Page 37 A C793, C794, C795 0.1uF between B+ and GND - EMI
 04.Page 35 M JP8, JP605 Footprint TESTPAD change to JUMPER1-SHORT
 05.Page 35 M JP602 Footprint TESTPAD change to JUMPER1-SHORT
 06.Page 36 M D600, D603 20-320340-870G change to 20-020050-870G
 07.Page 32 M J1 Footprint TESTPAD change to JUMPER1-SHORT

2006/06/20 08.Page 05 D C301, C298, C302, C192, C193
 01.Page 35 D JP605 - POWER
 02.Page 30 M R201, R637 NA
 03.Page 30 M M13, M604 Footprint MOSFET-8P-1P27 change to MOSFET-8P-1P27-M
 04.Page 32 M C622 NA

2006/06/21 01.Page 32 M C70 NA
 02.Page 21 D CE607
 03.Page 21 A C796, C797 10uF NA