

SHEET

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SHEET

TITLE

28	AZALIA ALC883
29	AUDIO JACK
30	VCORE PWM ISL6312
31	DISCRETE POWER
32	ATX POWER
33	IDE RAID IT8212
34	1394 TI TSB43AB23
35	LAN MARVELL 88E8053
36	FAN CONTROL
37	FRONT PANEL ,FUSB ,FDD

Gigabyte Technology

Title		
Cover Sheet		
Size	Document Number	Rev
Custom	965P-DS3	3.3
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Circuit or PCB layout change for next version

Component value change history

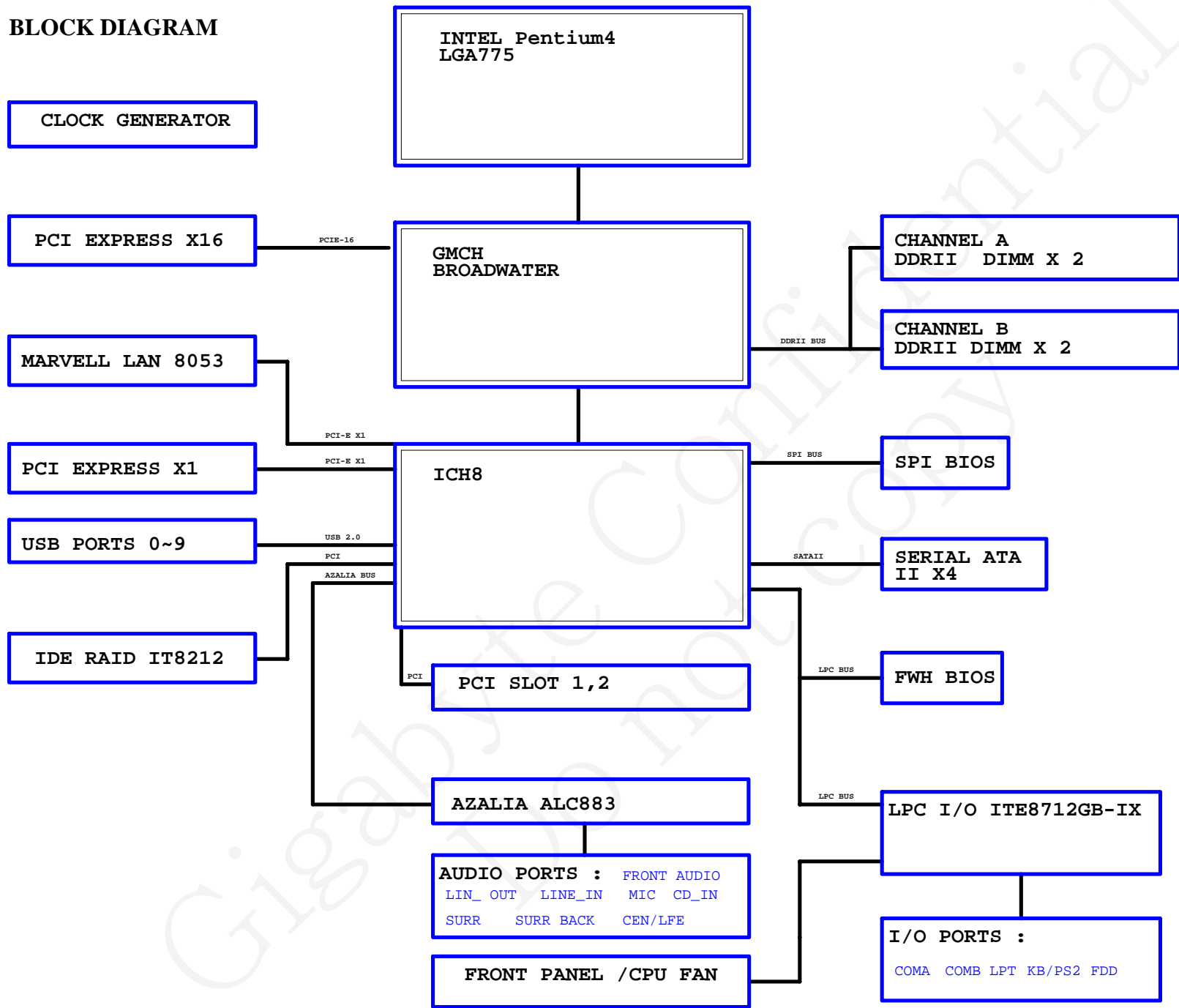
Data	Change Item	Reason
1.1A	EVT release	
1.1B	1. ADD F_AUDIO HEADPHONE FOR VISTA CR103-CR106	
2.0A	1. DVT RELEASE	
2.0B	1. SB, N/B HEATSINK CHANGE	
	2. 包材修改	
	3. PWM 阻值修改	
	4. 電阻,電容種類統一化	
	5. EC174 560uF -- >100uF	
	6. U11 加替料 WINBOND 8M FLASH:10HP4-152580-11R	
	7. USB_LAN1 加替料 UDE:11NR6-702009-09R	
	8. U49替料移除:CLK GEN ICS9LPRS587BGLF-T	
2.0C	1. 1.2uH修改料號:1.2uH/20A/PMU109/W/D	
	2. N/B , S/B HEATSINK順序修改 (965P-DS3/965G-DS3 Silent-pipe , 965P-S3 New Heatsink)	
	3 .Q330,Q331 Q_SOT23 Remove to Q330 Q_TO252	
	4 .R1955-R1958 R0402-2-SHORT10 to R0402-2	
	5. R1970 1K/4 --> 100/4/1	
	6. ADD Peci CTRL CIRCUIT	
	7. F1,F2,F3 SMD FUSE 1.1A --> 1.6A	
	8. PCB REV2.01 --> REV2.02	
2.0C-PVT	1. 包材修改	
2.0D-DVT	1. PCB REV2.02 --> REV2.03	
2.0E	1. DL2-DL7 0.3uH --> 0.4uH	
	2. DR11 3.16K/4/1 --> 3.24K/4/1	
	3. PWM FS CHANGE 200KHz DR103 82K/4 --> 120K/4	
	4. DDRVTT CHANGE R1882 1.78K/6/1 REMOVE , ADD 1876 1K/6/1	
	5. DDR18V_OV3 4.02K/6/1 --> 3.01K/6/1	
2.0E-ECN	3. PWM FS CHANGE 200KHz DR103 120K/4 --> 82K/4	
2.0F	1. PCB REV2.02 --> REV2.03	
2.0G	1. 0.4uH修改料號	
	2. DR105 加替料10RC4-002433-23R	
	3. 主料10CM2-024704-51R,加替料10CM2-024704-53R	

DATE	Change Item	Reason
1.02	EVT release	
2.0	1. PWM 3 PHASE --> 6 PHASE	
	2. SUPPORT VISTA FUNCTION	
	3. APPROVE POWER-ON SHUN DOWN ISSUE	
2.01	1. REMOVE CQ10,CR101,CR102	
	2. PWR_FAN R1814 VCC --> +12V	
	3. PWM 6 PHASE COPY FROM 946GZ-S3 Rev2.0	
	4. U11 FOOTPRINT IC8SO-SOCKET-1 --> IC8SO-SOCKET-2	
	5. EC174 EC10D8MM --> EC6D8MM,BC730 C0603 --> C0805	
2.02	1 .Q330,Q331 Q_SOT23 Remove a Q330 Q_TO252 FOR SYS_FAN POWER	
	2 .R1955-R1958 R0402-2-SHORT10 a R0402-2	
	3. ADD CPU PIN.E7=CPU_TP21	
	4. L15,L16 Footprint change to "CHOKE2U-20A-SQ-1"	
	5. CHANGE Peci CTRL CIRCUIT	
2.03	1. RU2 PIN40,41 NET CHANGE TO GND	
2.04	1. L13,L14 1.2uH Change Footprint "CHOKE08U-15A 1P-1"	
	2. L15,L15 2uH Change Footprint "CHOKE2U-20A-SQ-2"	
3.3	1. FOR FSB1333 CPU SUPPORT	
下板修改	1. 增加 CPU FSBSEL2 Pull uP 電阻	
BOM		
2.0H	1. PCB REV2.03 --> REV 2.04	
	2. 470uF/6.3V & 560uF/4V --> 560uF/6.3V(僅試產,量產版本要改回來)	
	3. D3,D9,CD1,CD2,PD1 10DS1-124148-04R/05R--> 10DS7-734148-01R/02R (僅試產,量產版本要改回來)	
	4. U54 M8056/A2 --> M8056/B0	
3.3A	1. FOR FSB1333 CPU SUPPORT	
	BOM要再建立9M965PS3-00-33A	
3.3B	1. VTT_GMCH Q329 AP40T03H/TO252/655pF/25m-->2SK3918/TO252	
	2. U51,U53 REMOVE LD7120	
3.3C	1. U54 8056 REV.A2 --> B0	
	2. CD DRIVER REV1.02 --> REV1.03	

Gigabyte Technology

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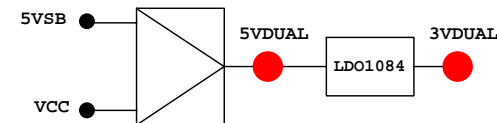
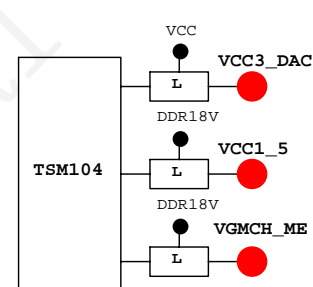
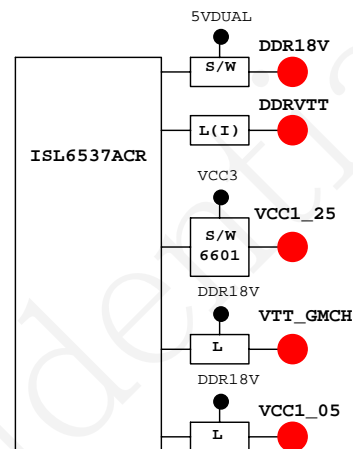
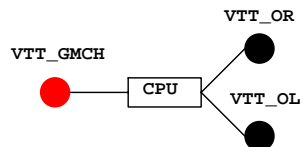
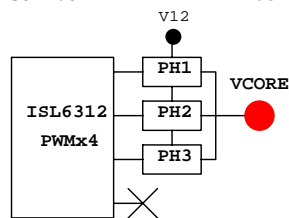
BLOCK DIAGRAM



ICH8 GPIO LIST TABLE

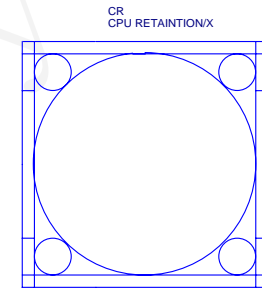
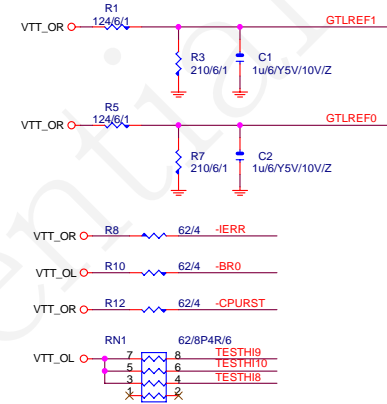
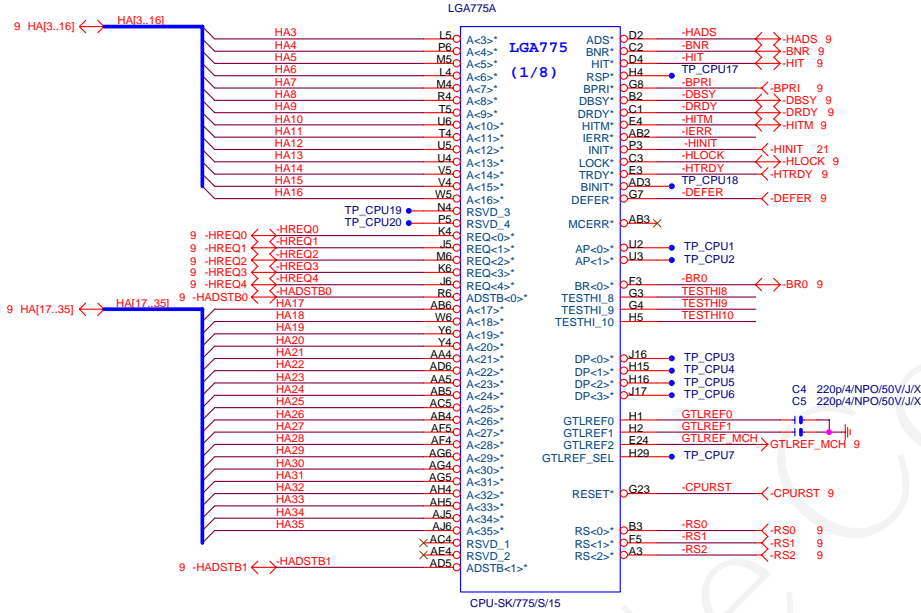
PIN NAME	PWR WELL	AFTER/ PLTRST	USAGE	NOTE
GP0	MAIN	IN	-ACZ_DET	P/U 8.2K VCC3
GP1/TACH1	MAIN	IN	ICH_FAN_TACH1	P/U 8.2K VCC3
GP2/PIRQE#	MAIN	IN	-PIRQE	P/U 8.2K VCC3
GP3/PIRQF#	MAIN	IN	-PIRQF	P/U 8.2K VCC3
GP4/PIRQG#	MAIN	IN	-PIRQG	P/U 8.2K VCC3
GP5/PIRQH#	MAIN	IN	-PIRQH	P/U 8.2K VCC3
GP6/TACH2	MAIN	IN	ICH_FAN_TACH2	P/U 8.2K VCC3
GP7/TACH3	MAIN	IN	ICH_FAN_TACH3	P/U 8.2K VCC3
GP8	STBY	IN	GPIO8(DUALBIOS_INPUT)	P/U 8.2K 3VDUAL
GP9	STBY	OUT	WOL_ONLY	P/D 100K GND
GP10	STBY	IN	CLGPIO1	P/U 8.2K 3VDUAL
GP11/SMBALERT#	STBY	OUT	-SMBALRT	P/U 8.2K 3VDUAL
GP12	STBY	IN	MB_ID0	P/U 8.2K 3VDUAL
GP13	STBY	IN	-LPCPME	P/U 8.2K 3VDUAL
GP14	STBY	IN	CLGPIO2	P/U 8.2K 3VDUAL
GP15	STBY	OUT	LAN_DISABLE(STP_PCI-)	N/A
GP16	MAIN	OUT/LOW	RESET	N/A
GP17/TACH0	MAIN	IN	ICH_FAN_TACH0	P/U 8.2K VCC3
GP18	MAIN	OUT	MB_ID1	P/U 8.2K VCC3
GP19	MAIN	IN	SATA1GP	P/U 8.2K VCC3
GP20	MAIN	OUT	-SPI_WP0	P/U 1K 3VCL
GP21	MAIN	IN	SATA0GP	P/U 8.2K VCC3
GP22	MAIN	IN	SCLOCK	P/U 8.2K VCC3
GP23	MAIN	OUT	-LDRQ1	P/U 8.2K VCC3
GP24	STBY	OUT	CLGPIO0	P/U 8.2K 3VDUAL
GP25	STBY	IN	MB_ID2(STP_CPU-)	P/U 8.2K 3VDUAL
GP26/S4_STATE#	STBY	OUT	S4_STATE#	P/U 8.2K 3VDUAL
GP27	STBY	OUT/LOW	GPIO27(EL_STATE0)	P/U 8.2K 3VDUAL
GP28	STBY	OUT/LOW	PWR_LED(EL_STATE1)	N/A
GP29/OC5#	STBY	IN	-USBOC_R	P/U FUSEVCC
GP30/OC6#	STBY	IN	-USBOC_R	P/U FUSEVCC
GP31/OC7#	STBY	IN	-USBOC_R	P/U FUSEVCC
GP32	MAIN	OUT	DUAL BIOS	P/U 100K+1M VCC3
GP33	MAIN	OUT	DUAL BIOS	P/U 8.2K VCC3
GP34	MAIN	OUT/LOW	GPIO34/SMB_RST	N/A
GP35	MAIN	OUT	SATACLKREQ#	N/A
GP36	MAIN	IN	SATA2GP	P/U 8.2K VCC3
GP37	MAIN	IN	SATA3GP	P/U 8.2K VCC3
GP38	MAIN	IN	SLOAD	P/U 8.2K VCC3
GP39	MAIN	IN	GPIO39	P/D 8.2K GND
GP48	MAIN	IN	GPIO48	P/U 8.2K VCC3
GP49	MAIN	IN	CPUPWROK	P/U 100 VTT_OL

VCORE:3 PHASE PWM--ISL6312

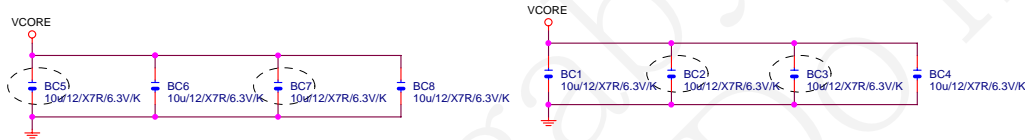


Gigabyte Technology			
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Size	Document Number	Rev	
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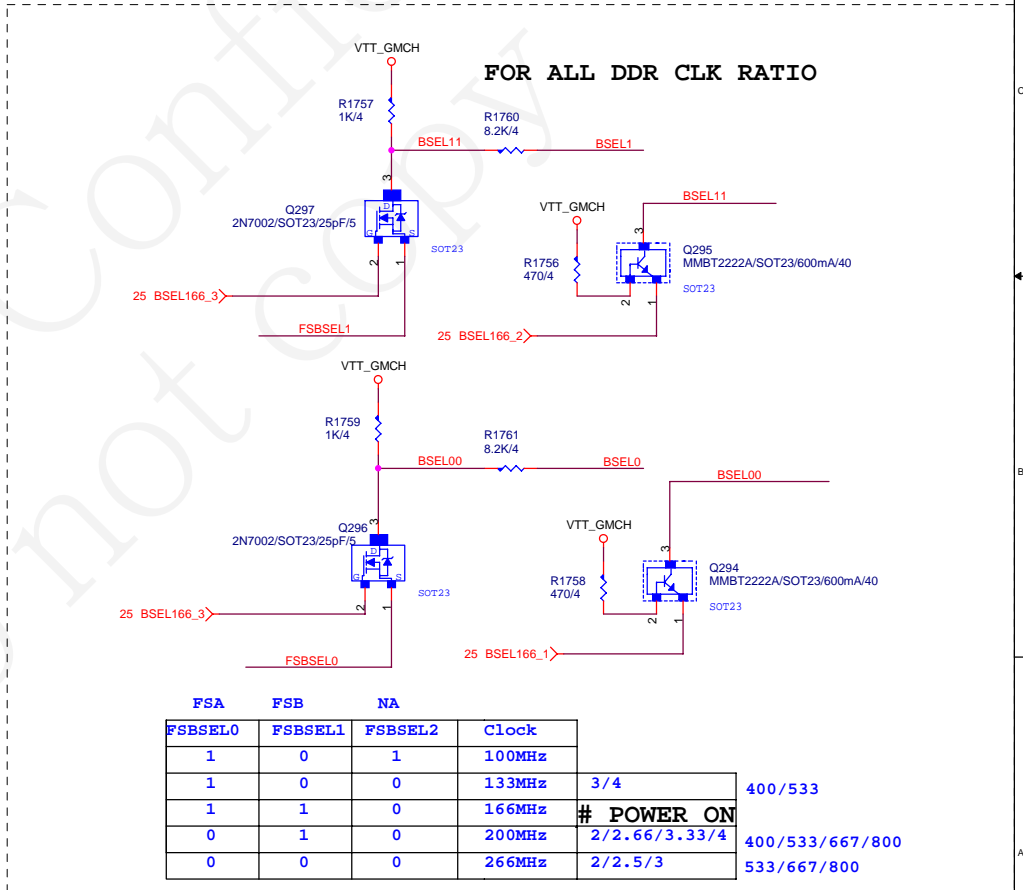
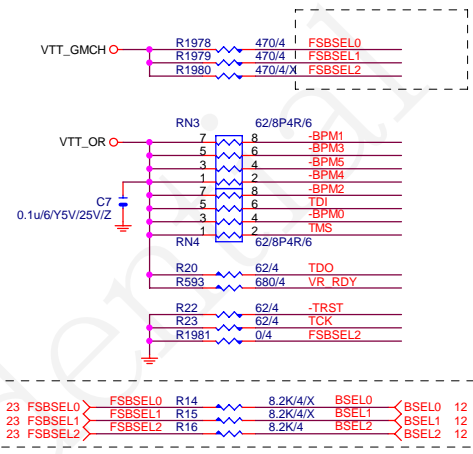
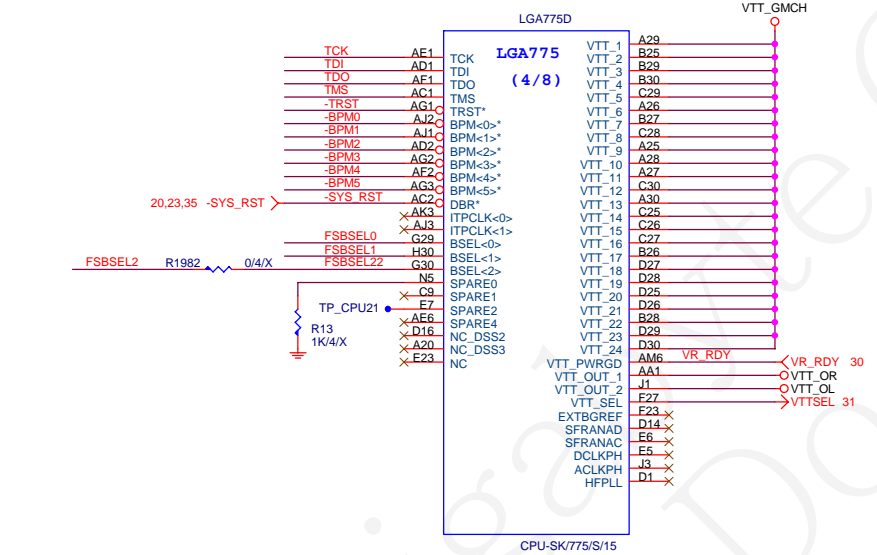
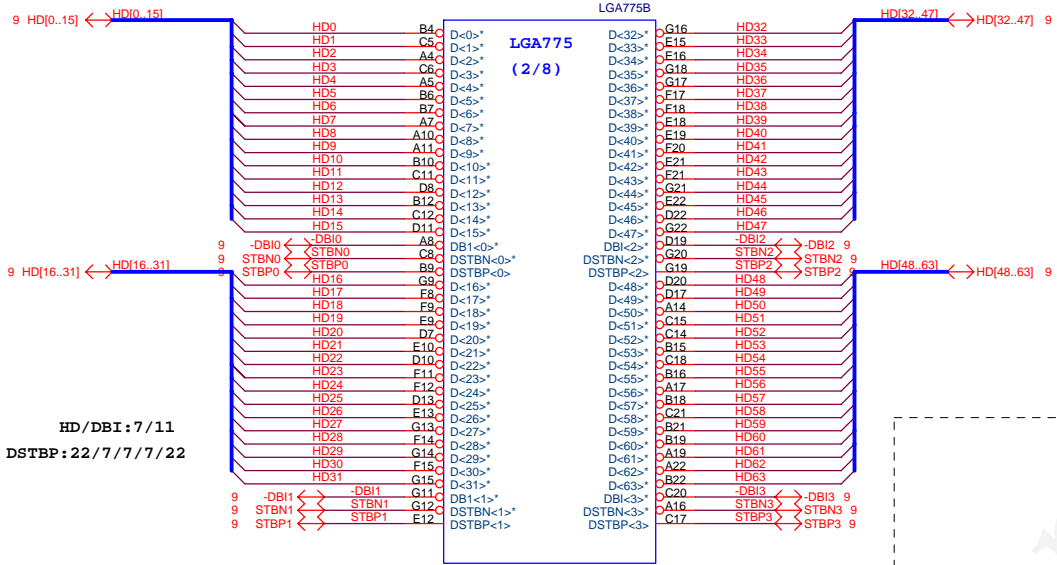
HA/REQ: 4/14
ADSTB: 4/17

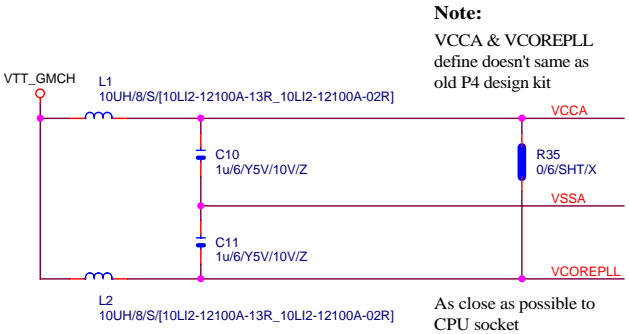


Impedance=50 +- 15% for 4-layer

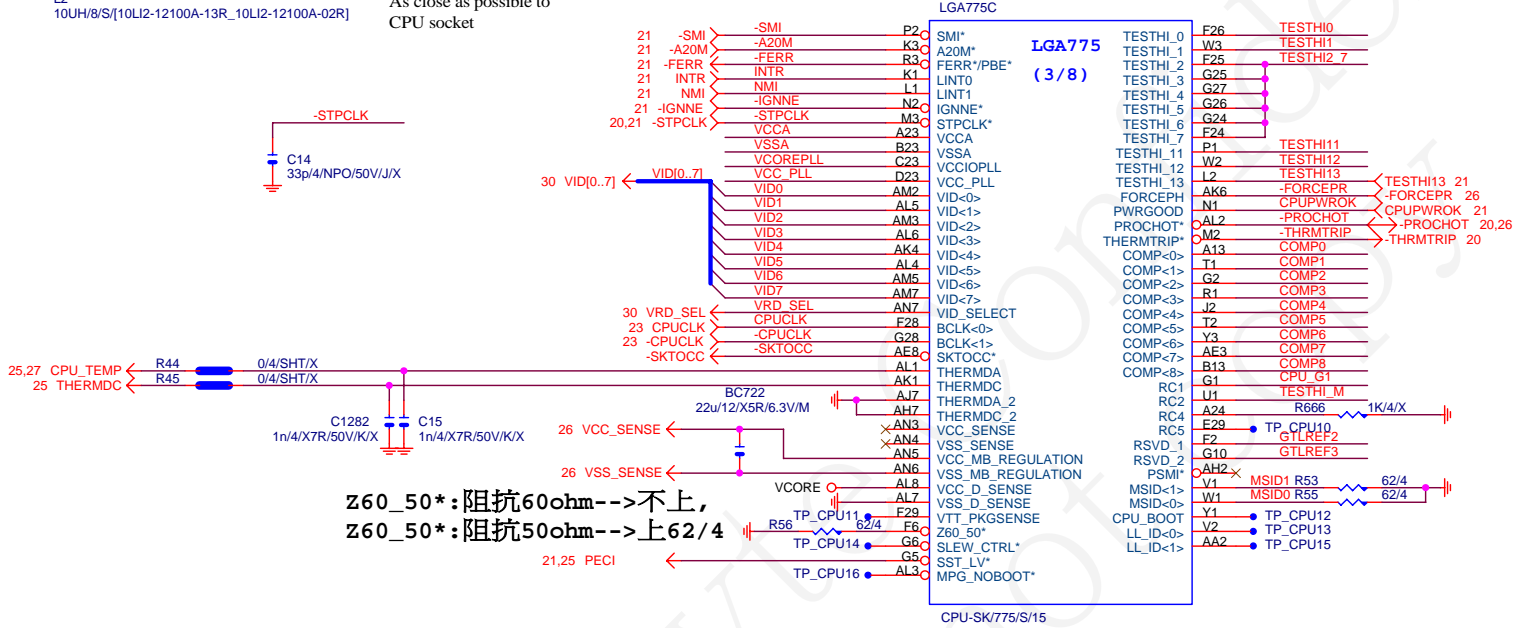
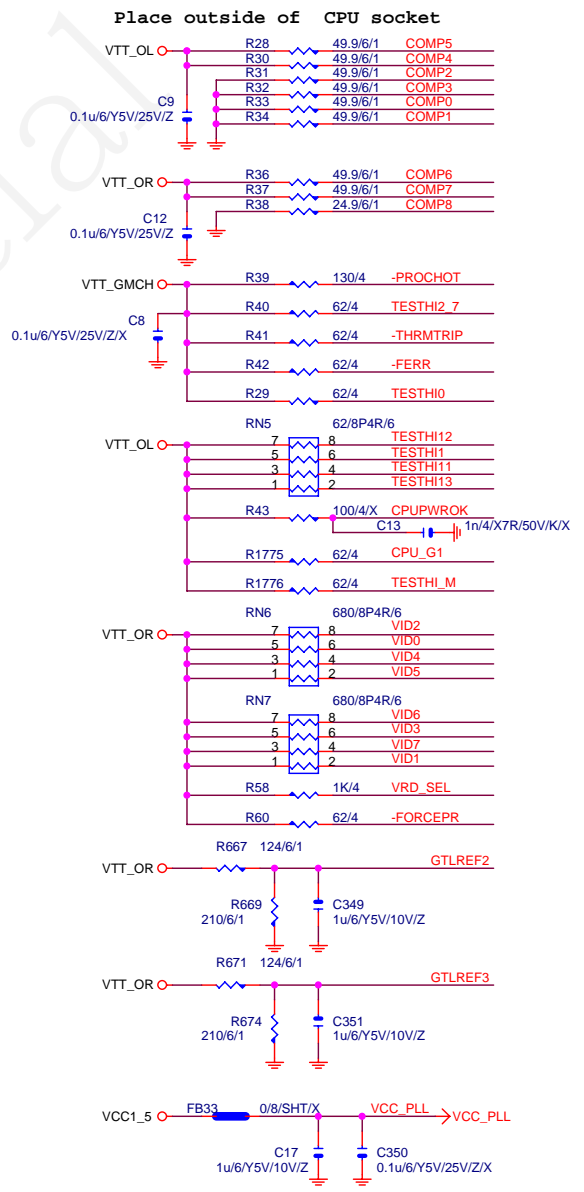


Gigabyte Technology		
Title		
P4_LGA775-A		
Size	Document Number	Rev
Custom	965P-DS3	3.3
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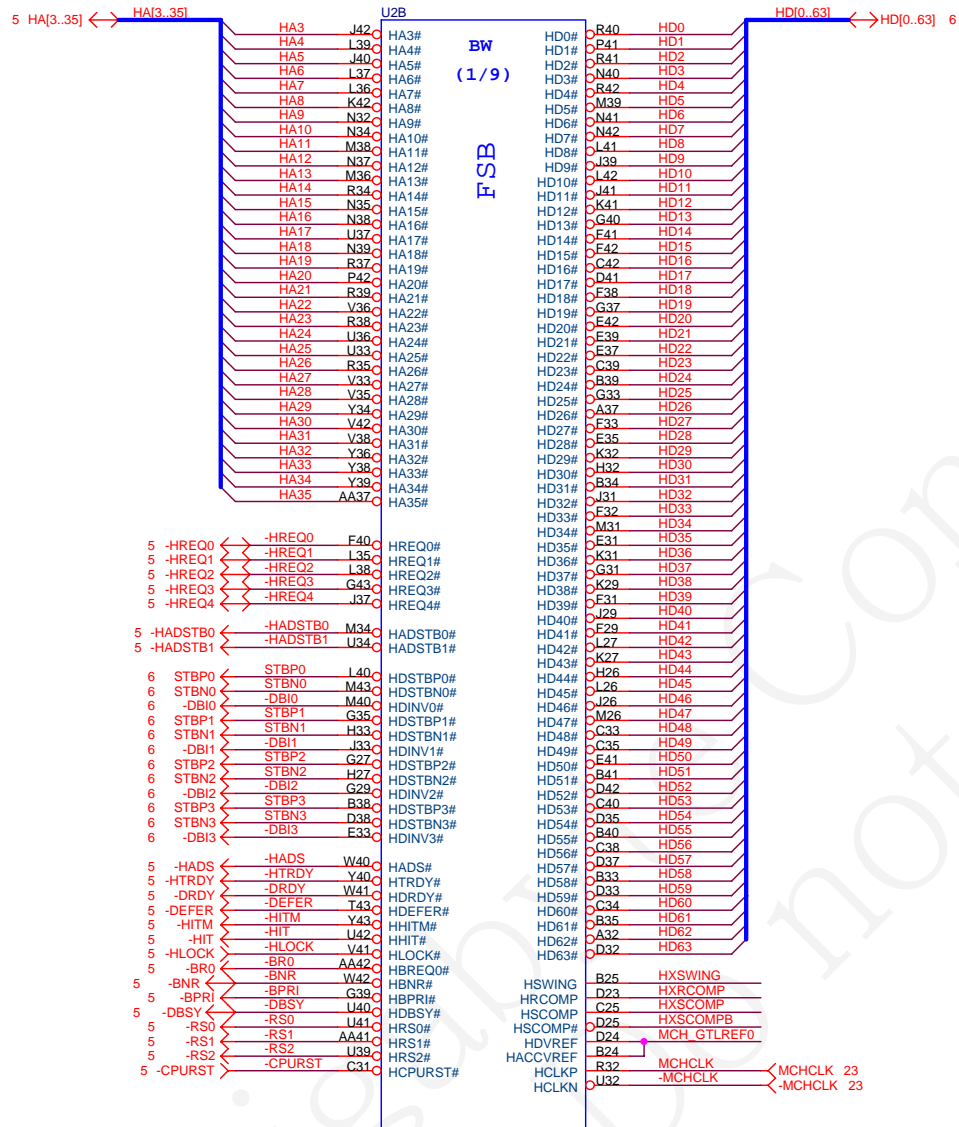
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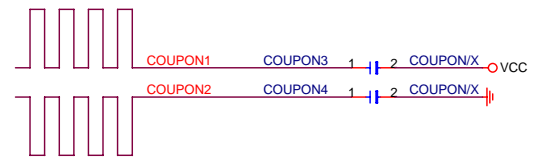
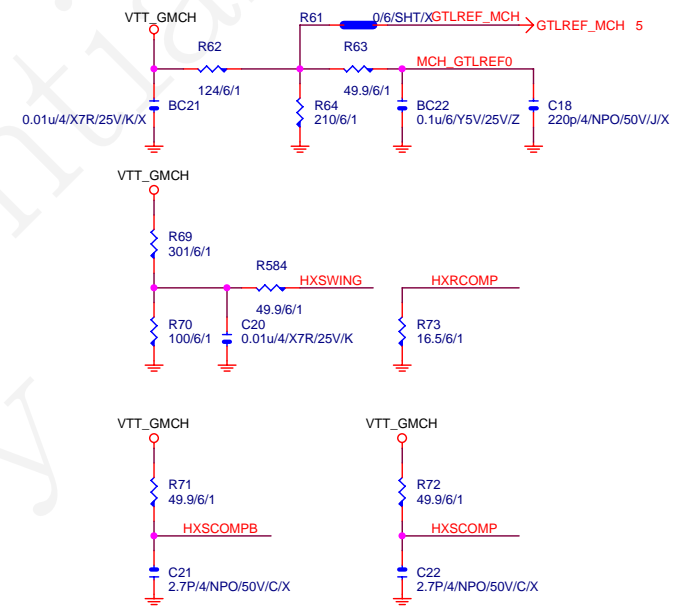
Z60_50*: 阻抗60ohm-->不上,
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Gigabyte Technology		
Title P4_LGA775-C		
Size B	Document Number 965P-DS3	Rev 3.3
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PECl: Platform Environment Control Interface



LE82P965-C2/BAG1226
 CPU INTERFACE



Gigabyte Technology		
Title GMCH-HOST		
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U2C

MAAA0	BA31	SMA_A0	BW	SDQS_A0	AU4	DOSA0
MAAA1	BA25	SMA_A1		SDQS_A0#	AR3	-DOSA0
MAAA2	BA26	SMA_A2	(3/9)	SDM_A0	AR2	DMA0
MAA03	BA25	SMA_A3				
MAAA4	AY25	SMA_A4		SDQ_A0	AR6	MDA0
MAA05	BA23	SMA_A5		SDQ_A1	AR4	MDA1
MAA06	AY24	SMA_A6		SDQ_A2	AV2	MDA3
MAA07	AY23	SMA_A7		SDQ_A3	AV2	MDA3
MAA08	BA22	SMA_A8		SDQ_A5	AP3	MDA5
MAA09	BA22	SMA_A9		SDQ_A6	AP2	MDA5
MAAA10	AY33	SMA_A10		SDQ_A6	AU1	MDA6
MAA11	BA22	SMA_A11		SDQ_A7	AV4	MDA7
MAAA12	AW21	SMA_A12				
MAAA13	AY38	SMA_A13		SDQS_A1	BB3	DOSA1
MAAA14	BA21	SMA_A14		SDQS_A1#	BA4	DMA1
SDM_A1						
15.17 -SWEA	-SWEA	SWE_A#		SDQ_A8	AY2	MDA8
15.17 -SCASA	-SCASA	SCAS_A#		SDQ_A9	AY3	MDA9
15.17 -SRASA	-SRASA	SRAS_A#		SDQ_A10	BB6	MDA10
15.17 SBAA0	SBA00	SBS_A0		SDQ_A10	AY8	MDA11
15.17 SBAA1	SBA01	SBS_A1		SDQ_A11	AW2	MDA12
15.17 SBAA2	SBA02	SBS_A2		SDQ_A13	AW3	MDA13
15.17 CSA0	-CSA0	SCS_A0#		SDQ_A14	BA5	MDA14
15.17 CSA1	-CSA1	SCS_A1#		SDQ_A15	BB4	MDA15
15.17 CSA2	-CSA2	SCS_A2#		SDQS_A2	BB6	DOSA2
15.17 CSA3	-CSA3	SCS_A3#		SDQS_A2#	BA9	-DOSA2
15.17 CKEA0	-CKEA0	SCKE_A0		SDM_A2	AY9	DMA2
15.17 CKEA1	-CKEA1	SCKE_A1		SDQ_A16	AY7	MDA16
15.17 CKEA2	-CKEA2	SCKE_A2		SDQ_A17	BC7	MDA17
15.17 CKEA3	-CKEA3	SCKE_A3		SDQ_A18	AW11	MDA18
15.17 CKEA3	-CKEA3	SCKE_A3		SDQ_A19	AY11	MDA19
MODT_A0	AY37	SODT_A0		SDQ_A20	BB6	MDA20
MODT_A1	BA38	SODT_A1		SDQ_A21	BA6	MDA21
MODT_A2	BB35	SODT_A2		SDQ_A21	BA10	MDA22
MODT_A3	BA39	SODT_A3		SDQ_A23	BB10	MDA23

15 DCLKA0	-DCLKA0	SCLK_A0#		SDQS_A3	AT20	DOSA3
15 DCLKA1	-DCLKA1	SCLK_A1#		SDQS_A3#	AU18	-DOSA3
15 DCLKA1	-DCLKA1	SCLK_A1#		SDM_A3	AN18	DMA3
15 DCLKA1	-DCLKA1	SCLK_A1#				
15 DCLKA2	-DCLKA2	SCLK_A2#		SDQ_A24	AT18	MDA24
15 DCLKA2	-DCLKA2	SCLK_A2#		SDQ_A25	AR18	MDA25
15 DCLKA3	-DCLKA3	SCLK_A3#		SDQ_A26	AU21	MDA26
15 DCLKA3	-DCLKA3	SCLK_A3#		SDQ_A27	AT21	MDA27
15 DCLKA4	-DCLKA4	SCLK_A4#		SDQ_A27	AP17	MDA28
15 DCLKA4	-DCLKA4	SCLK_A4#		SDQ_A28	DCLKB4	MDA28
15 DCLKA4	-DCLKA4	SCLK_A4#		SDQ_A29	AN17	MDA29
15 DCLKA5	-DCLKA5	SCLK_A5#		SDQ_A30	AP20	MDA30
15 DCLKA5	-DCLKA5	SCLK_A5#		SDQ_A31	AV20	MDA31
SDQS_A4	AR41	DOSA4				
SDQS_A4#	AR40	-DOSA4				
SDM_A4	AU43	DMA4				
SDQ_A32	AV42	MDA32				
SDQ_A33	AU40	MDA33				
SDQ_A34	AP42	MDA34				
SDQ_A35	AN39	MDA35				
SDQ_A36	AV40	MDA36				
SDQ_A37	AV41	MDA37				
SDQ_A38	AB42	MDA38				
SDQ_A39	AP41	MDA39				
SDQS_A5	AL41	DOSA5				
SDQS_A5#	AL40	-DOSA5				
SDM_A5	AM43	DMA5				
SDQ_A40	AN41	MDA40				
SDQ_A41	AM39	MDA41				
SDQ_A42	AK42	MDA42				
SDQ_A43	AK41	MDA43				
SDQ_A44	AN40	MDA44				
SDQ_A45	AM42	MDA45				
SDQ_A46	AL42	MDA46				
SDQ_A47	AL39	MDA47				
SDQS_A6	AG42	DOSA6				
SDQS_A6#	AG41	-DOSA6				
SDM_A6	AG40	DMA6				
SDQ_A48	AL40	MDA48				
SDQ_A49	AH43	MDA49				
SDQ_A50	AF39	MDA50				
SDQ_A51	AE40	MDA51				
SDQ_A52	AJ42	MDA52				
SDQ_A53	AK41	MDA53				
SDQ_A54	AE41	MDA54				
SDQ_A55	AE42	MDA55				
SDQS_A7	AC42	DOSA7				
SDQS_A7#	AC41	-DOSA7				
SDM_A7	AC40	DMA7				
SDQ_A56	AD40	MDA56				
SDQ_A57	AD43	MDA57				
SDQ_A58	AB41	MDA58				
SDQ_A59	AE42	MDA59				
SDQ_A60	AE42	MDA60				
SDQ_A61	AE41	MDA61				
SDQ_A62	AG39	MDA62				
SDQ_A63	AB42	MDA63				

DDR_0

LE82P965-C2/BAG1226

DDR INTERFACE

U2D

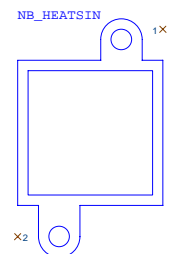
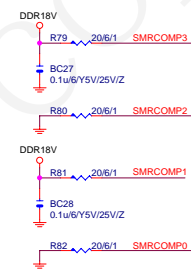
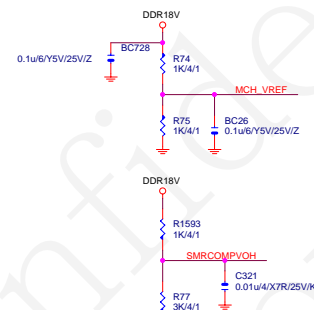
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MAAB2	BA17	SMA_B2	(4/9)	SDM_B0	AR7	DMA0
MAAB3	BC16	SMA_B3				
MAAB4	AW15	SMA_B4		SDQ_B0	AN7	MDB0
MAAB5	BA15	SMA_B5		SDQ_B1	AN6	MDB1
MAAB6	BB15	SMA_B6		SDQ_B2	AN6	MDB2
MAAB7	BA14	SMA_B7		SDQ_B3	AW7	MDB3
MAAB8	BB14	SMA_B8		SDQ_B4	AN6	MDB5
MAAB9	BA14	SMA_B9		SDQ_B5	AN9	MDB6
MAAB10	AW18	SMA_B10		SDQ_B6	AN9	MDB6
MAAB11	BB14	SMA_B11		SDQ_B7	AU7	MDB7
MAAB12	BB13	SMA_B12				
MAAB13	AY23	SMA_B13		SDQS_B1	AR12	DOSA1
MAAB14	AY13	SMA_B14		SDQS_B1#	AR12	-DOSA1
SDM_B1				SDM_B1	AW2	DMA1
15.17 -SWEB	-SWEB	SWE_B#		SDQ_B8	AT11	MDB8
15.17 -SCASB	-SCASB	SCAS_B#		SDQ_B9	AU11	MDB9
15.17 -SRASB	-SRASB	SRAS_B#		SDQ_B9	AP13	MDB10
15.17 SBAB0	SBA00	SBS_B0		SDQ_B10	AR13	MDB11
15.17 SBAB1	SBA01	SBS_B1		SDQ_B11	AR11	MDB12
15.17 SBAB2	SBA02	SBS_B2		SDQ_B12	AU9	MDB13
15.17 CSB0	-CSB0	SCS_B0#		SDQ_B13	AY12	MDB14
15.17 CSB1	-CSB1	SCS_B1#		SDQ_B14	AU12	MDB15
15.17 CSB2	-CSB2	SCS_B2#		SDQ_B15	AP15	DOSA2
15.17 CSB3	-CSB3	SCS_B3#		SDQS_B2	AR15	-DOSA2
15.17 CKEB0	-CKEB0	SCKE_B0		SDQS_B2#	AW13	DMA2
15.17 CKEB1	-CKEB1	SCKE_B1		SDQ_B16	AU15	MDB16
15.17 CKEB2	-CKEB2	SCKE_B2		SDQ_B17	AV13	MDB17
15.17 CKEB3	-CKEB3	SCKE_B3		SDQ_B18	AU17	MDB18
15.17 CKEB3	-CKEB3	SCKE_B3		SDQ_B19	AT17	MDB19
MODT_B0	BA23	SODT_B0		SDQ_B20	AU13	MDB20
MODT_B1	BA30	SODT_B1		SDQ_B21	AM15	MDB21
MODT_B2	BB21	SODT_B2		SDQ_B21	AV15	MDB22
MODT_B3	BB31	SODT_B3		SDQ_B23	AW17	MDB23

16 DCLKB0	-DCLKB0	SCLK_B0#		SDQS_B3	AT24	DOSA3
16 DCLKB1	-DCLKB1	SCLK_B1#		SDQS_B3#	AU26	-DOSA3
16 DCLKB1	-DCLKB1	SCLK_B1#		SDM_B3	AP23	DMA3
16 DCLKB2	-DCLKB2	SCLK_B2#		SDQ_B3	AV23	MDB3
16 DCLKB2	-DCLKB2	SCLK_B2#		SDQ_B4	AV24	MDB24
16 DCLKB3	-DCLKB3	SCLK_B3#		SDQ_B4	AT23	MDB25
16 DCLKB3	-DCLKB3	SCLK_B3#		SDQ_B26	AT26	MDB26
16 DCLKB4	-DCLKB4	SCLK_B4#		SDQ_B27	AT26	MDB27
16 DCLKB4	-DCLKB4	SCLK_B4#		SDQ_B28	AV23	MDB28
16 DCLKB5	-DCLKB5	SCLK_B5#		SDQ_B29	AV23	MDB29
16 DCLKB5	-DCLKB5	SCLK_B5#		SDQ_B30	AR24	MDB30
16 DCLKB5	-DCLKB5	SCLK_B5#		SDQ_B31	AN26	MDB31
SDQS_B4	AV39	DOSA4				
SDQS_B4#	AU39	-DOSA4				
SDM_B4	AU37	DMA4				
SDQ_B32	AW37	MDB32				
SDQ_B33	AV38	MDB33				
SDQ_B34	AV36	MDB34				
SDQ_B35	AN37	MDB35				
SDQ_B36	AU35	MDB37				
SDQ_B37	AV35	MDB38				
SDQ_B38	AN35	MDB38				
SDQ_B39	AR37	MDB39				
SDQS_B5	AL35	DOSA5				
SDQS_B5#	AL34	-DOSA5				
SDM_B5	AM37	DMA5				
SDQ_B40	AM35	MDB40				
SDQ_B41	AM38	MDB41				
SDQ_B42	AJ34	MDB42				
SDQ_B43	AL39	MDB43				
SDQ_B44	AR39	MDB44				
SDQ_B45	AM54	MDB45				
SDQ_B46	AL37	MDB46				
SDQ_B47	AL32	MDB47				
SDQS_B6	AG36	DOSA6				
SDQS_B6#	AG36	-DOSA6				
SDM_B6	AG39	DMA6				
SDQ_B48	AG38	MDB48				
SDQ_B49	AJ38	MDB49				
SDQ_B50	AF36	MDB50				
SDQ_B51	AF33	MDB51				
SDQ_B52	AJ37	MDB52				
SDQ_B53	AJ35	MDB53				
SDQ_B54	AG33	MDB54				
SDQ_B55	AF34	MDB55				
SDQS_B7	AC36	DOSA7				
SDQS_B7#	AC37	-DOSA7				
SDM_B7	AD38	DMA7				
SDQ_B56	AD36	MDB56				
SDQ_B57	AC33	MDB57				
SDQ_B58	AA34	MDB58				
SDQ_B59	AF36	MDB59				
SDQ_B60	AD34	MDB60				
SDQ_B61	AF38	MDB61				
SDQ_B62	AC34	MDB62				
SDQ_B63	AA33	MDB63				

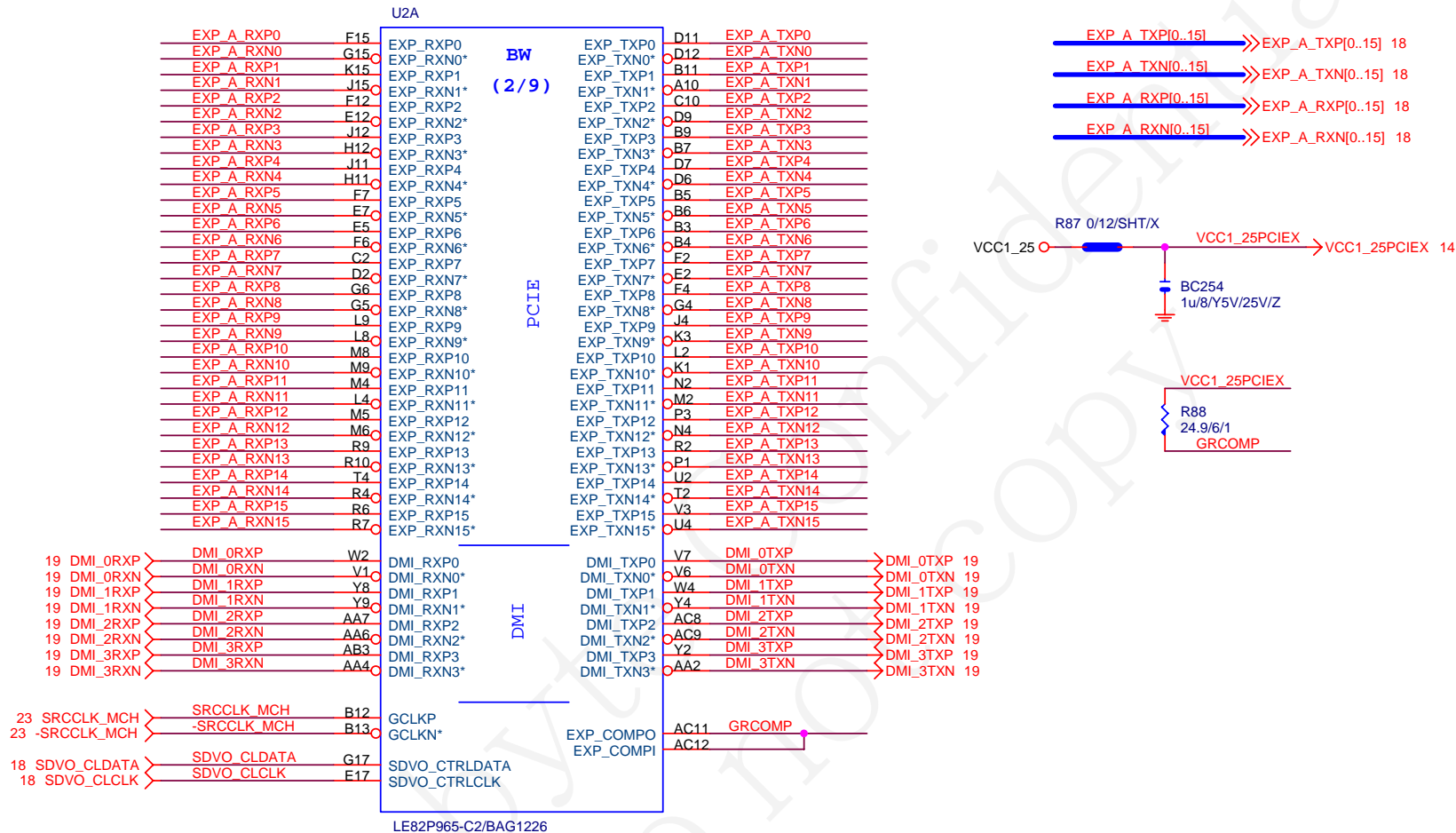
DDR_1

LE82P965-C2/BAG1226

15.17 MODT_A[0..3]	MODT_A0..3
15.17 MODT_B[0..3]	MODT_B0..3
16 -DQSB[0..7]	-DQSB0..7
16.17 MAAB[0..14]	MAAB0..14
16 DMB[0..7]	DMB0..7
16 MDB[0..63]	MDB0..63
16 DOSA[0..7]	DOSA0..7
15.17 MAA[0..14]	MAA0..14
15 DMA[0..7]	DMA0..7
15 MD[A0..63]	MDA0..63
15 DOSA[0..7]	DOSA0..7
15 -DQSA[0..7]	-DQSA0..7

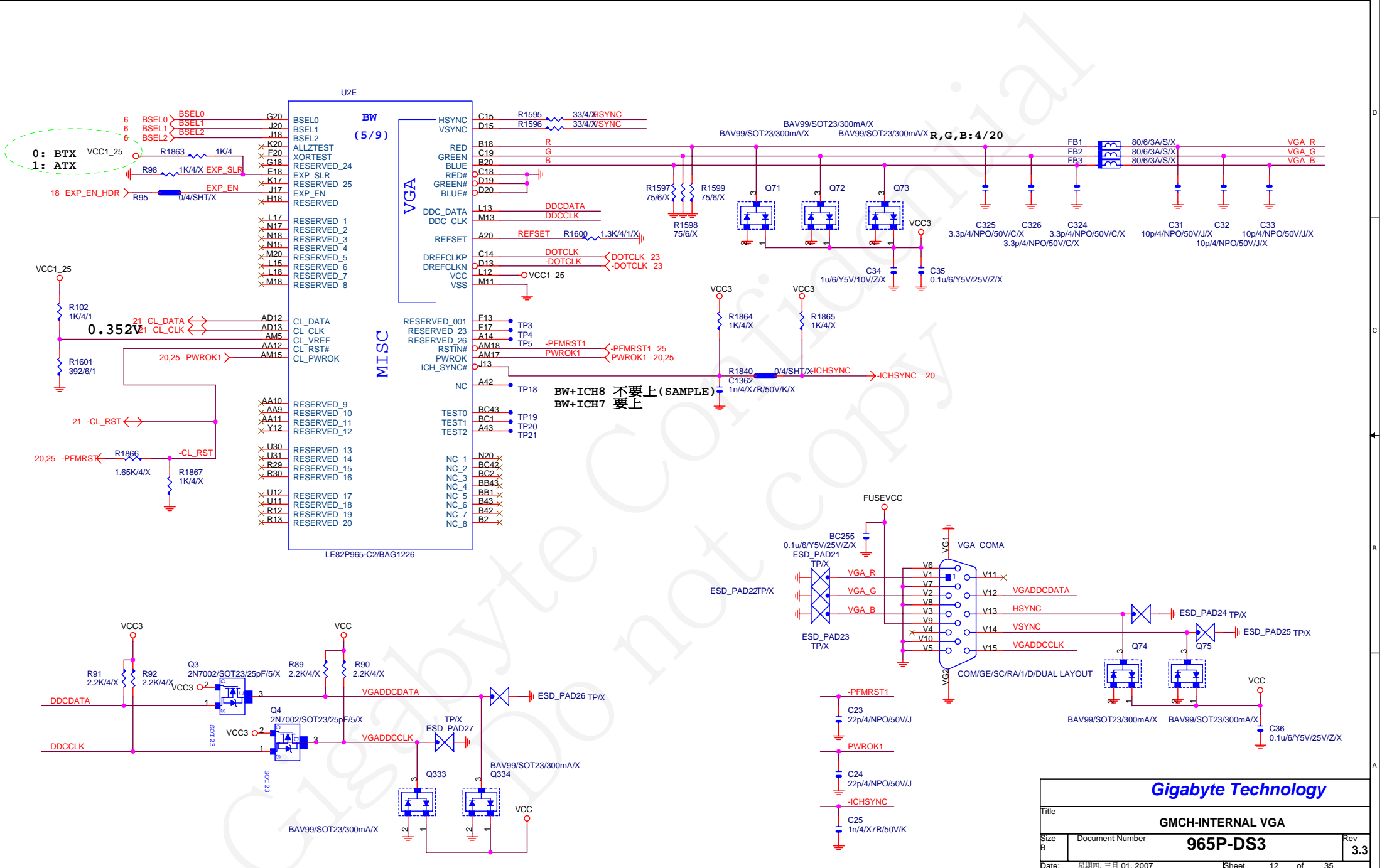


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GMCH-INTERNAL VGA		
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U2G

BC37	VSS_1	BW	VSS_181	AF5
BC32	VSS_2	(7/9)	VSS_182	AF3
BC28	VSS_3		VSS_183	AF2
BC24	VSS_4		VSS_184	AF1
BC10	VSS_5		VSS_185	AD42
BC5	VSS_6	GND 1/2	VSS_186	AD39
BB7	VSS_7		VSS_187	AD37
AY4	VSS_8		VSS_188	AD35
AW43	VSS_9		VSS_189	AD33
AW41	VSS_10		VSS_190	AD25
AW1	VSS_11		VSS_191	AD21
AV37	VSS_12		VSS_192	AD19
AV35	VSS_13		VSS_193	AC38
AV27	VSS_14		VSS_194	AC35
AV23	VSS_15		VSS_195	AC24
AV21	VSS_16		VSS_196	AC22
AV17	VSS_17		VSS_197	AC20
AV11	VSS_18		VSS_198	K43
AV9	VSS_19		VSS_199	K28
AV7	VSS_20		VSS_200	K21
AU42	VSS_21		VSS_201	AC5
AU38	VSS_22		VSS_202	AB43
AU32	VSS_23		VSS_203	AB25
AU24	VSS_24		VSS_204	AB23
AU20	VSS_25		VSS_205	AB21
AU16	VSS_26		VSS_206	AB19
AU2	VSS_27		VSS_207	AB2
AT31	VSS_28		VSS_208	AB1
AT29	VSS_29		VSS_209	AA38
AT15	VSS_30		VSS_210	AA35
AT13	VSS_31		VSS_211	AA24
AT12	VSS_32		VSS_212	AA22
AR38	VSS_33		VSS_213	AA20
AR33	VSS_34		VSS_214	AA8
AR32	VSS_35		VSS_215	AA5
AR27	VSS_36		VSS_216	Y42
AR26	VSS_37		VSS_217	Y37
AR23	VSS_38		VSS_218	Y35
AR21	VSS_39		VSS_219	Y33
AR20	VSS_40		VSS_220	Y25
AR17	VSS_41		VSS_221	Y23
AR9	VSS_42		VSS_222	Y21
AR6	VSS_43		VSS_223	Y19
AP43	VSS_44		VSS_224	X10
AP24	VSS_45		VSS_225	X7
AP18	VSS_46		VSS_226	Y5
AP1	VSS_47		VSS_227	Y1
AN38	VSS_48		VSS_228	W3
AN31	VSS_49		VSS_229	V43
AN29	VSS_50		VSS_230	V39
AN24	VSS_51		VSS_231	V37
AN23	VSS_52		VSS_232	V34
AN20	VSS_53		VSS_233	V32
AN15	VSS_54		VSS_234	V11
AN13	VSS_55		VSS_235	V8
AN12	VSS_56		VSS_236	V5
AN11	VSS_57		VSS_237	V2
AN4	VSS_58		VSS_238	U38
AM40	VSS_59		VSS_239	U35
AM36	VSS_60		VSS_240	U8
AM33	VSS_61		VSS_241	U7
AM29	VSS_62		VSS_242	U5
AM24	VSS_63		VSS_243	T42
AM23	VSS_64		VSS_244	T1
AM20	VSS_65		VSS_245	R36
AM11	VSS_66		VSS_246	R33
AM9	VSS_67		VSS_247	R31
AM7	VSS_68		VSS_248	R11
AM4	VSS_69		VSS_249	R8
AM2	VSS_70		VSS_250	R6
AL36	VSS_71		VSS_251	R3
AL33	VSS_72		VSS_252	P43
AK43	VSS_73		VSS_253	P30
AJ39	VSS_74		VSS_254	P21
AJ36	VSS_75		VSS_255	P18
AH42	VSS_76		VSS_256	P17
AG37	VSS_77		VSS_257	P2
AG34	VSS_78		VSS_258	N36
AF43	VSS_79		VSS_259	N33
AF37	VSS_80		VSS_260	N31
AF36	VSS_81		VSS_261	N27
AF10	VSS_82		VSS_262	N21
AF9	VSS_83		VSS_263	N13
AF8	VSS_84		VSS_264	N10
AF7	VSS_85		VSS_265	N7
AF6	VSS_86		VSS_266	N5
	VSS_87		VSS_267	M42
	VSS_88		VSS_268	M37
	VSS_89		VSS_269	M35
	VSS_90		VSS_270	M33

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U2I

M27	VSS_91	BW	VSS_271	BC41
M21	VSS_92	(9/9)	VSS_272	BC3
M17	VSS_93		VSS_273	BA1
M15	VSS_94		VSS_274	AY40
M10	VSS_95	GND 2/2	VSS_275	AF23
M7	VSS_96		VSS_276	AF21
M1	VSS_97		VSS_277	AF19
L33	VSS_98		VSS_278	AE24
L32	VSS_99		VSS_279	AE22
L31	VSS_100		VSS_280	AE20
L29	VSS_101		VSS_281	AE18
L21	VSS_102		VSS_282	AC18
L20	VSS_103		VSS_283	AA18
L11	VSS_104		VSS_284	W24
L7	VSS_105		VSS_285	W22
L5	VSS_106		VSS_286	W20
L3	VSS_107		VSS_287	R21
K43	VSS_108		VSS_288	E1
K28	VSS_109		VSS_289	C43
K21	VSS_110		VSS_290	C1
K18	VSS_111		VSS_291	A41
K13	VSS_112		VSS_292	A5
K12	VSS_113		VSS_293	A3
K2	VSS_114			
J38	VSS_115			
J35	VSS_116			
J32	VSS_117			
J27	VSS_118			
J21	VSS_119			
J9	VSS_120			
J7	VSS_121			
J5	VSS_122			
H31	VSS_123			
H29	VSS_124			
H21	VSS_125			
H20	VSS_126			
H17	VSS_127			
H15	VSS_128			
H13	VSS_129			
G42	VSS_130			
G38	VSS_131			
G32	VSS_132			
G21	VSS_133			
G13	VSS_134			
G12	VSS_135			
G11	VSS_136			
G9	VSS_137			
G7	VSS_138			
G1	VSS_139			
F37	VSS_140			
F35	VSS_141			
F27	VSS_142			
F21	VSS_143			
F18	VSS_144			
F3	VSS_145			
E43	VSS_146			
E32	VSS_147			
E24	VSS_148			
E21	VSS_149			
E20	VSS_150			
E15	VSS_151			
E13	VSS_152			
E11	VSS_153			
E9	VSS_154			
E3	VSS_155			
D40	VSS_156			
D31	VSS_157			
D21	VSS_158			
D17	VSS_159			
D3	VSS_160			
C26	VSS_161			
C11	VSS_162			
C6	VSS_163			
C5	VSS_164			
C4	VSS_165			
B37	VSS_166			
B32	VSS_167			
B31	VSS_168			
B26	VSS_169			
B23	VSS_170			
B22	VSS_171			
B19	VSS_172			
B14	VSS_173			
B10	VSS_174			
A39	VSS_175			
A34	VSS_176			
A26	VSS_177			
A18	VSS_178			
A12	VSS_179			
A7	VSS_180			

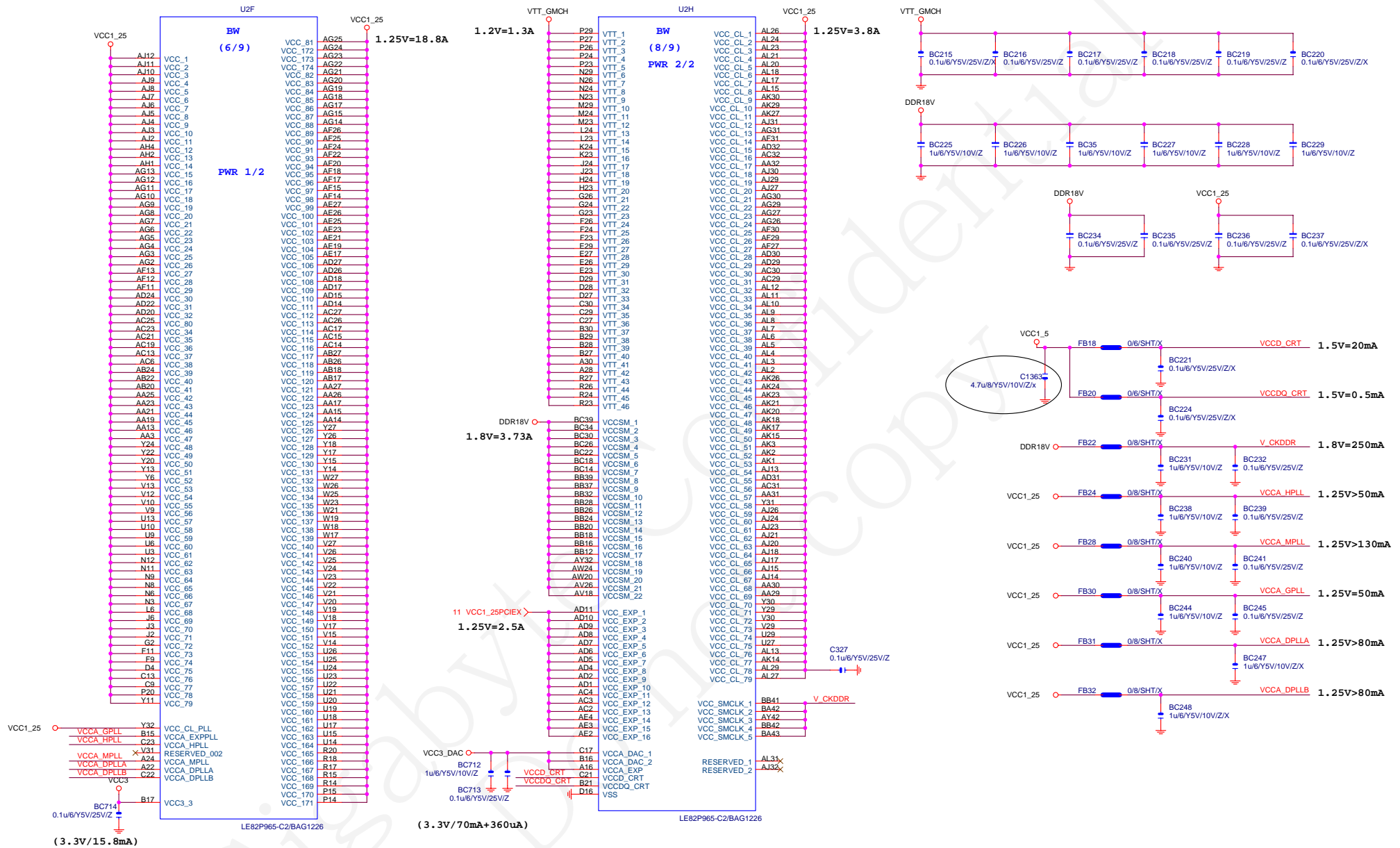
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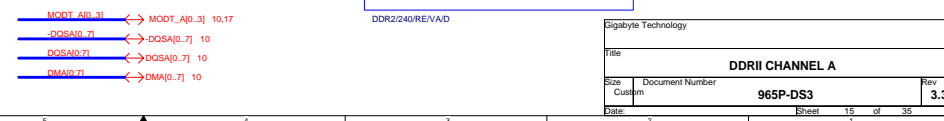
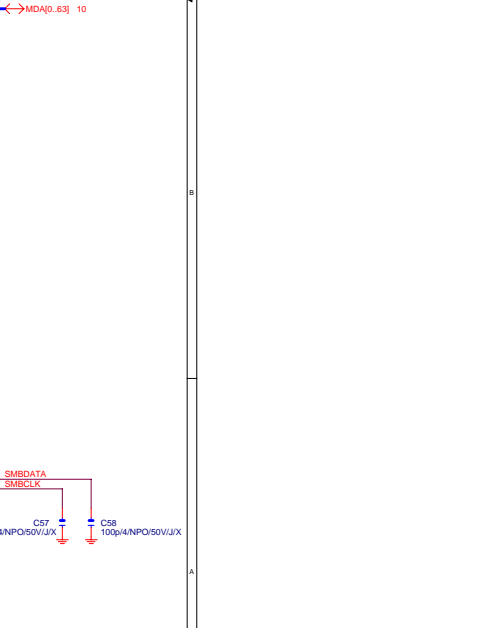
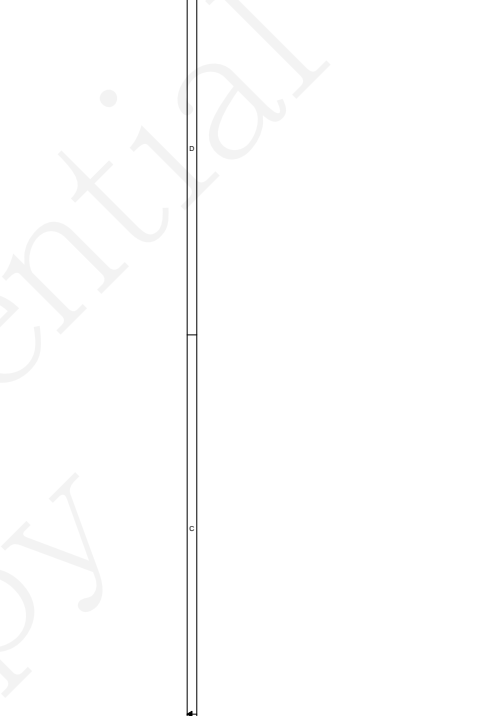
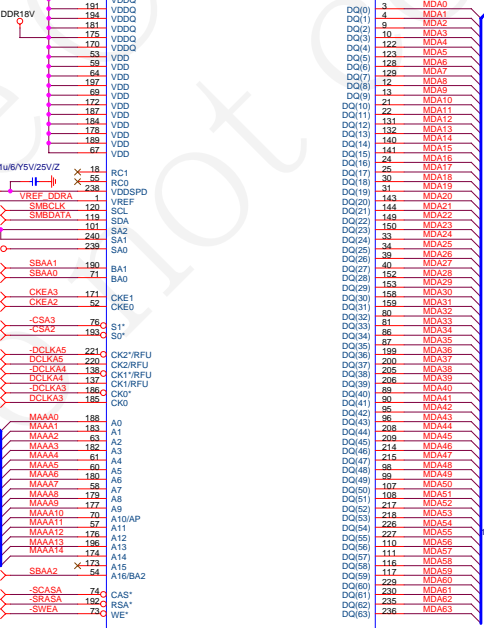
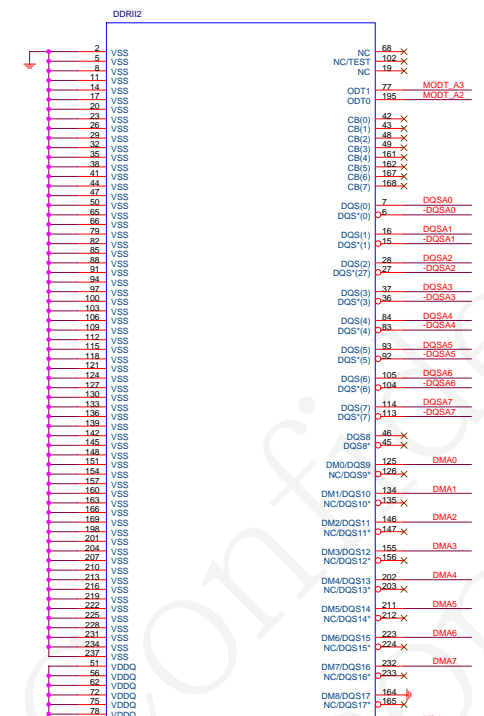
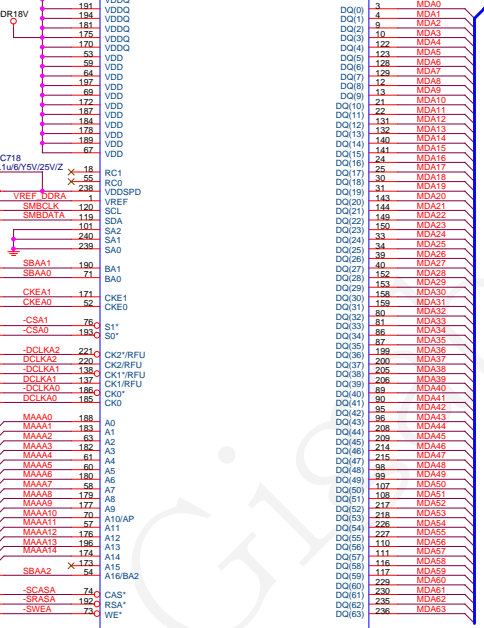
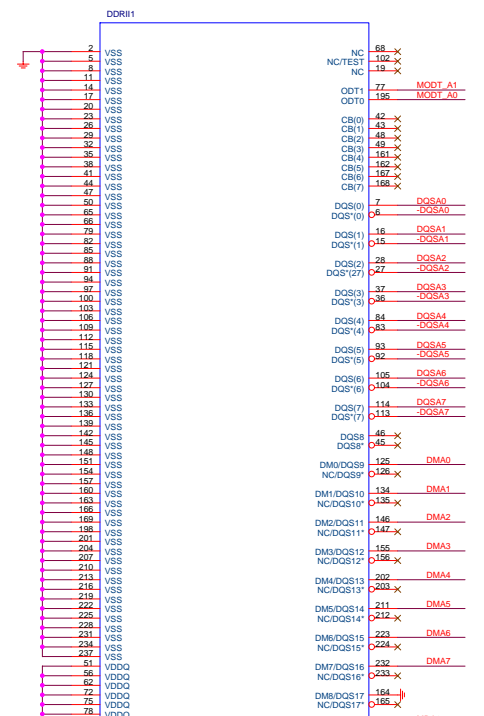
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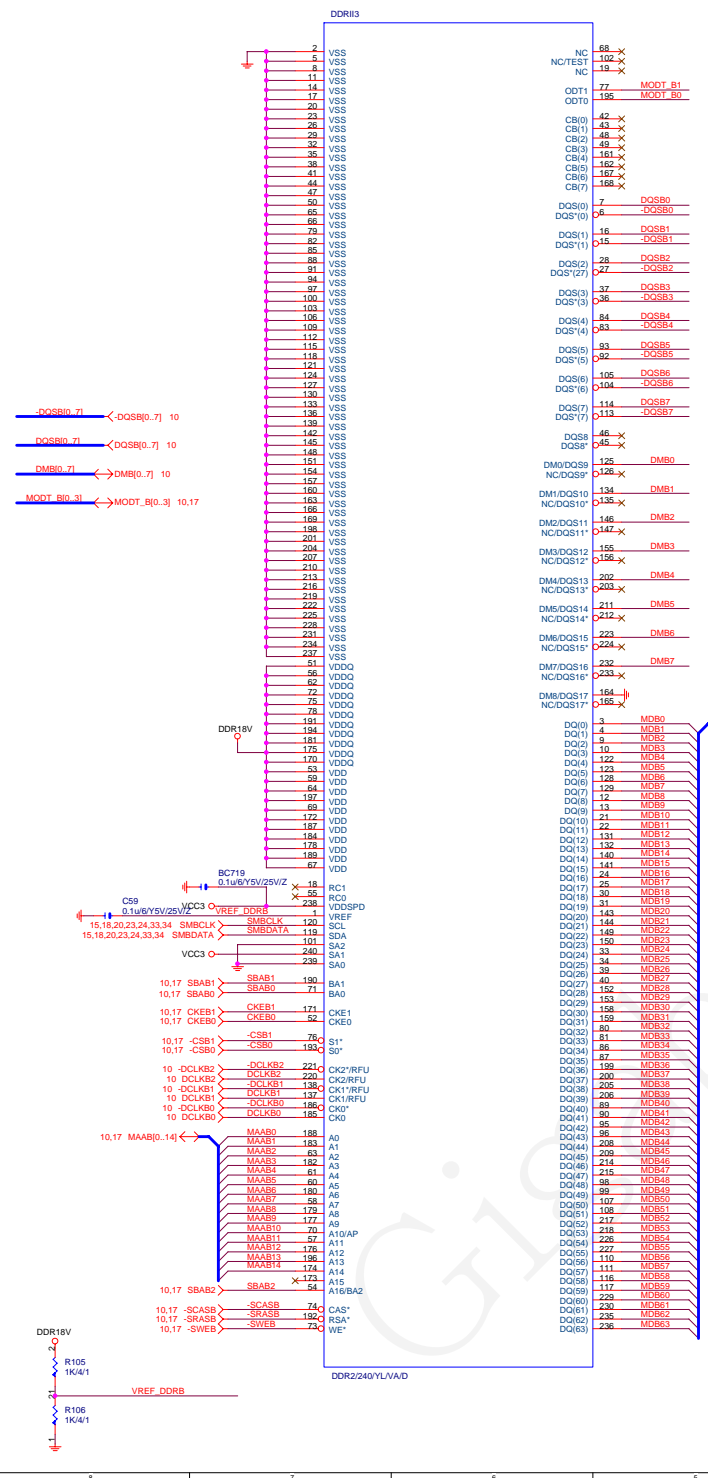
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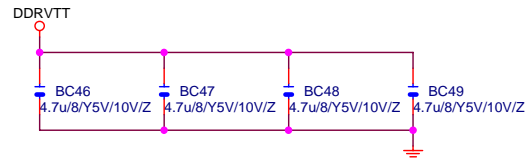
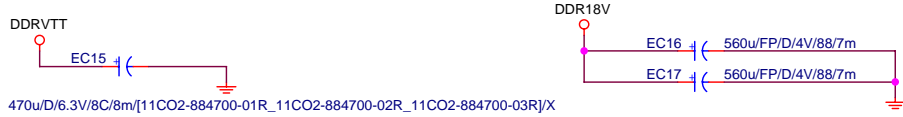


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DDRII CHANNEL A	
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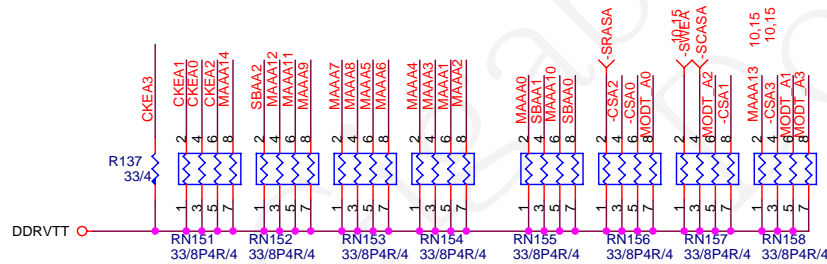
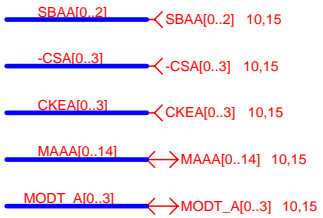
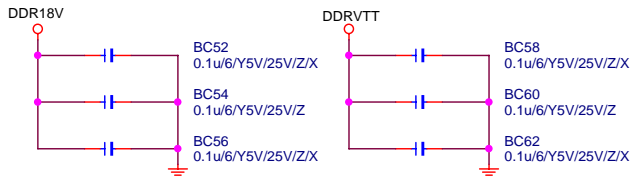
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DDRVTT Decouple



DDR18V Decouple

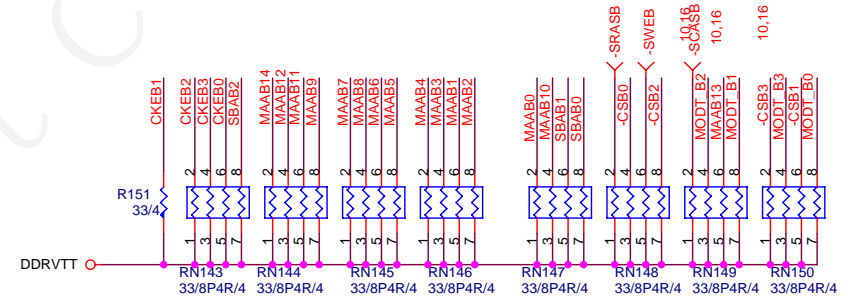
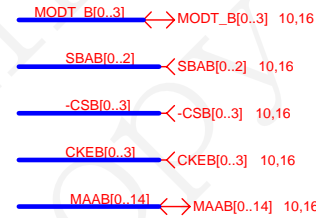
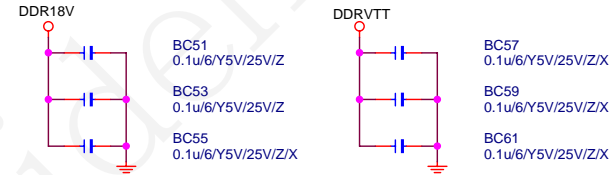
DDRVTT Decouple



DDR TERMINATION CHANNEL B

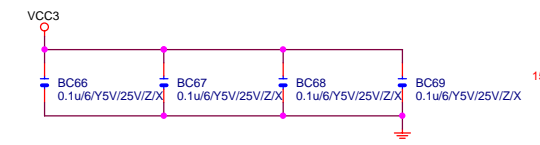
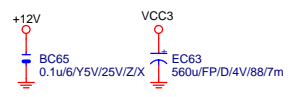
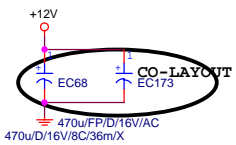
DDR18V Decouple

DDRVTT Decouple



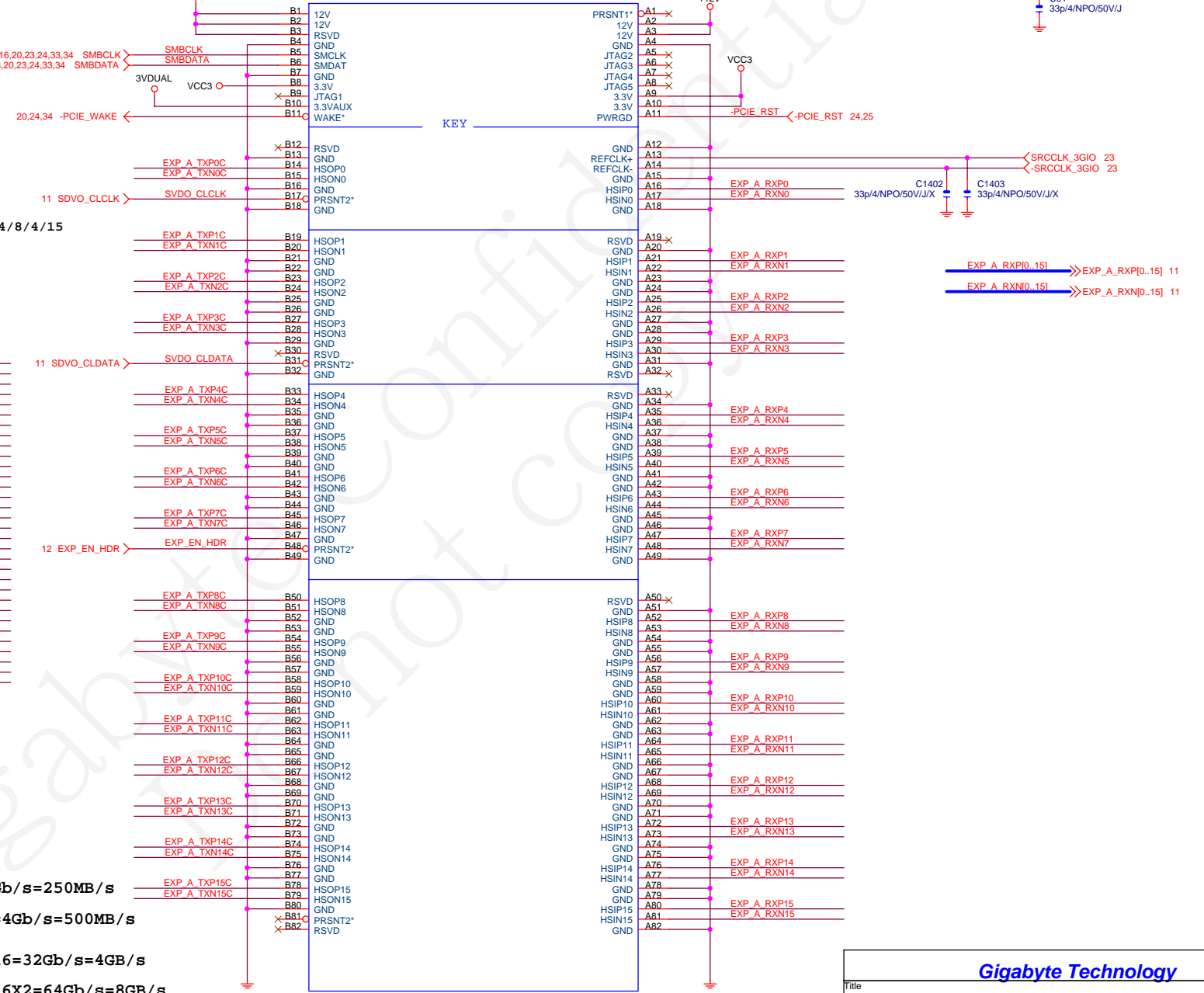
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DDRII TERMINATOR		
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15,16,20,23,24,33,34 SMBCLK
15,16,20,23,24,33,34 SMBDATA

PCIESLOT-164DN-2
3GIO_*16



PCIE16:15/4/8/4/15

EXP_A_TXP[0..15] >>> EXP_A_TXP[0..15] 11
EXP_A_TXN[0..15] >>> EXP_A_TXN[0..15] 11

EXP_A_TXP0	C92	0.1u4/Y5V/16V/Z	EXP_A_TXP0C
EXP_A_TXN0	C93	0.1u4/Y5V/16V/Z	EXP_A_TXN0C
EXP_A_TXP1	C94	0.1u4/Y5V/16V/Z	EXP_A_TXP1C
EXP_A_TXN1	C95	0.1u4/Y5V/16V/Z	EXP_A_TXN1C
EXP_A_TXP2	C96	0.1u4/Y5V/16V/Z	EXP_A_TXP2C
EXP_A_TXN2	C97	0.1u4/Y5V/16V/Z	EXP_A_TXN2C
EXP_A_TXP3	C98	0.1u4/Y5V/16V/Z	EXP_A_TXP3C
EXP_A_TXN3	C99	0.1u4/Y5V/16V/Z	EXP_A_TXN3C
EXP_A_TXP4	C100	0.1u4/Y5V/16V/Z	EXP_A_TXP4C
EXP_A_TXN4	C101	0.1u4/Y5V/16V/Z	EXP_A_TXN4C
EXP_A_TXP5	C102	0.1u4/Y5V/16V/Z	EXP_A_TXP5C
EXP_A_TXN5	C103	0.1u4/Y5V/16V/Z	EXP_A_TXN5C
EXP_A_TXP6	C104	0.1u4/Y5V/16V/Z	EXP_A_TXP6C
EXP_A_TXN6	C105	0.1u4/Y5V/16V/Z	EXP_A_TXN6C
EXP_A_TXP7	C106	0.1u4/Y5V/16V/Z	EXP_A_TXP7C
EXP_A_TXN7	C107	0.1u4/Y5V/16V/Z	EXP_A_TXN7C
EXP_A_TXP8	C108	0.1u4/Y5V/16V/Z	EXP_A_TXP8C
EXP_A_TXN8	C109	0.1u4/Y5V/16V/Z	EXP_A_TXN8C
EXP_A_TXP9	C110	0.1u4/Y5V/16V/Z	EXP_A_TXP9C
EXP_A_TXN9	C111	0.1u4/Y5V/16V/Z	EXP_A_TXN9C
EXP_A_TXP10	C112	0.1u4/Y5V/16V/Z	EXP_A_TXP10C
EXP_A_TXN10	C113	0.1u4/Y5V/16V/Z	EXP_A_TXN10C
EXP_A_TXP11	C114	0.1u4/Y5V/16V/Z	EXP_A_TXP11C
EXP_A_TXN11	C115	0.1u4/Y5V/16V/Z	EXP_A_TXN11C
EXP_A_TXP12	C116	0.1u4/Y5V/16V/Z	EXP_A_TXP12C
EXP_A_TXN12	C117	0.1u4/Y5V/16V/Z	EXP_A_TXN12C
EXP_A_TXP13	C118	0.1u4/Y5V/16V/Z	EXP_A_TXP13C
EXP_A_TXN13	C119	0.1u4/Y5V/16V/Z	EXP_A_TXN13C
EXP_A_TXP14	C120	0.1u4/Y5V/16V/Z	EXP_A_TXP14C
EXP_A_TXN14	C121	0.1u4/Y5V/16V/Z	EXP_A_TXN14C
EXP_A_TXP15	C122	0.1u4/Y5V/16V/Z	EXP_A_TXP15C
EXP_A_TXN15	C123	0.1u4/Y5V/16V/Z	EXP_A_TXN15C

PCI-E REV:1.1--> 2.5GHZ

PCE-E X1(單向) BANDWIDTH=2.5GHZ*(8b/10b)=2Gb/s=250MB/s

PCE-E X1(雙向) BANDWIDTH=2.5GHZ*(8b/10b)X2=4Gb/s=500MB/s

PCE-E X16(單向) BANDWIDTH=2.5GHZ*(8b/10b)X16=32Gb/s=4GB/s

PCE-E X16(雙向) BANDWIDTH=2.5GHZ*(8b/10b)X16X2=64Gb/s=8GB/s

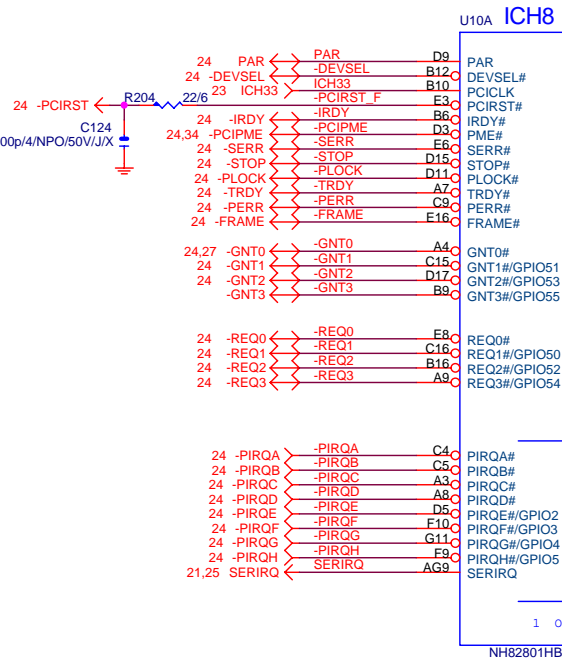
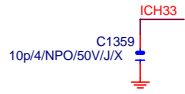
PCI-E REV:2.0--> 5GHZ

PCI-E/16X-164P/BU-297C/RIGHT PUSH

Gigabyte Technology

PCI EXPRESS * 16

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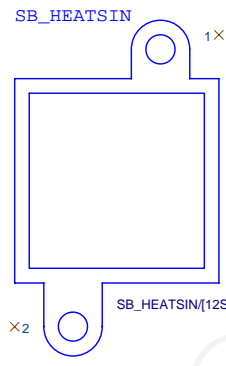


PCI

Interrupt Interface

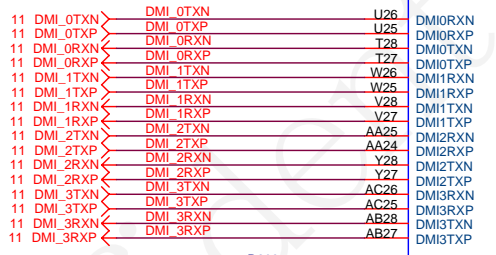
NH82801HB-B0/S

1 OF 6

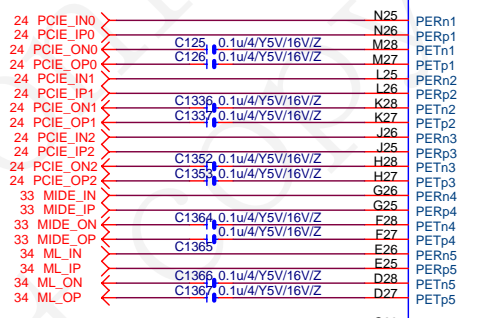


SB_HEATSIN[12SP2-030010-81R_12SP2-030010-82R_12SP2-030010-83R_12SP2-030010-84R]

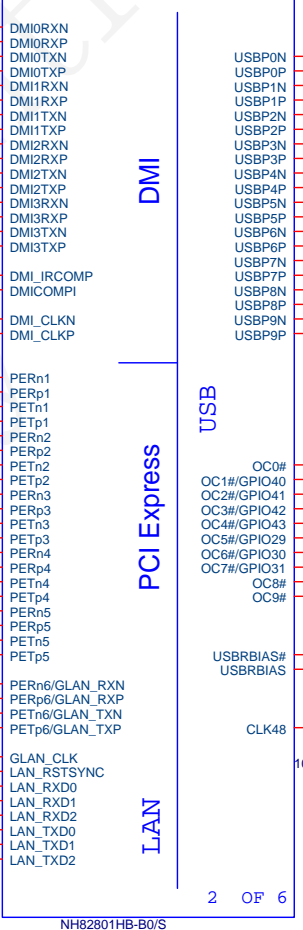
DMI:15/4/8/4/15



PCIEX1:15/4/8/4/15



U10B ICH8

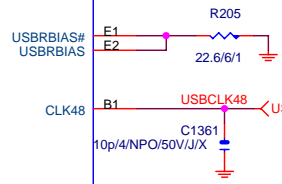
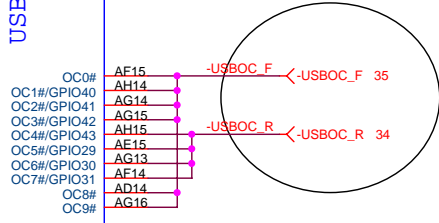
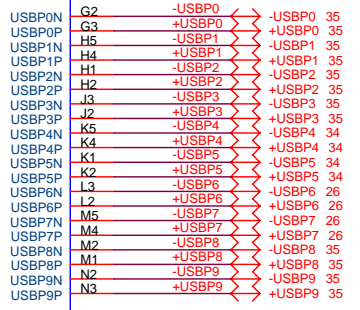


LAN

2 OF 6

NH82801HB-B0/S

USB:15/4.5/7.5/4.5/15

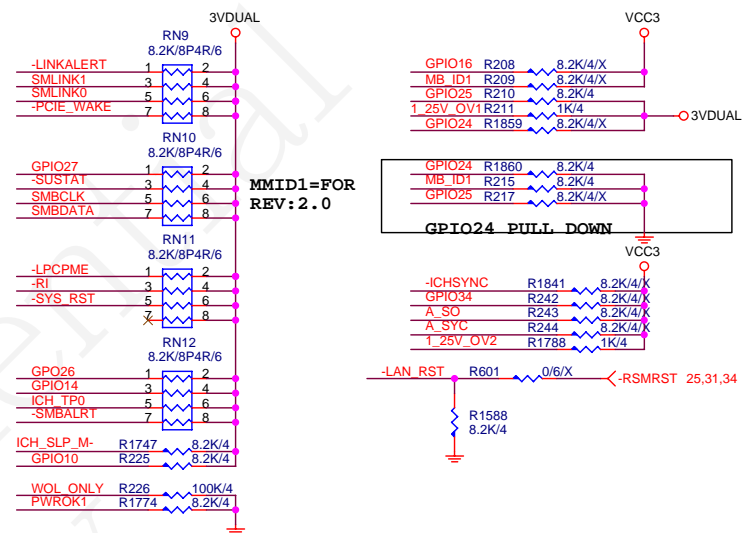
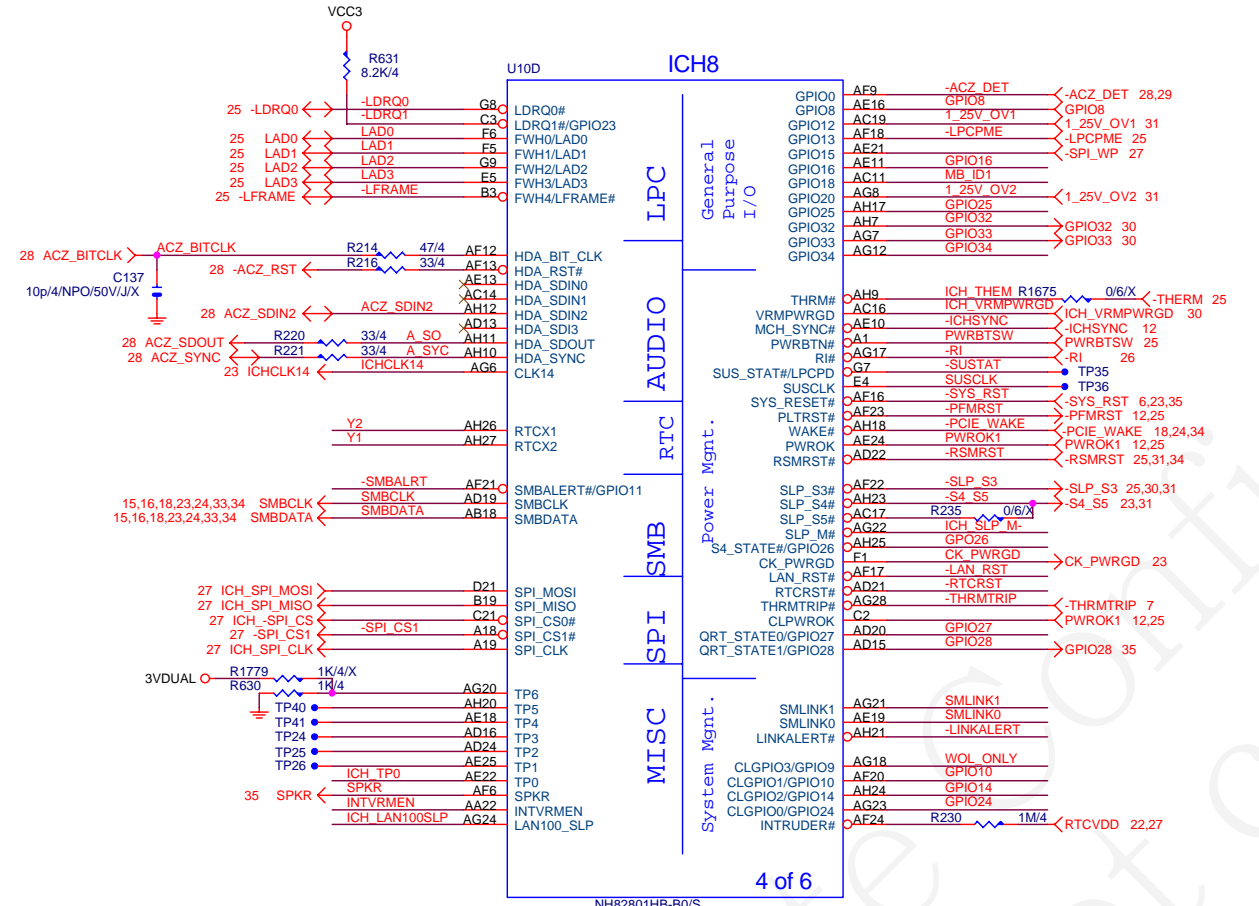


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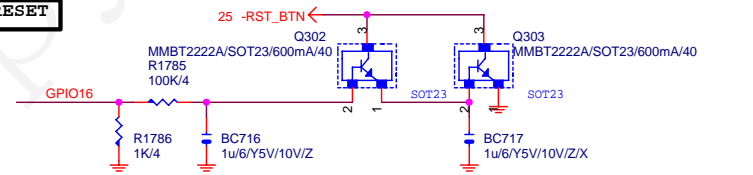
Title: **ICH8-PCI, DMI, LAN, USB**

Size: **965P-DS3**

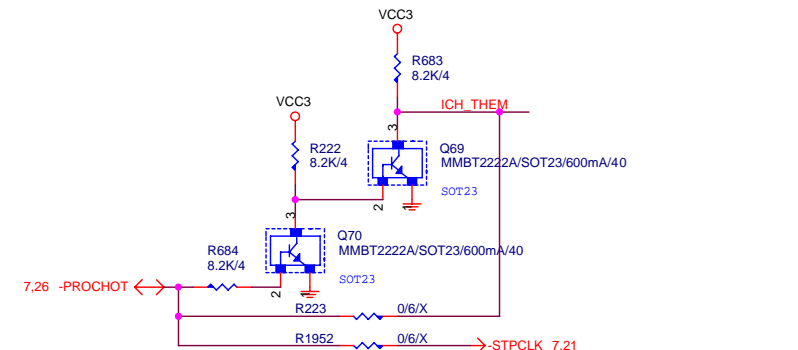
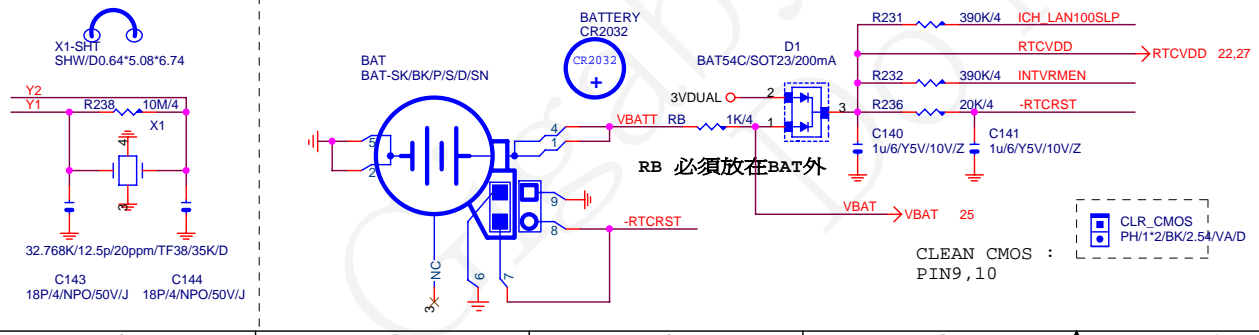
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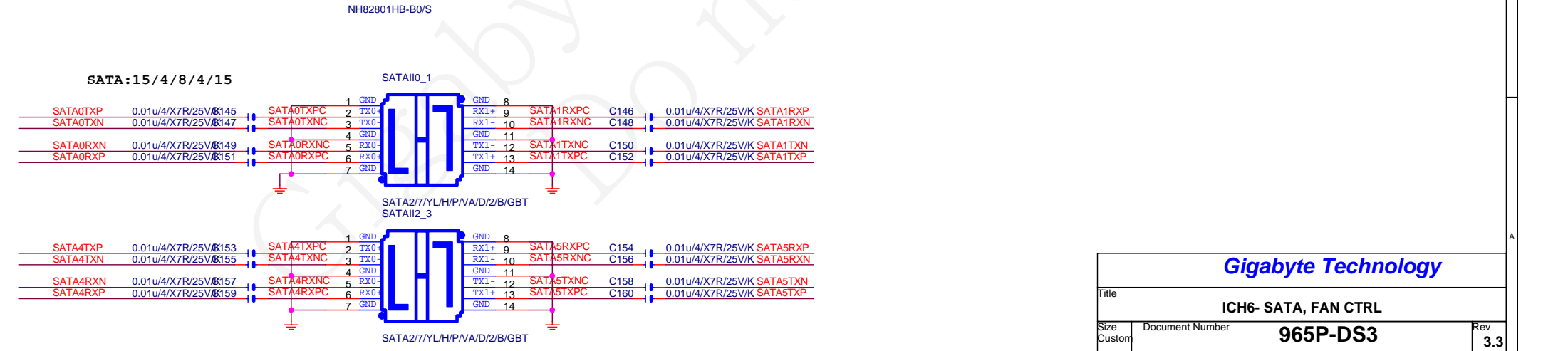
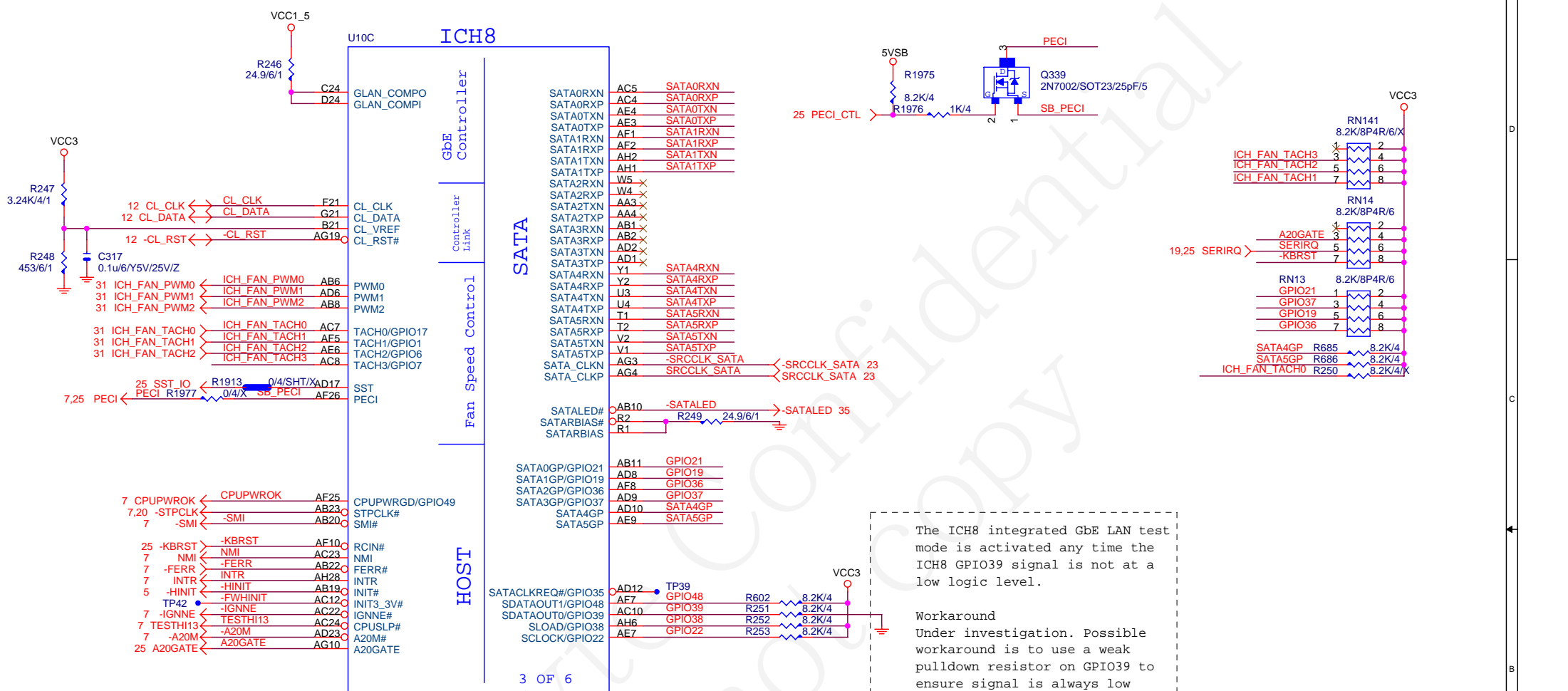
H/W RESET



FOR ICH7R POWER ON 瞬間會HIGH 到1.8V 之後0V, 必須PULL DOWN 1K/6



Gigabyte Technology		
Title ICH8 GPIO, CTRL		
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50 欧姆: [18/4/10/4/18]

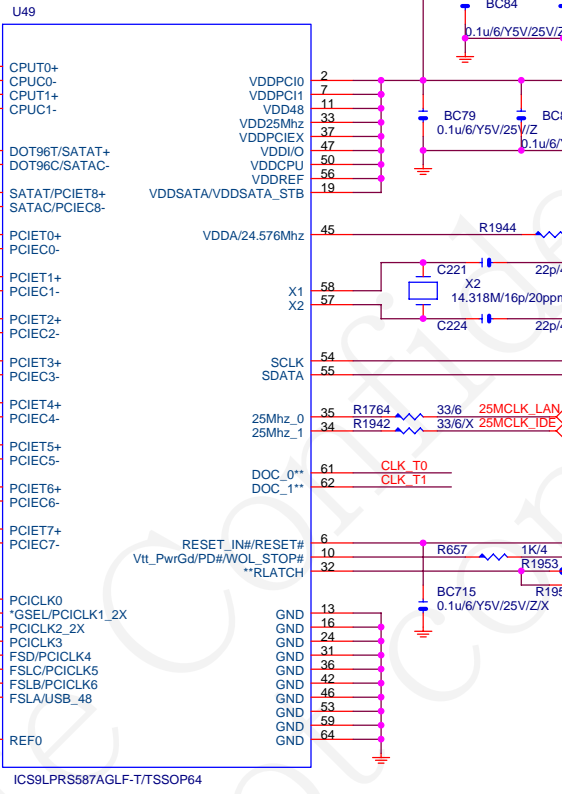
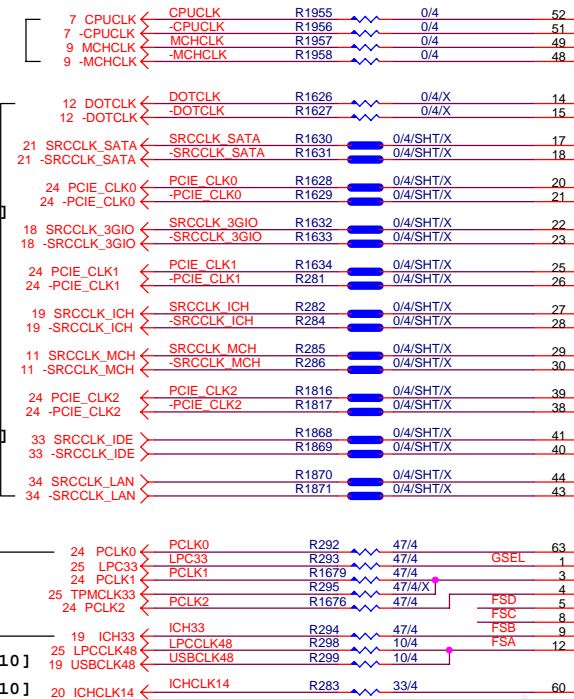
50 欧姆: [18/4/10/4/18]

50 欧姆: [18/4/10/4/18]

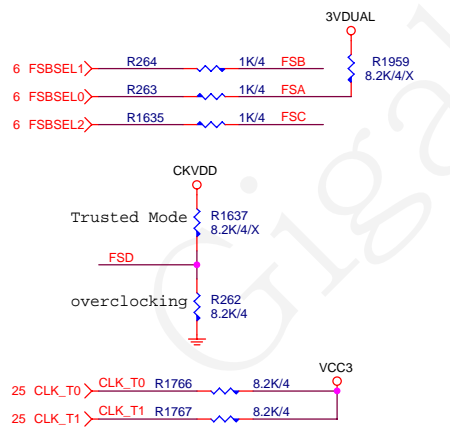
50 欧姆: [4/10]

50 欧姆: [4/10]

50 欧姆: [4/10]



GSEL=1,96Mhz from 14/15,SATACLK from 17/18
 GSEL=0,SATACLK from 14/15,PCIECLK from 17/18

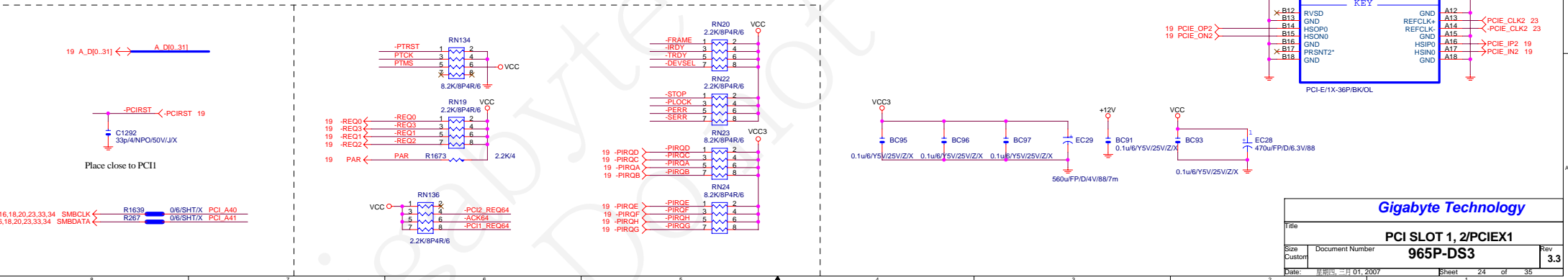
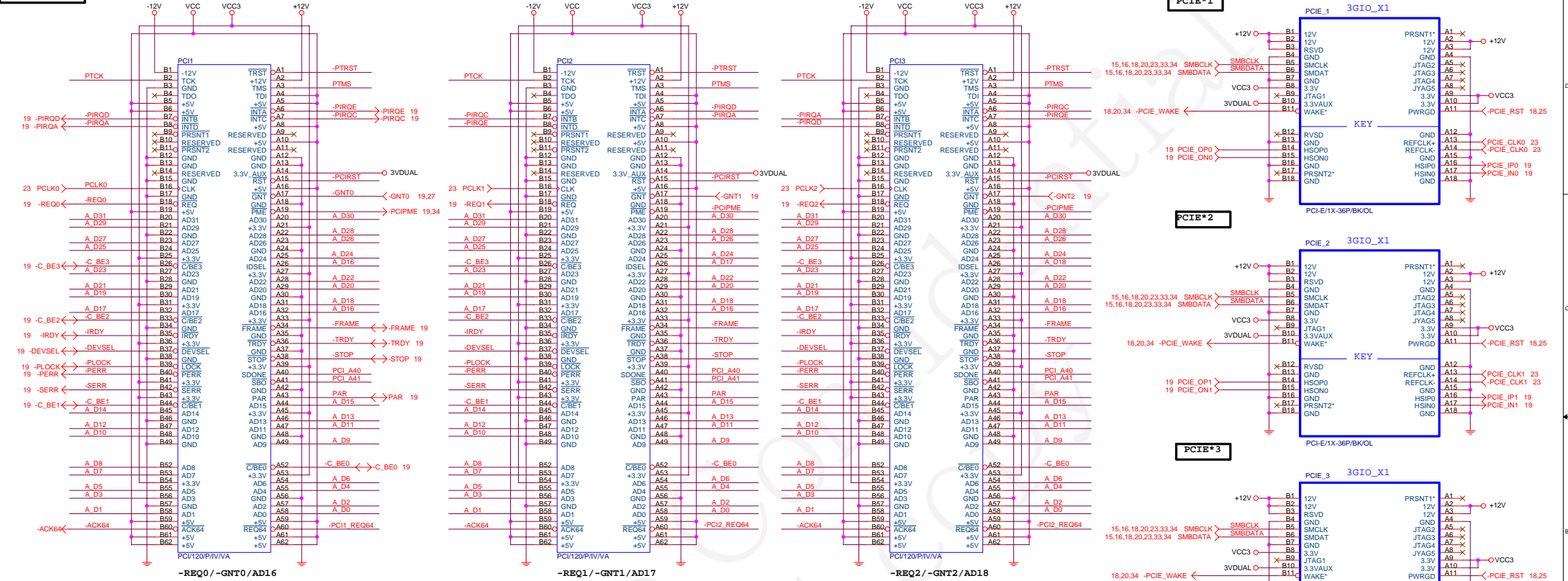


- 25MCLK_LAN C1334 10p/4/NPO/50V/J/X
- 25MCLK_IDE C1393 10p/4/NPO/50V/J/X
- ICHCLK14 C1291 10p/4/NPO/50V/J/X
- PCLK0 C214 10p/4/NPO/50V/J/X
- PCLK1 C215 10p/4/NPO/50V/J/X
- ICH33 C216 10p/4/NPO/50V/J/X
- LPC33 C218 10p/4/NPO/50V/J/X
- USBCLK48 C219 10p/4/NPO/50V/J/X
- LPCCLK48 C220 10p/4/NPO/50V/J/X
- PCLK2 C1298 10p/4/NPO/50V/J/X
- CPUCLK C1396 10p/4/NPO/50V/J
- CPUCLK C1397 10p/4/NPO/50V/J
- MCHCLK C1398 10p/4/NPO/50V/J/X
- MCHCLK C1399 10p/4/NPO/50V/J/X
- SRCCLK_3GIO C1400 10p/4/NPO/50V/J
- SRCCLK_3GIO C1401 10p/4/NPO/50V/J

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Title		
CK505 CLK GEN		
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PCI1,2 SLOT

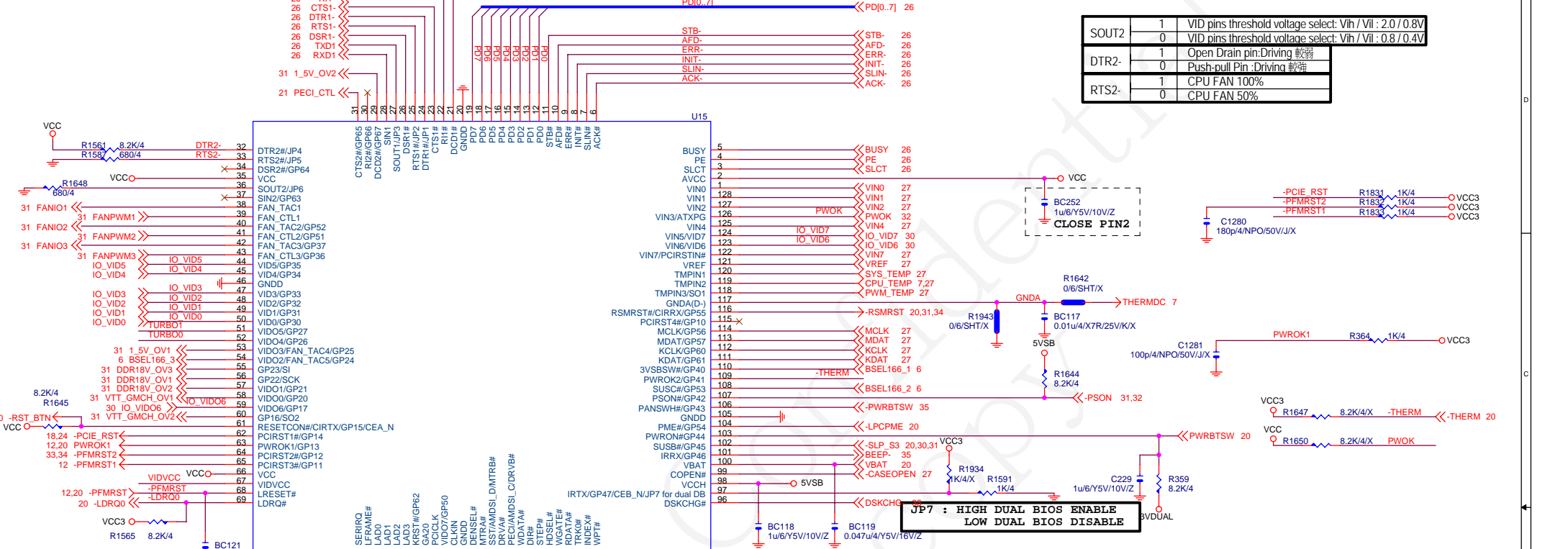


Gigabyte Technology

Title		PCI SLOT 1, 2/PCIEX1	
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Rev 3.3

IT8712F LPC I/O

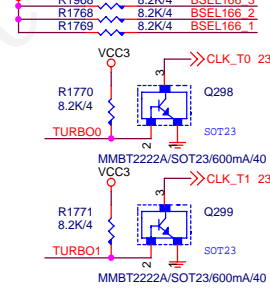


SOUT2	1	VID pins threshold voltage select: Vih / Vil : 2.0 / 0.8V
	0	VID pins threshold voltage select: Vih / Vil : 0.8 / 0.4V
DTR2-	1	Open Drain pin: Driving 軟弱
	0	Push-pull Pin : Driving 軟弱
RTS2-	1	CPU FAN 100%
	0	CPU FAN 50%

CLOSE PIN2

**JP7 : HIGH DUAL BIOS ENABLE
LOW DUAL BIOS DISABLE**

如果BSEL0/1 直接拉TO N/B, R1768/R1769 不上



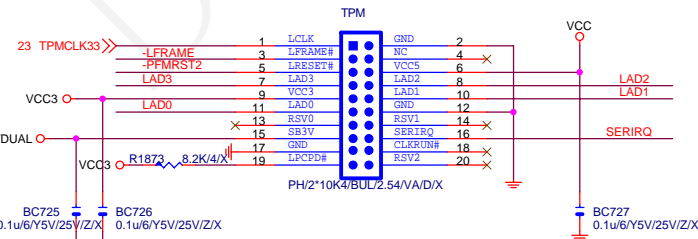
FOR REV: BX / CX

FOR ITE8718	RN159	0/8P4R/SHT/X	1	2	<< IO_VID1
			3	4	<< IO_VID3
			5	6	<< IO_VID2
			7	8	<< IO_VID0
	DR71	0/4/SHT/X			<< IO_VID4
	DR72	0/4/SHT/X			<< IO_VID5

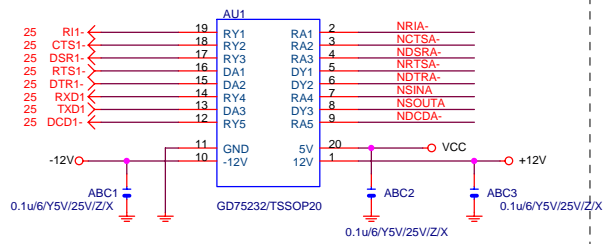
SOUT2	1	VID pins threshold voltage select: Vih / Vil : 2.0 / 0.8V
	0	VID pins threshold voltage select: Vih / Vil : 0.8 / 0.4V

1.2V or 3.3V tolerance select.
1.2V OUTPUT 接 VTT_GMCH
3.3V OUTPUT 接 3.3V
LPCPD# = VIDVCC

VCC3 ○ R1946 0/4/SHT/X VIDVCC
VTT_GMCH ○ R1947 0/4/X
VTT_GMCH/VCC3/VIDVCC 請走 20~30

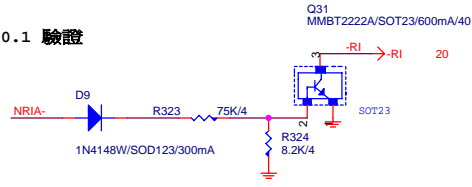


COMA

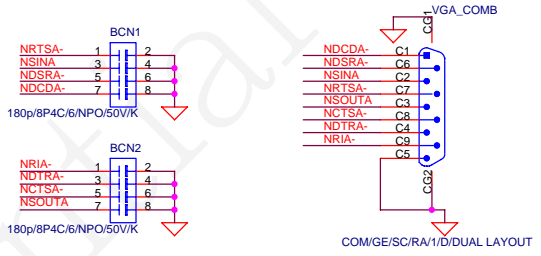


COM RI

REV:0.1 驗證

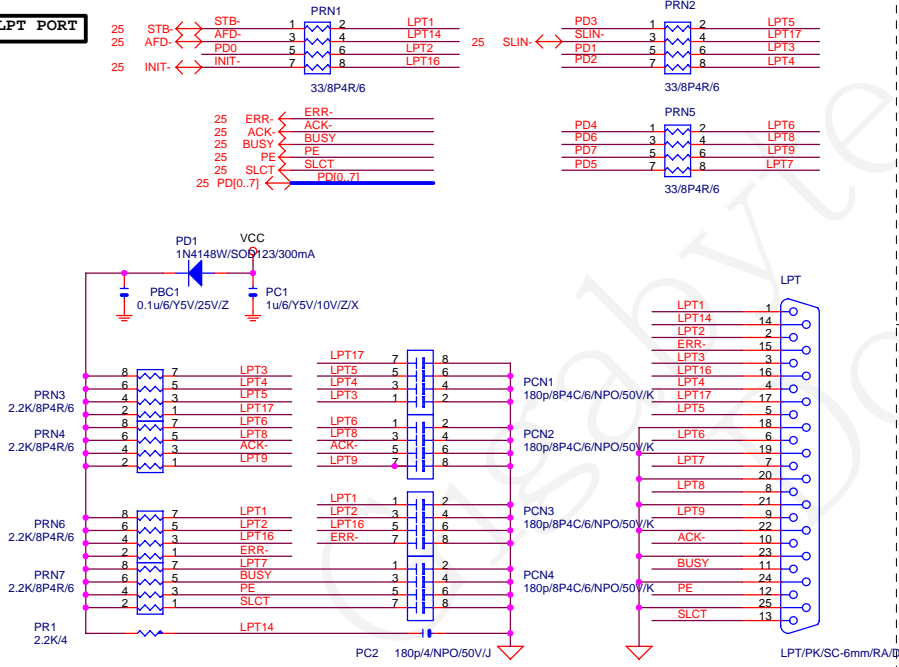


EXTERNAL COMB

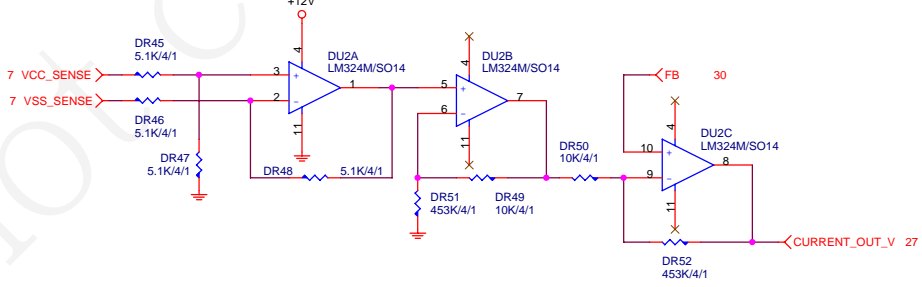


PLACE NEAR VGA_COM CONNECTOR

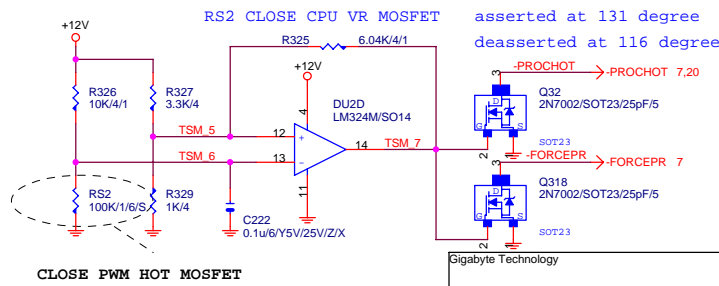
LPT PORT



DYNAMIC CURRENT OC

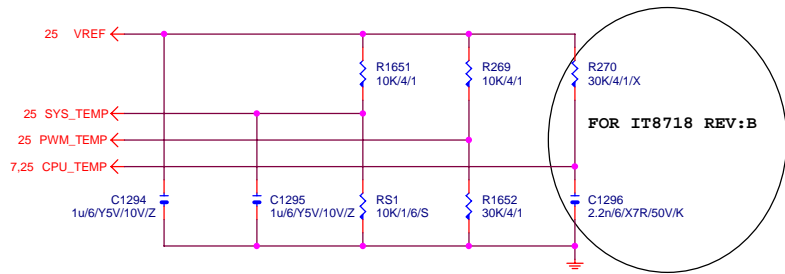


-PROHOT

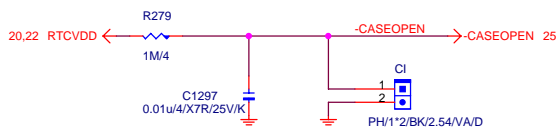


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Title		
COM & LPT PORT		
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TEMP H/W MONITOR

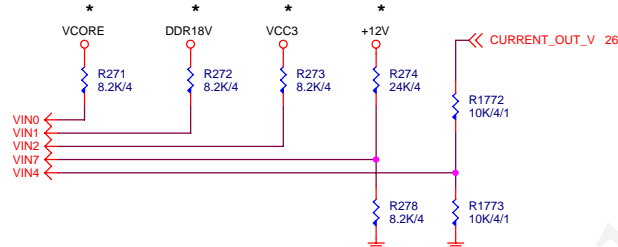


CASE OPEN

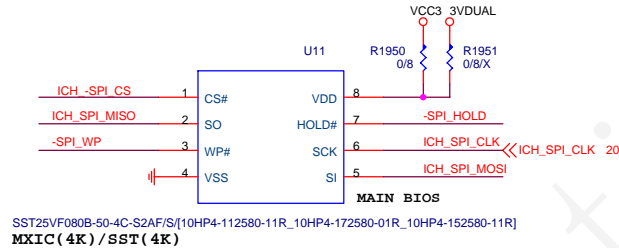
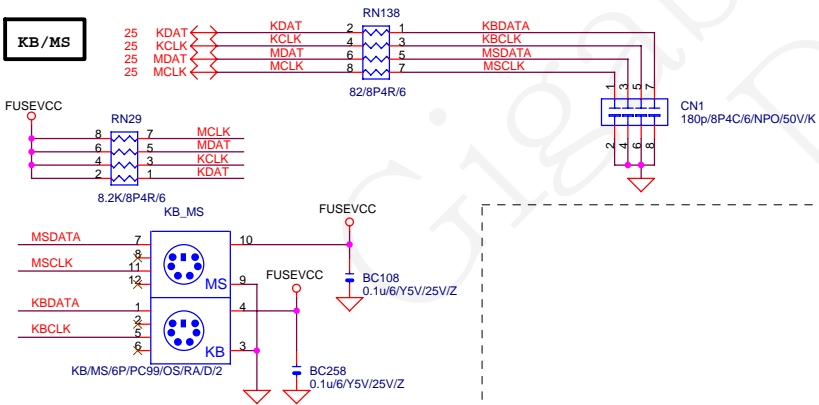


Case Open Circuits

VOLTAGE-- H/W MONITOR

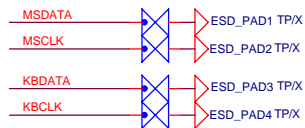
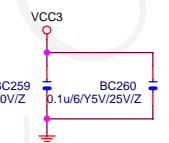
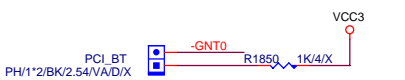
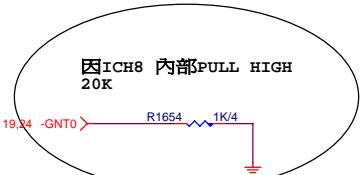
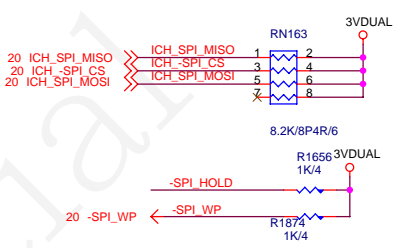


KB/MS



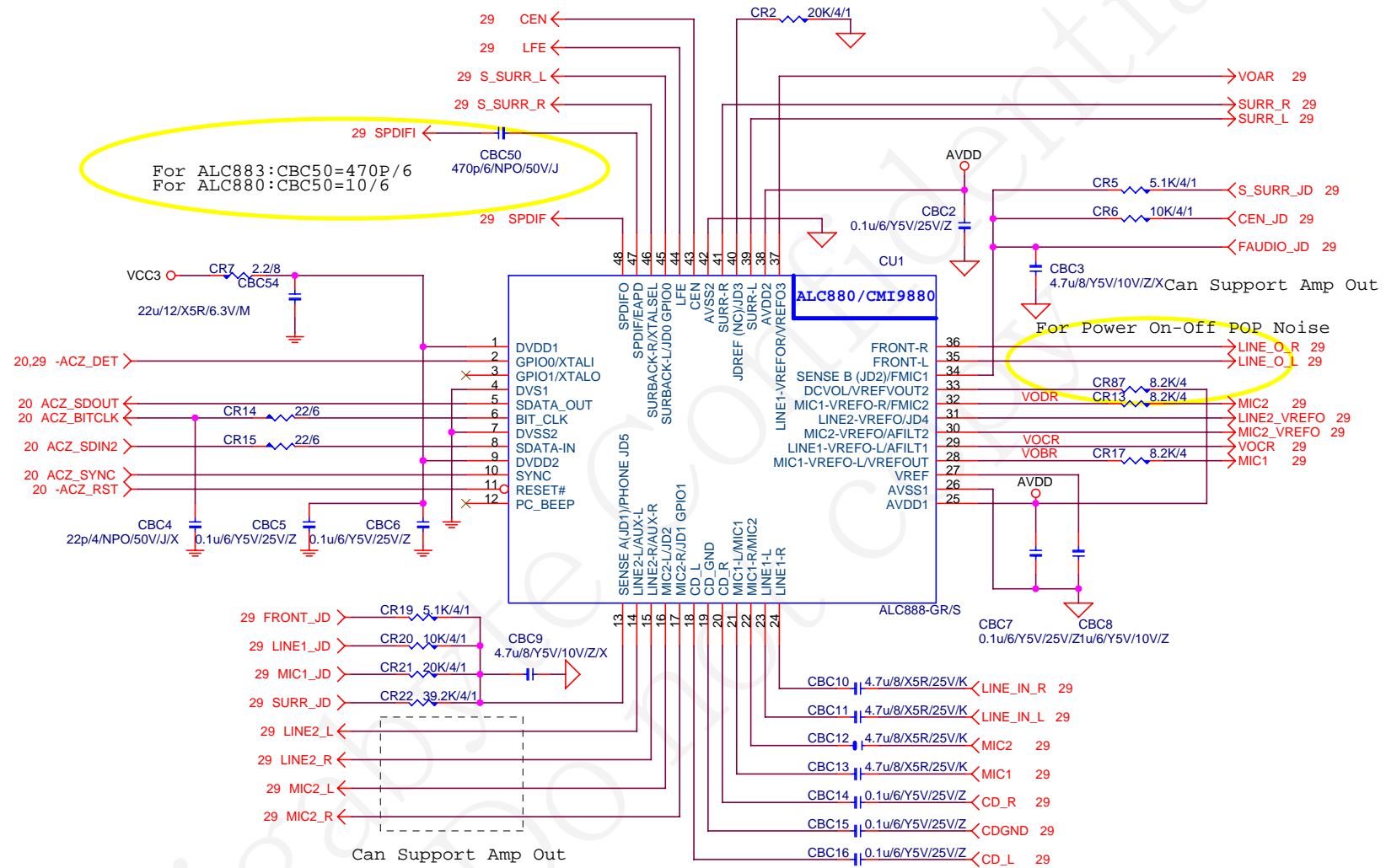
SST25VF080B-50-4C-S2AF/S(10HP4-112580-11R_10HP4-172580-01R_10HP4-152580-11R)
MXIC (4K) / SST (4K)

BOOT DEVICE	GNT0	CS1
SPI	0	X
PCI	1	0
FWH	1	1



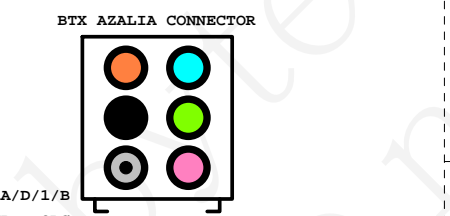
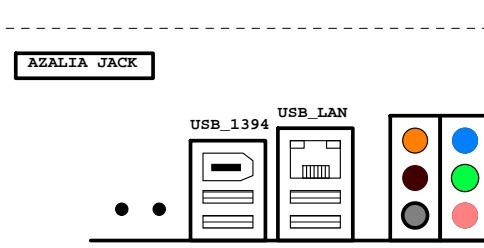
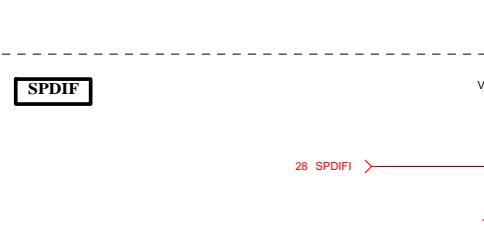
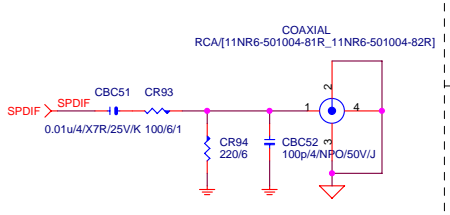
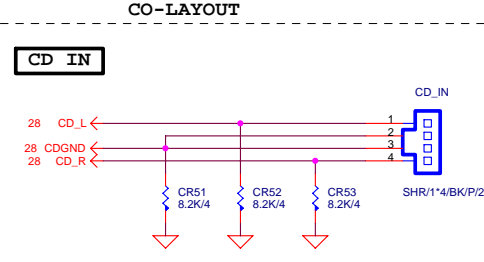
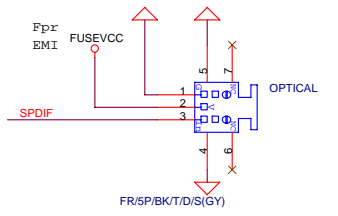
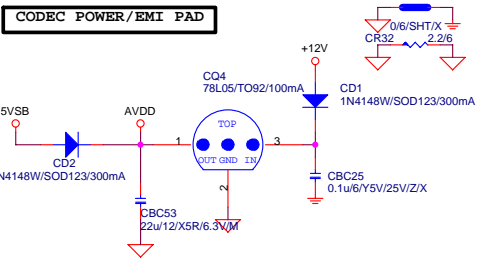
Gigabyte Technology

Title		
BIOS/HW-MONITOR/CI/KB/MS		
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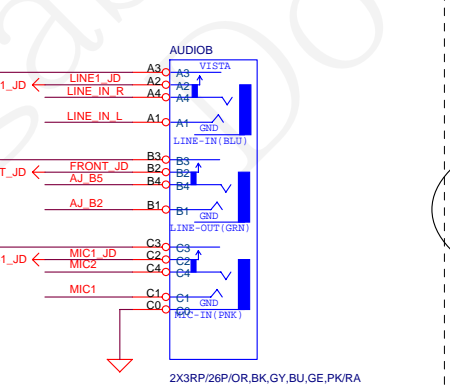
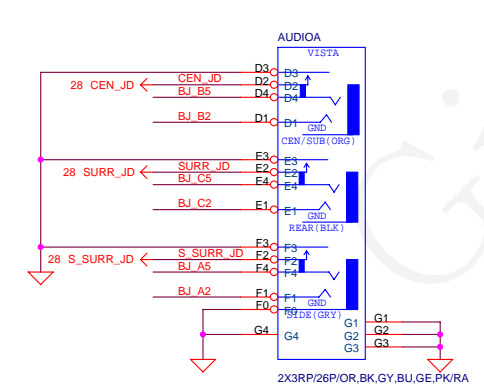


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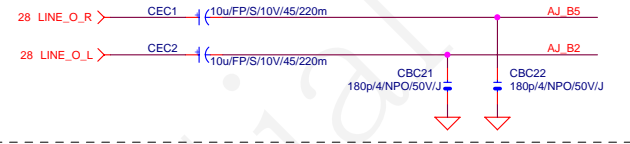
Title		
AC97 ALC658		
Size Custom	Document Number	Rev
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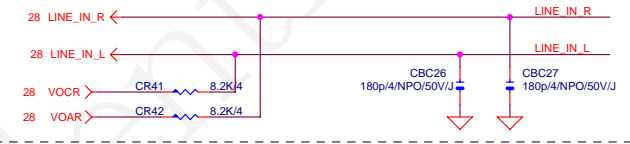
3RP/26P/OR,BK,GY,BU,GE,PK/RA/D/1/B
VISTA規範: REAR-->BLK, CEN/SUB-->ORG



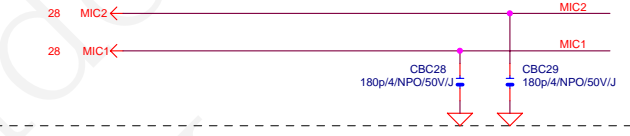
LINE-OUT



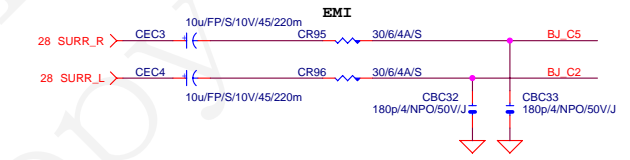
LINE-IN



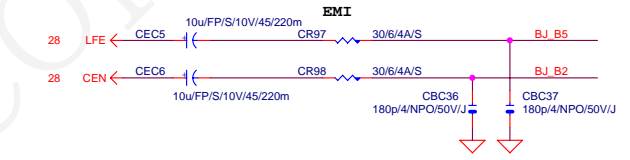
MIC-IN



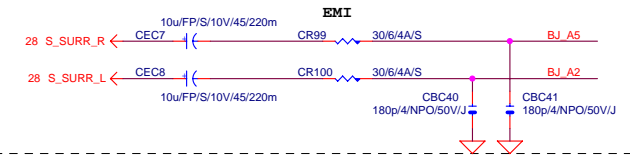
SURROUND



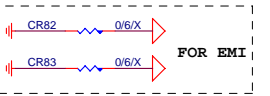
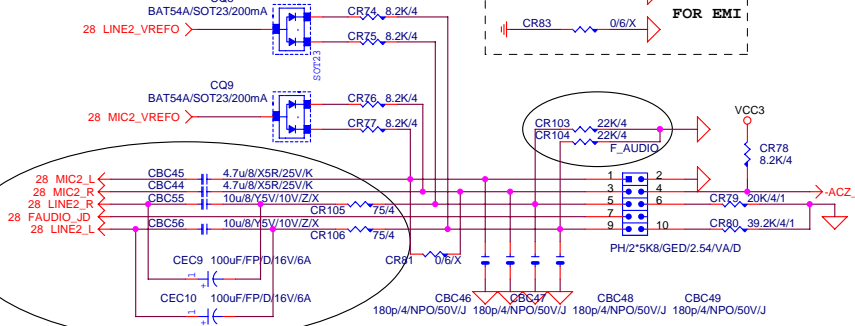
CEN/LFE



SURR BACK



AZALIA FRONT PANEL

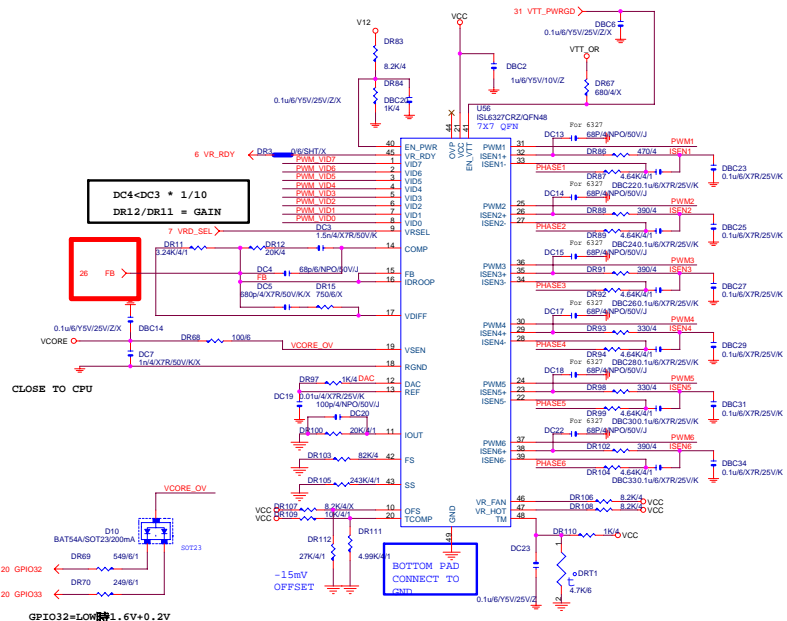


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AUDIO JACK		
965P-DS3		
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MOSFET SOLDER SIDE
需加 ICT TEMP.

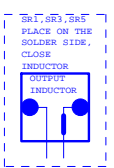
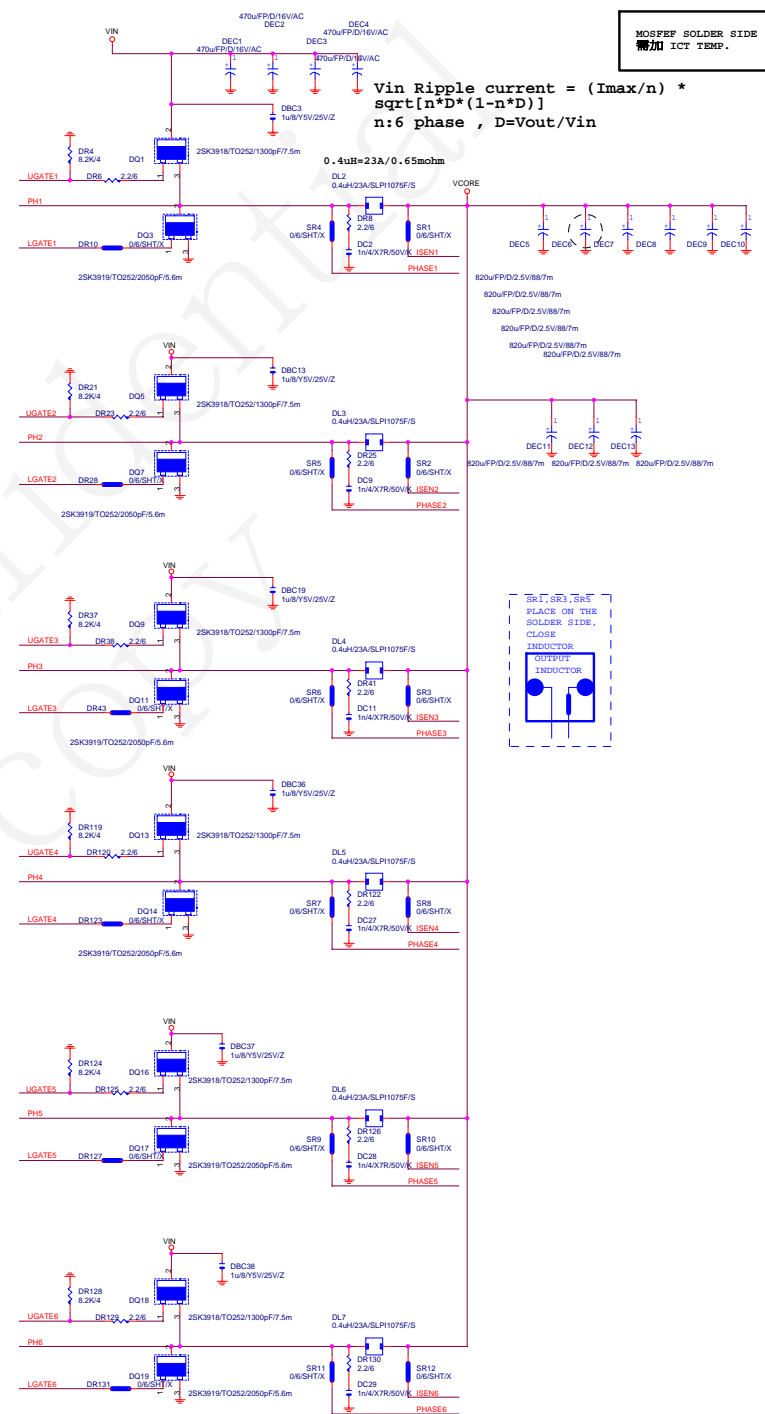
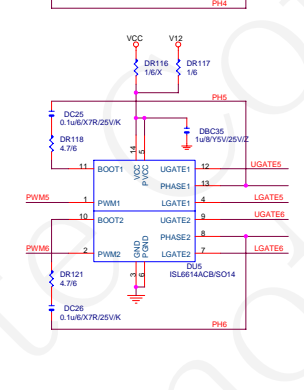
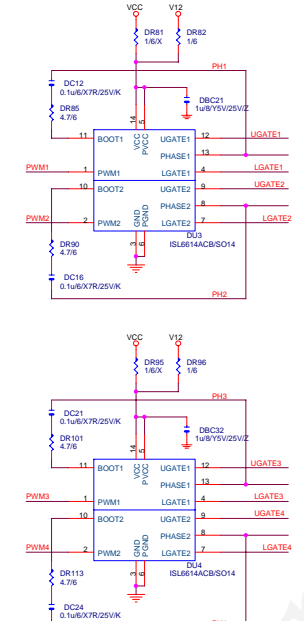
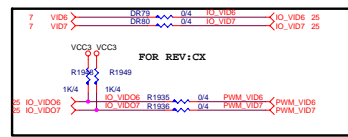
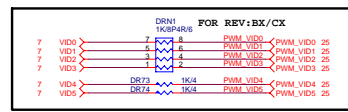
$$V_{in} \text{ Ripple current} = (I_{max}/n) * \sqrt{n * D * (1 - n * D)}$$

n:6 phase, D=Vout/Vin

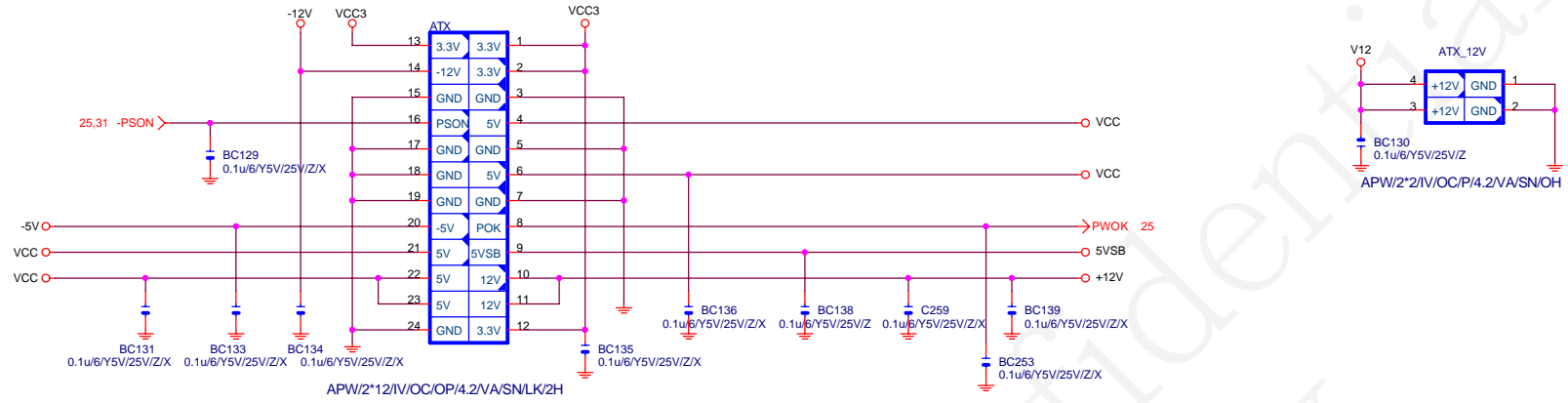


GPIO32=LOW 1.6V+0.2V
GPIO33=LOW 1.6V+0.4V

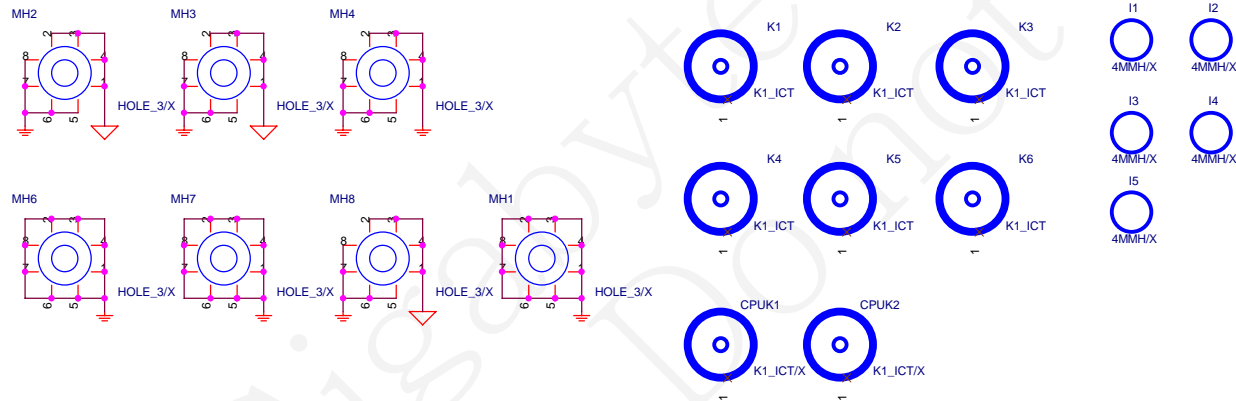
2.8U/V/20A/3P/55052/D



ATX POWER CONNECTOR



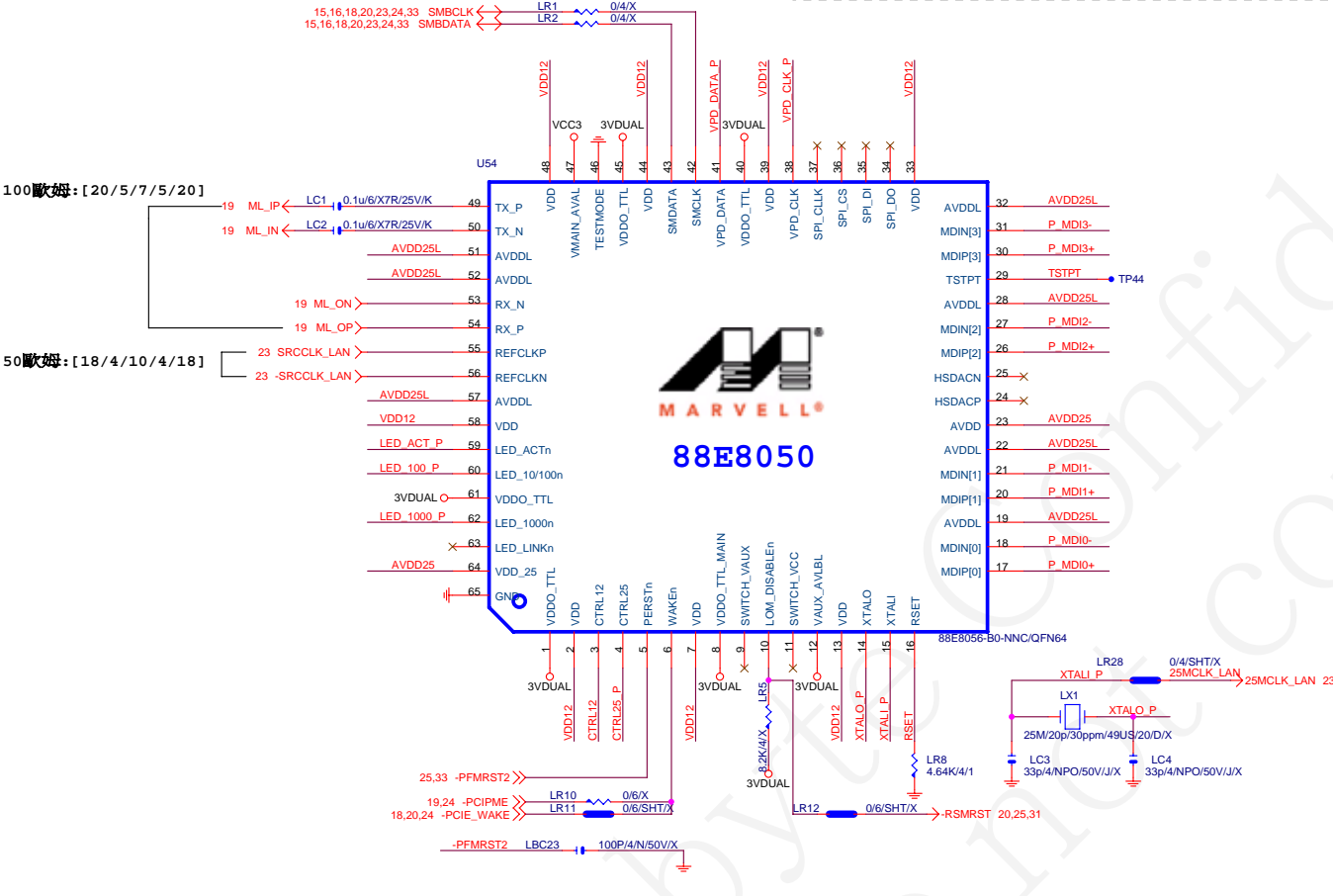
HOLE_3-2--->有鉛



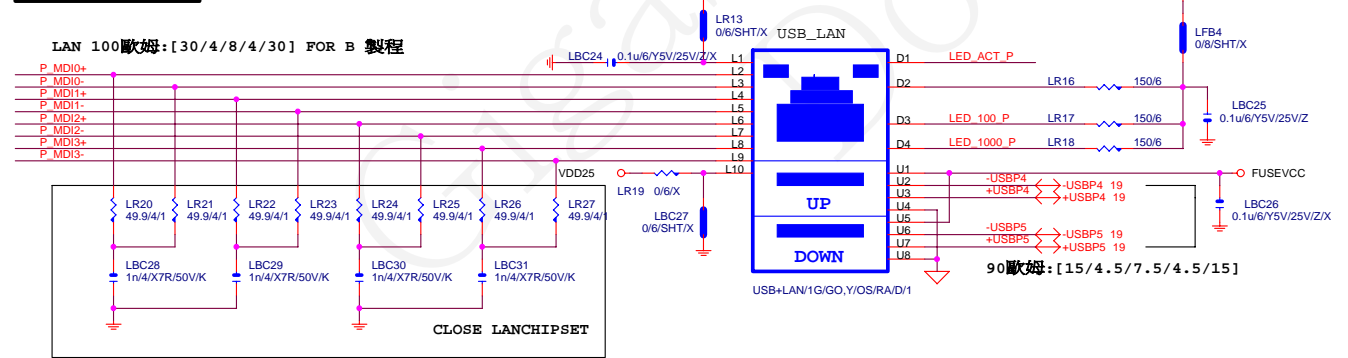
Gigabyte Technology			
Title			
ATX POWER CONNECTOR			
Size	Document Number	965P-DS3	Rev
B			3.3
Date:	星期四, 三月 01, 2007	Sheet	32 of 35
	2		1

PCIE-1G LAN

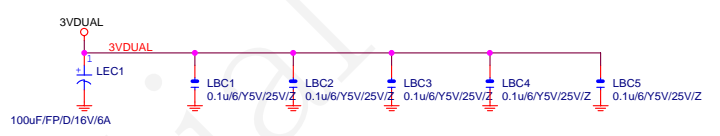
- # Layout Check 注意事項
1. L1 Pin65 需下內層GND, 打 12 VIA
 2. 3VDUAL, VCC3, VDD15_L, AVDD25_L 至少走20mil寬, 並且電容擺設每兩pin至少放一顆Bypass Cap.
 3. X'TAL 25MHz 兩訊號線, TRACE 愈短愈好, 線寬12mil
 4. MDI正負0~3, TRACE 8:7:8, 每對之間保持 40mil



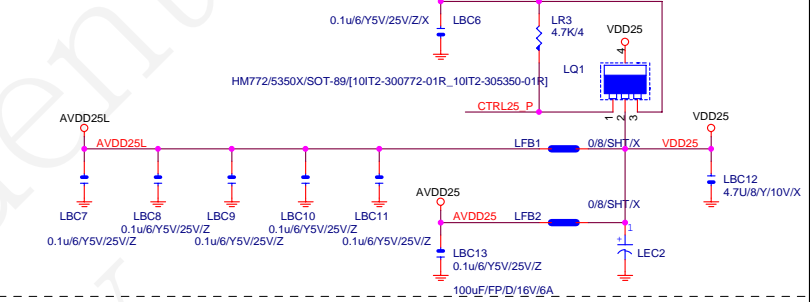
USB_LAN CONNECTOR



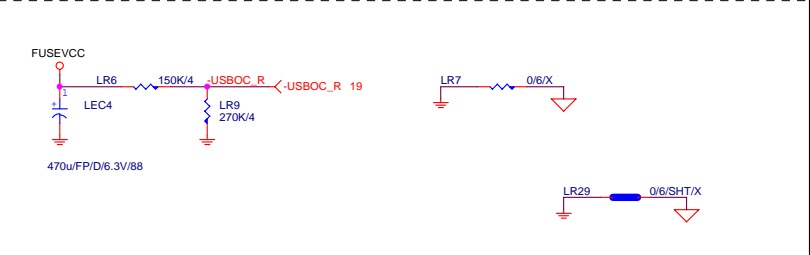
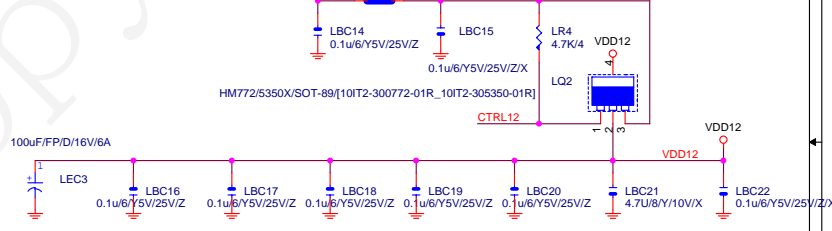
3VDUAL



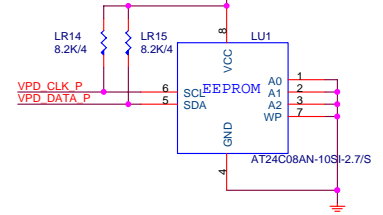
2.5V



1.2V



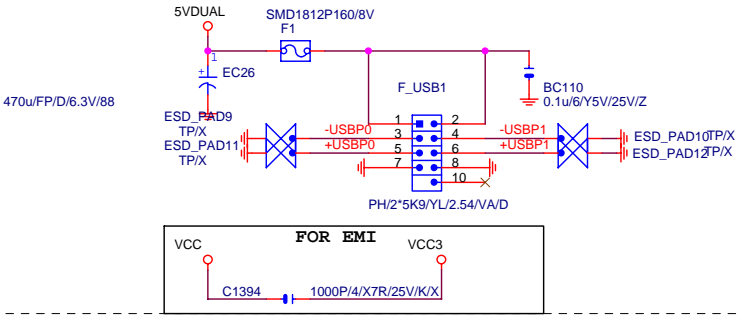
EEPROM



Gigabyte Technology		
Title	MARVELL 88E8001	
Size Custom	Document Number	Rev
	965P-DS3	3.3
Date:	星期四, 三月 01, 2007	Sheet 34 of 35

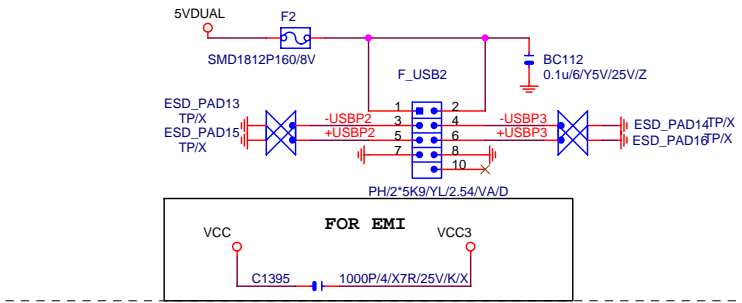
FRONT USB1

- 19 +USBP0 <-> +USBP0
- 19 -USBP0 <-> -USBP0
- 19 +USBP1 <-> +USBP1
- 19 -USBP1 <-> -USBP1



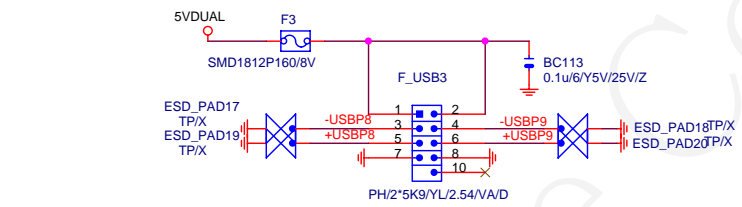
FRONT USB2

- 19 +USBP2 <-> +USBP2
- 19 -USBP2 <-> -USBP2
- 19 +USBP3 <-> +USBP3
- 19 -USBP3 <-> -USBP3



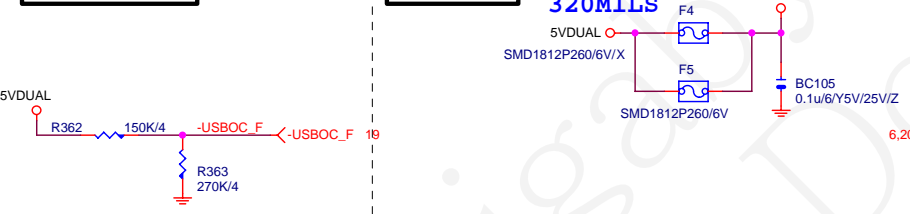
FRONT USB3

- 19 -USBP9 <-> -USBP9
- 19 +USBP9 <-> +USBP9
- 19 -USBP8 <-> -USBP8
- 19 +USBP8 <-> +USBP8

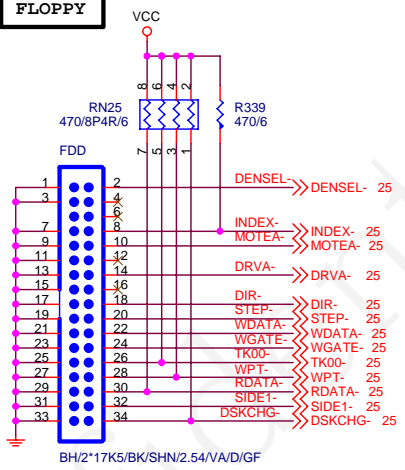


FRONT USB OC1

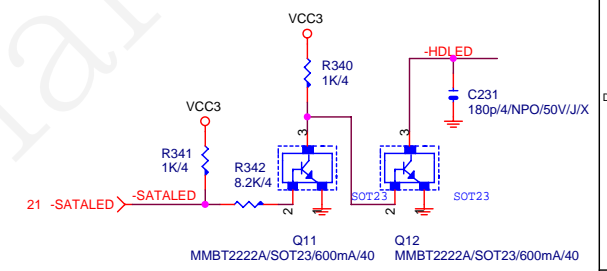
USB POWER



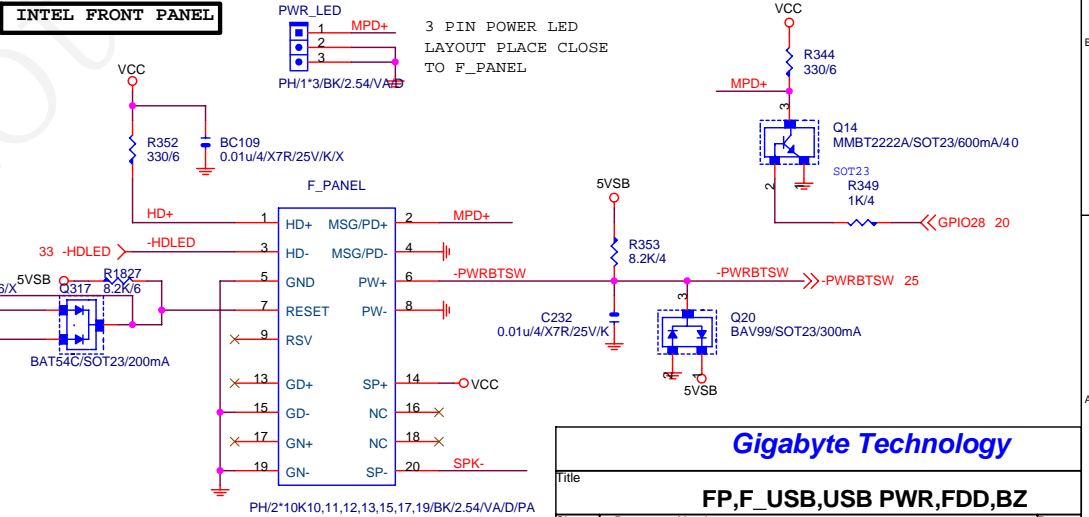
FLOPPY



SATA LED



INTEL FRONT PANEL



Gigabyte Technology		
FP,F_USB,USB PWR,FDD,BZ		
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