

SHEET TITLE

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04	POWER MAP
05	P4_LGA775_A
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07	P4_LGA775_C
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10	GMCH-BROADWATER_DDRII
11	GMCH-BROADWATER_PCI E, DMI
12	GMCH-BROADWATER_INT VGA
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22	ICH8 VCC, GND
23	CLOCK GEN ICS9LPRS587
24	PCI EXPRESS X4 PORT1 SLOT
25	PCI SLOT 1, 2, 3
26	PCIE_1,SW
27	ITE 8718 CX

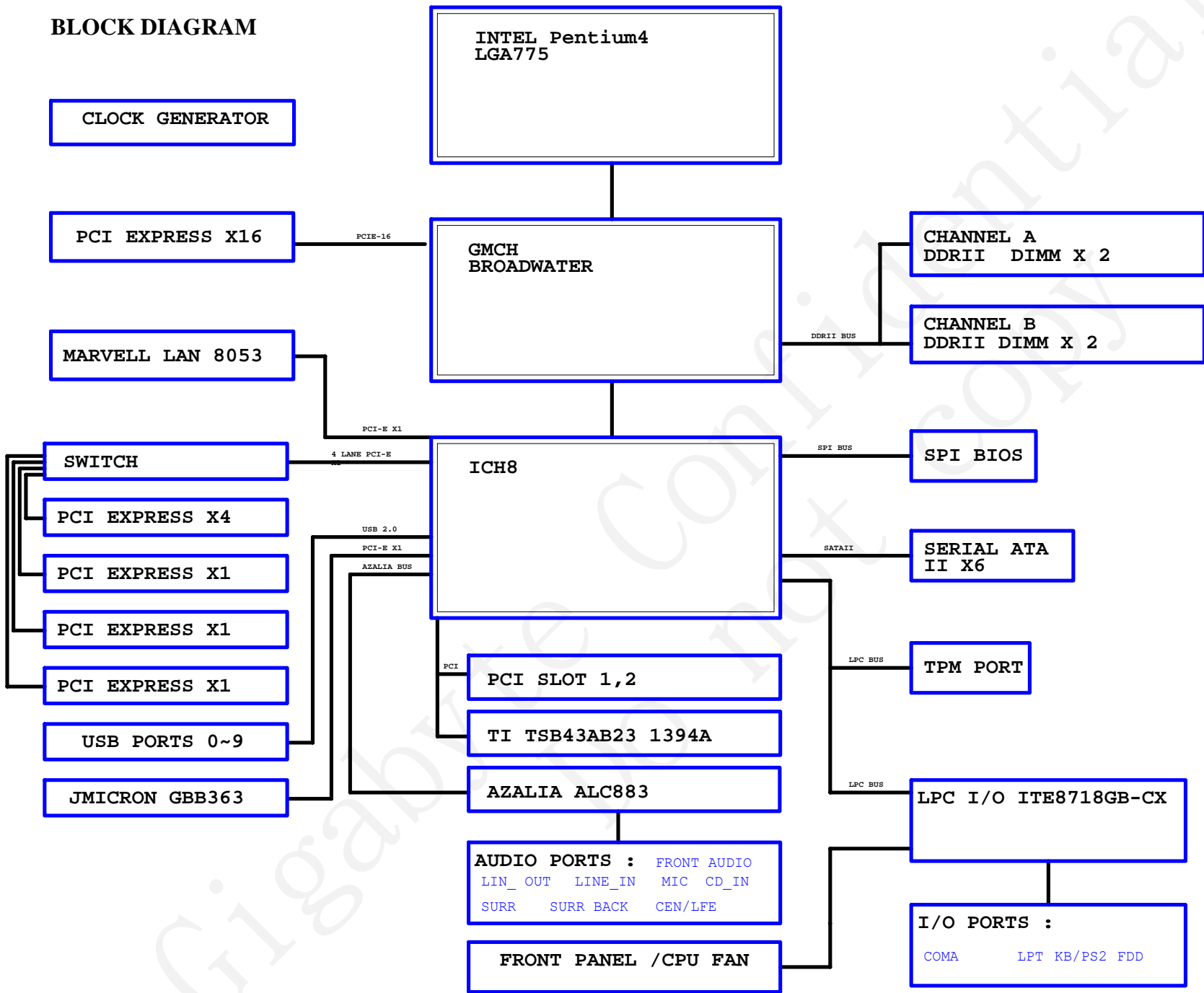
SHEET TITLE

28	COM_LPT, -PROHOT,DYNAMIC OC
29	DUAL BIOS
30	ALC883
31	REAR AUDIO JACK
32	VCORE PWM_ISL6307-1
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41	CI,HWM,KB/MS, FAN CTRL
42	
43	
44	

Gigabyte Technology

Title		
Cover Sheet		
Size	Document Number	Rev
Custom	965P-DS3P	20B
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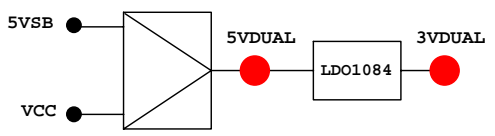
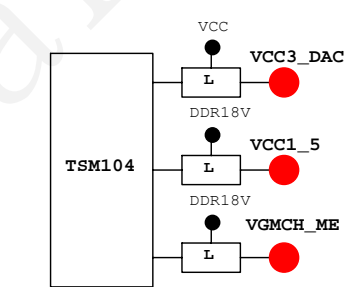
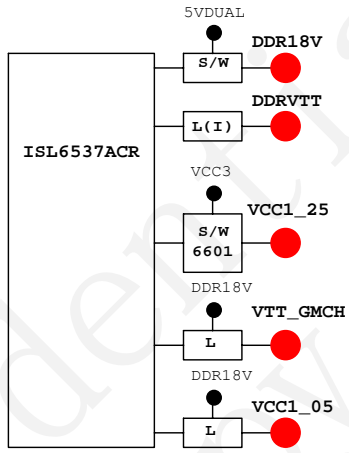
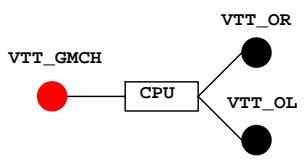
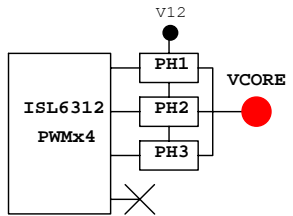
BLOCK DIAGRAM



ICH8 GPIO LIST TABLE

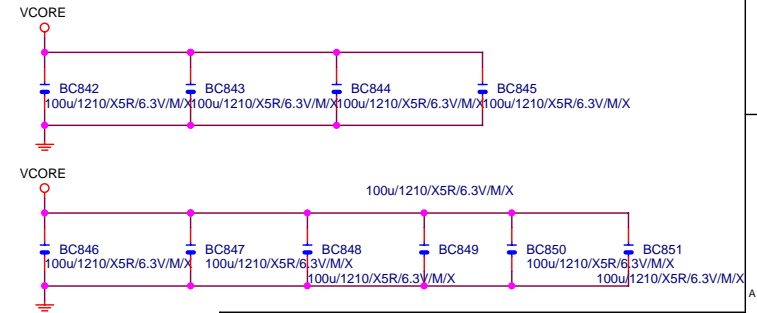
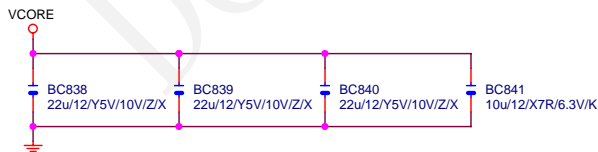
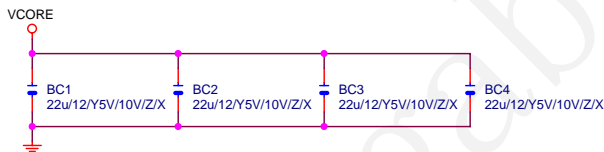
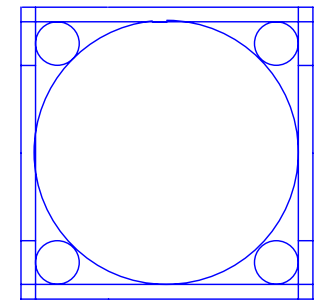
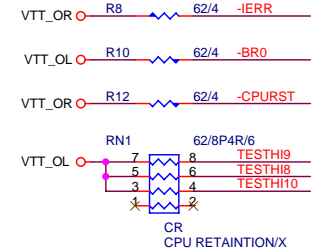
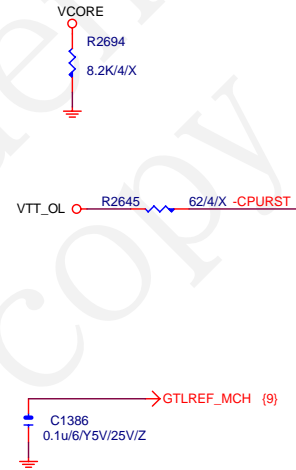
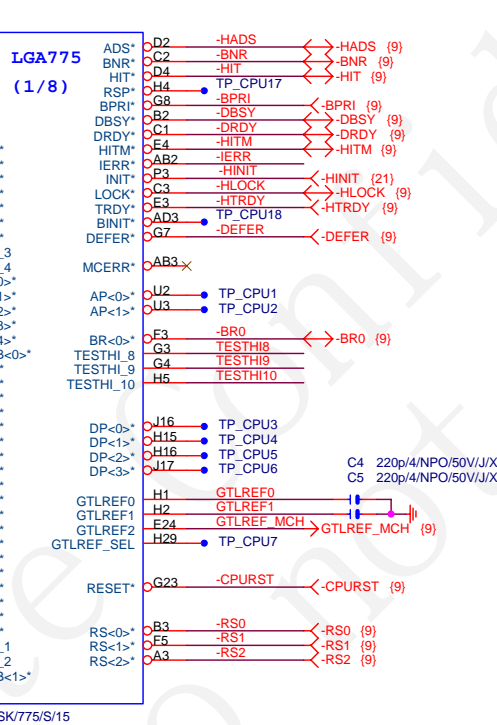
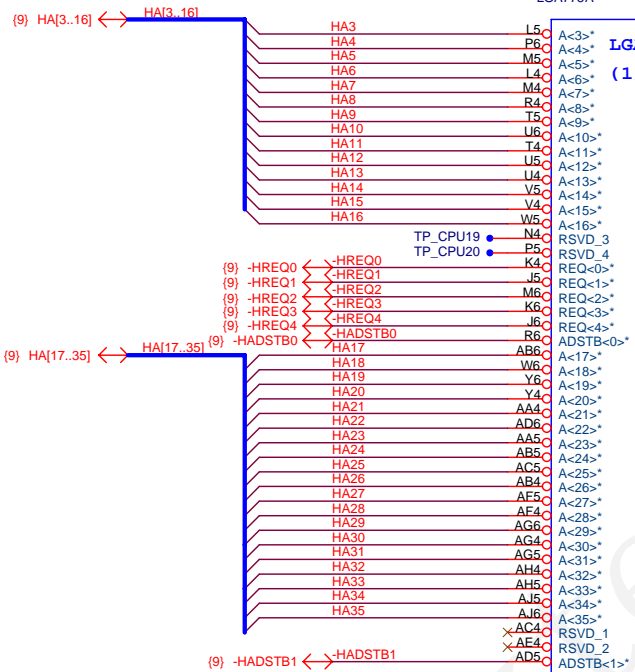
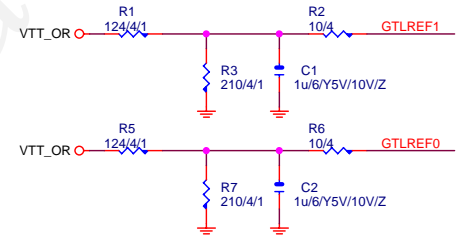
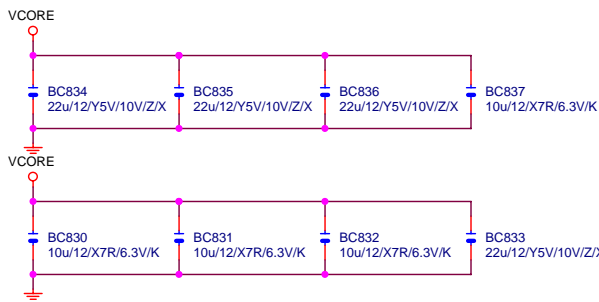
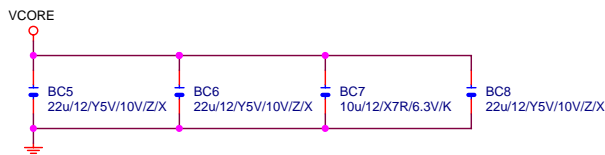
PIN NAME	PWR WELL	AFTER/ FLTRST	USAGE	NOTE
GP0	MAIN	IN	-ACZ_DET	P/U 8.2K VCC3
GP1/TACH1	MAIN	IN	ICH_FAN_TACH1	P/U 8.2K VCC3
GP2/PIRQE#	MAIN	IN	-PIRQE	P/U 8.2K VCC3
GP3/PIRQF#	MAIN	IN	-PIRQF	P/U 8.2K VCC3
GP4/PIRQG#	MAIN	IN	-PIRQG	P/U 8.2K VCC3
GP5/PIRQH#	MAIN	IN	-PIRQH	P/U 8.2K VCC3
GP6/TACH2	MAIN	IN	ICH_FAN_TACH2	P/U 8.2K VCC3
GP7/TACH3	MAIN	IN	VTT_GMCH_OV3	P/U 8.2K VCC3
GP8	STBY	IN	GPIO8(DUALBIOS_INPUT)	P/U 8.2K 3VDUAL
GP9	STBY	OUT	WOL_ONLY	P/D 100K GND
GP10	STBY	IN	CLGPIO1	P/U 8.2K 3VDUAL
GP11/SMBALERT#	STBY	OUT	-SMBALRT	P/U 8.2K 3VDUAL
GP12	STBY	IN	DDR18V_OV5	P/U 8.2K 3VDUAL
GP13	STBY	IN	-LPCPME	P/U 8.2K 3VDUAL
GP14	STBY	IN	CLGPIO2	P/U 8.2K 3VDUAL
GP15	STBY	OUT	RM	N/A
GP16	MAIN	OUT/LOW	RESET	N/A
GP17/TACH0	MAIN	IN	ICH_FAN_TACH0	P/U 8.2K VCC3
GP18	MAIN	OUT	MB_ID1	P/U 8.2K VCC3
GP19	MAIN	IN	VCC15_OV1	P/U 8.2K VCC3
GP20	MAIN	OUT	-SPI_WP0	P/U 1K 3VCL
GP21	MAIN	IN	VCC15_OV3	P/U 8.2K VCC3
GP22	MAIN	IN	VCORE_OV3	P/U 8.2K VCC3
GP23	MAIN	OUT	-LDRQ1	P/U 8.2K VCC3
GP24	STBY	OUT	TLS	P/U 8.2K 3VDUAL
GP25	STBY	IN	MB_ID2(STP_CPU-)	P/U 8.2K 3VDUAL
GP26/S4_STATE#	STBY	OUT	S4_STATE#	P/U 8.2K 3VDUAL
GP27	STBY	OUT/LOW	GPIO27(EL_STATE0)	P/U 8.2K 3VDUAL
GP28	STBY	OUT/LOW	-SPI_WP0	N/A
GP29/OC5#	STBY	IN	-USBOC_R	P/U FUSEVCC
GP30/OC6#	STBY	IN	-USBOC_R	P/U FUSEVCC
GP31/OC7#	STBY	IN	-USBOC_R	P/U FUSEVCC
GP32	MAIN	OUT	DUAL BIOS	P/U 100K+1M VCC3
GP33	MAIN	OUT	DUAL BIOS	P/U 8.2K VCC3
GP34	MAIN	OUT/LOW	GPIO34/SMB_RST	N/A
GP35	MAIN	OUT	VCC15_OV2	N/A
GP36	MAIN	IN	VCORE_OV4	P/U 8.2K VCC3
GP37	MAIN	IN	VCORE_OV5	P/U 8.2K VCC3
GP38	MAIN	IN	VCORE_OV2	P/U 8.2K VCC3
GP39	MAIN	IN	GPIO39	P/D 8.2K GND
GP48	MAIN	IN	VCORE_OV1	P/U 8.2K VCC3
GP49	MAIN	IN	CPUPWROK	P/U 100 VTT_OL

VCORE:3 PHASE PWM--ISL6312



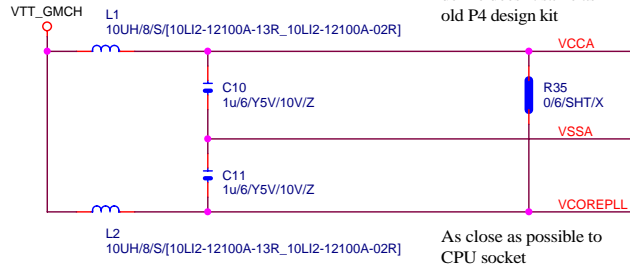
	3 pin FAN control	4 pin FAN control	FAN speed	Controller
CPU FAN	FANPWM1	FANPWM3	FANIO1	IT8718
	ICH_FAN_PWM2	ICH_FAN_PWM0	ICH_FAN_TACH0	ICH8
SYS FAN	FANPWM2	N/A	FANIO2	IT8718
	ICH_FAN_PWM1	N/A	ICH_FAN_TACH1	ICH8
PWR FAN	N/A	N/A	FANIO3	IT8718
			ICH_FAN_TACH2	ICH8

Gigabyte Technology			
Title			
TABLE LIST			
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B	965P-DS3P	2.0B	
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Title		
P4_LGA775-A		
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B	965P-DS3P	2.0B
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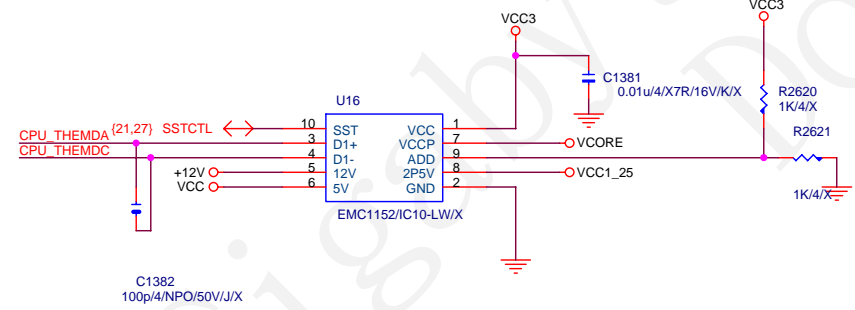
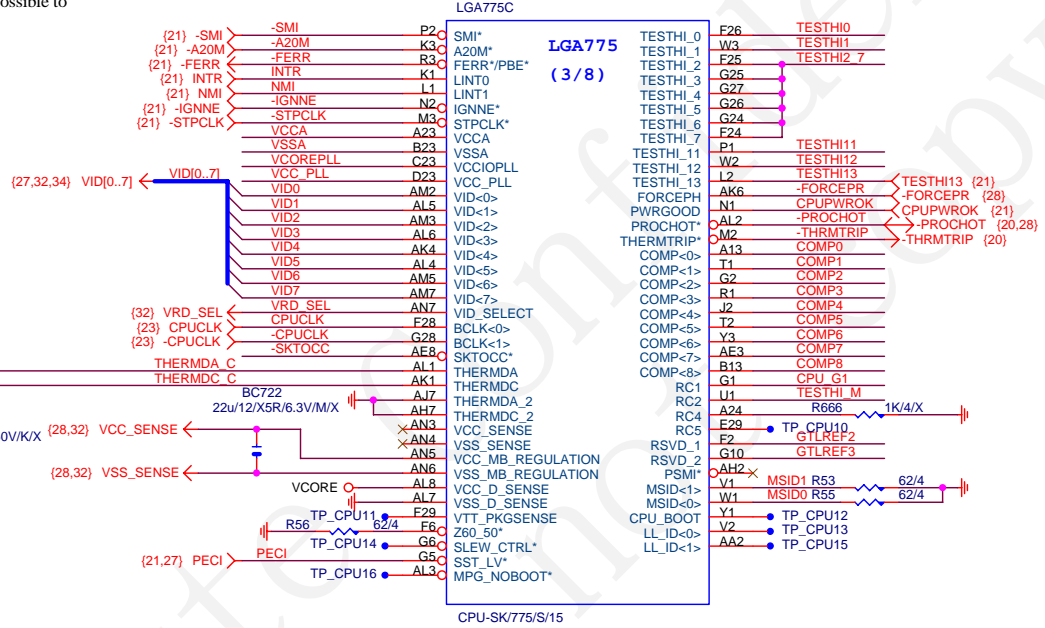
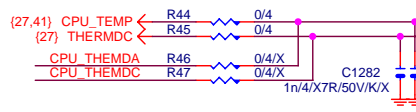
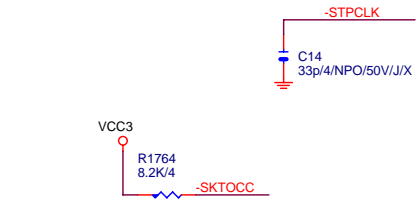
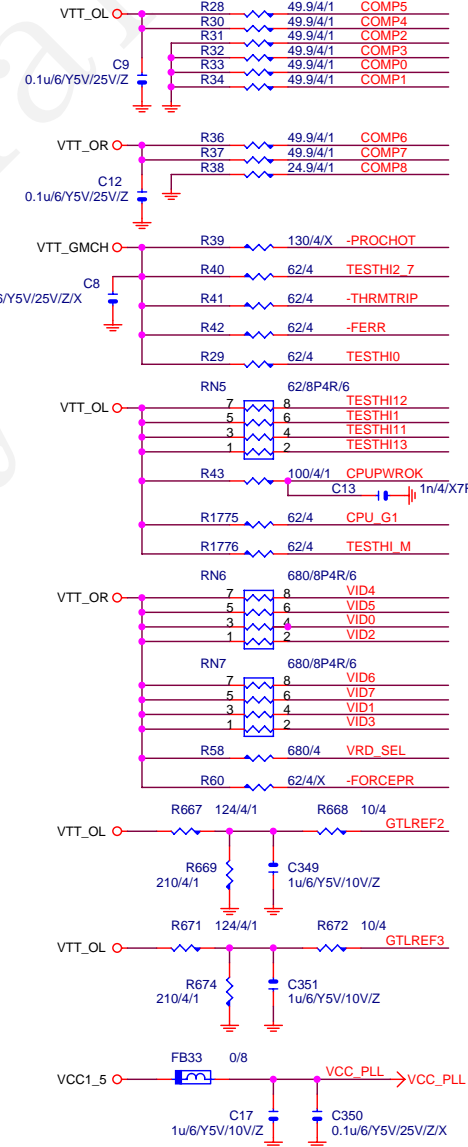
Note:
VCCA & VCOREPLL
define doesn't same as
old P4 design kit



Trace width doesn't
less than 12 Mil

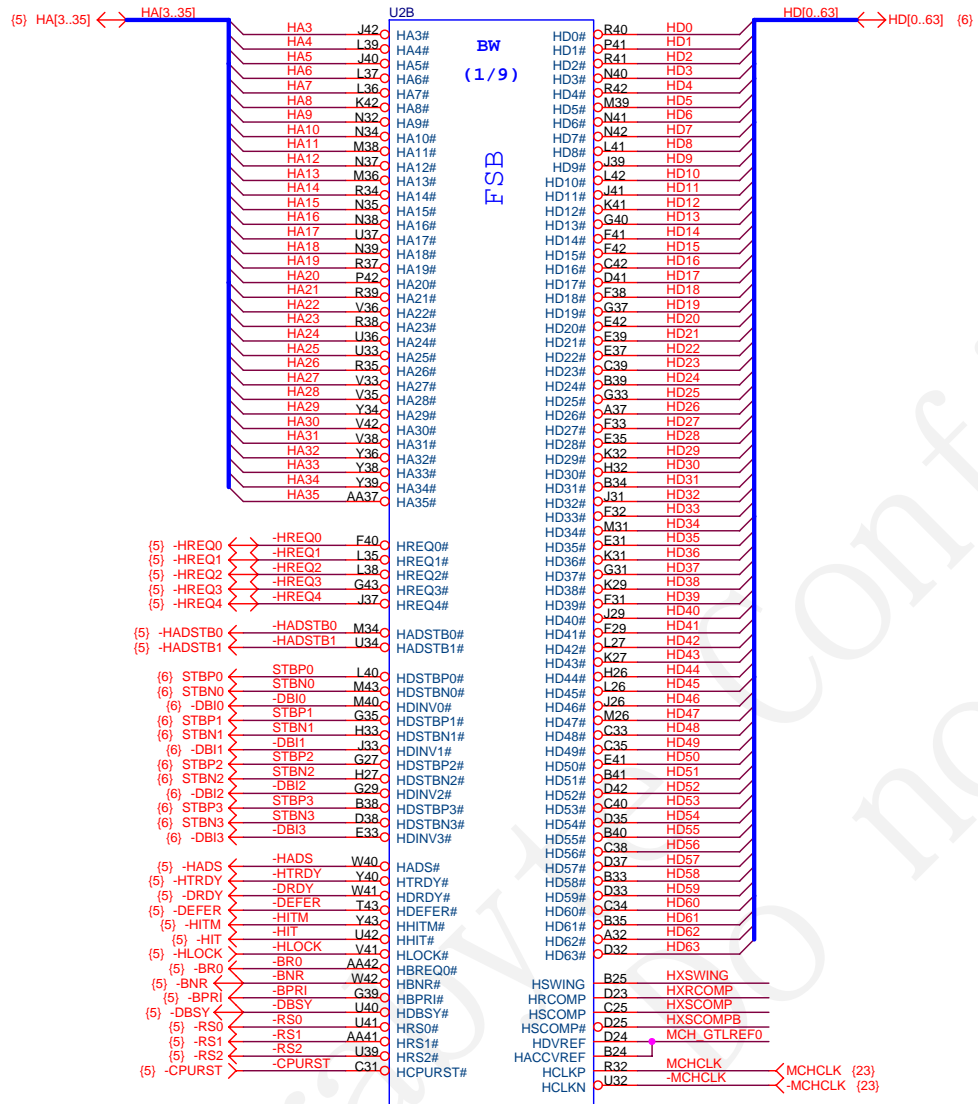
As close as possible to
CPU socket

Place outside of CPU socket

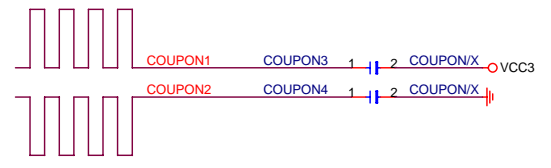
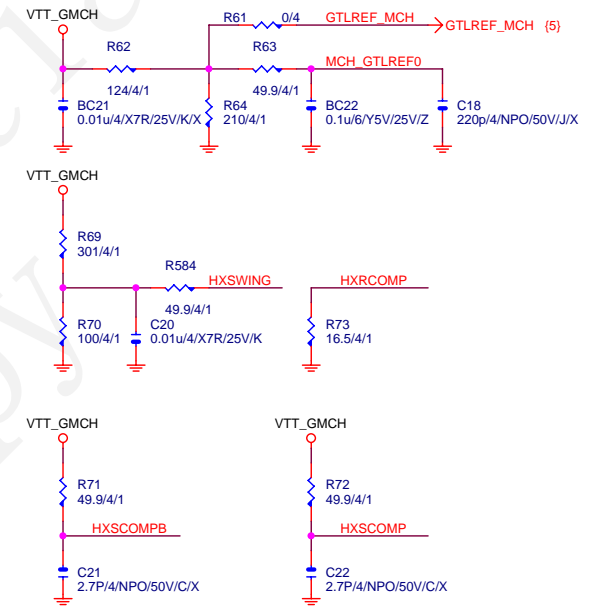


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P4_LGA775-C		
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PECE: Platform Environment Control Interface



LE82P965-C2/BAG1226
CPU INTERFACE



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GMCH-HOST		
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U2C

MAAA0	BA31	SMA_A0	BW	SDOS_A0	AU4	DOSA0
MAAA1	BA25	SMA_A1	(3/9)	SDOS_A0#	AR3	-DOSA0
MAAA2	BA26	SMA_A2		SDM_A0	AR2	DMA0
MAAA3	BA25	SMA_A3		SDO_A0	AR6	MDA0
MAAA4	AY25	SMA_A4		SDO_A1	AR4	MDA1
MAAA5	BA23	SMA_A5		SDO_A2	AV2	MDA3
MAAA6	AY24	SMA_A7		SDO_A3	AP3	MDA4
MAAA7	AY23	SMA_A6		SDO_A4	AV2	MDA3
MAAA8	BA22	SMA_A8		SDO_A5	AP2	MDA5
MAAA9	BA22	SMA_A9		SDO_A6	AU1	MDA6
MAAA10	AY33	SMA_A10		SDO_A7	AV4	MDA7
MAAA11	AW21	SMA_A10		SDOS_A1	BB3	DOSA1
MAAA12	BA22	SMA_A11		SDOS_A1#	BA4	DMA1
MAAA13	AY38	SMA_A12		SDM_A1	AY2	MDA8
MAAA14	BA21	SMA_A14		SDO_A8	AV3	MDA9
(15.17) -SWEA	-SWEA	SWE_A#		SDO_A9	BB6	MDA10
(15.17) -SCASA	-SCASA	SCAS_A#		SDO_A10	BB8	MDA11
(15.17) -SRASA	-SRASA	SRAS_A#		SDO_A11	AW2	MDA12
(15.17) SBAA0	SBA0	SBS_A0		SDO_A12	AW3	MDA13
(15.17) SBAA1	SBA1	SBS_A1		SDO_A13	BA5	MDA14
(15.17) SBAA2	SBA2	SBS_A2		SDO_A14	BB4	MDA15
(15.17) CSA0	-CSA0	AW35	SCS_A0#	SDO_A15	BB0	DOSA2
(15.17) CSA1	-CSA1	BA35	SCS_A1#	SDOS_A2	BA9	-DOSA2
(15.17) CSA2	-CSA2	BA34	SCS_A2#	SDM_A2	AY9	DMA2
(15.17) CSA3	-CSA3	BB38	SCS_A3#	SDO_A16	AY7	MDA16
(15.17) CKEA0	-CKEA0	BC20	SCKE_A0	SDO_A17	BC7	MDA17
(15.17) CKEA1	-CKEA1	AY20	SCKE_A1	SDO_A18	AW11	MDA18
(15.17) CKEA2	-CKEA2	AY21	SCKE_A2	SDO_A19	BB6	MDA20
(15.17) CKEA3	-CKEA3	BA19	SCKE_A3	SDO_A20	BB4	MDA21
(15) MODT_A0	AY37	SODT_A0		SDO_A21	BA10	MDA22
(15) MODT_A1	BA38	SODT_A1		SDO_A22	BB2	MDA21
(15) MODT_A2	BB35	SODT_A2		SDO_A23	BA10	MDA22
(15) MODT_A3	BA39	SODT_A3		SDO_A24	BB10	MDA23
(15) DCLKA0	-DCLKA0	AU31	SCLK_A0	SDOS_A3	AT20	DOSA3
(15) -DCLKA0	-DCLKA0	AR31	SCLK_A0#	SDOS_A3#	AU18	-DOSA3
(15) DCLKA1	-DCLKA1	AR27	SCLK_A1	SDM_A3	AN18	DMA3
(15) -DCLKA1	-DCLKA1	AN27	SCLK_A1#	SDO_A24	AT18	MDA24
(15) DCLKA2	-DCLKA2	AV33	SCLK_A2	SDO_A25	AR18	MDA25
(15) -DCLKA2	-DCLKA2	AV33	SCLK_A2#	SDO_A26	AU21	MDA26
(15) DCLKA3	-DCLKA3	AP29	SCLK_A3	SDO_A27	AT21	AV29
(15) -DCLKA3	-DCLKA3	AP29	SCLK_A3#	SDO_A28	AP17	MDA28
(15) DCLKA4	-DCLKA4	AP31	SCLK_A4	SDO_A29	AN17	MDA29
(15) -DCLKA4	-DCLKA4	AP31	SCLK_A4#	SDO_A30	AP20	MDA30
(15) DCLKA5	-DCLKA5	AT33	SCLK_A5	SDO_A31	AV20	MDA31
(15) -DCLKA5	-DCLKA5	AT33	SCLK_A5#	SDO_A32	AR41	DOSA4
(15) DCLKA5	-DCLKA5	AU33	SCLK_A5#	SDOS_A4	AR40	-DOSA4
(15) -DCLKA5	-DCLKA5	AU33	SCLK_A5#	SDOS_A4#	AU43	DMA4
(15) -DCLKA5	-DCLKA5	AU33	SCLK_A5#	SDM_A4	AV42	MDA32
(15) -DCLKA5	-DCLKA5	AU33	SCLK_A5#	SDO_A32	AU40	MDA33
(15) -DCLKA5	-DCLKA5	AU33	SCLK_A5#	SDO_A33	AR2	MDA34
(15) -DCLKA5	-DCLKA5	AU33	SCLK_A5#	SDO_A34	AN39	MDA35
(15) -DCLKA5	-DCLKA5	AU33	SCLK_A5#	SDO_A35	AV40	MDA36
(15) -DCLKA5	-DCLKA5	AU33	SCLK_A5#	SDO_A36	AV41	MDA37
(15) -DCLKA5	-DCLKA5	AU33	SCLK_A5#	SDO_A37	AR42	MDA38
(15) -DCLKA5	-DCLKA5	AU33	SCLK_A5#	SDO_A38	AV41	MDA38
(15) -DCLKA5	-DCLKA5	AU33	SCLK_A5#	SDO_A39	AP41	MDA39
(15) -DCLKA5	-DCLKA5	AU33	SCLK_A5#	SDO_A40	AL41	DOSA5
(15) -DCLKA5	-DCLKA5	AU33	SCLK_A5#	SDOS_A5	AL40	-DOSA5
(15) -DCLKA5	-DCLKA5	AU33	SCLK_A5#	SDOS_A5#	AM43	DMA5
(15) -DCLKA5	-DCLKA5	AU33	SCLK_A5#	SDM_A5	AN41	MDA40
(15) -DCLKA5	-DCLKA5	AU33	SCLK_A5#	SDO_A40	AM39	MDA41
(15) -DCLKA5	-DCLKA5	AU33	SCLK_A5#	SDO_A41	AK42	MDA42
(15) -DCLKA5	-DCLKA5	AU33	SCLK_A5#	SDO_A42	AK41	MDA43
(15) -DCLKA5	-DCLKA5	AU33	SCLK_A5#	SDO_A43	AN40	MDA44
(15) -DCLKA5	-DCLKA5	AU33	SCLK_A5#	SDO_A44	AM42	MDA45
(15) -DCLKA5	-DCLKA5	AU33	SCLK_A5#	SDO_A45	AL42	MDA46
(15) -DCLKA5	-DCLKA5	AU33	SCLK_A5#	SDO_A46	AL39	MDA47
(15) -DCLKA5	-DCLKA5	AU33	SCLK_A5#	SDO_A47	AG42	DOSA6
(15) -DCLKA5	-DCLKA5	AU33	SCLK_A5#	SDOS_A6	AG41	-DOSA6
(15) -DCLKA5	-DCLKA5	AU33	SCLK_A5#	SDOS_A6#	AG40	DMA6
(15) -DCLKA5	-DCLKA5	AU33	SCLK_A5#	SDM_A6	AJ40	MDA48
(15) -DCLKA5	-DCLKA5	AU33	SCLK_A5#	SDO_A48	AH43	MDA49
(15) -DCLKA5	-DCLKA5	AU33	SCLK_A5#	SDO_A49	AF39	MDA50
(15) -DCLKA5	-DCLKA5	AU33	SCLK_A5#	SDO_A50	AE40	MDA51
(15) -DCLKA5	-DCLKA5	AU33	SCLK_A5#	SDO_A51	AJ42	MDA52
(15) -DCLKA5	-DCLKA5	AU33	SCLK_A5#	SDO_A52	AK41	MDA53
(15) -DCLKA5	-DCLKA5	AU33	SCLK_A5#	SDO_A53	AE41	MDA54
(15) -DCLKA5	-DCLKA5	AU33	SCLK_A5#	SDO_A54	AL42	MDA55
(15) -DCLKA5	-DCLKA5	AU33	SCLK_A5#	SDO_A55	AE42	MDA55
(15) -DCLKA5	-DCLKA5	AU33	SCLK_A5#	SDO_A56	AC42	DOSA7
(15) -DCLKA5	-DCLKA5	AU33	SCLK_A5#	SDOS_A7	AC41	-DOSA7
(15) -DCLKA5	-DCLKA5	AU33	SCLK_A5#	SDOS_A7#	AC40	DMA7
(15) -DCLKA5	-DCLKA5	AU33	SCLK_A5#	SDM_A7	AD40	MDA56
(15) -DCLKA5	-DCLKA5	AU33	SCLK_A5#	SDO_A56	AD43	MDA57
(15) -DCLKA5	-DCLKA5	AU33	SCLK_A5#	SDO_A57	AB41	MDA58
(15) -DCLKA5	-DCLKA5	AU33	SCLK_A5#	SDO_A58	AE39	MDA59
(15) -DCLKA5	-DCLKA5	AU33	SCLK_A5#	SDO_A59	AE42	MDA60
(15) -DCLKA5	-DCLKA5	AU33	SCLK_A5#	SDO_A60	AE41	MDA61
(15) -DCLKA5	-DCLKA5	AU33	SCLK_A5#	SDO_A61	AG39	MDA62
(15) -DCLKA5	-DCLKA5	AU33	SCLK_A5#	SDO_A62	AB42	MDA63
(15) -DCLKA5	-DCLKA5	AU33	SCLK_A5#	SDO_A63	AB42	MDA63

LE82P965-C2/BAG1226

DDR INTERFACE

DDR_0

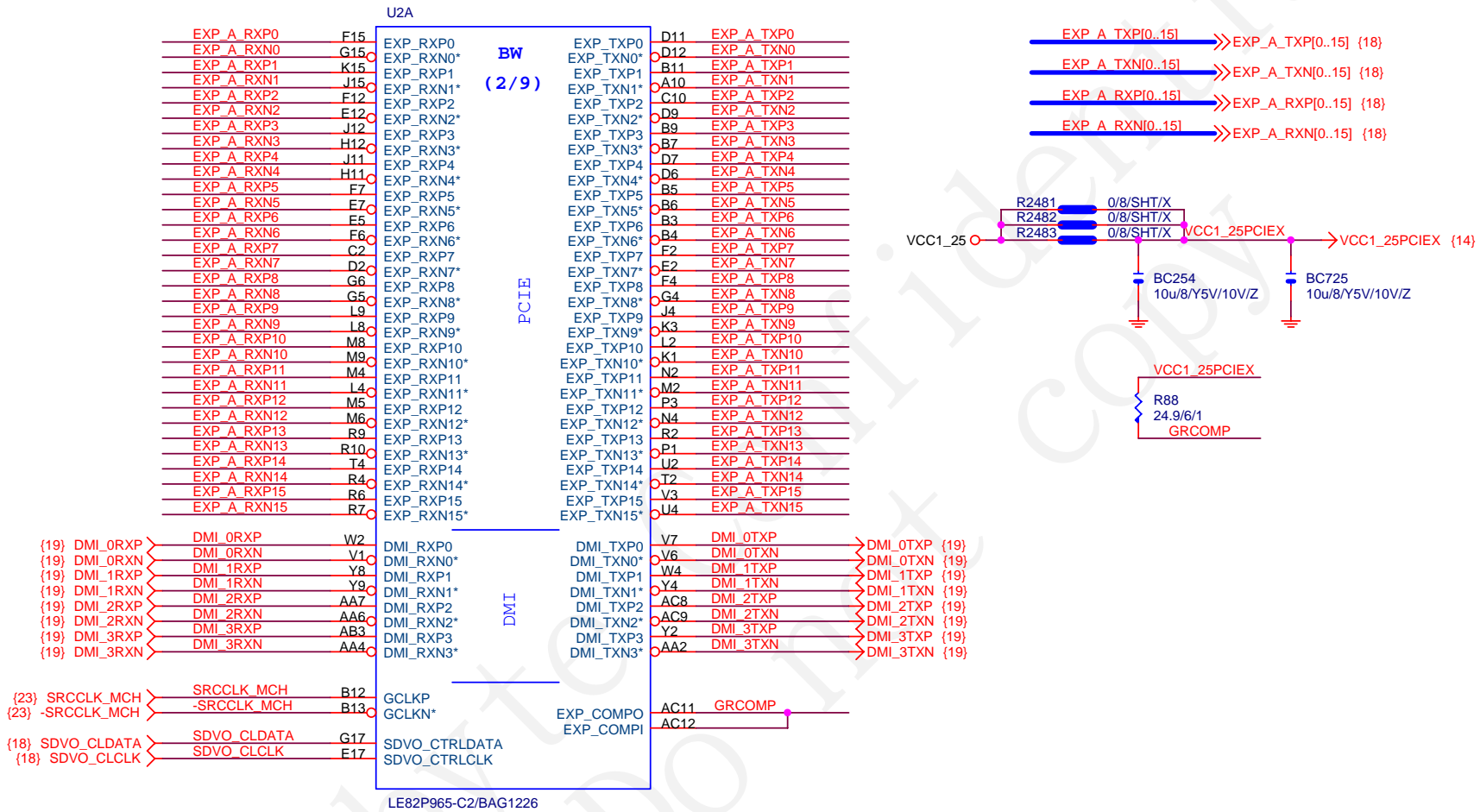
RESERVED

RESERVED

RESERVED

U2D

MAAB0	BB17	SMA_B0	BW	SDOS_B0	AV6	DOSB0
MAAB1	AV17	SMA_B1	(4/9)	SDOS_B0#	AU5	-DOSB0
MAAB2	BA17	SMA_B2		SDM_B0	AR7	DMB0
MAAB3	BC16	SMA_B3		SDO_B0	AN7	MDB0
MAAB4	AW15	SMA_B4		SDO_B1	AN6	MDB1
MAAB5	BA15	SMA_B5		SDO_B2	AW7	MDB3
MAAB6	BB15	SMA_B6		SDO_B3	AN6	MDB5
MAAB7	BA14	SMA_B6		SDO_B4	AN6	MDB5
MAAB8	BB14	SMA_B7		SDO_B5	AN6	MDB6
MAAB9	AV15	SMA_B8		SDO_B6	AU7	MDB7
MAAB10	MD14	SMA_B9		SDOS_B1	AR12	DOSB1
MAAB11	BB14	SMA_B10		SDOS_B1#	AR12	-DOSB1
MAAB12	BB13	SMA_B11		SDM_B1	AV2	DMB1
MAAB13	AV23	SMA_B12		SDO_B8	AT11	MD8
MAAB14	AV13	SMA_B14		SDO_B9	AU11	MD9
(16.17) -SWEB	-SWEB	SWE_B#		SDO_B9#	AP13	MDB10
(16.17) -SCASB	-SCASB	SCAS_B#		SDO_B10	AR13	MDB11
(16.17) -SRASB	-SRASB	SRAS_B#		SDO_B11	AR11	MDB12
(16.17) SBAB0	SBA0	SBS_B0		SDO_B12	AU9	MDB13
(16.17) SBAB1	SBA1	SBS_B1		SDO_B14	AV12	MDB14
(16.17) SBAB2	SBA2	SBS_B2		SDO_B15	AU12	MDB15
(16.17) CSB0	-CSB0	BB27C	SCS_B0#	SDOS_B2	AP15	DOSB2
(16.17) CSB1	-CSB1	BB30C	SCS_B1#	SDOS_B2#	AR15	-DOSB2
(16.17) CSB2	-CSB2	AV27C	SCS_B2#	SDM_B2	AW13	DMB2
(16.17) CSB3	-CSB3	AV13C	SCS_B3#	SDO_B16	AU15	MDB16
(16.17) CKEB0	-CKEB0	AY12	SCKE_B0	SDO_B17	AV13	MDB17
(16.17) CKEB1	-CKEB1	AY12	SCKE_B1	SDO_B18	AU17	MDB18
(16.17) CKEB2	-CKEB2	BB11	SCKE_B2	SDO_B19	AT17	MDB19
(16.17) CKEB3	-CKEB3	BB11	SCKE_B3	SDO_B20	AU13	MDB20
(16.17) CKEB3	-CKEB3	BB11	SCKE_B3	SDO_B21	AM15	MDB21
(16) MODT_B0	BA23	SODT_B0		SDO_B22	AV15	MDB22
(16) MODT_B1	BA30	SODT_B1		SDO_B23	AW17	MDB23
(16) MODT_B2	BB29	SODT_B2		SDOS_B3	AT24	DOSB3
(16) MODT_B3	BB31	SODT_B3		SDOS_B3#	AU28	-DOSB3
(16) DCLKB0	-DCLKB0	AV31	SCLK_B0	SDM_B3	AP23	DMB3
(16) -DCLKB0	-DCLKB0	AV31C	SCLK_B0#	SDO_B24	AV24	MDB24
(16) DCLKB1	-DCLKB1	AT27	SCLK_B1	SDO_B25	AT23	MDB25
(16) -DCLKB1	-DCLKB1	AT27C	SCLK_B1#	SDO_B26	AT26	MDB26
(16) DCLKB2	-DCLKB2	AV23	SCLK_B2	SDO_B27	AT26	MDB27
(16) -DCLKB2	-DCLKB2	AV23C	SCLK_B2#	SDO_B28	AT27	MDB28
(16) DCLKB3	-DCLKB3	AR29C	SCLK_B3	SDO_B29	AW23	MDB29
(16) -DCLKB3	-DCLKB3	AR29C	SCLK_B3#	SDO_B30	AR24	MDB30
(16) DCLKB4	-DCLKB4	AV29	SCLK_B4	SDO_B31	AN26	MDB31
(16) -DCLKB4	-DCLKB4	AV29C	SCLK_B4#	SDO_B32	AV39	DOSB4
(16) DCLKB5	-DCLKB5	AN33	SCLK_B5	SDOS_B4	AU39	-DOSB4
(16) -DCLKB5	-DCLKB5	AN33C	SCLK_B5#	SDM_B4	AV37	DMB4
(16) -DCLKB5	-DCLKB5	AN33C	SCLK_B5#	SDO_B32	AV37	MDB32
(16) -DCLKB5	-DCLKB5	AN33C	SCLK_B5#	SDO_B33	AV38	MDB33
(16) -DCLKB5	-DCLKB5	AN33C	SCLK_B5#	SDO_B34	AN36	MDB34
(16) -DCLKB5	-DCLKB5	AN33C	SCLK_B5#	SDO_B35	AN37	MDB35
(16) -DCLKB5	-DCLKB5	AN33C	SCLK_B5#	SDO_B36	AU35	MDB37
(16) -DCLKB5	-DCLKB5	AN33C	SCLK_B5#	SDO_B37	AN35	MDB38
(16) -DCLKB5	-DCLKB5	AN33C	SCLK_B5#	SDO_B38	AN35	MDB38
(16) -DCLKB5	-DCLKB5	AN33C	SCLK_B5#	SDO_B39	AR37	MDB39
(16) -DCLKB5	-DCLKB5	AN33C	SCLK_B5#	SDOS_B5	AL35	DOSB5
(16) -DCLKB5	-DCLKB5	AN33C	SCLK_B5#	SDOS_B5#	AL34	-DOSB5
(16) -DCLKB5	-DCLKB5	AN33C	SCLK_B5#	SDM_B5	AM37	DMB5
(16) -DCLKB5	-DCLKB5	AN33C	SCLK_B5#	SDO_B40	AM35	MDB40
(16) -DCLKB5	-DCLKB5	AN33C	SCLK_B5#	SDO_B41	AM38	MDB41
(16) -DCLKB5	-DCLKB5	AN33C	SCLK_B5#	SDO_B42	AJ34	MDB42
(16) -DCLKB5	-DCLKB5	AN33C	SCLK_B5#	SDO_B43	AR39	MDB44
(16) -DCLKB5	-DCLKB5	AN33C	SCLK_B5#	SDO_B44	AM44	MDB45
(16) -DCLKB5	-DCLKB5	AN33C	SCLK_B5#	SDO_B45	AL37	MDB46
(16) -DCLKB5	-DCLKB5	AN33C	SCLK_B5#	SDO_B46	AL32	MDB47
(16) -DCLKB5	-DCLKB5	AN33C	SCLK_B5#	SDO_B47	AG35	DOSB6
(16) -DCLKB5	-DCLKB5	AN33C	SCLK_B5#	SDOS_B6	AG36	-DOSB6
(16) -DCLKB5	-DCLKB5	AN33C	SCLK_B5#	SDOS_B6#	AG39	DMB6
(16) -DCLKB5	-DCLKB5	AN33C	SCLK_B5#	SDO_B48	AG38	MDB48
(16) -DCLKB5	-DCLKB5	AN33C	SCLK_B5#	SDO_B49	AJ38	MDB49
(16) -DCLKB5	-DCLKB5	AN33C	SCLK_B5#	SDO_B50	AF36	MDB50
(16) -DCLKB5	-DCLKB5	AN33C	SCLK_B5#	SDO_B51	AF33	MDB51
(16) -DCLKB5	-DCLKB5	AN33C	SCLK_B5#	SDO_B52	AF35	MDB52
(16) -DCLKB5	-DCLKB5	AN33C	SCLK_B5#	SDO_B53	AJ37	MDB53
(16) -DCLKB5	-DCLKB5	AN33C	SCLK_B5#	SDO_B54	AJ38	MDB53
(16) -DCLKB5	-DCLKB5	AN33C	SCLK_B5#	SDO_B55	AG33	MDB54
(16) -DCLKB5	-DCLKB5	AN33C	SCLK_B5#	SDO_B56	AF34	MDB55
(16) -DCLKB5	-DCLKB5	AN33C	SCLK_B5#	SDOS_B7	AC36	DOSB7
(16) -DCLKB5	-DCLKB5	AN33C	SCLK_B5#	SDOS_B7#	AC37	-DOSB7
(16) -DCLKB5	-DCLKB5	AN33C	SCLK_B5#	SDM_B7	AD38	DMB7
(16) -DCLKB5	-DCLKB5	AN33C	SCLK_B5#	SDO_B56	AD36	MDB56
(16) -DCLKB5	-DCLKB5	AN33C	SCLK_B5#	SDO_B57	AC33	MDB57
(16) -DCLKB5	-DCLKB5	AN33C	SCLK_B5#	SDO_B58	AA34	MDB58
(16) -DCLKB5	-DCLKB5	AN33C	SCLK_B5#	SDO_B59	AA36	MDB59
(16) -DCLKB5	-DCLKB5	AN33C	SCLK_B5#	SDO_B60	AD34	MDB60
(16) -DCLKB5	-DCLKB5	AN33C	SCLK_B5#	SDO_B61	AF38	MDB61
(16) -DCLKB5	-DCLKB5	AN33C	SCLK_B5#	SDO_B62	AC34	MDB62
(16) -DCLKB5	-DCLKB5	AN33C	SCLK_B5#	SDO_B		



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BC37	VSS_1	BW	VSS_181	AF5
BC32	VSS_2	(7/9)	VSS_182	AF3
BC28	VSS_3		VSS_183	AF2
BC24	VSS_4		VSS_184	AF1
BC10	VSS_5		VSS_185	AD42
BC5	VSS_6	GND 1/2	VSS_186	AD39
BB7	VSS_7		VSS_187	AD37
AY4	VSS_8		VSS_188	AD35
AW43	VSS_9		VSS_189	AD33
AW41	VSS_10		VSS_190	AD25
AW1	VSS_11		VSS_191	AD21
AV37	VSS_12		VSS_192	AD19
AV35	VSS_13		VSS_193	AC38
AV27	VSS_14		VSS_194	AC35
AV23	VSS_15		VSS_195	AC24
AV21	VSS_16		VSS_196	AC22
AV17	VSS_17		VSS_197	AC20
AV11	VSS_18		VSS_198	K43
AV9	VSS_19		VSS_199	K28
AV7	VSS_20		VSS_200	K21
AU42	VSS_21		VSS_201	K18
AU38	VSS_22		VSS_202	K13
AU32	VSS_23		VSS_203	K12
AU24	VSS_24		VSS_204	K2
AU20	VSS_25		VSS_205	J38
AL6	VSS_26		VSS_206	J35
AL2	VSS_27		VSS_207	J32
AT31	VSS_28		VSS_208	J27
AT29	VSS_29		VSS_209	J21
AT15	VSS_30		VSS_210	J9
AT13	VSS_31		VSS_211	J7
AT12	VSS_32		VSS_212	J5
AR38	VSS_33		VSS_213	H31
AR33	VSS_34		VSS_214	H29
AR32	VSS_35		VSS_215	H21
AR27	VSS_36		VSS_216	H20
AR26	VSS_37		VSS_217	H17
AR23	VSS_38		VSS_218	H15
AR21	VSS_39		VSS_219	H13
AR20	VSS_40		VSS_220	H12
AR17	VSS_41		VSS_221	H11
AR9	VSS_42		VSS_222	H15
AP6	VSS_43		VSS_223	H13
AP43	VSS_44		VSS_224	H25
AP24	VSS_45		VSS_225	G42
AP18	VSS_46		VSS_226	G38
AP1	VSS_47		VSS_227	G32
AN38	VSS_48		VSS_228	G21
AN31	VSS_49		VSS_229	G13
AN29	VSS_50		VSS_230	G12
AN24	VSS_51		VSS_231	G11
AN23	VSS_52		VSS_232	G9
AN20	VSS_53		VSS_233	G7
AN15	VSS_54		VSS_234	G1
AN13	VSS_55		VSS_235	F37
AN12	VSS_56		VSS_236	F35
AN11	VSS_57		VSS_237	F34
AN4	VSS_58		VSS_238	F27
AM42	VSS_59		VSS_239	F18
AM40	VSS_60		VSS_240	F11
AM36	VSS_61		VSS_241	F3
AM33	VSS_62		VSS_242	E43
AM29	VSS_63		VSS_243	E32
AM24	VSS_64		VSS_244	E24
AM23	VSS_65		VSS_245	E21
AM20	VSS_66		VSS_246	E20
AM11	VSS_67		VSS_247	E15
AM9	VSS_68		VSS_248	E13
AM7	VSS_69		VSS_249	E11
AM4	VSS_70		VSS_250	E9
AM2	VSS_71		VSS_251	E3
AM1	VSS_72		VSS_252	D40
AL36	VSS_73		VSS_253	D31
AL33	VSS_74		VSS_254	D21
AK43	VSS_75		VSS_255	D17
AJ39	VSS_76		VSS_256	D3
AJ36	VSS_77		VSS_257	C26
AJ33	VSS_78		VSS_258	C11
AH42	VSS_79		VSS_259	C6
AG37	VSS_80		VSS_260	C5
AG34	VSS_81		VSS_261	C4
AF43	VSS_82		VSS_262	B37
AF37	VSS_83		VSS_263	B32
AF36	VSS_84		VSS_264	B31
AF10	VSS_85		VSS_265	B28
AF9	VSS_86		VSS_266	B23
AF8	VSS_87		VSS_267	B22
AF7	VSS_88		VSS_268	B19
AF6	VSS_89		VSS_269	B14
	VSS_90		VSS_270	B10

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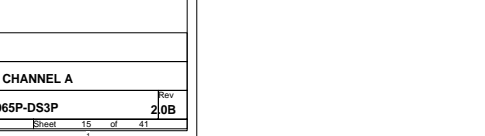
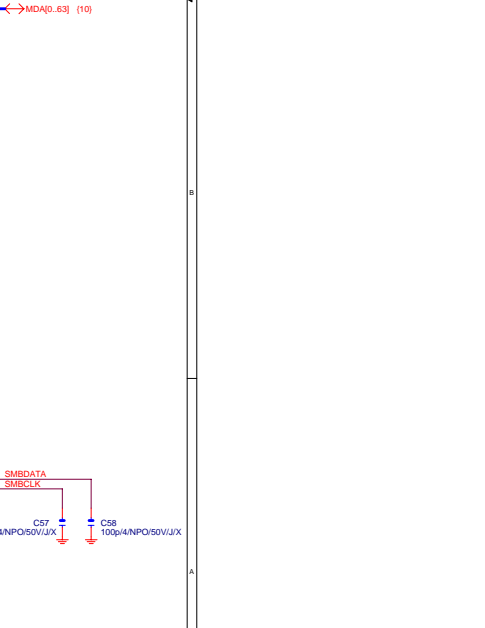
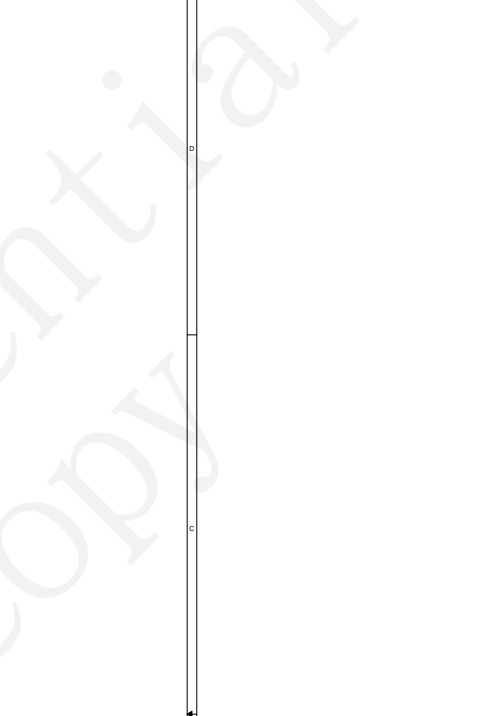
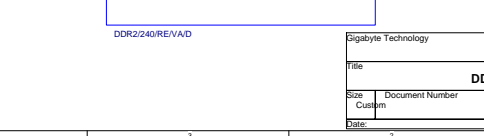
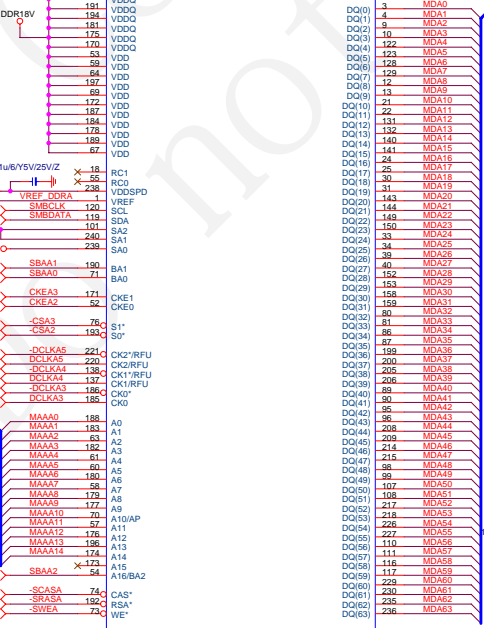
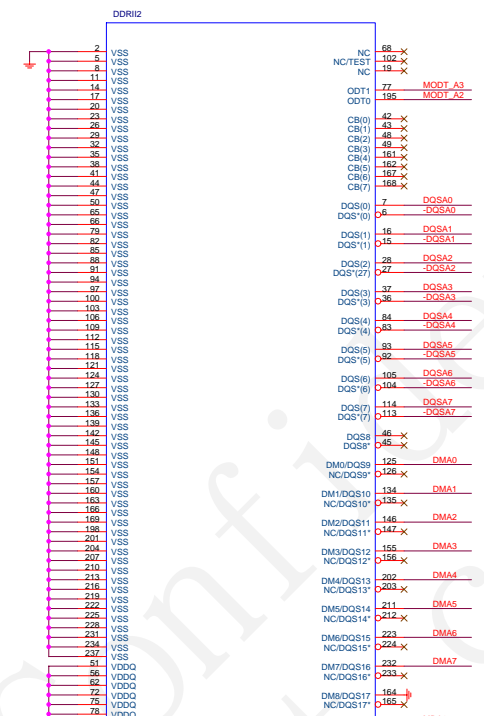
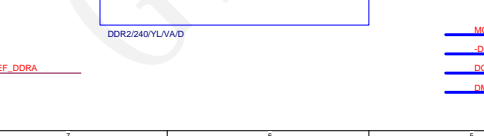
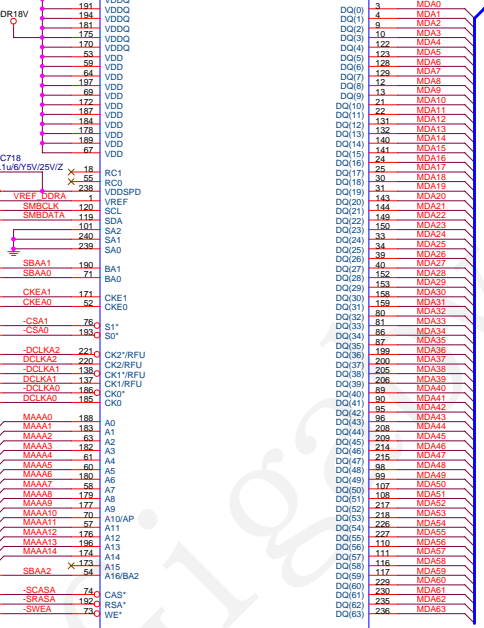
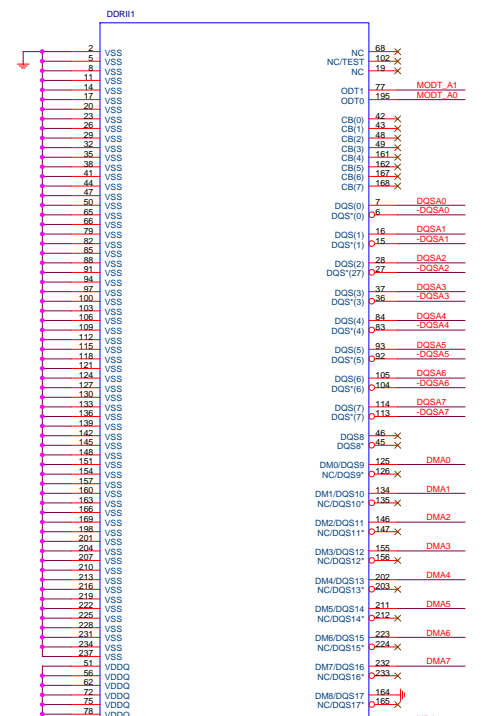
U2I

M27	VSS_91	BW	VSS_271	BC41
M21	VSS_92	(9/9)	VSS_272	BC3
M17	VSS_93		VSS_273	BA1
M15	VSS_94		VSS_274	AY40
M10	VSS_95	GND 2/2	VSS_275	AF23
M7	VSS_96		VSS_276	AF21
M1	VSS_97		VSS_277	AF19
L33	VSS_98		VSS_278	AE24
L32	VSS_99		VSS_279	AE22
L31	VSS_100		VSS_280	AE20
L29	VSS_101		VSS_281	AE18
L21	VSS_102		VSS_282	AC18
L20	VSS_103		VSS_283	AA18
L11	VSS_104		VSS_284	W24
L7	VSS_105		VSS_285	W22
L5	VSS_106		VSS_286	W20
L3	VSS_107		VSS_287	R21
K43	VSS_108		VSS_288	E1
K28	VSS_109		VSS_289	C43
K21	VSS_110		VSS_290	C1
K18	VSS_111		VSS_291	A41
K13	VSS_112		VSS_292	A5
K12	VSS_113		VSS_293	A3
K2	VSS_114			
J38	VSS_115			
J35	VSS_116			
J32	VSS_117			
J27	VSS_118			
J21	VSS_119			
J9	VSS_120			
J7	VSS_121			
J5	VSS_122			
H31	VSS_123			
H29	VSS_124			
H21	VSS_125			
H20	VSS_126			
H17	VSS_127			
H15	VSS_128			
H13	VSS_129			
H12	VSS_130			
H11	VSS_131			
H15	VSS_132			
H13	VSS_133			
H25	VSS_134			
G42	VSS_135			
G38	VSS_136			
G32	VSS_137			
G21	VSS_138			
G13	VSS_139			
G12	VSS_140			
G11	VSS_141			
G9	VSS_142			
G7	VSS_143			
G1	VSS_144			
F37	VSS_145			
F35	VSS_146			
F34	VSS_147			
F27	VSS_148			
F18	VSS_149			
F11	VSS_150			
F3	VSS_151			
E43	VSS_152			
E32	VSS_153			
E24	VSS_154			
E21	VSS_155			
E20	VSS_156			
E15	VSS_157			
E13	VSS_158			
E11	VSS_159			
E9	VSS_160			
E3	VSS_161			
D40	VSS_162			
D31	VSS_163			
D21	VSS_164			
D17	VSS_165			
D3	VSS_166			
C26	VSS_167			
C11	VSS_168			
C6	VSS_169			
C5	VSS_170			
C4	VSS_171			
B37	VSS_172			
B32	VSS_173			
B31	VSS_174			
B28	VSS_175			
B23	VSS_176			
B22	VSS_177			
B19	VSS_178			
B14	VSS_179			
B10	VSS_180			
A39				
A34				
A26				
A18				
M37				
M35				
M33				
A7				

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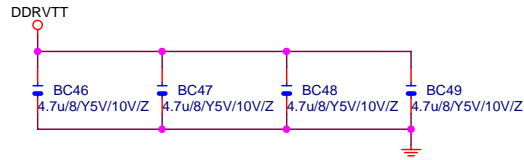
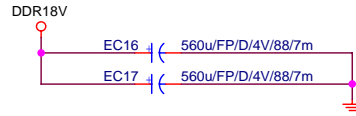
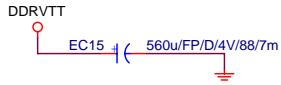
Title		GMCH-GND	
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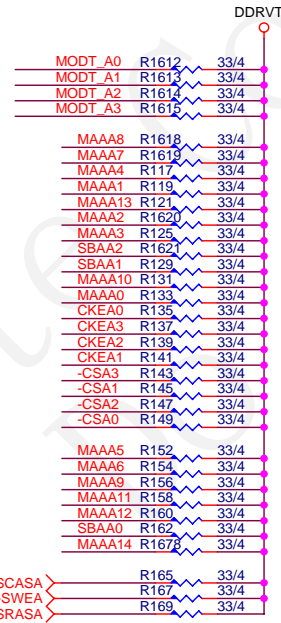
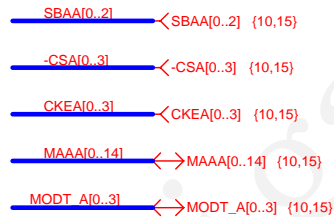
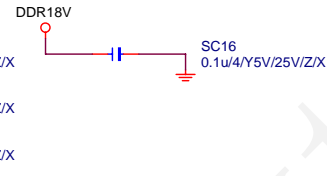
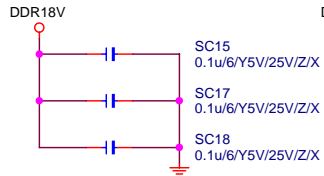
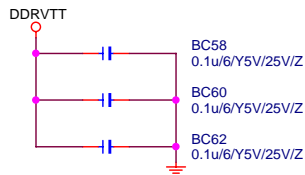
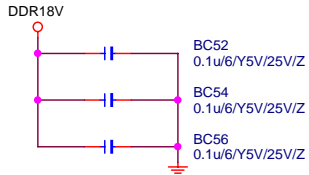
DDR TERMINATION CHANNEL A

DDRVTT Decouple



DDR18V Decouple

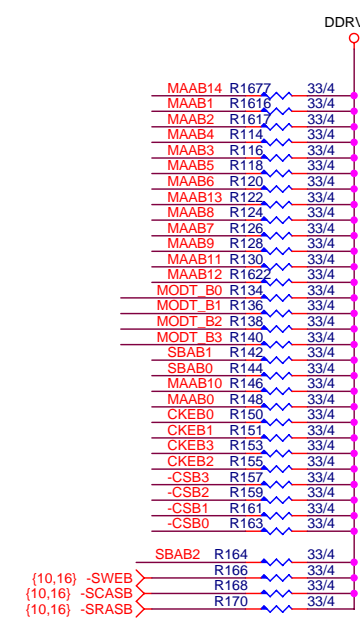
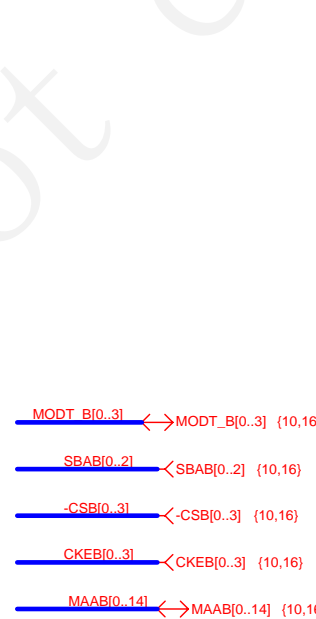
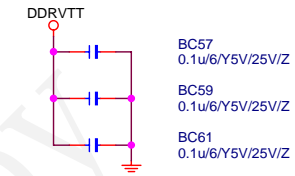
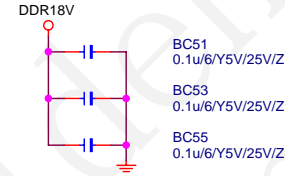
DDRVTT Decouple



DDR TERMINATION CHANNEL B

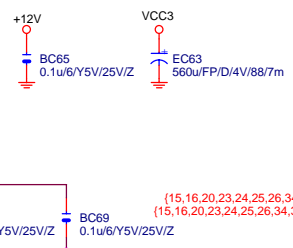
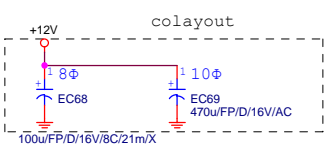
DDR18V Decouple

DDRVTT Decouple

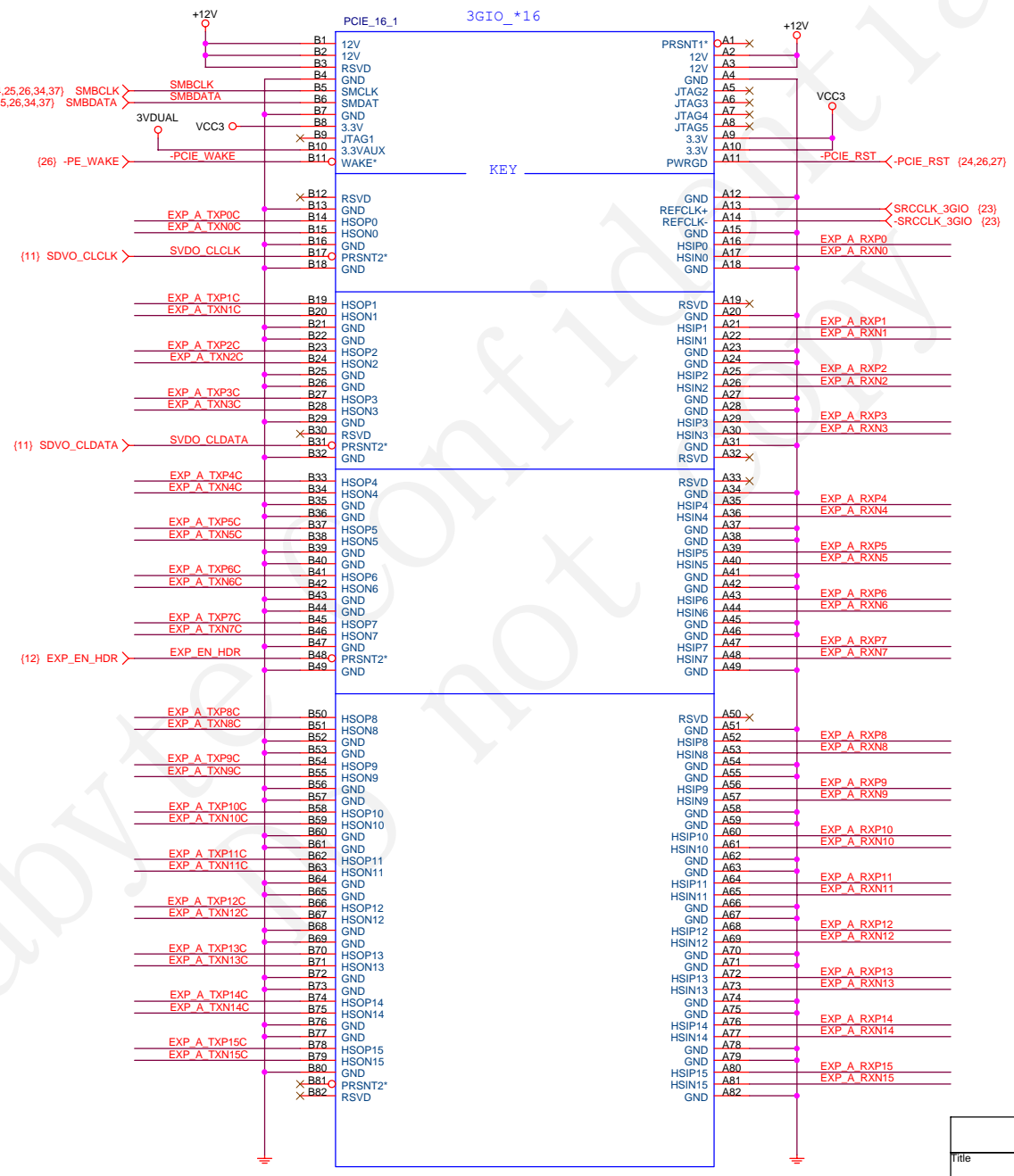


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DDRII TERMINATOR		
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	965P-DS3P	2.0B
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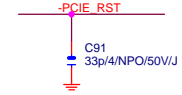
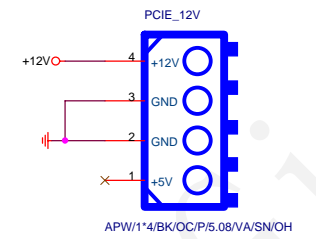


PCIESLOT-164DN-2



EXP_A_TXP[0..15] >>> EXP_A_TXP[0..15] (11)
 EXP_A_TXN[0..15] >>> EXP_A_TXN[0..15] (11)

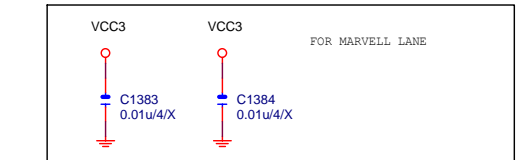
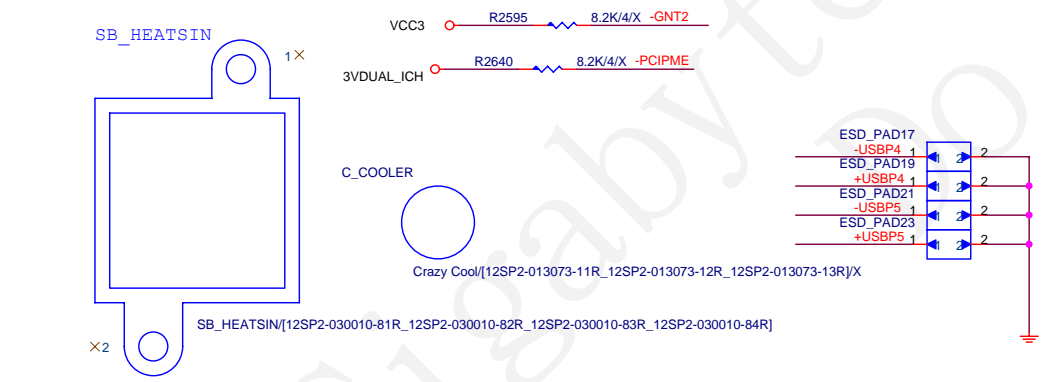
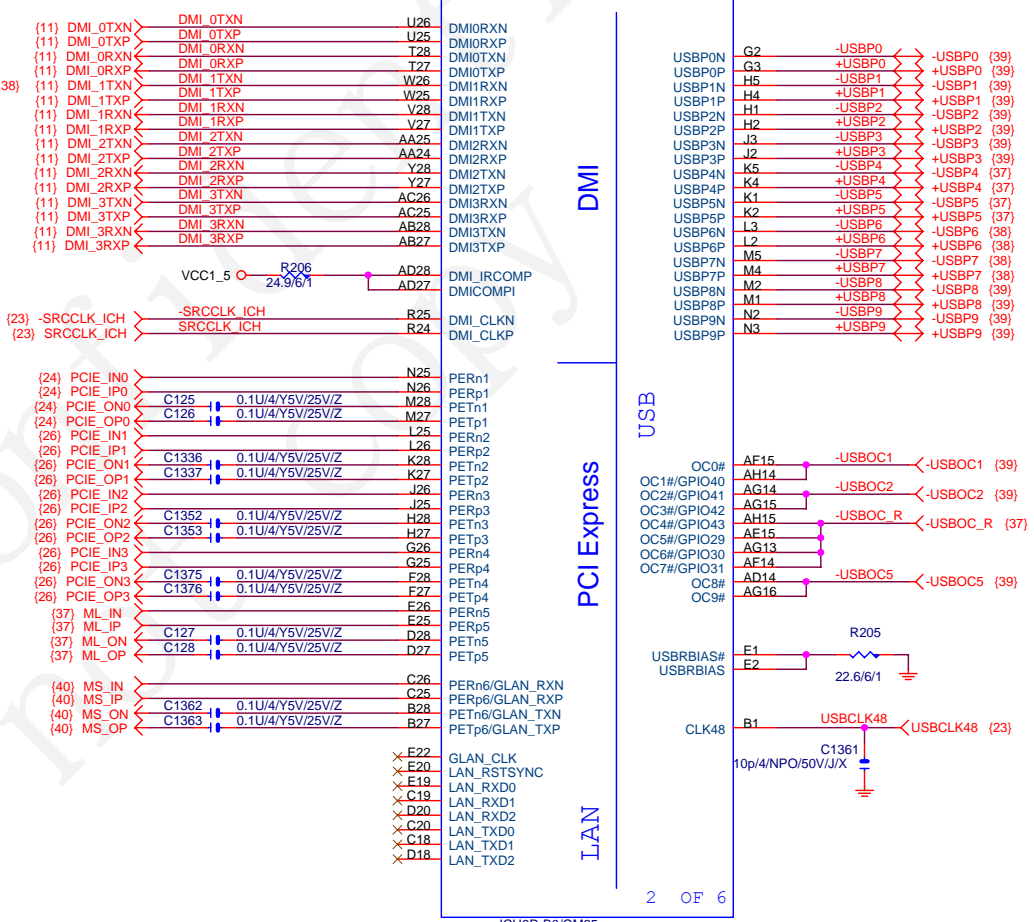
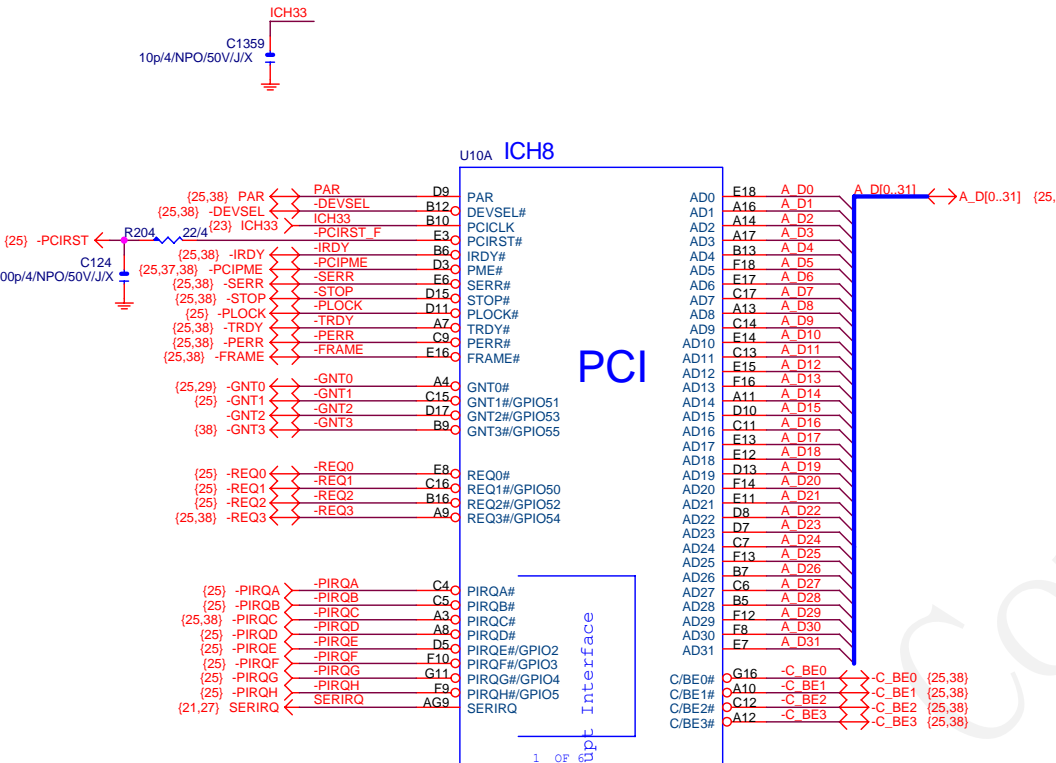
EXP_A_TXP0	C92	0.1u4/Y5V/16V/Z	EXP_A_TXP0C
EXP_A_TXN0	C93	0.1u4/Y5V/16V/Z	EXP_A_TXN0C
EXP_A_TXP1	C94	0.1u4/Y5V/16V/Z	EXP_A_TXP1C
EXP_A_TXN1	C95	0.1u4/Y5V/16V/Z	EXP_A_TXN1C
EXP_A_TXP2	C96	0.1u4/Y5V/16V/Z	EXP_A_TXP2C
EXP_A_TXN2	C97	0.1u4/Y5V/16V/Z	EXP_A_TXN2C
EXP_A_TXP3	C98	0.1u4/Y5V/16V/Z	EXP_A_TXP3C
EXP_A_TXN3	C99	0.1u4/Y5V/16V/Z	EXP_A_TXN3C
EXP_A_TXP4	C100	0.1u4/Y5V/16V/Z	EXP_A_TXP4C
EXP_A_TXN4	C101	0.1u4/Y5V/16V/Z	EXP_A_TXN4C
EXP_A_TXP5	C102	0.1u4/Y5V/16V/Z	EXP_A_TXP5C
EXP_A_TXN5	C103	0.1u4/Y5V/16V/Z	EXP_A_TXN5C
EXP_A_TXP6	C104	0.1u4/Y5V/16V/Z	EXP_A_TXP6C
EXP_A_TXN6	C105	0.1u4/Y5V/16V/Z	EXP_A_TXN6C
EXP_A_TXP7	C106	0.1u4/Y5V/16V/Z	EXP_A_TXP7C
EXP_A_TXN7	C107	0.1u4/Y5V/16V/Z	EXP_A_TXN7C
EXP_A_TXP8	C108	0.1u4/Y5V/16V/Z	EXP_A_TXP8C
EXP_A_TXN8	C109	0.1u4/Y5V/16V/Z	EXP_A_TXN8C
EXP_A_TXP9	C110	0.1u4/Y5V/16V/Z	EXP_A_TXP9C
EXP_A_TXN9	C111	0.1u4/Y5V/16V/Z	EXP_A_TXN9C
EXP_A_TXP10	C112	0.1u4/Y5V/16V/Z	EXP_A_TXP10C
EXP_A_TXN10	C113	0.1u4/Y5V/16V/Z	EXP_A_TXN10C
EXP_A_TXP11	C114	0.1u4/Y5V/16V/Z	EXP_A_TXP11C
EXP_A_TXN11	C115	0.1u4/Y5V/16V/Z	EXP_A_TXN11C
EXP_A_TXP12	C116	0.1u4/Y5V/16V/Z	EXP_A_TXP12C
EXP_A_TXN12	C117	0.1u4/Y5V/16V/Z	EXP_A_TXN12C
EXP_A_TXP13	C118	0.1u4/Y5V/16V/Z	EXP_A_TXP13C
EXP_A_TXN13	C119	0.1u4/Y5V/16V/Z	EXP_A_TXN13C
EXP_A_TXP14	C120	0.1u4/Y5V/16V/Z	EXP_A_TXP14C
EXP_A_TXN14	C121	0.1u4/Y5V/16V/Z	EXP_A_TXN14C
EXP_A_TXP15	C122	0.1u4/Y5V/16V/Z	EXP_A_TXP15C
EXP_A_TXN15	C123	0.1u4/Y5V/16V/Z	EXP_A_TXN15C



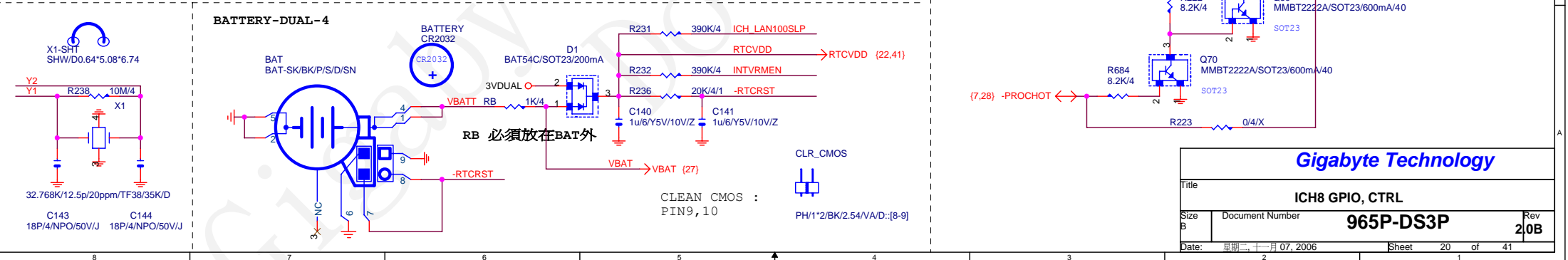
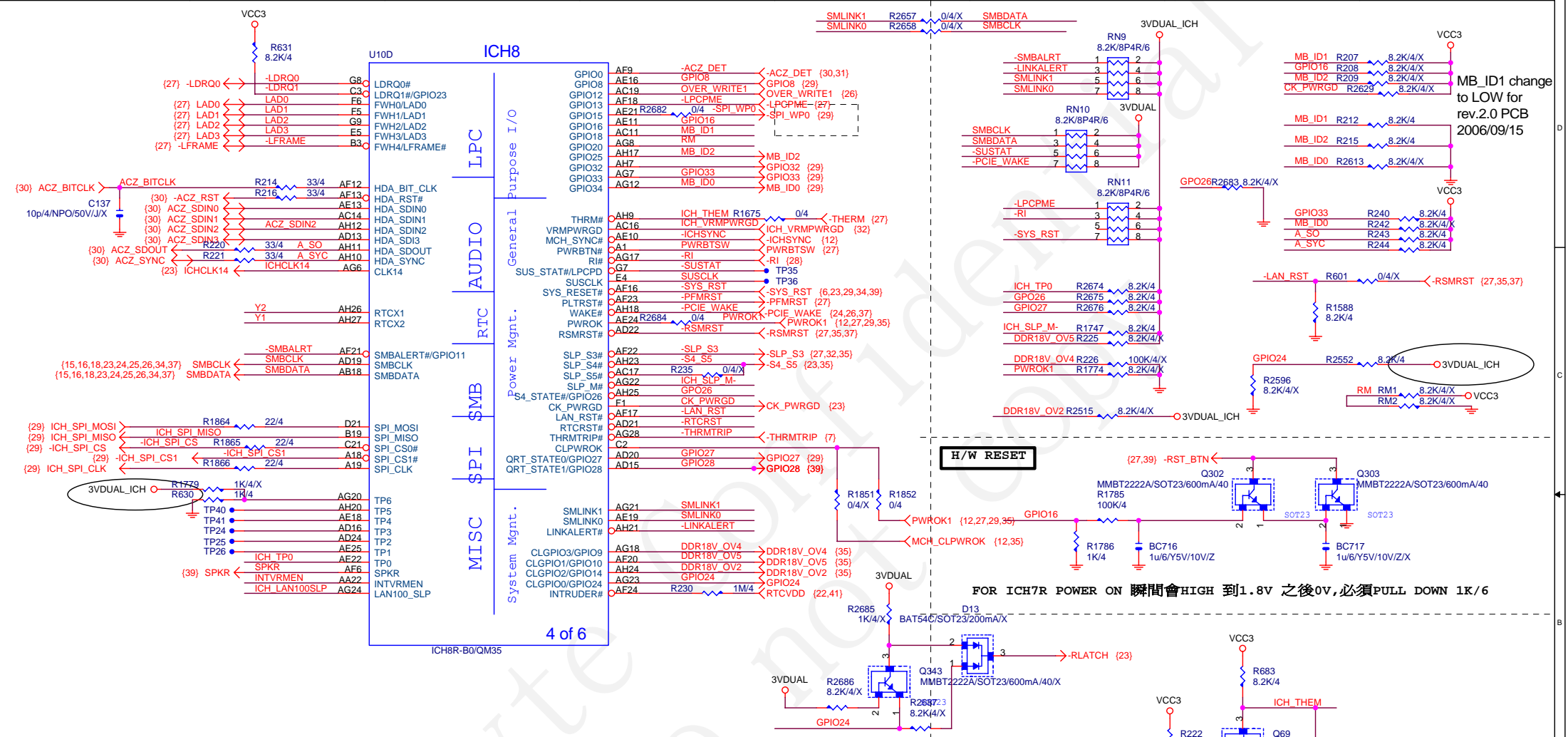
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PCI EXPRESS * 16

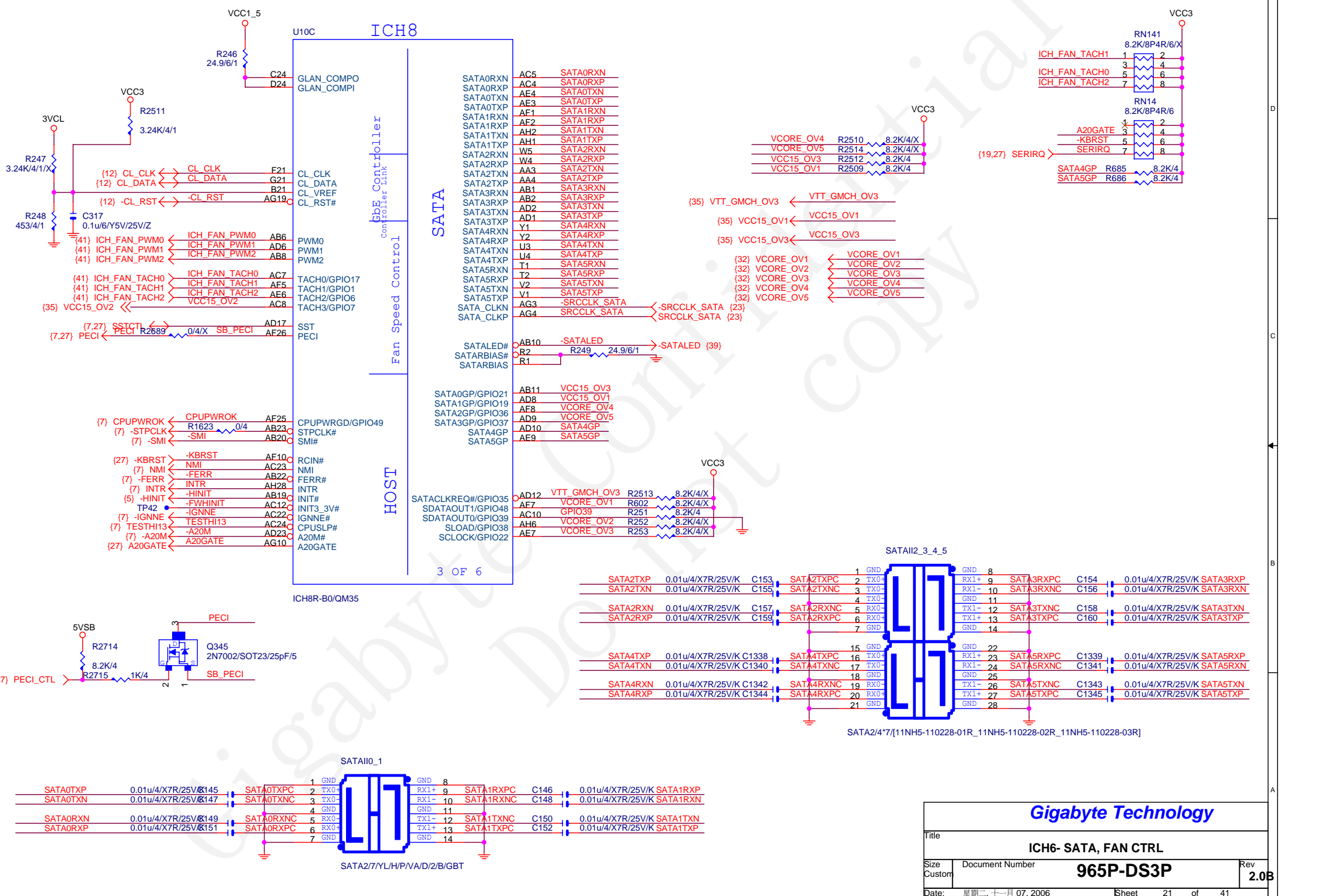
Title		965P-DS3P	
Size	Document Number	Rev 2.0B	
Custom			
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Gigabyte Technology		
ICH8-PCI, DMI, LAN, USB		
Size	Document Number	Rev
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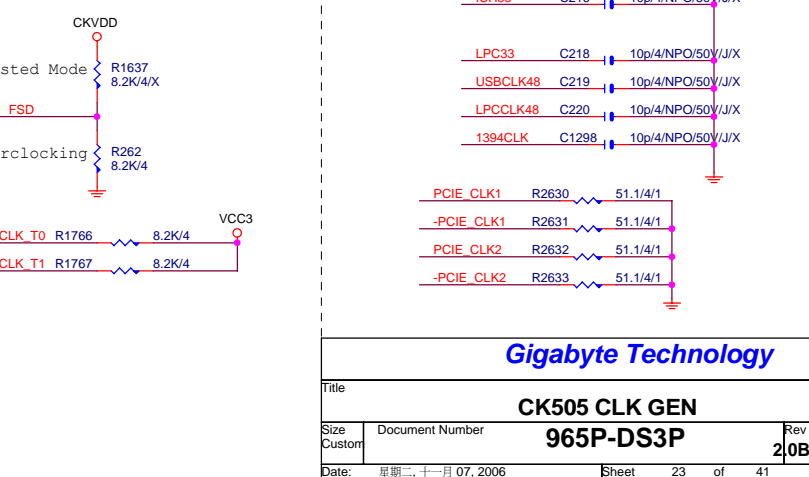
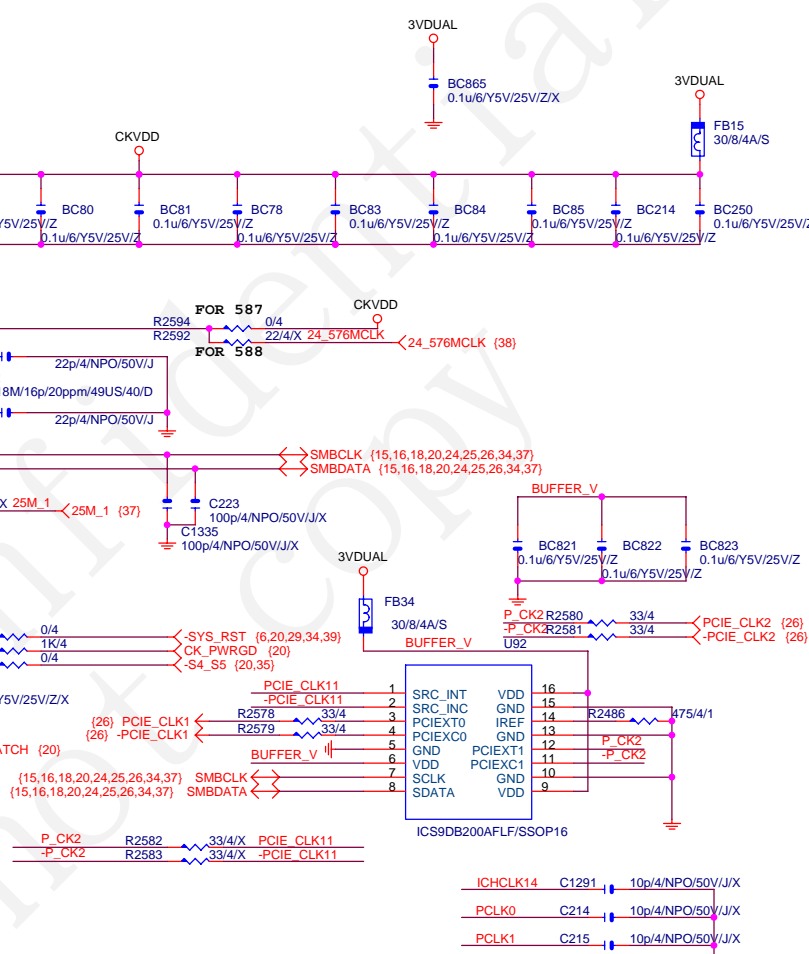
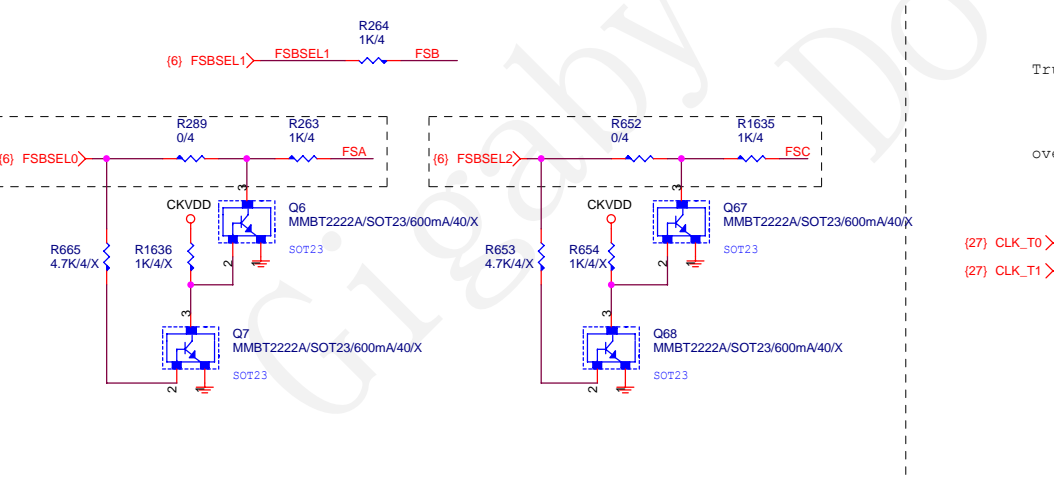
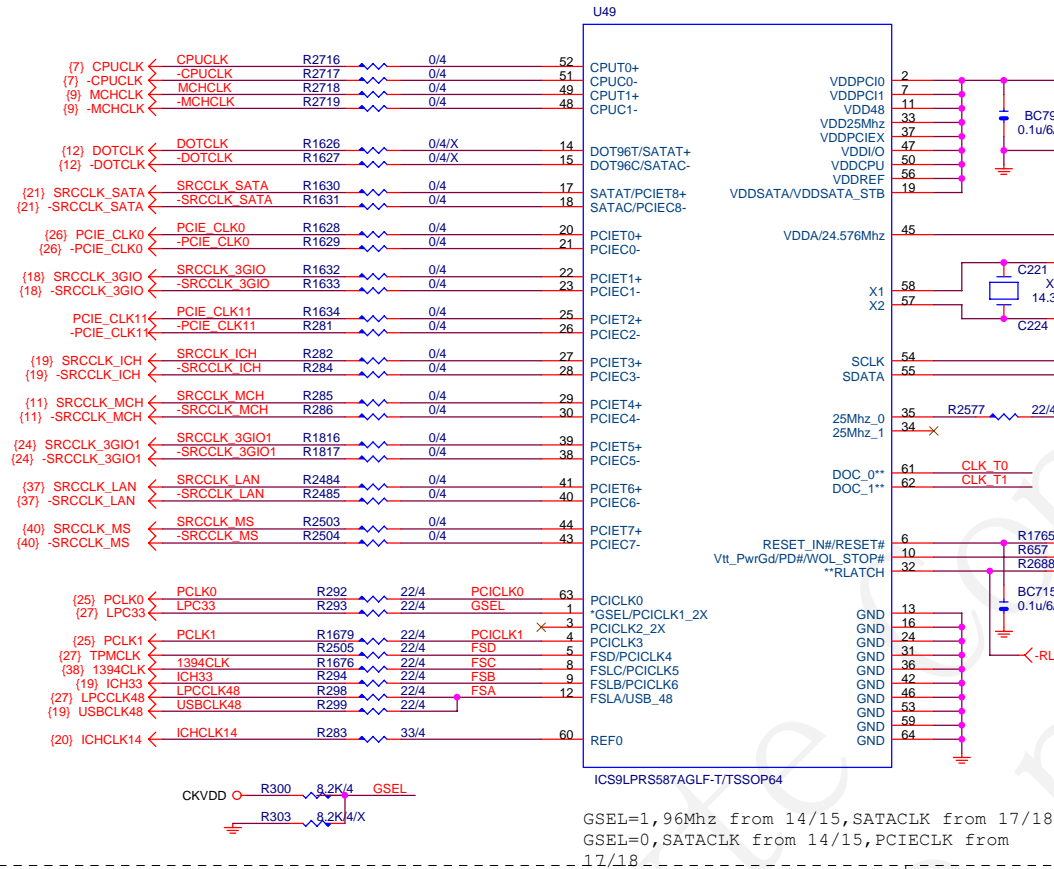
Gigabyte Technology		
Title ICH8 GPIO, CTRL		
Size B	Document Number 965P-DS3P	Rev 2.0B
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ICH6- SATA, FAN CTRL

Title	Document Number		Rev
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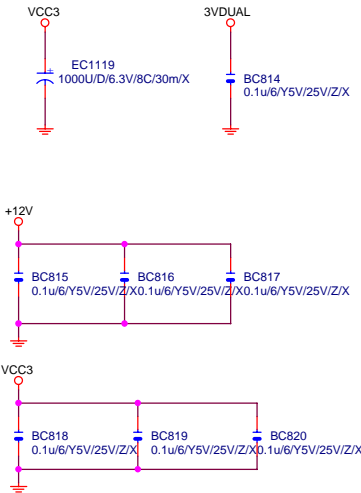
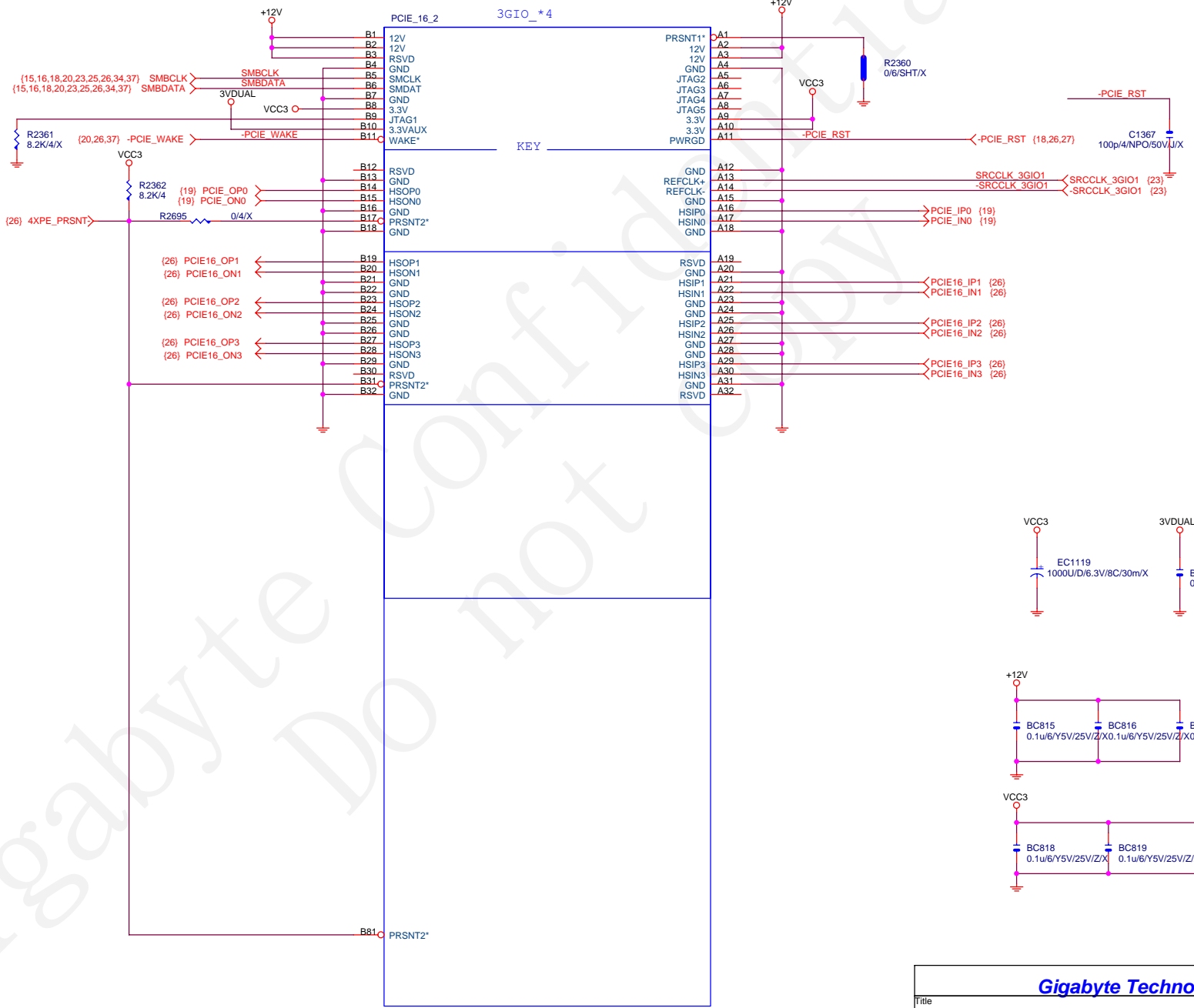
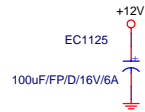


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CK505 CLK GEN

965P-DS3P

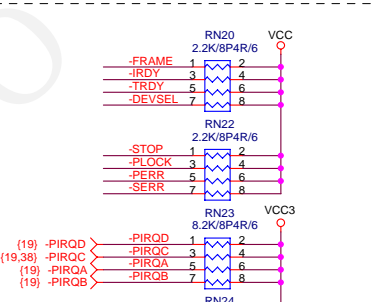
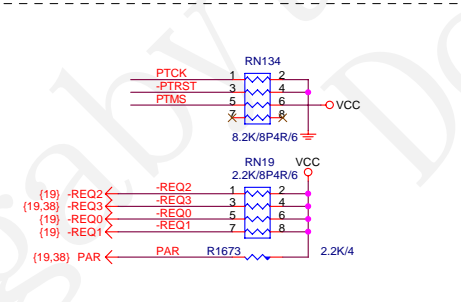
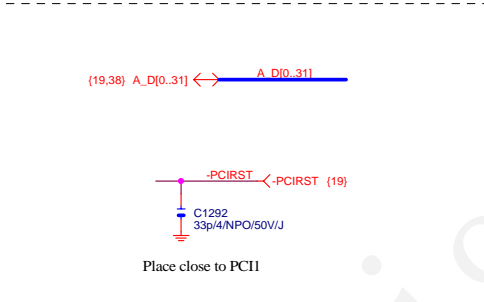
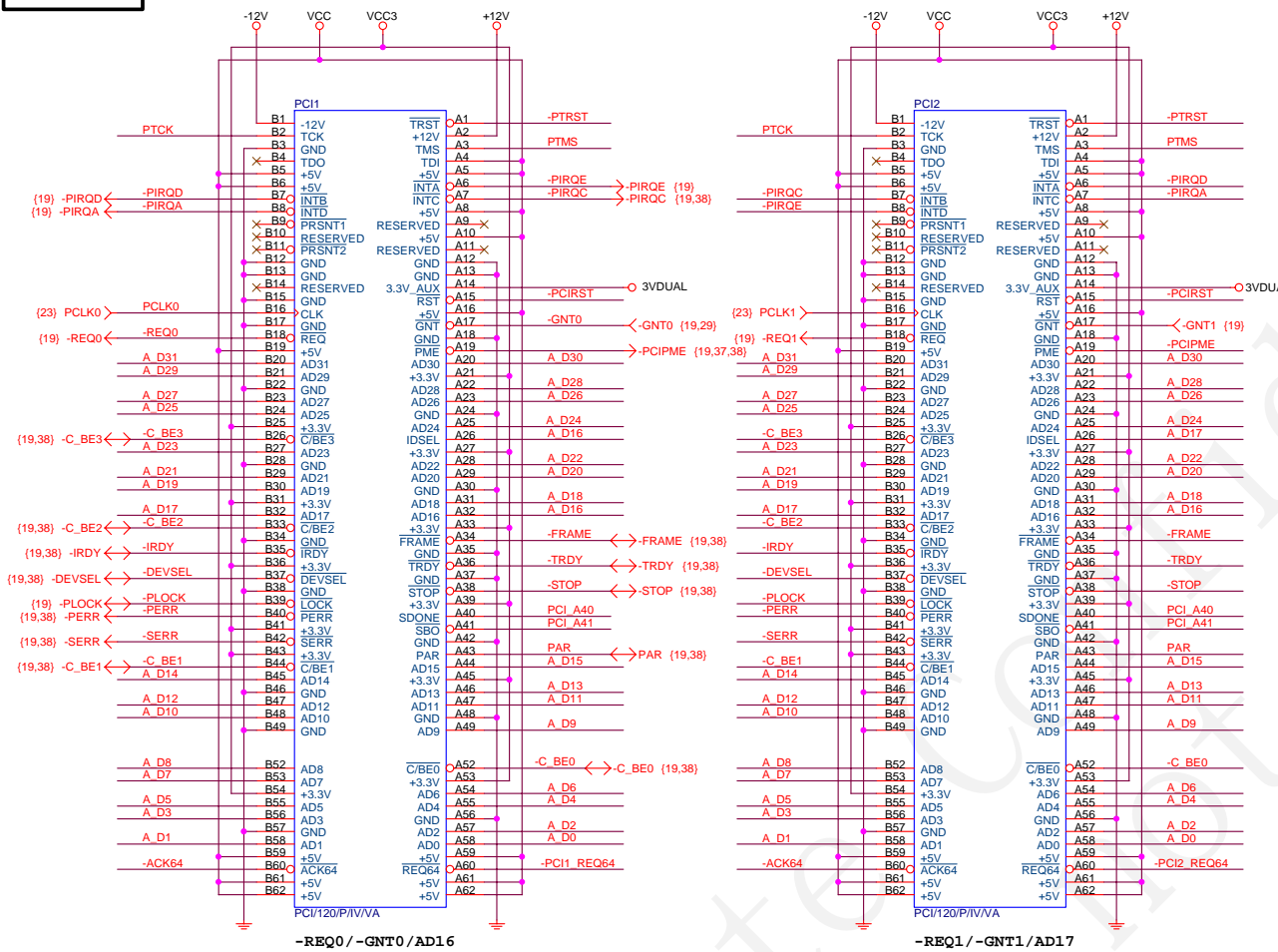
Title: CK505 CLK GEN
 Size: Custom
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PCI-E/16X-65P/BU/RIGHT PUSH

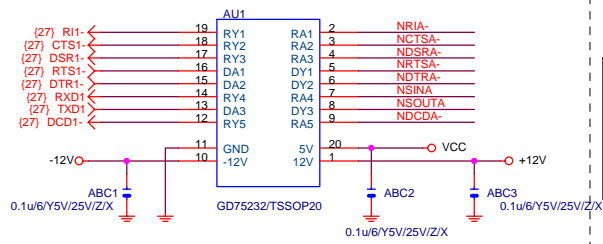
Gigabyte Technology		
PCI EXPRESS X 8 PORT 1		
Title		
Size	Document Number	Rev
Custom	965P-DS3P	2.0B
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PCI1, 2 SLOT

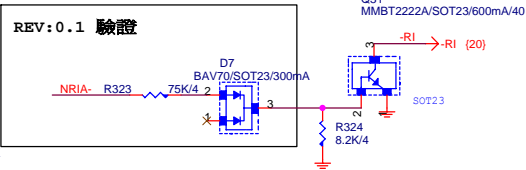


Gigabyte Technology			
Title PCI SLOT 1, 2, 3			
Size Custom	Document Number	965P-DS3P	
		Rev	20B
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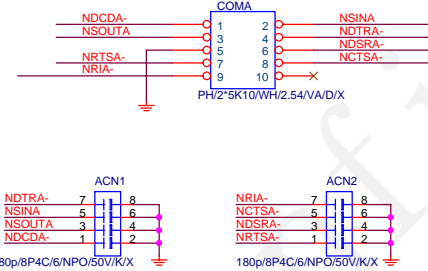
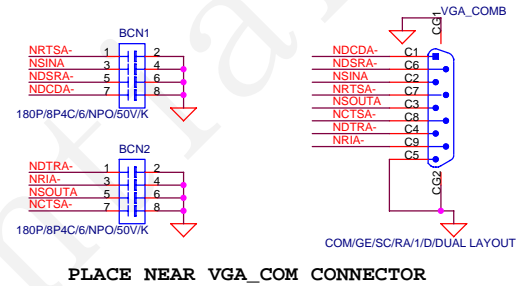
COMA



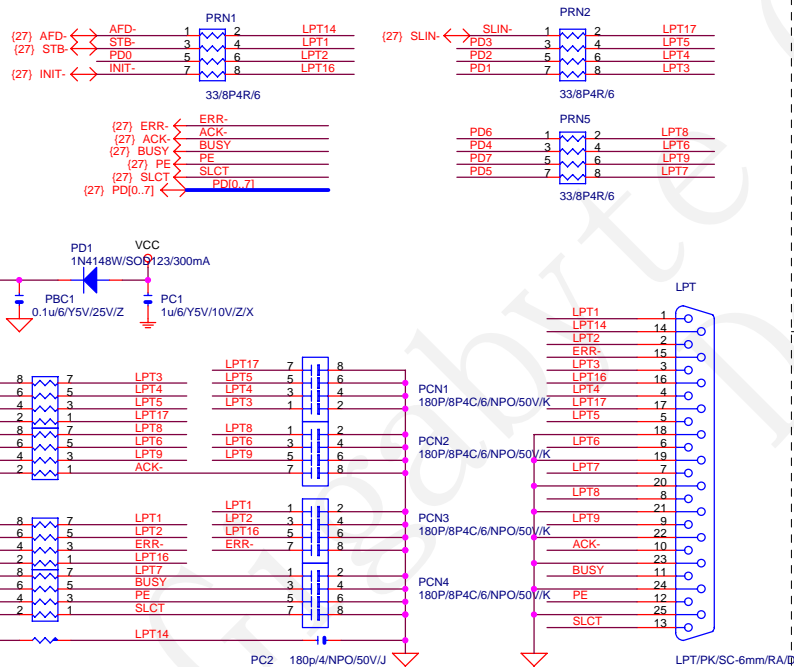
COM RI



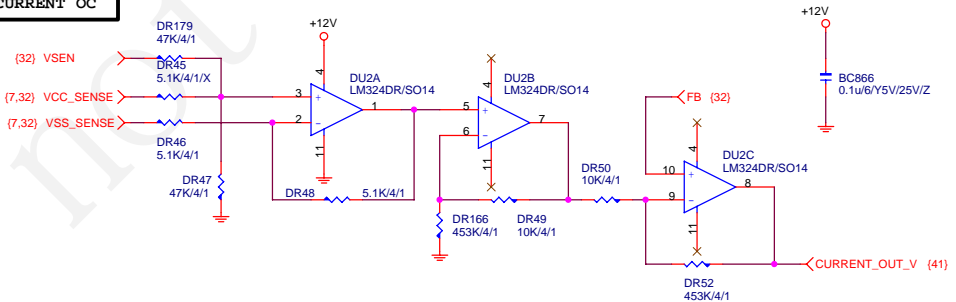
EXTERNAL COMB



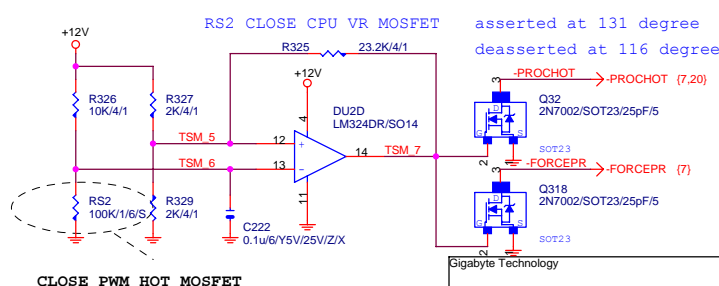
LPT PORT



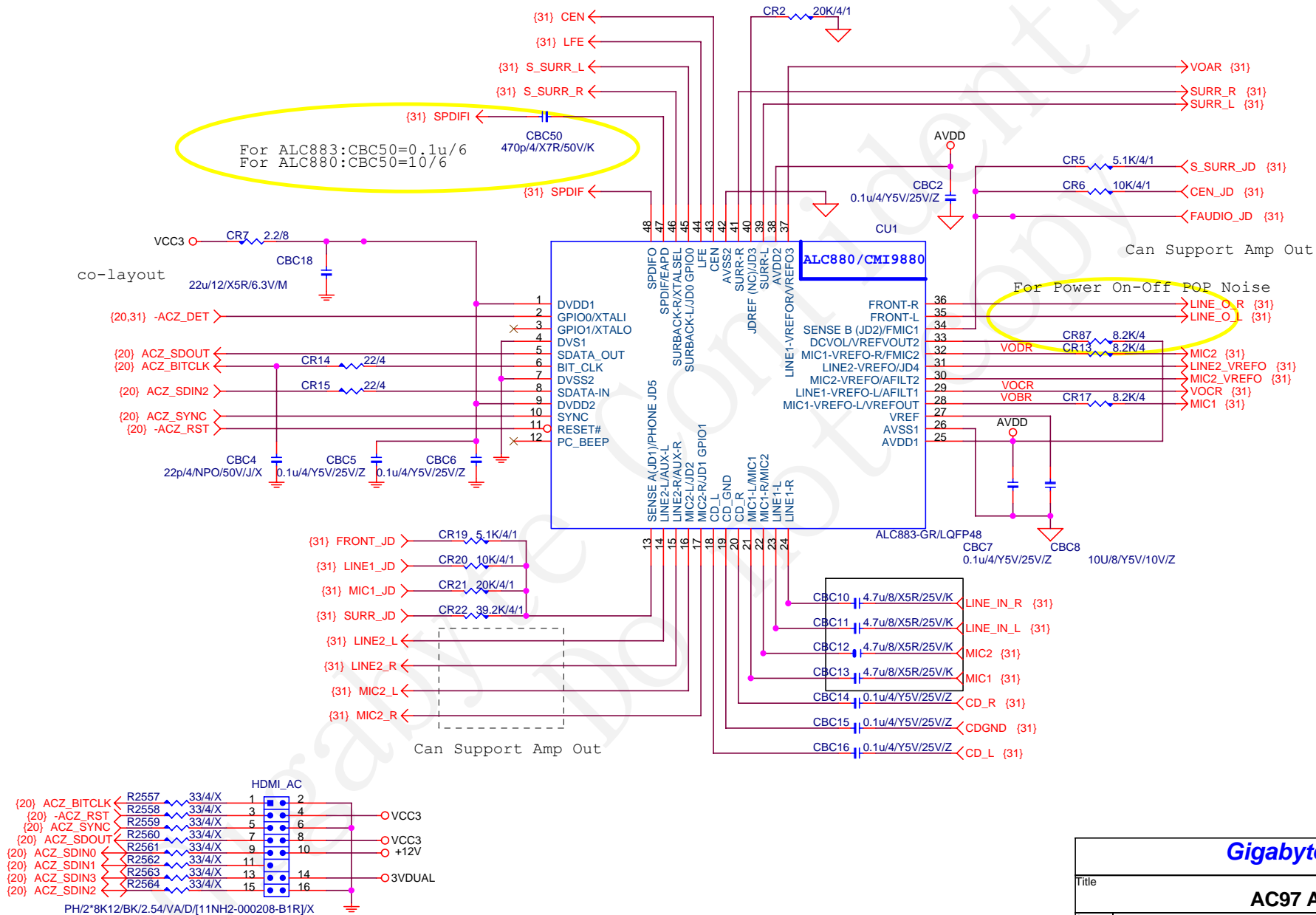
DYNAMIC CURRENT OC



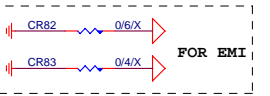
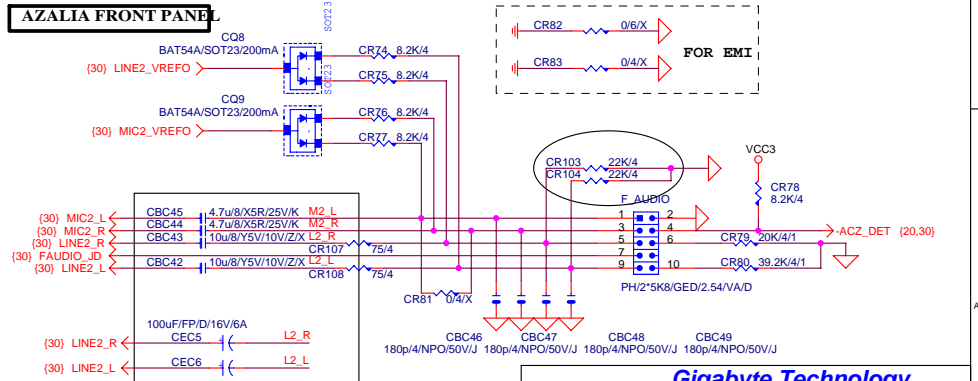
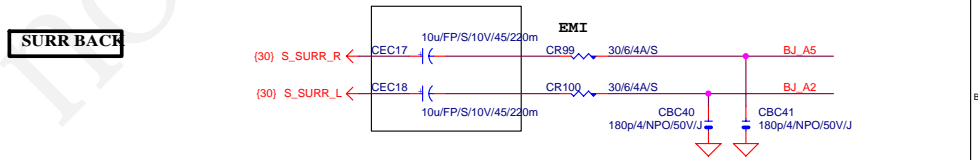
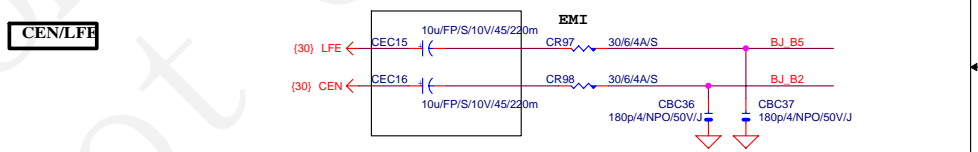
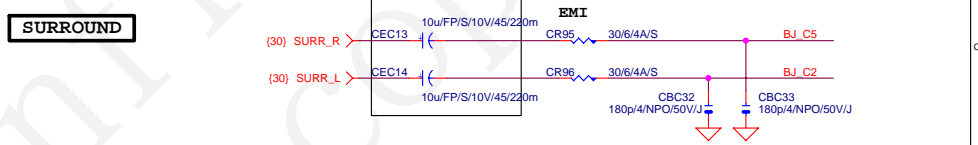
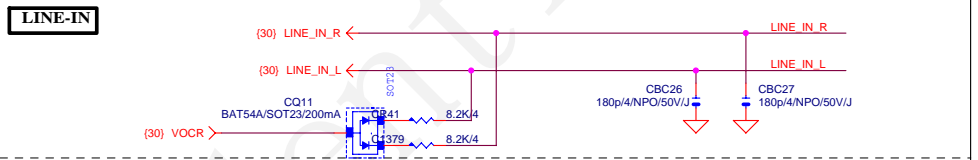
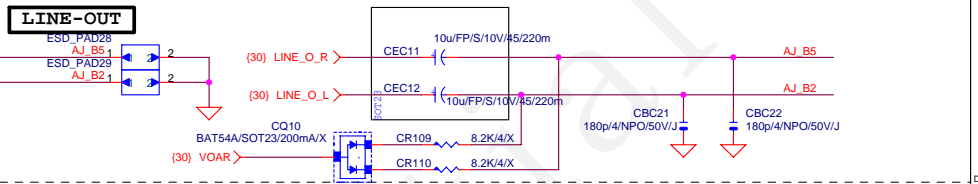
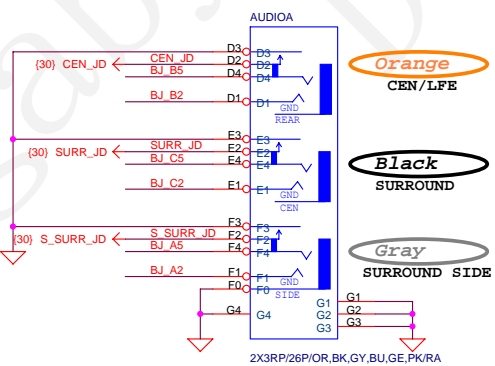
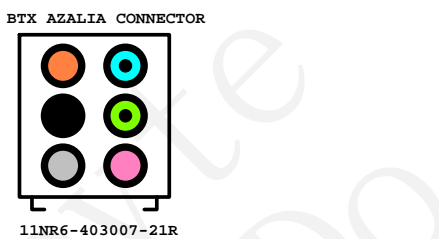
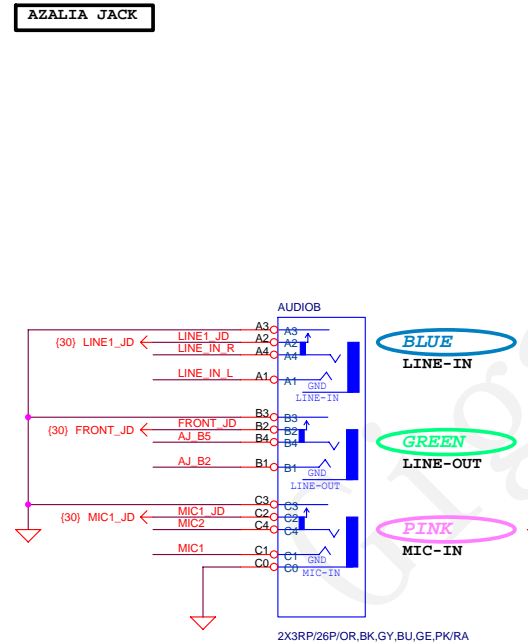
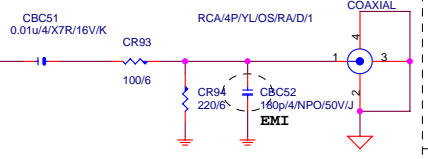
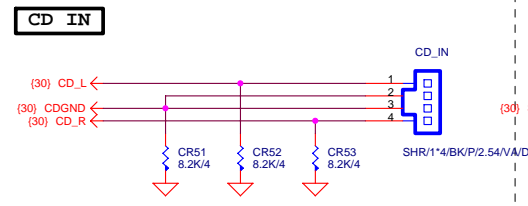
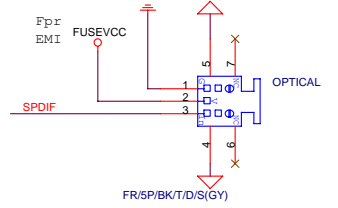
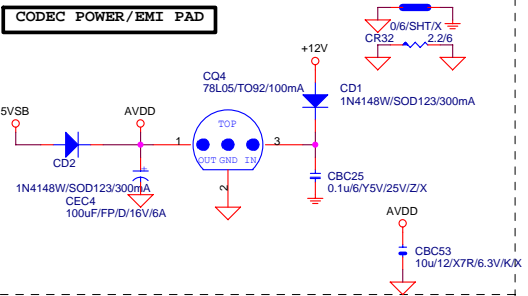
-PROHOT



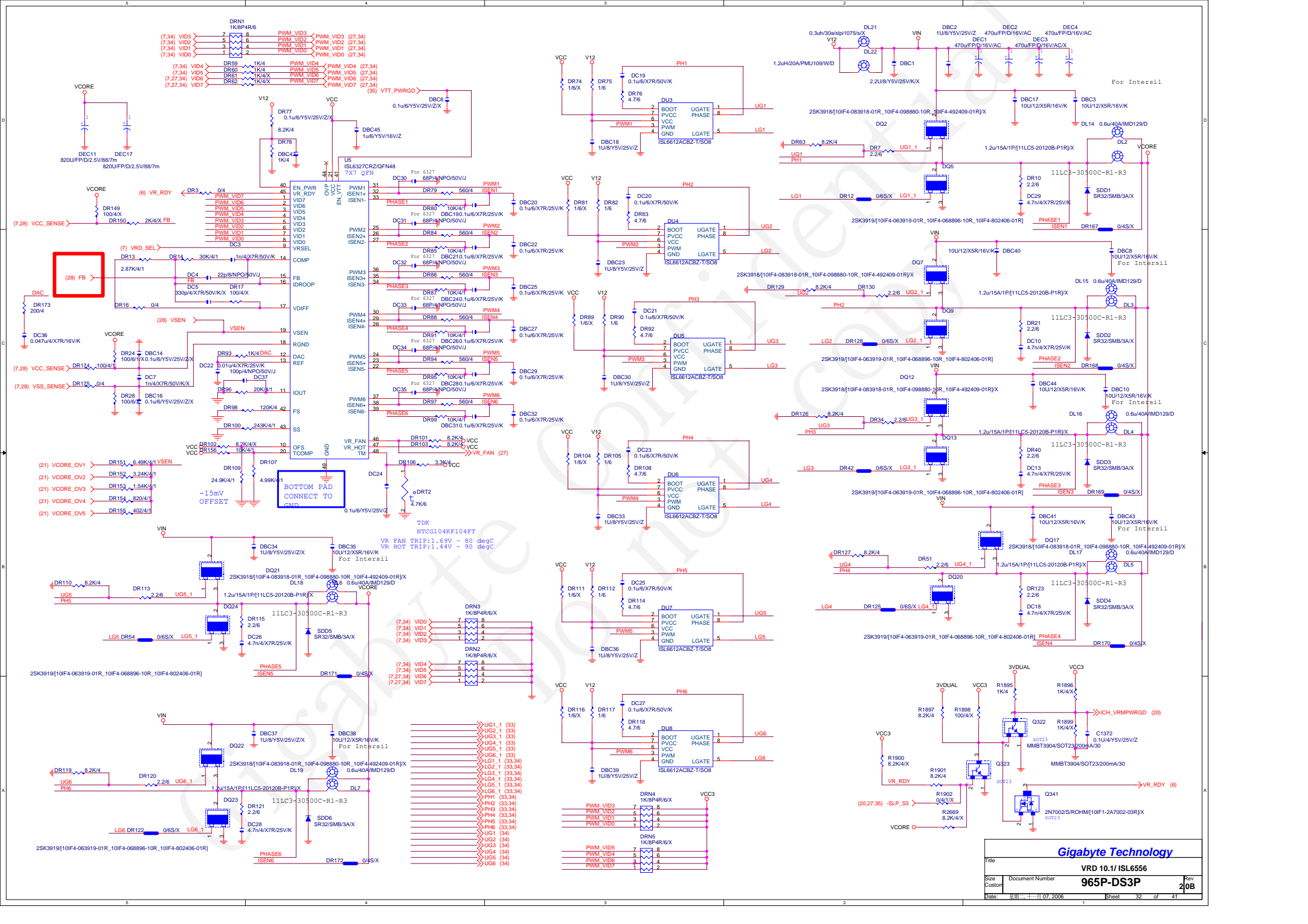
Gigabyte Technology		
Title		
COM & LPT PORT		
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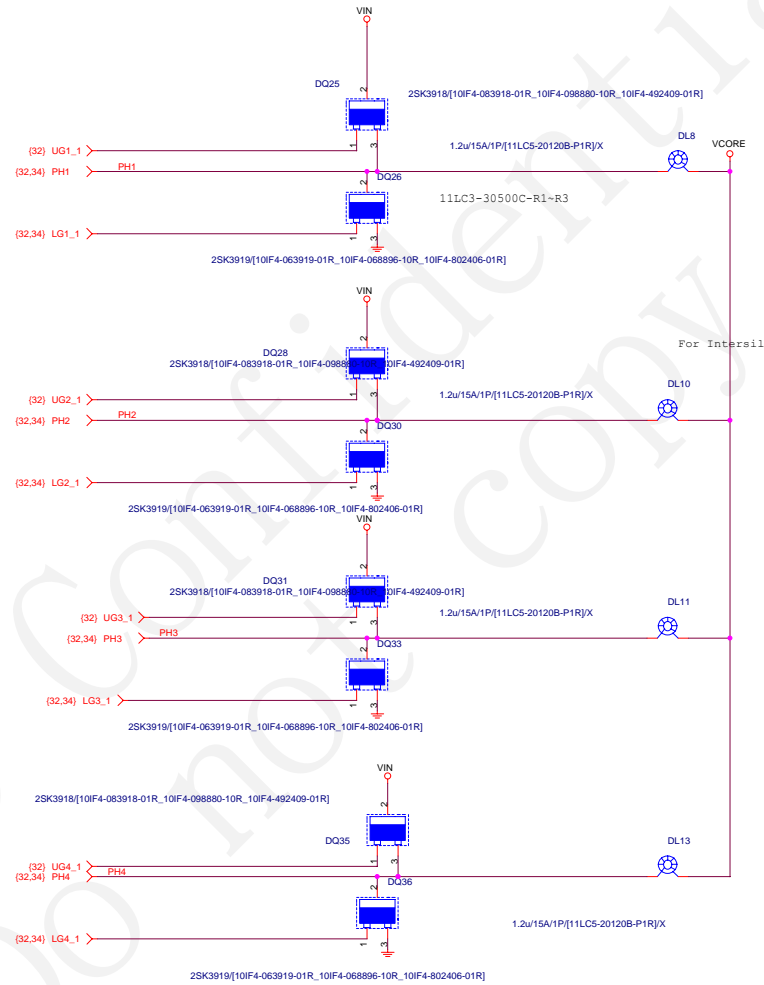
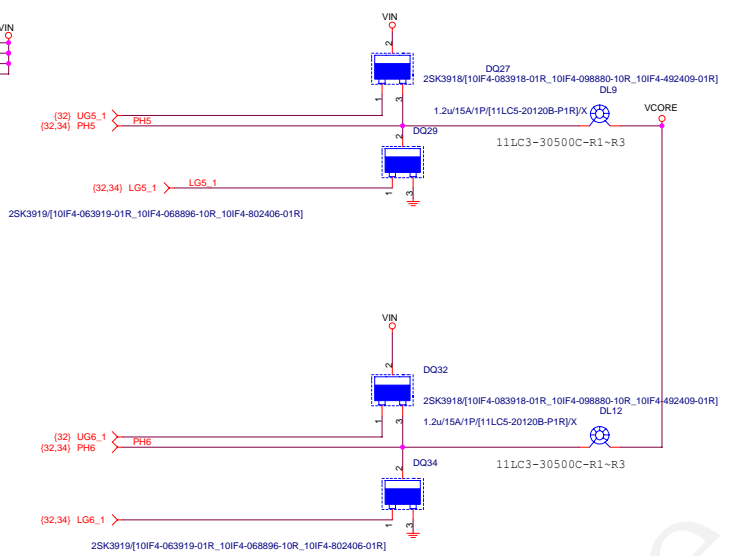
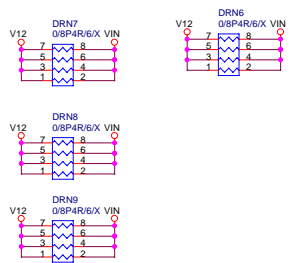
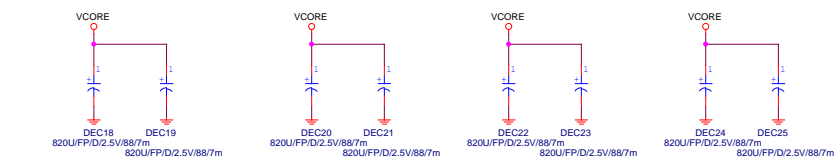
Gigabyte Technology		
Title AC97 ALC658		
Size	Document Number	Rev
Custom	965P-DS3P	2.0B
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AUDIO JACK	
965P-DS3P	
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Size Custom	Document Number
Date	210B

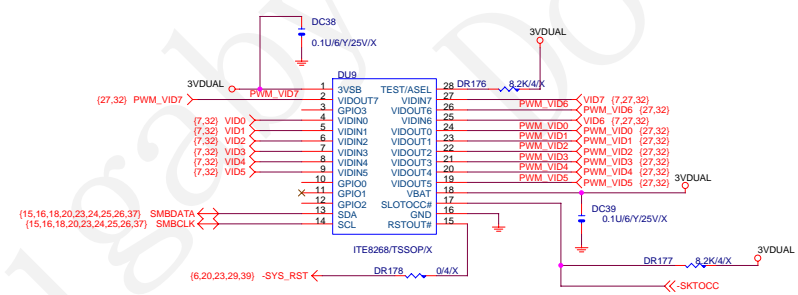
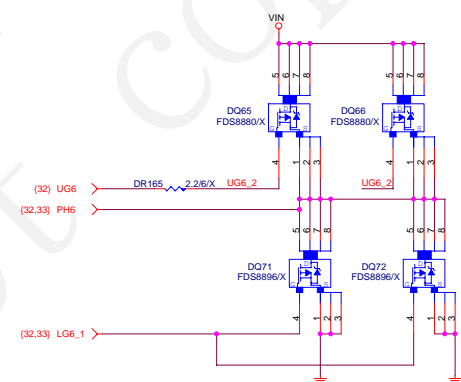
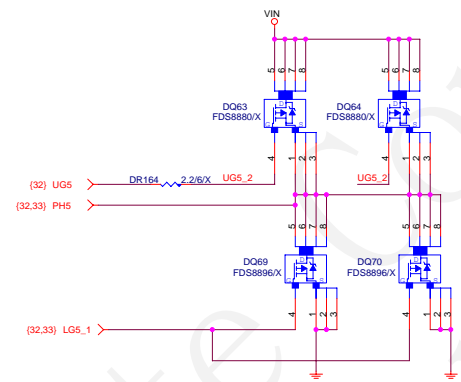
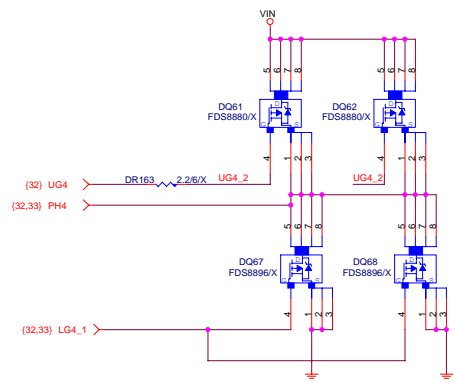
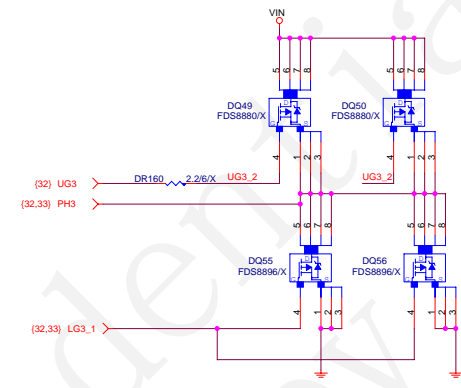
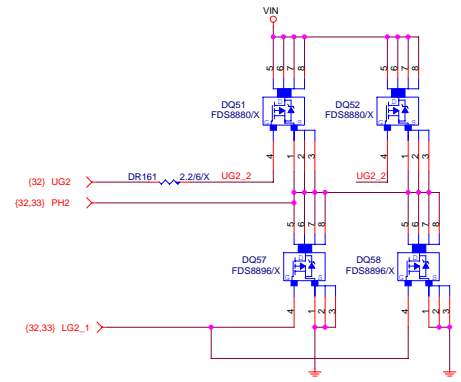
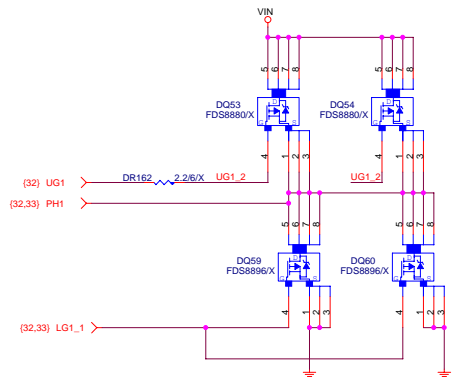


Gigabyte Technology		
File	VRD 10.1/7 ISL6556	
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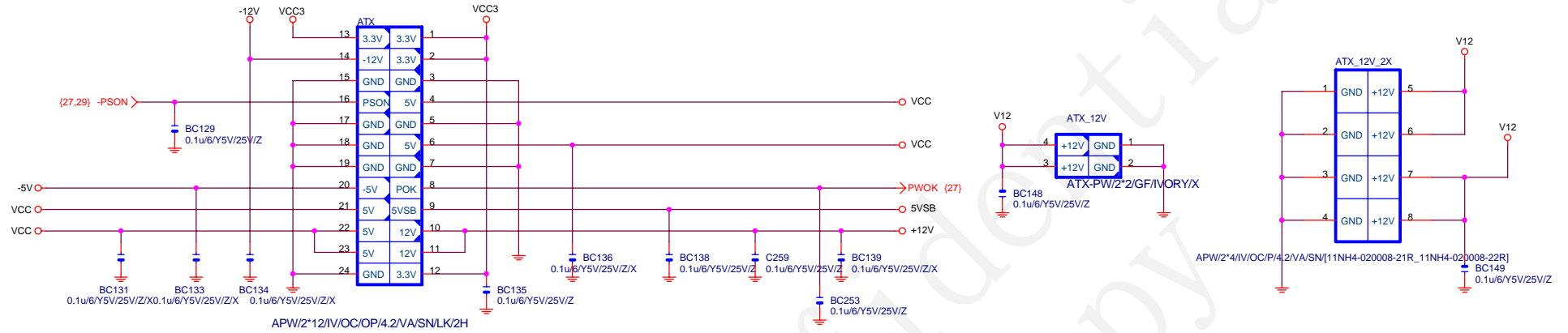


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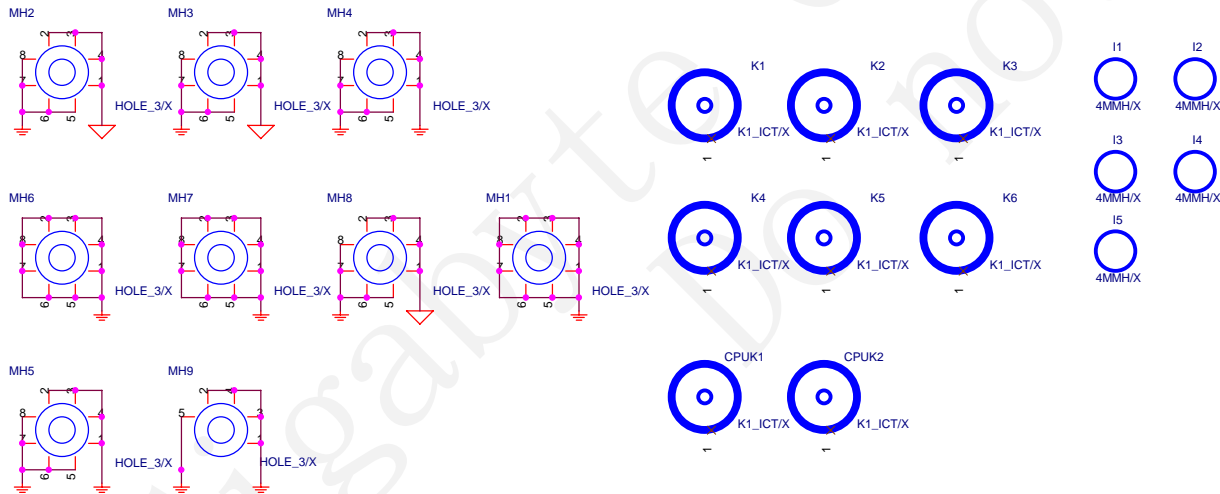
Gigabyte Technology		
File	VRD 10.1/ ISL6556	
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ATX POWER CONNECTOR



HOLE_3-2--->有鉛



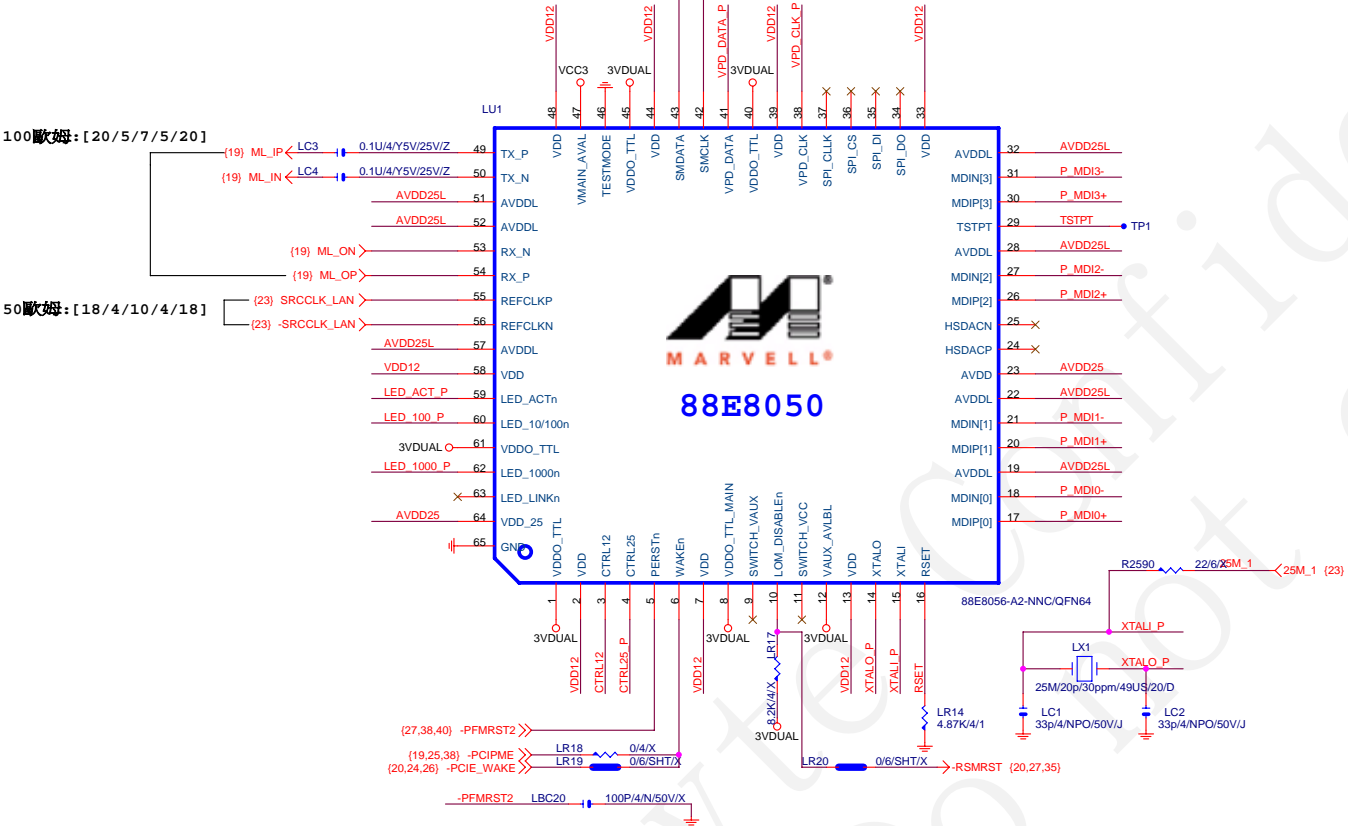
Gigabyte Technology

Title		ATX POWER CONNECTOR	
Size	Document Number	965P-DS3P	Rev
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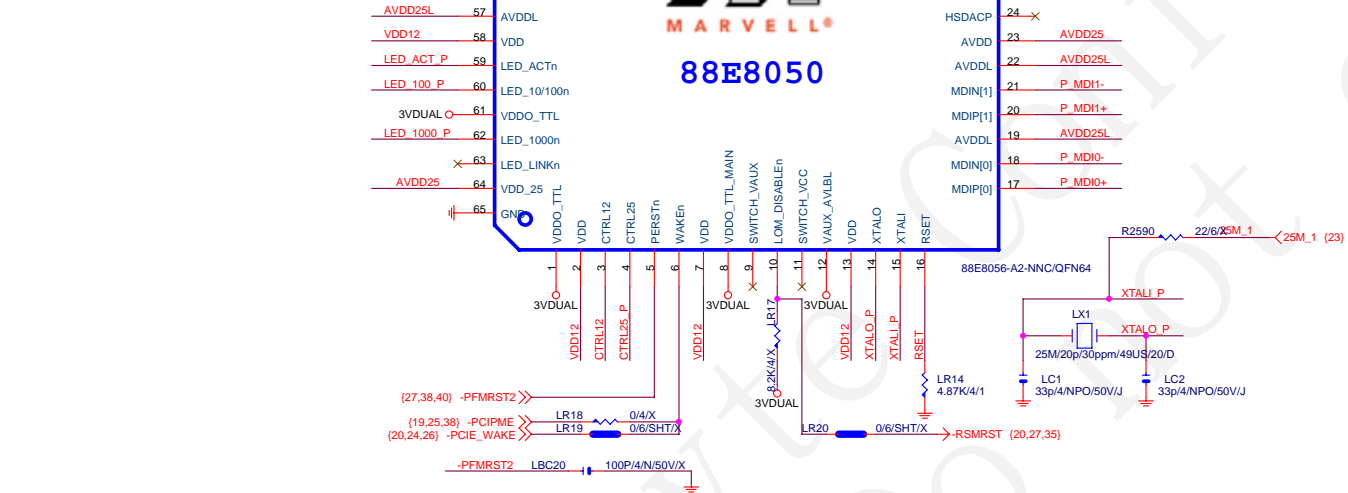
PCIE-1G LAN

- # Layout Check 注意事項
1. LU1 PIN65 需下內層GND, 打 12 VIA
 2. 3VDUAL, VCC3, VDD15 L, AVDD25 L 至少走20mil寬, 並且電容擺設每兩pin至少放一顆Bypass Cap.
 3. X'TAL 25MHz 兩訊號線, TRACE 愈短愈好, 線寬12mil
 4. MDI正負0~3, TRACE 8:7:8, 每對之間保持 40mil

100歐母: [20/5/7/5/20]

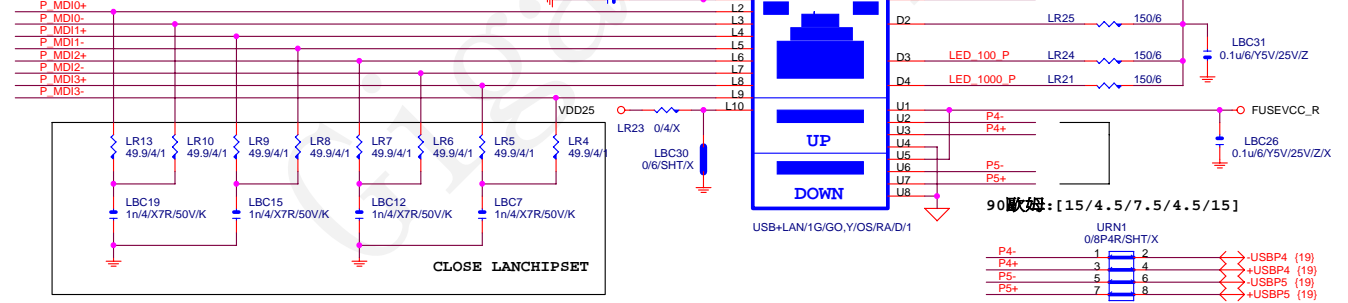


50歐母: [18/4/10/4/18]

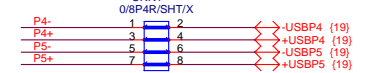


USB_LAN CONNECTOR

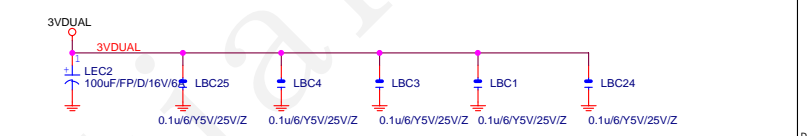
100歐母: [20/5/7/5/20]



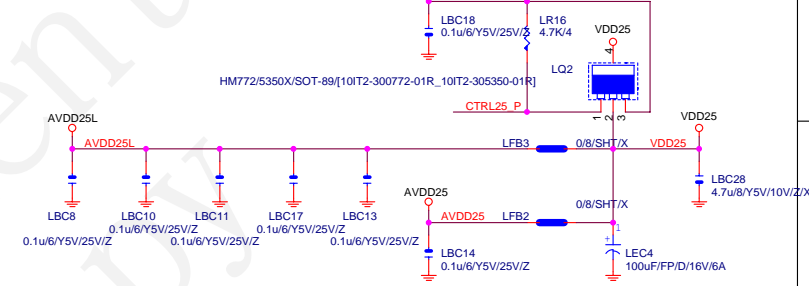
90歐母: [15/4.5/7.5/4.5/15]



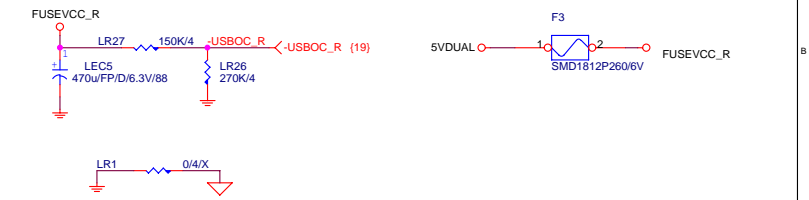
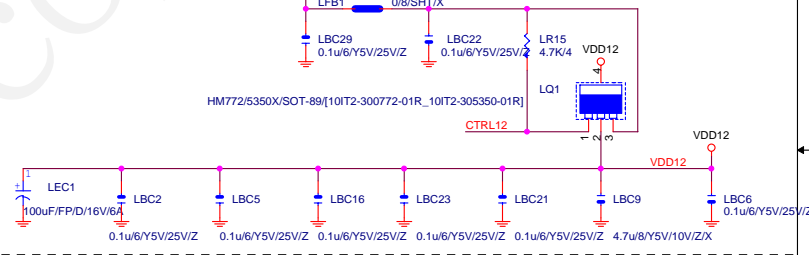
3VDUAL



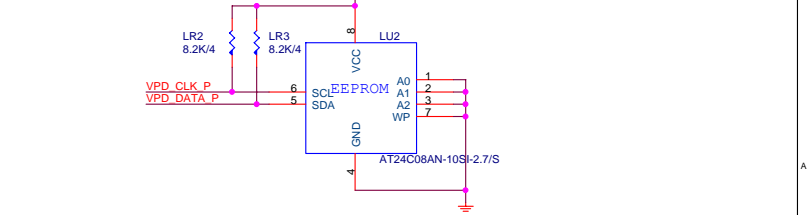
2.5V



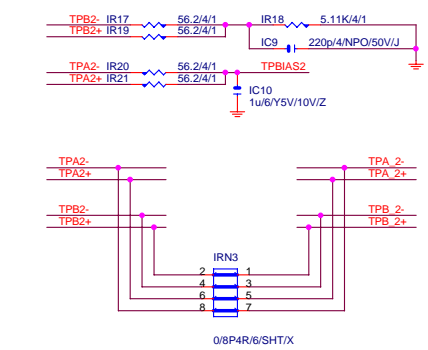
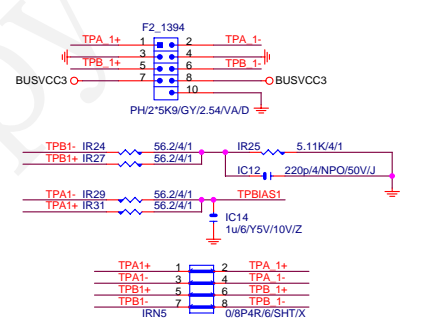
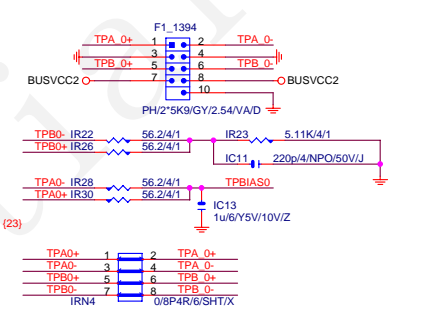
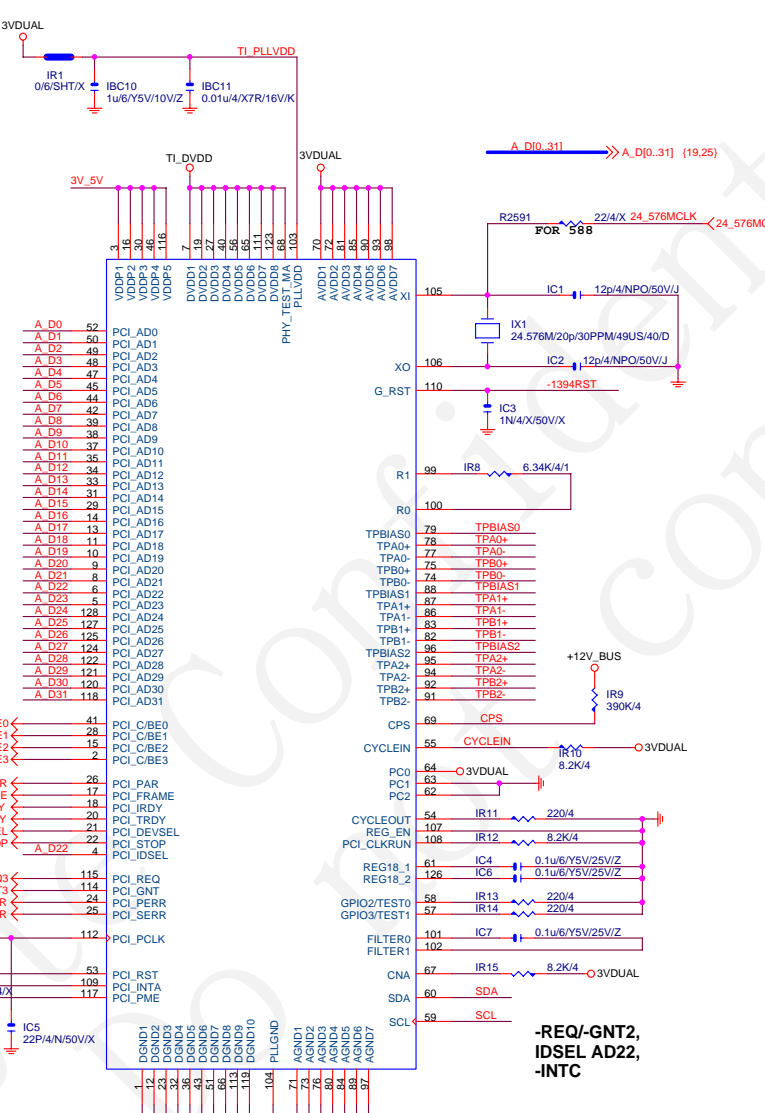
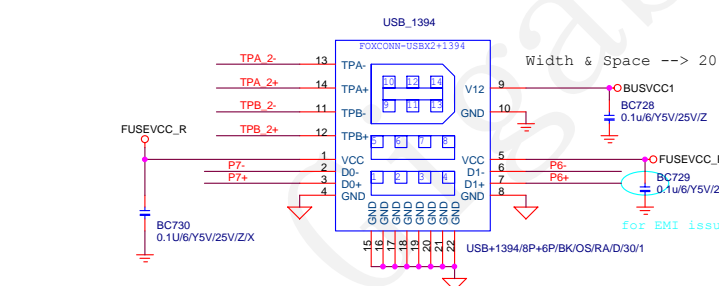
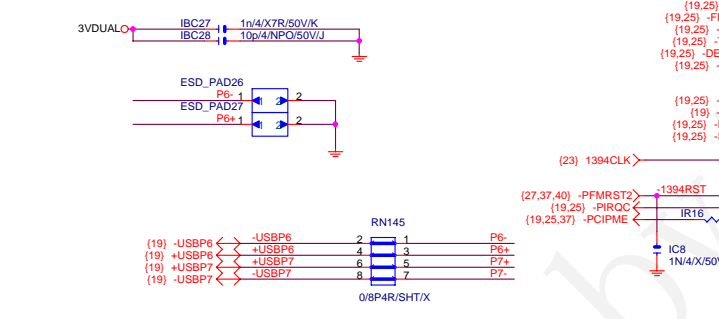
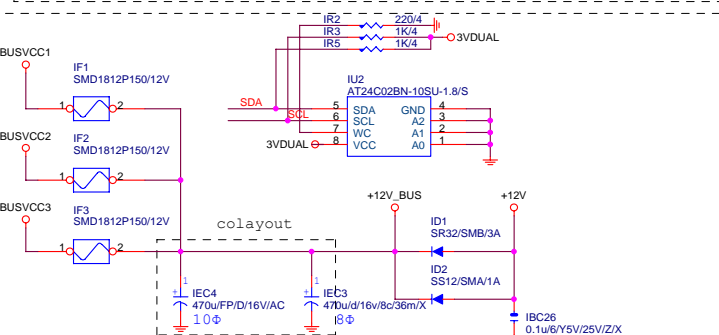
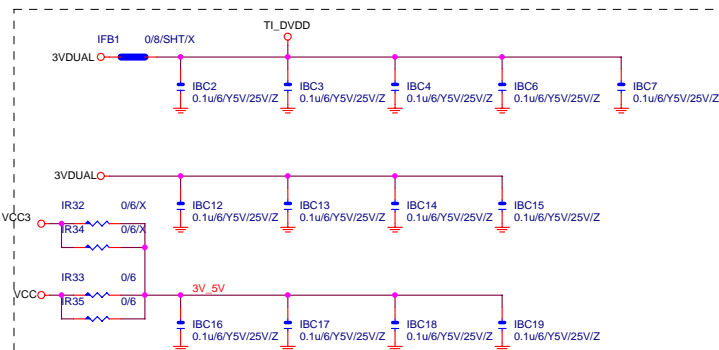
1.2V



EEPROM

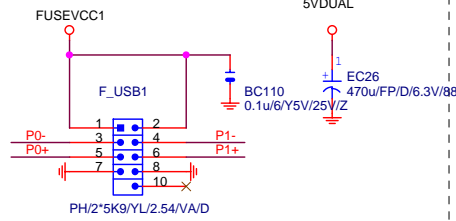
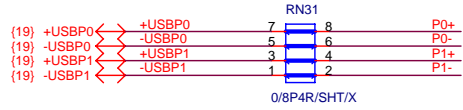


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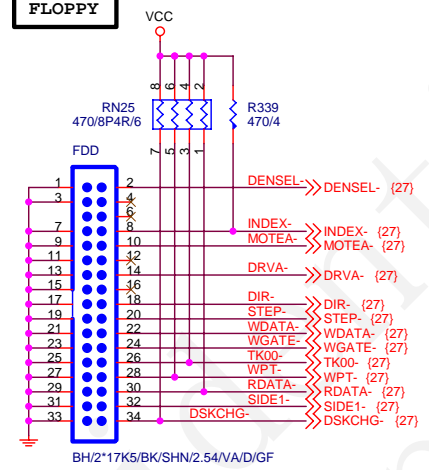


**-REQ-GNT2,
-IDSEL AD22,
-INTC**

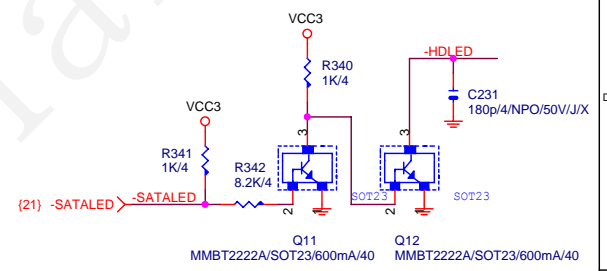
FRONT USB1



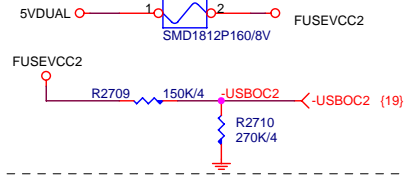
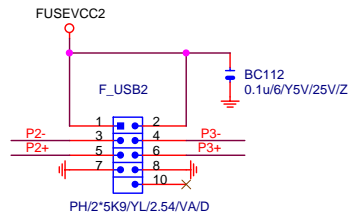
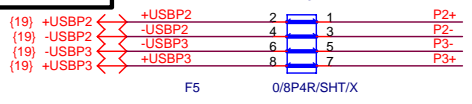
FLOPPY



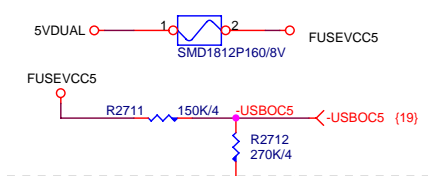
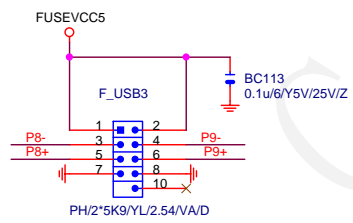
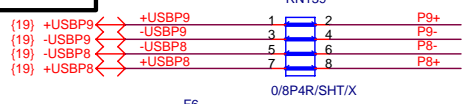
SATA LED



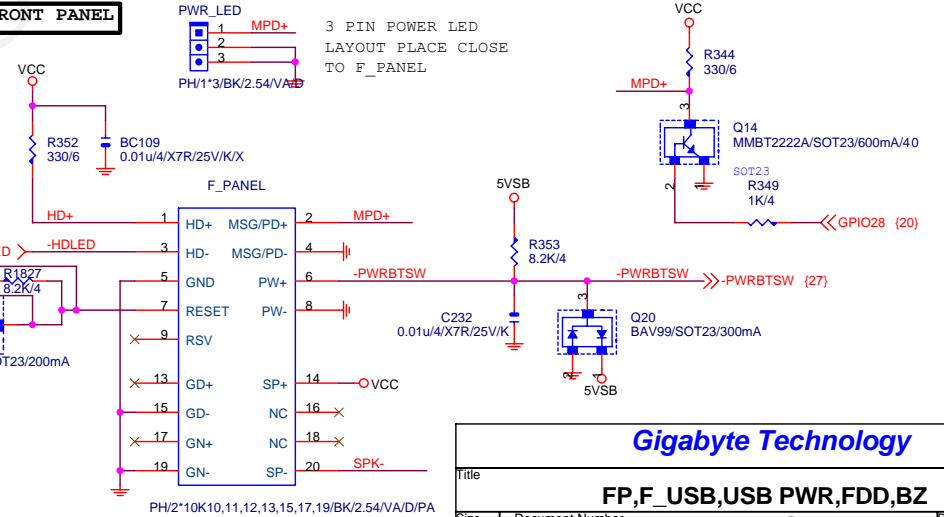
FRONT USB2



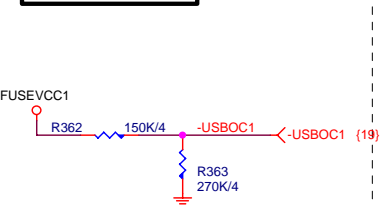
FRONT USB3



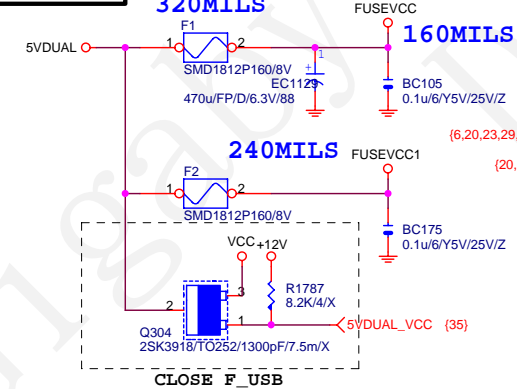
INTEL FRONT PANEL



FRONT USB OC1

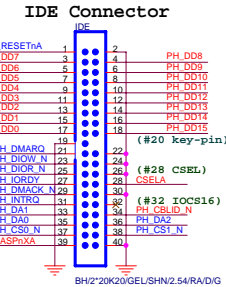
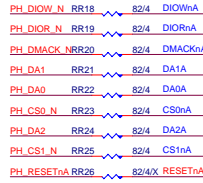
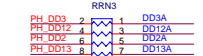
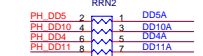
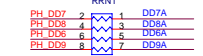
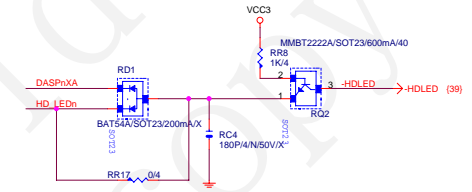
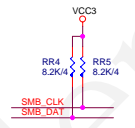
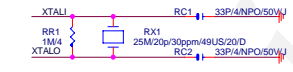
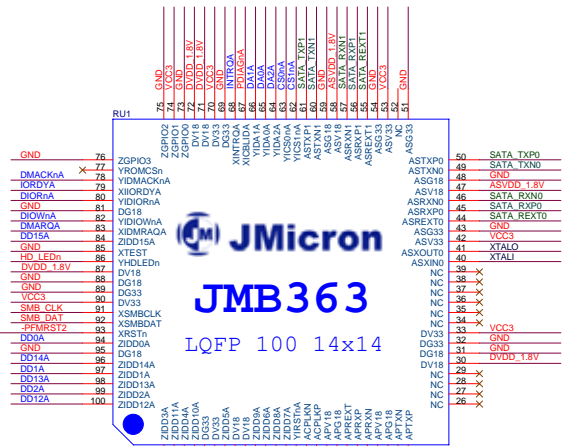
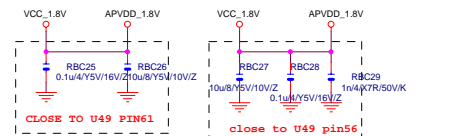
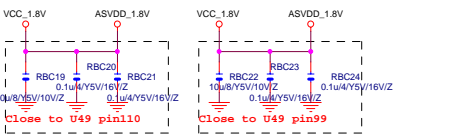
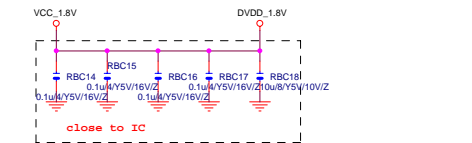
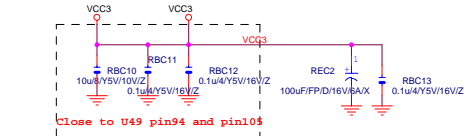
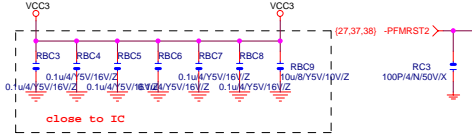
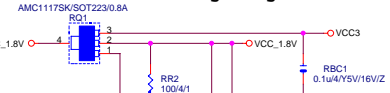


USB POWER

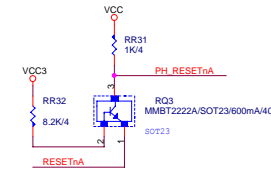


Gigabyte Technology			
FP,F_USB,USB PWR,FDD,BZ			
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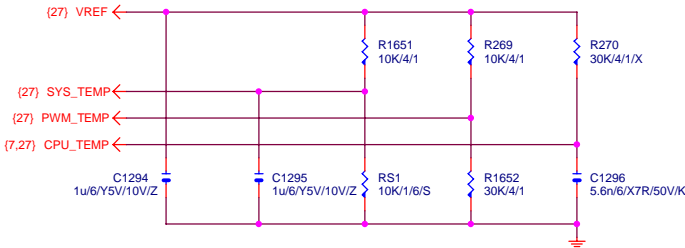
3.3V to 1.8V Voltage Regulator



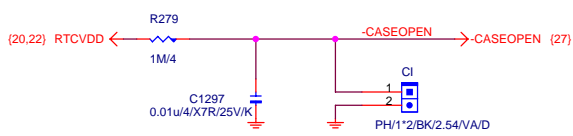
SATA Connector



TEMP H/W MONITOR

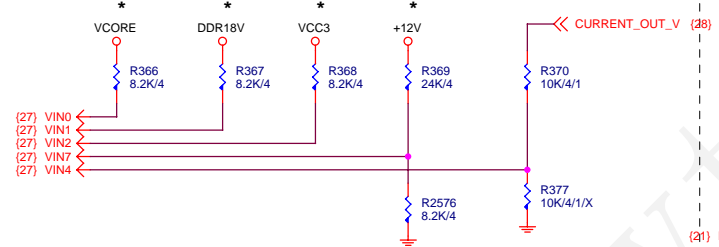


CASE OPEN

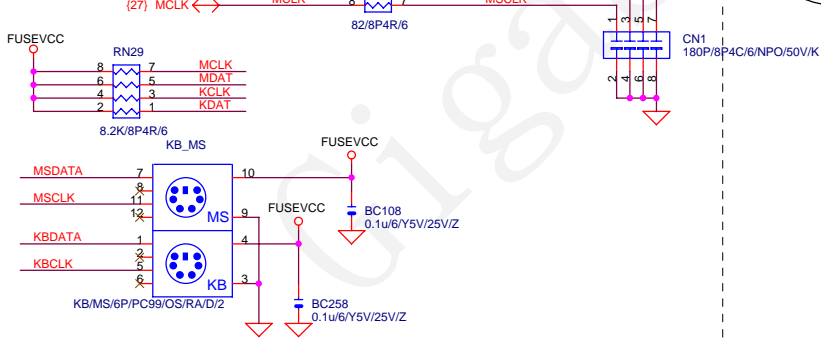


Case Open Circuits

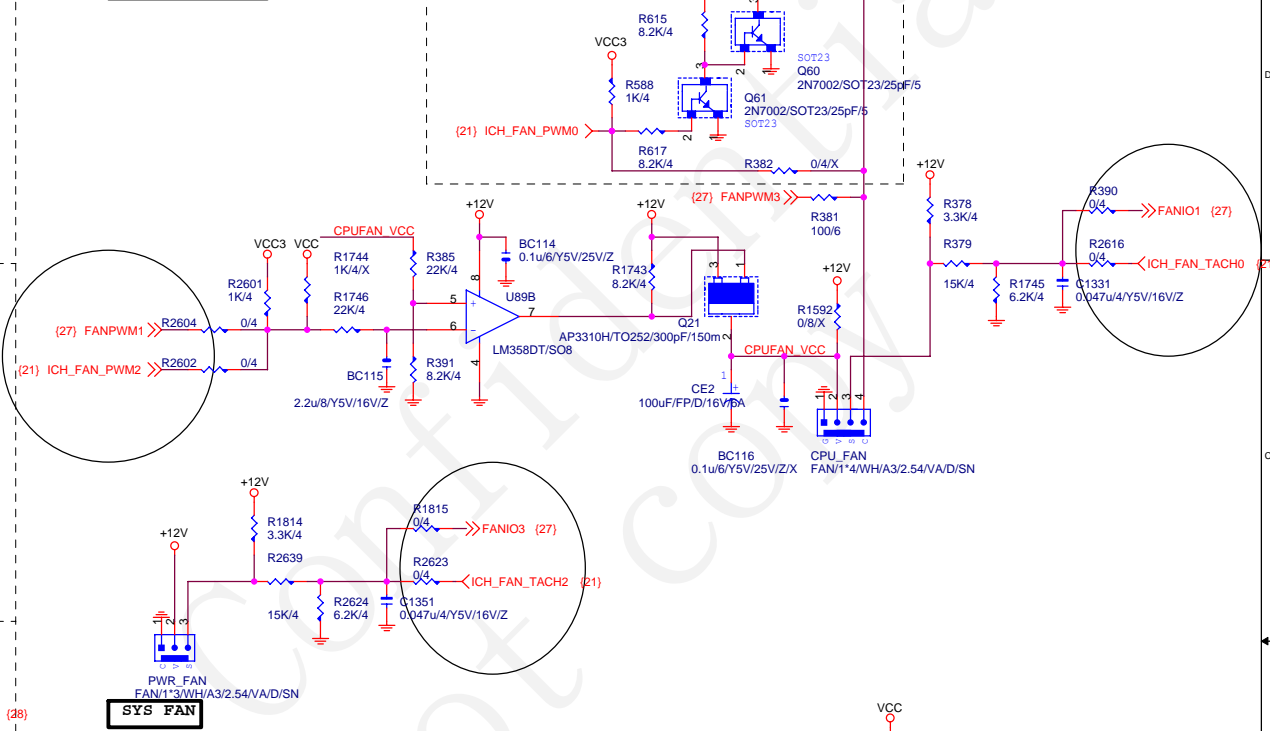
VOLTAGE-- H/W MONITOR



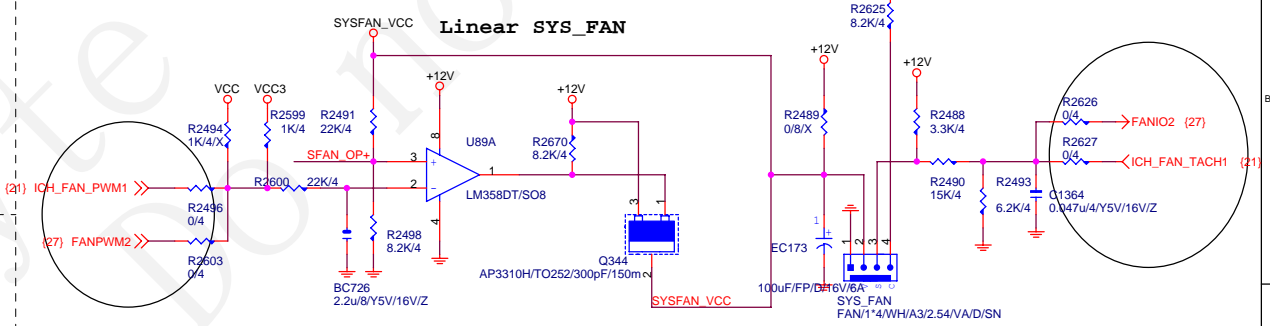
KB/MS



CPU SMART FAN



SYS FAN



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COM & LPT PORT			
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