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P. Leader	Appr. by	Check by	Design by
			Alex

Project Code & Schematics Subject:	**MS03 M/B-FUBAI**	**PCB P/N:**	1P-0055100-80SB
Project Code & Schematics Subject:	**MS03 M/B-HANNSTAR**	**PCB P/N:**	1P-0055500-80SB
Project Code & Schematics Subject:	**MS03 M/B-NAN YA**	**PCB P/N:**	1P-0055200-80SB

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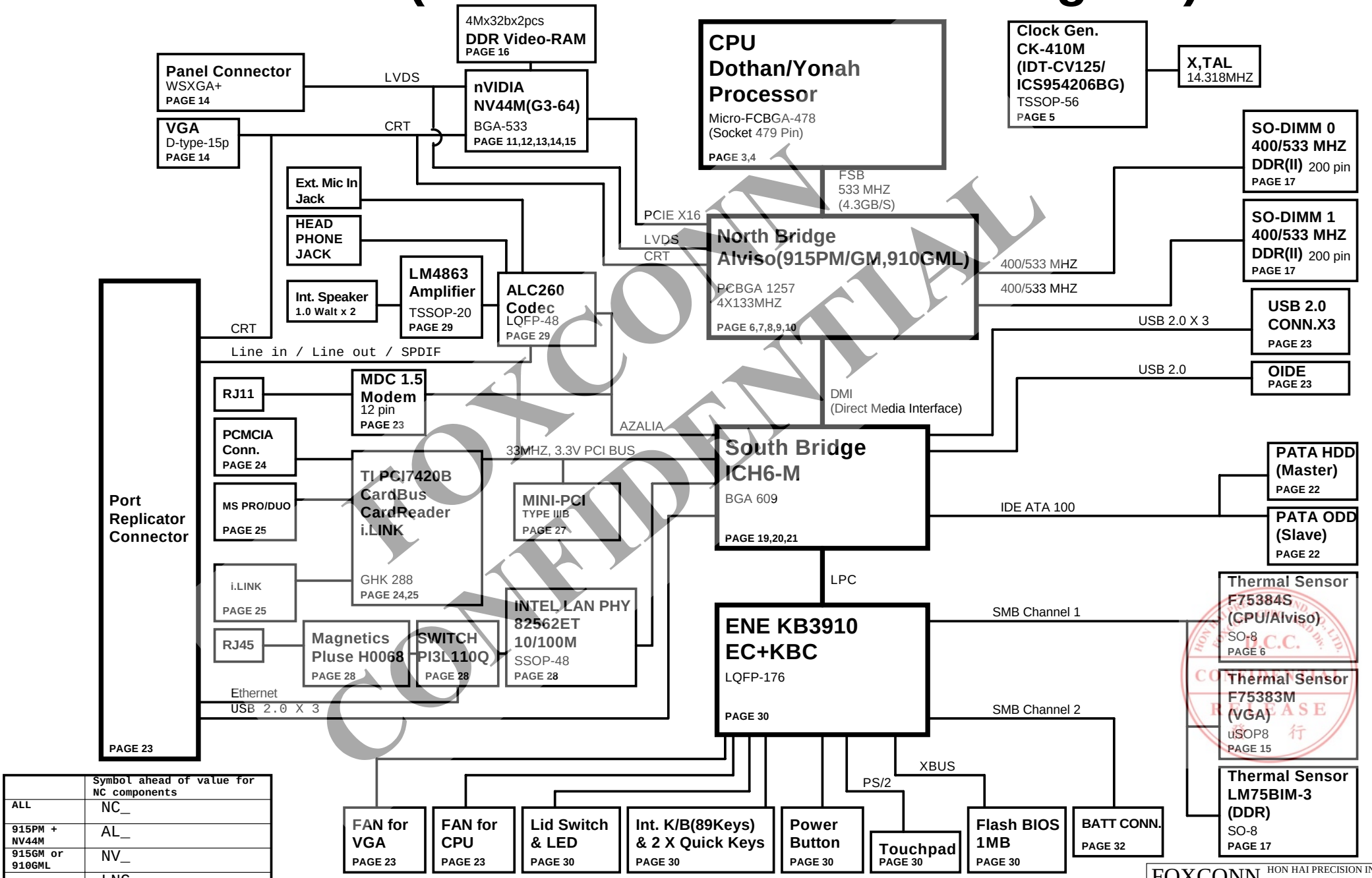
MS03(915PM/GM+Gfx Block Diagram)



	Symbol ahead of value for NC components
ALL	NC_
915PM + NV44M	AL_
915GM or 910GML	NV_
910GML	LNC_
915PM + NV44M or 915GM	HMNC_

BOM configuration

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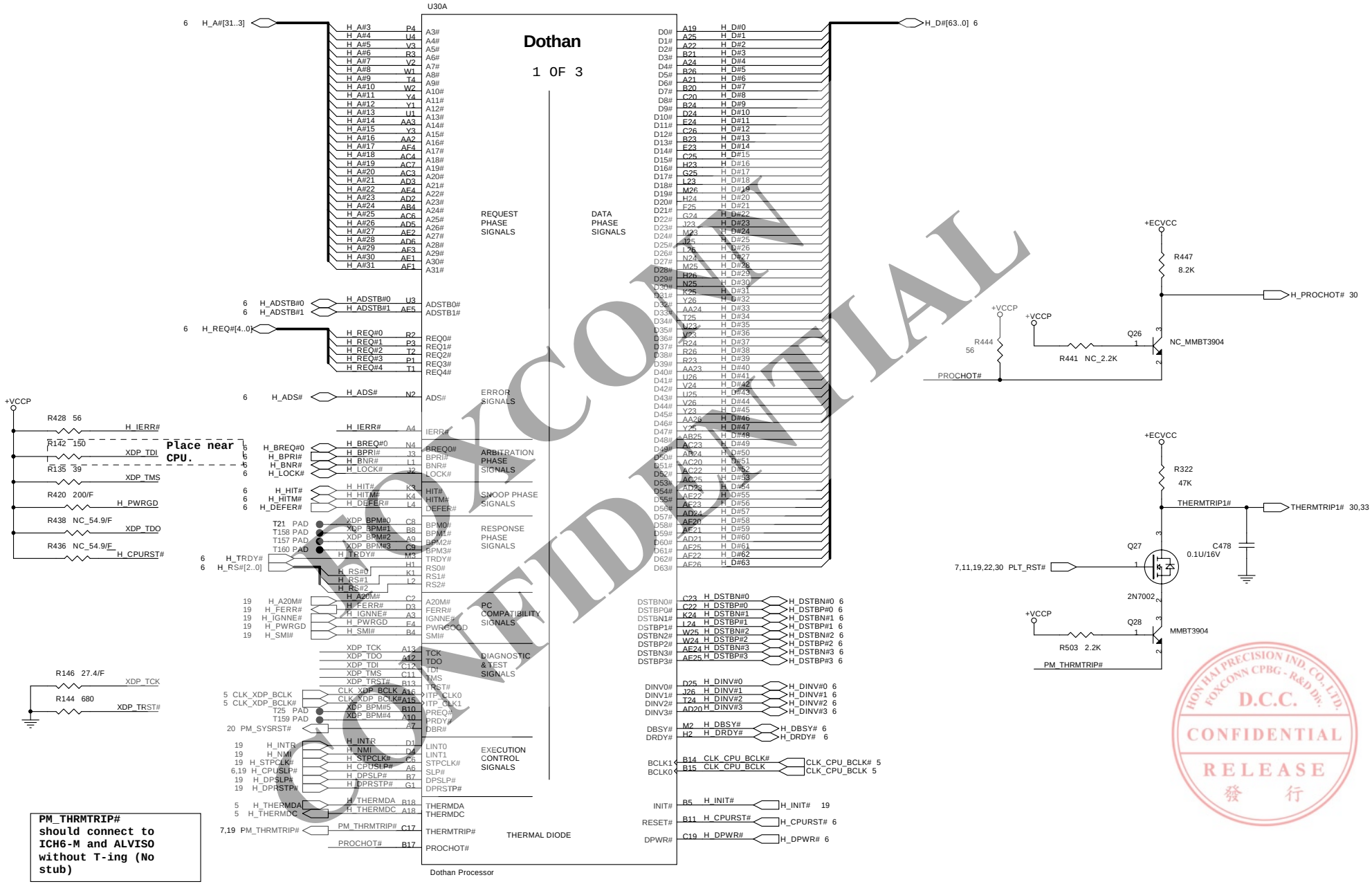

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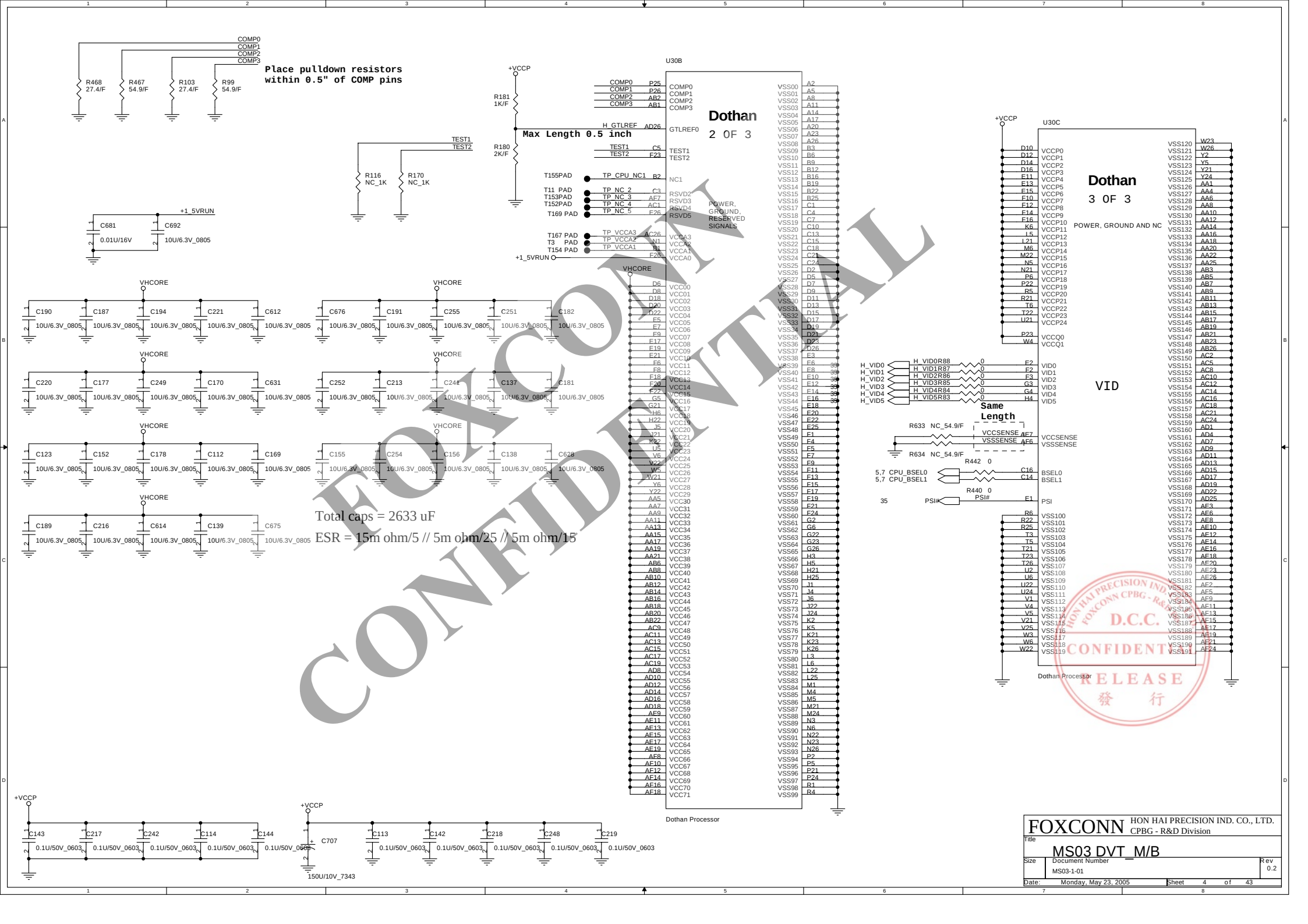

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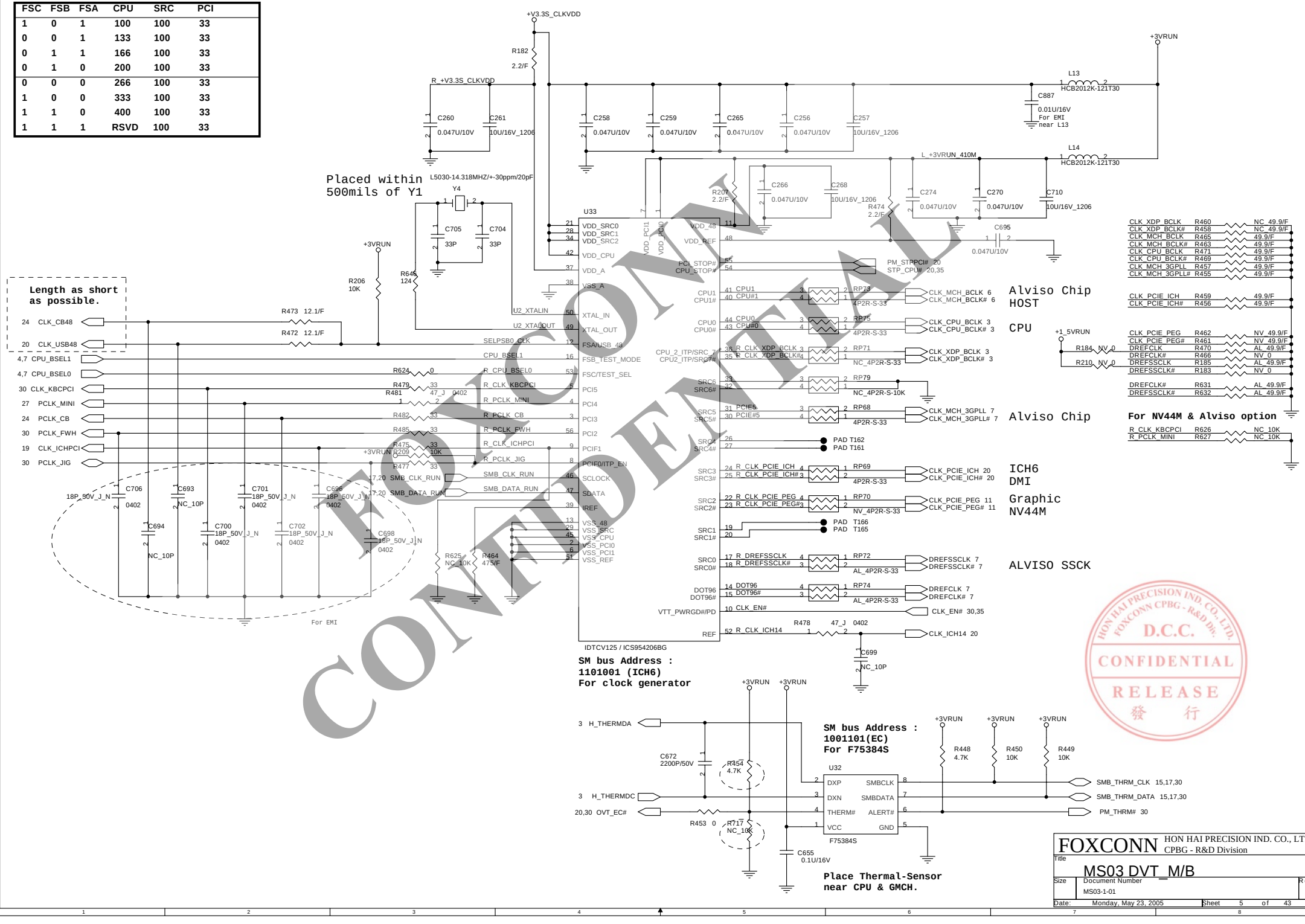
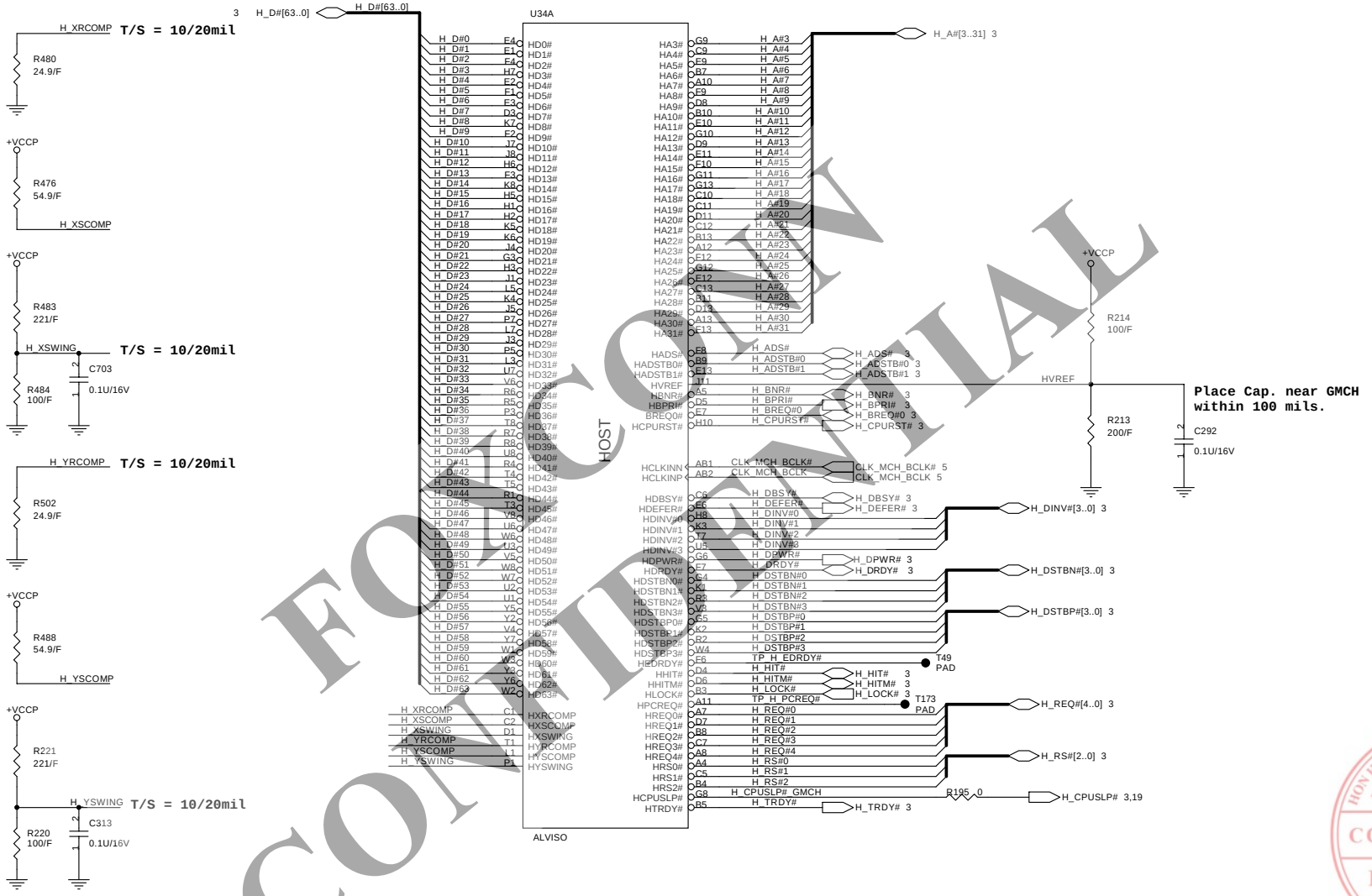
FSC	FSB	FSA	CPU	SRC	PCI
1	0	1	100	100	33
0	0	1	133	100	33
0	1	1	166	100	33
0	1	0	200	100	33
0	0	0	266	100	33
1	0	0	333	100	33
1	1	0	400	100	33
1	1	1	RSVD	100	33





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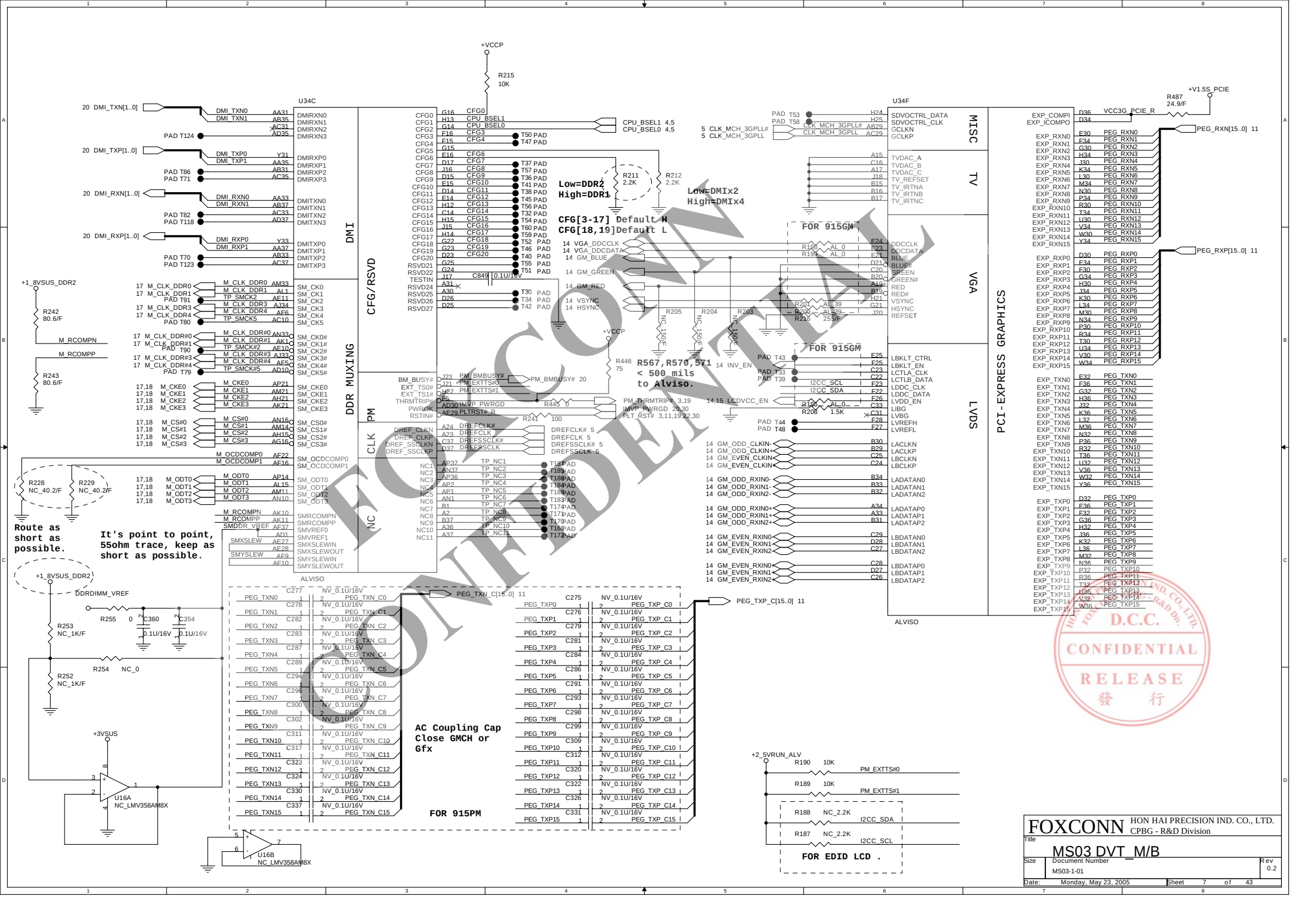

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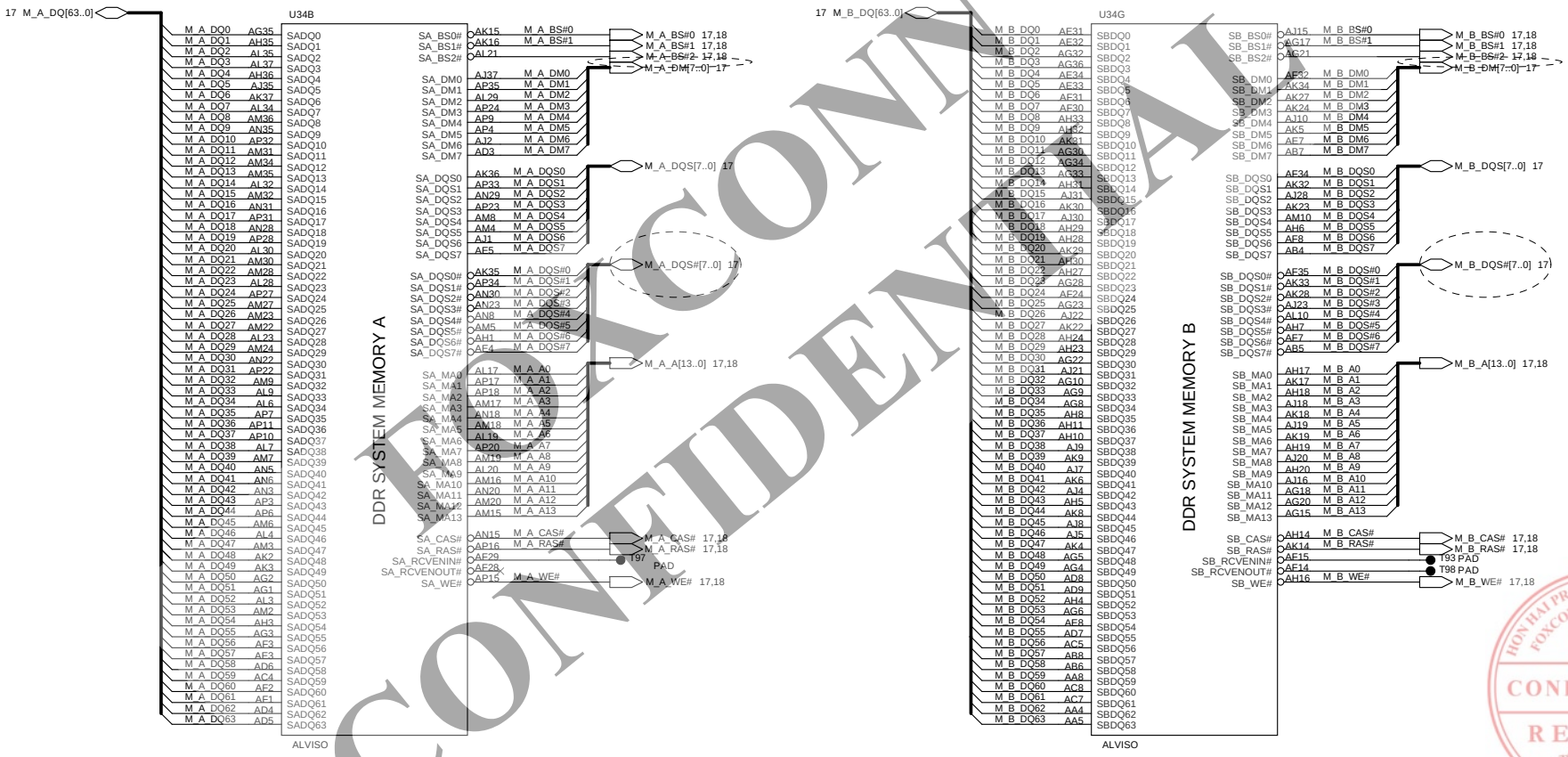

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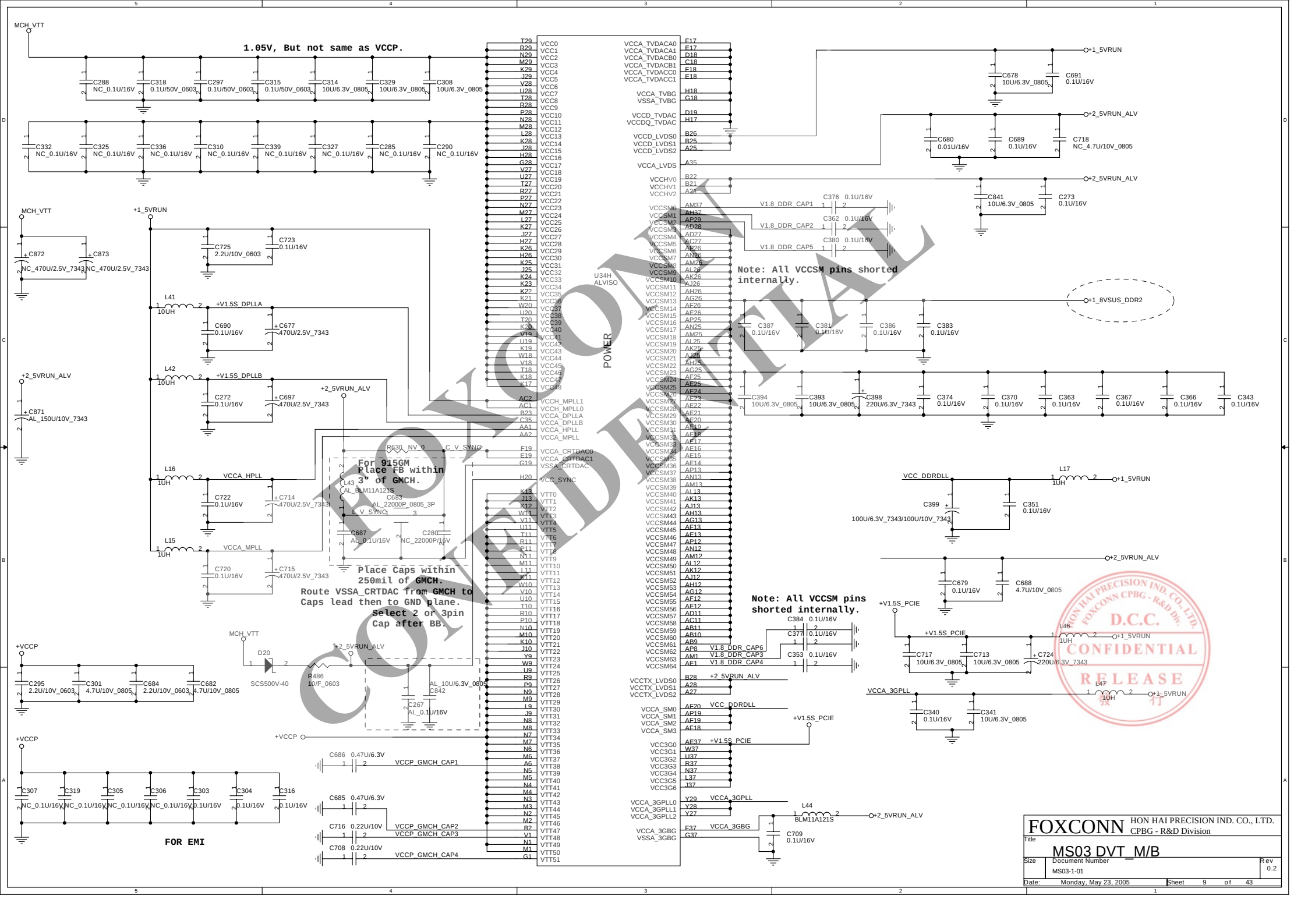
1.05V, But not same as VCCP.
MCH_VTT
+1_5VRUN
+2_5VRUN_ALV
+V1.5S_DPLLA
+V1.5S_DPLLB
VCCA_HPLL
VCCA_MPLL
+VCCP
U34H
ALVISO
POWER
Note: All VCCSM pins shorted internally.
Note: All VCCSM pins shorted internally.
For 915GM Place FB within 3" of GMCH.
Place Caps within 250mil of GMCH.
Route VSSA_CRTDAC from GMCH to Caps lead then to GND plane.
Select 2 or 3pin Cap after BB.
FOR EMI
+1_8VSUS_DDR2
VCC_DDRDLL
+V1.5S_PCIE
VCCA_3GPLL
VCCA_3GBG
V1.8_DDR_CAP1
V1.8_DDR_CAP2
V1.8_DDR_CAP5
V1.8_DDR_CAP6
V1.8_DDR_CAP3
V1.8_DDR_CAP4
VCCP_GMCH_CAP1
VCCP_GMCH_CAP2
VCCP_GMCH_CAP3
VCCP_GMCH_CAP4
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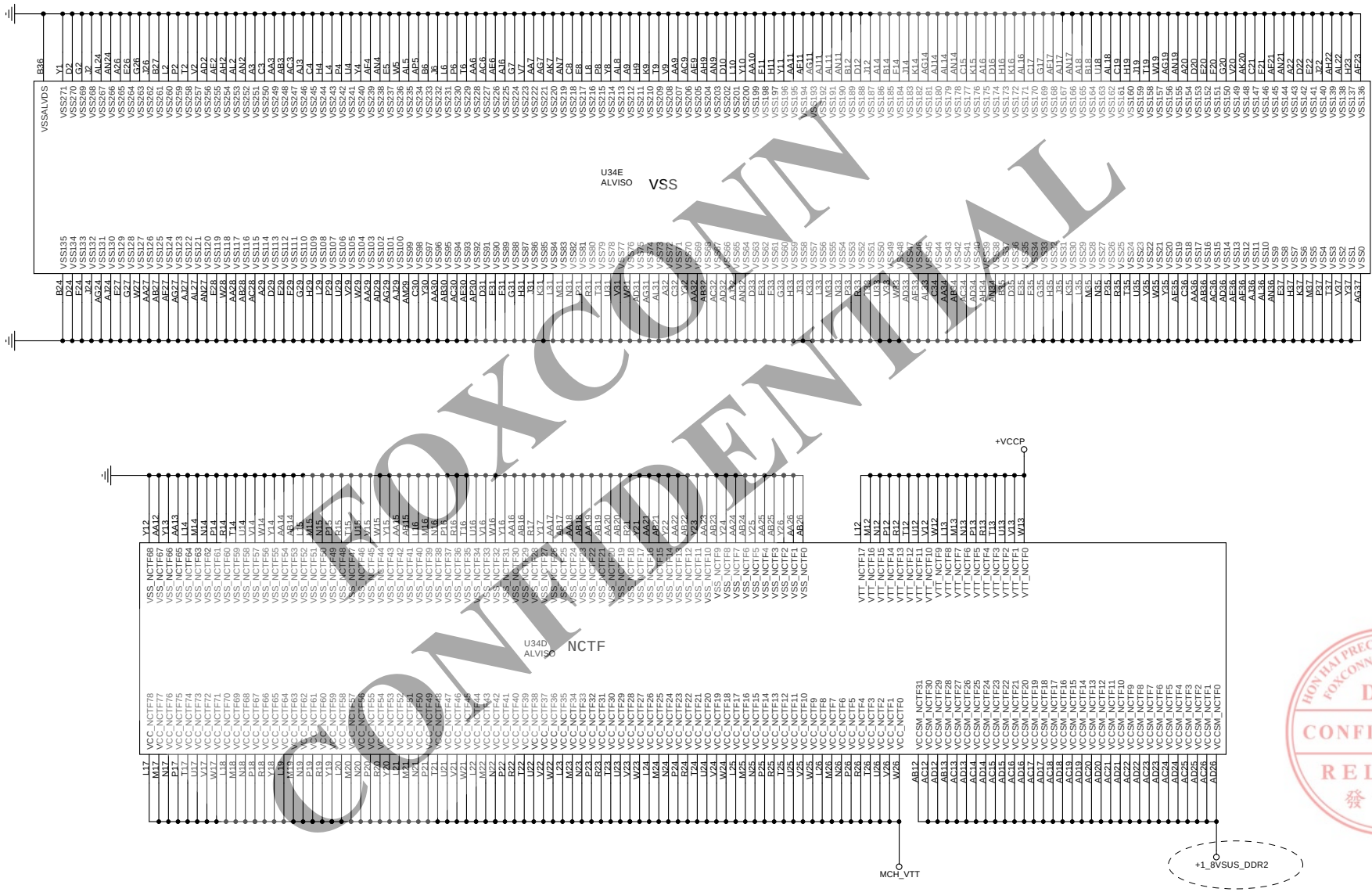

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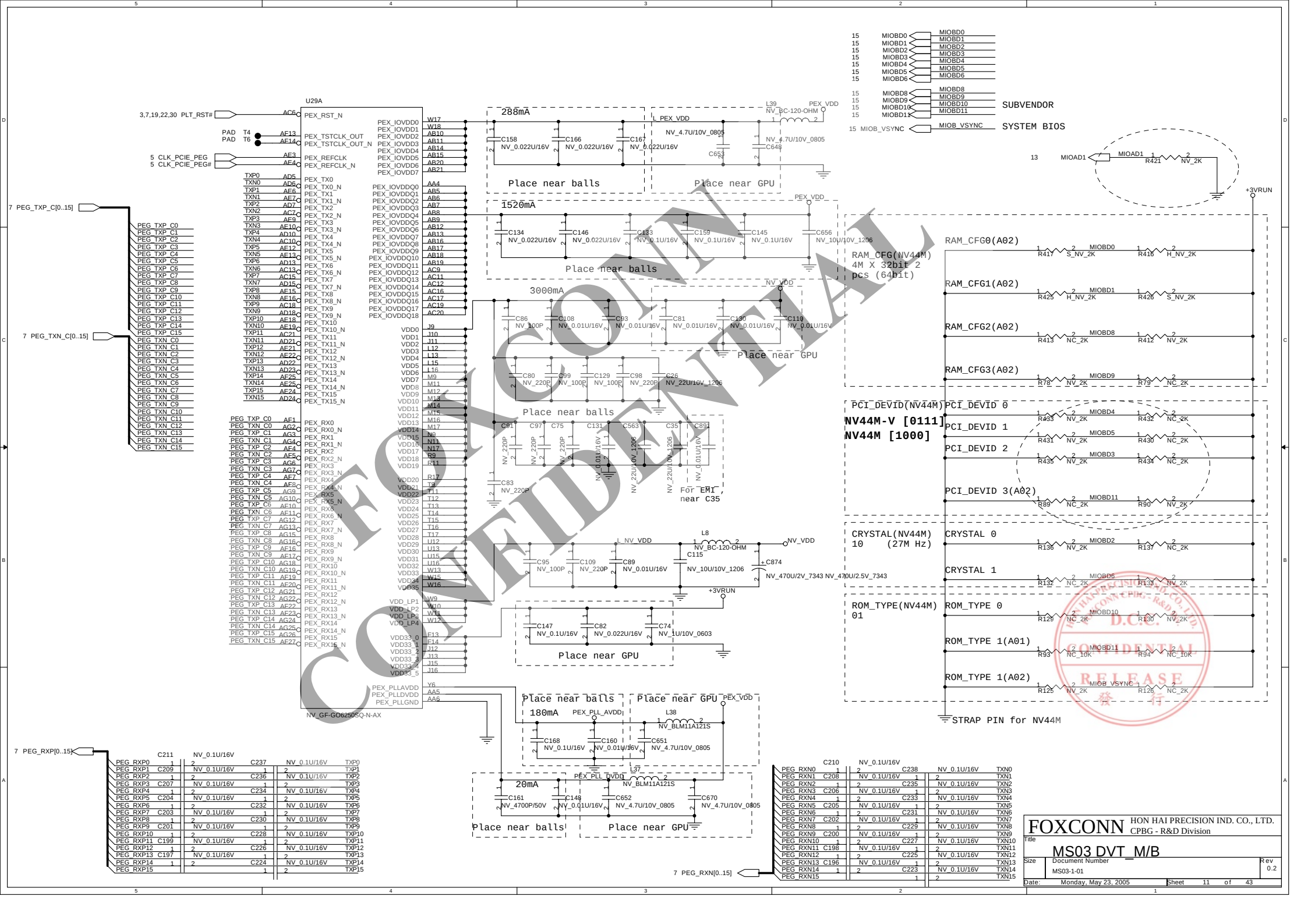

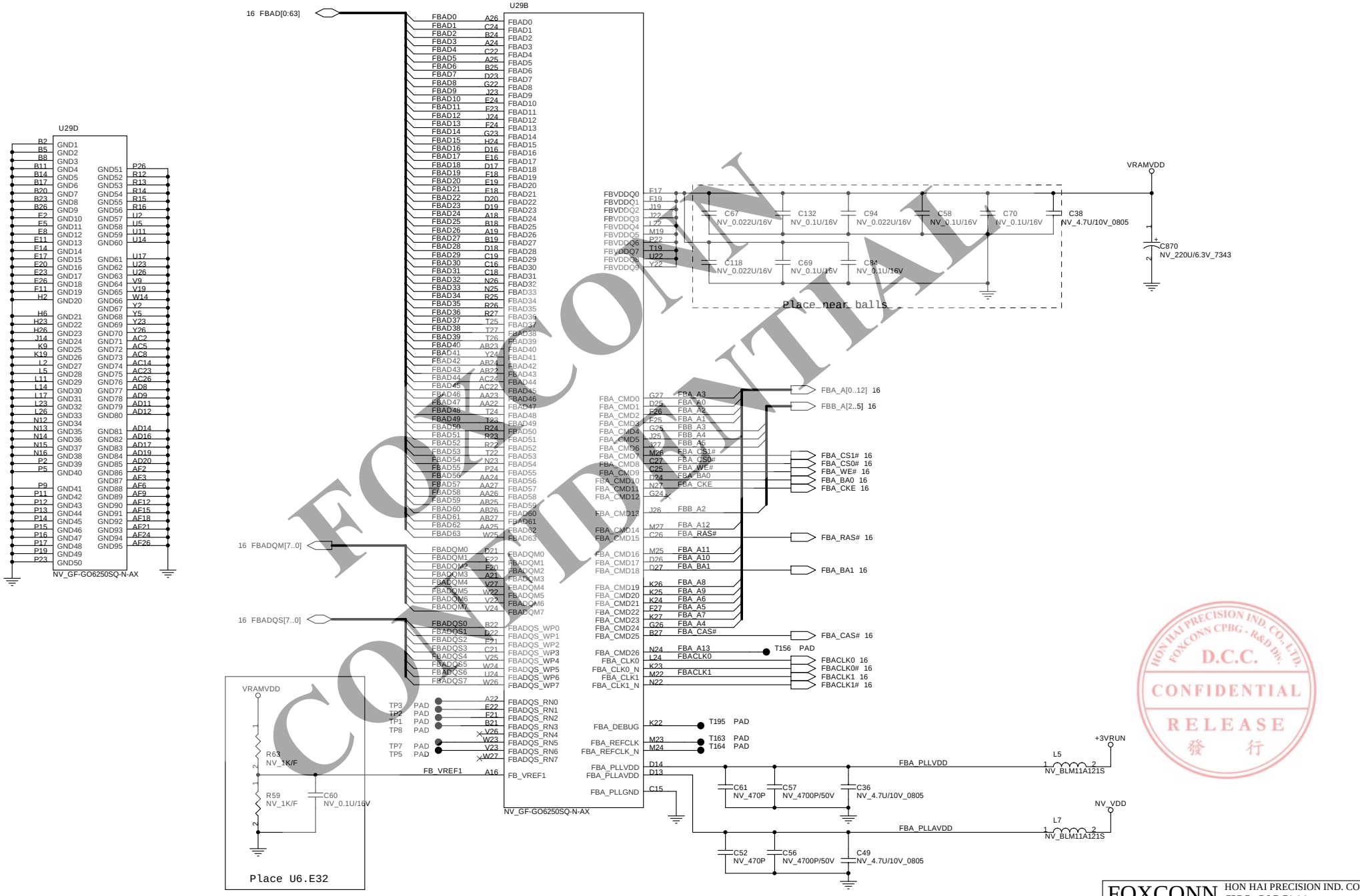

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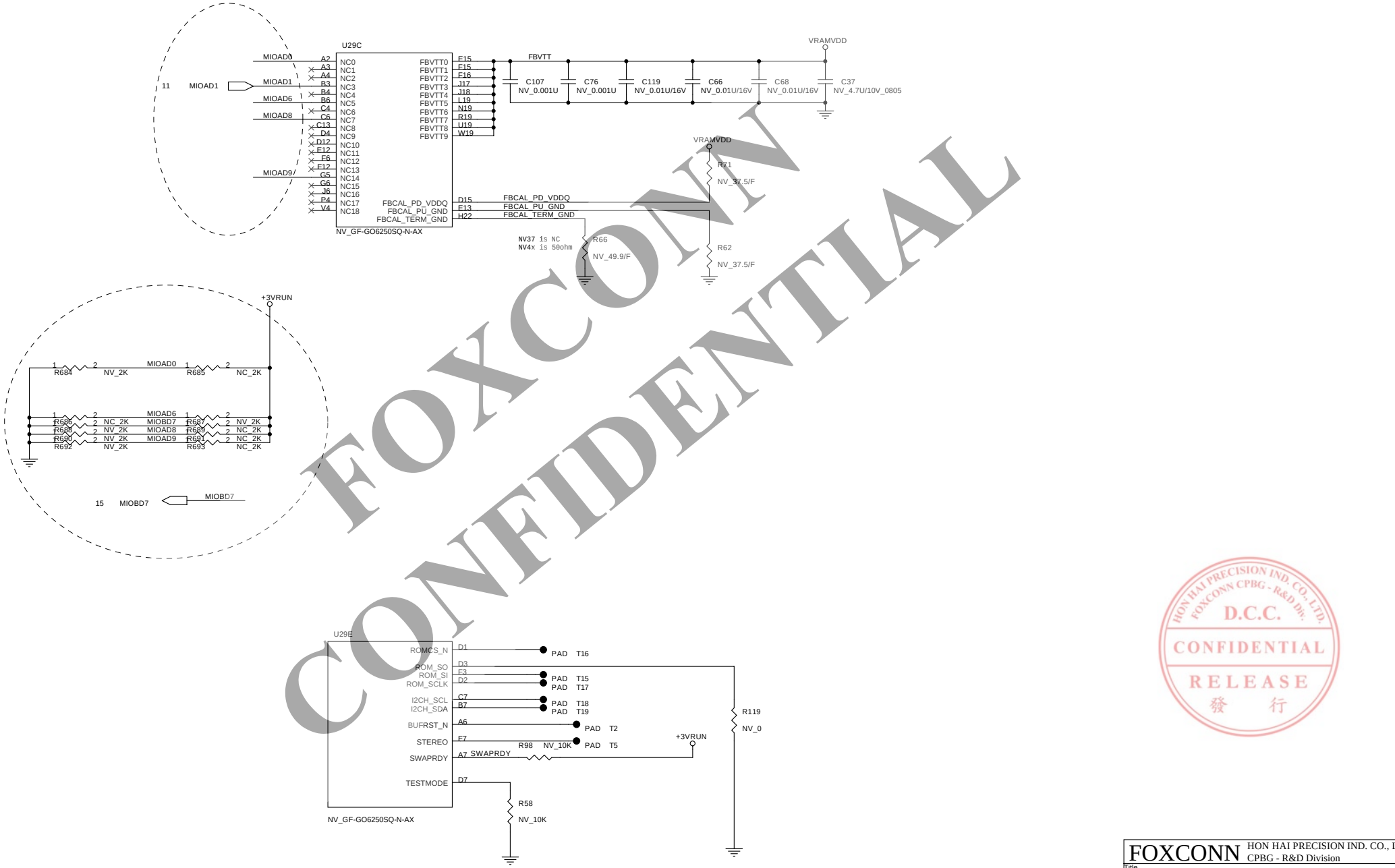

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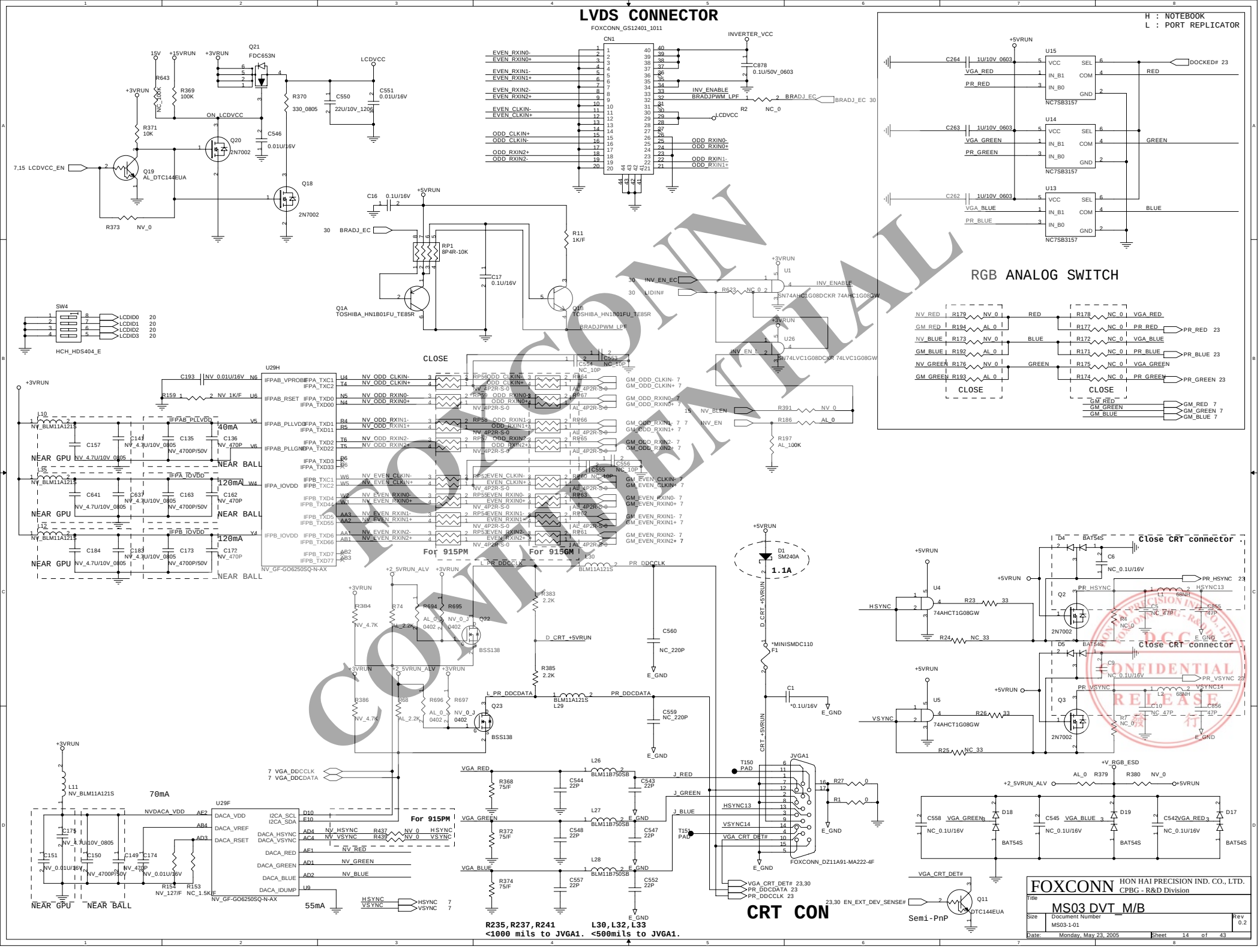
LVDS CONNECTOR



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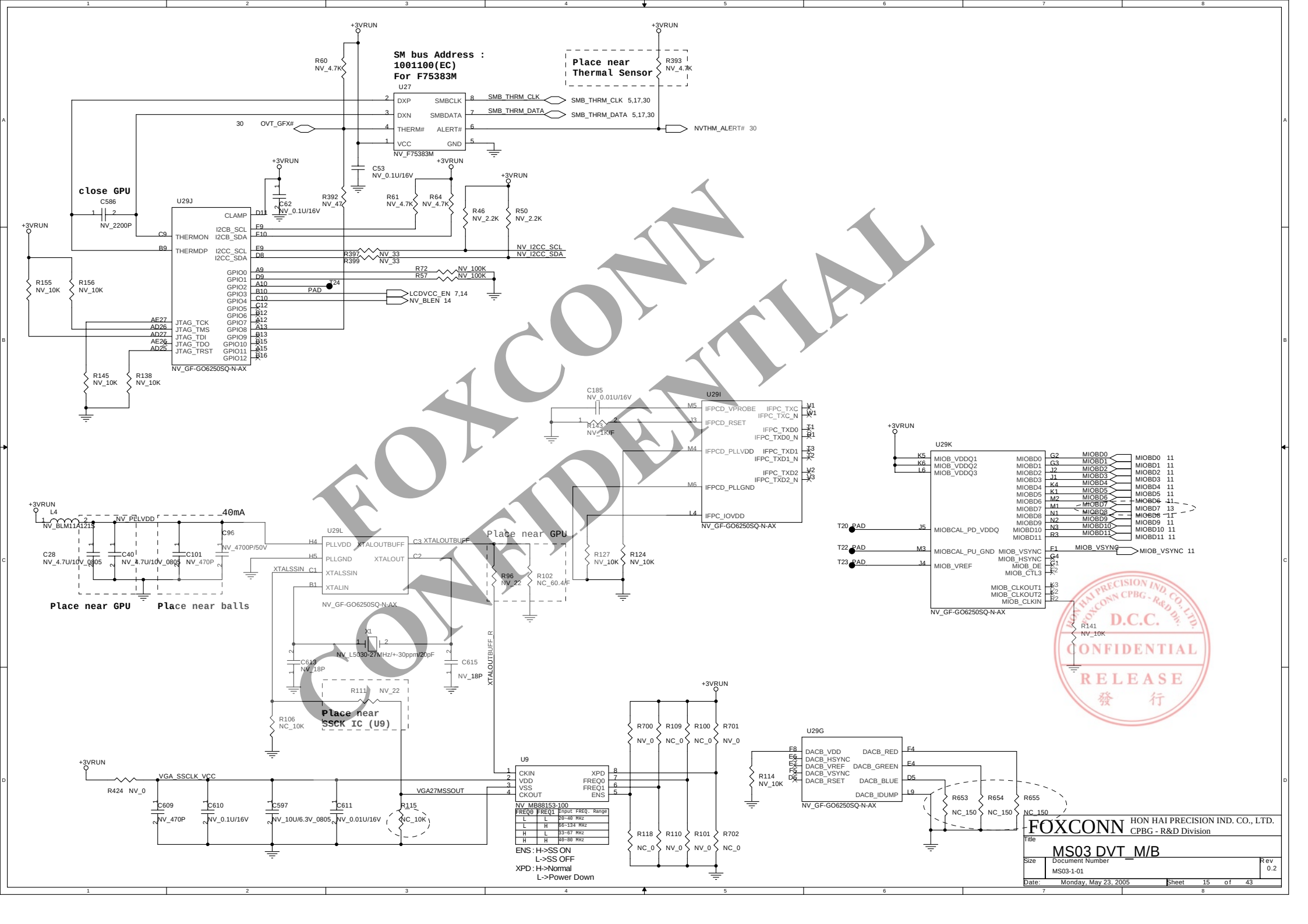
SM bus Address :
1001100(EC)
For F75383M
Place near
Thermal Sensor
close GPU
Place near GPU
Place near balls
Place near GPU
Place near
SSCK IC (U9)
40mA
NV_F75383M
NV_GF-GO6250SQ-N-AX
ENS : H->SS ON
L->SS OFF
XPD : H->Normal
L->Power Down
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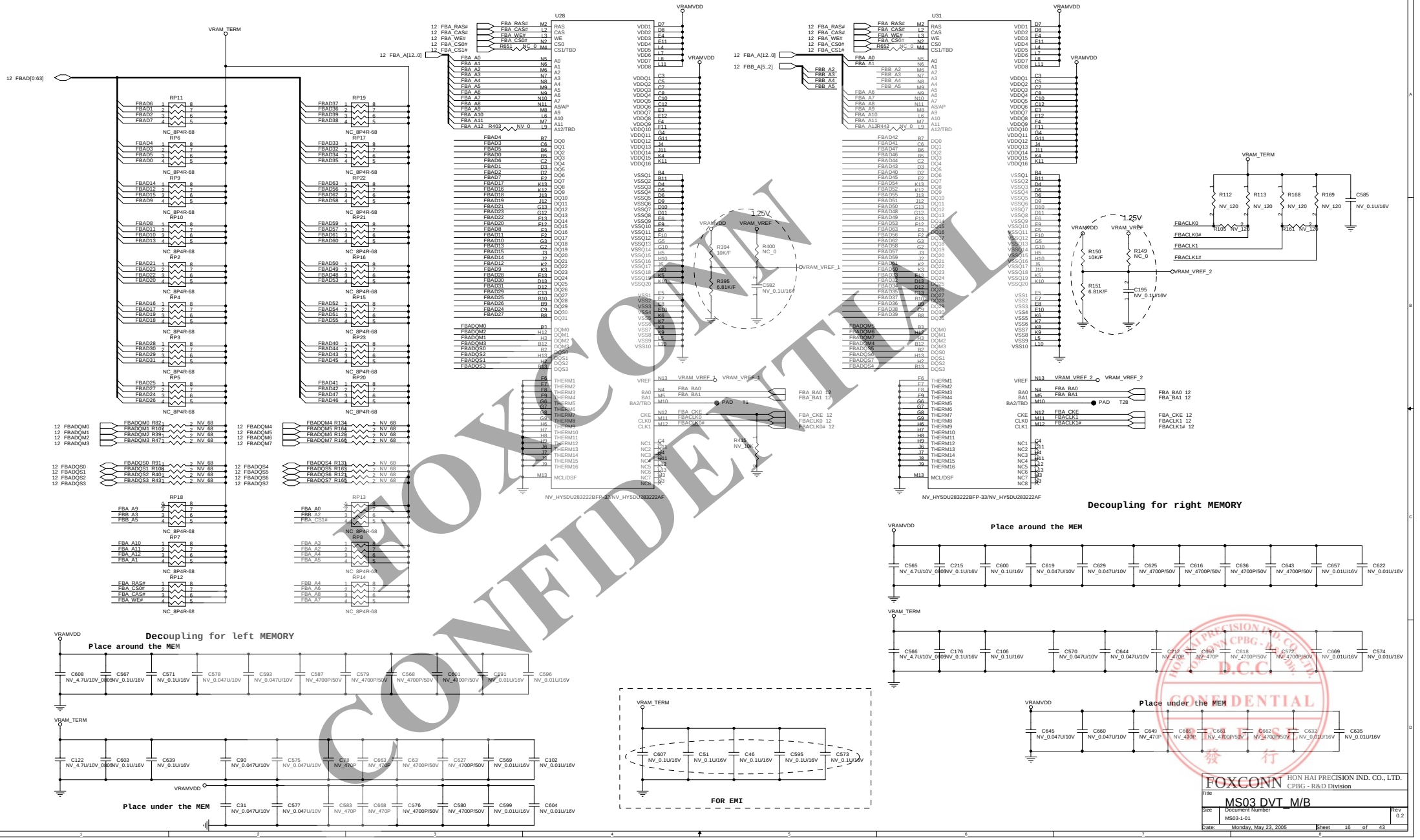

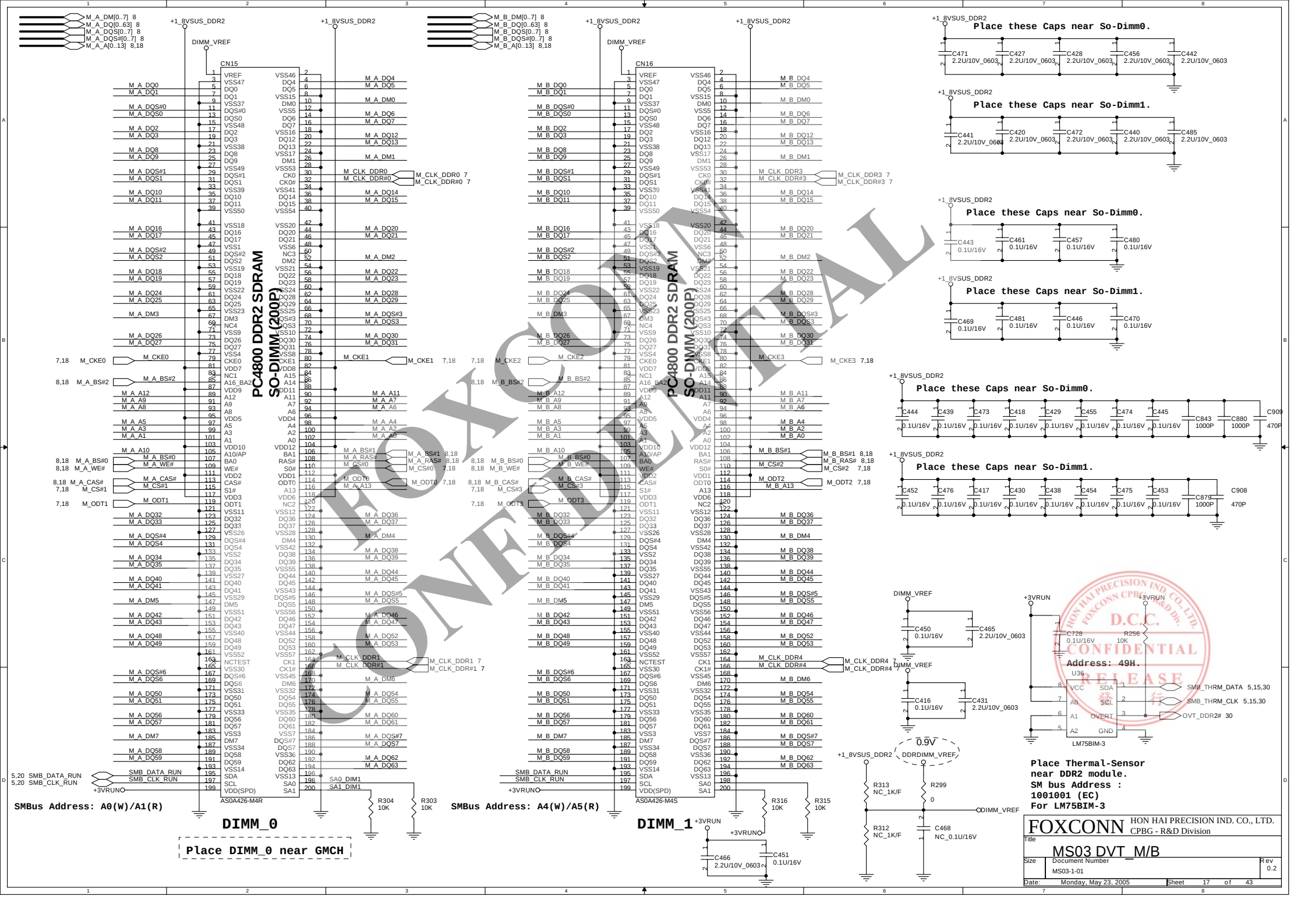
M_A_DM[0..7] 8
M_A_DQ[0..63] 8
M_A_DQS[0..7] 8
M_A_DQS#[0..7] 8
M_A_A[0..13] 8,18
+1_8VSUS_DDR2
DIMM_VREF
CN15
PC4800 DDR2 SDRAM
SO-DIMM (200P)
AS0A426-M4R
M_CLK_DDR0 7
M_CLK_DDR0#0 7
M_CLK_DDR1 7
M_CLK_DDR1#1 7
M_CKE0
M_CKE1 7,18
M_A_BS#2 8,18
M_A_BS#0 8,18
M_A_WE# 8,18
M_A_CAS# 8,18
M_CS#1 7,18
M_ODT1 7,18
M_ODT0 7,18
M_A_A13
M_CS#0 7,18
M_A_BS#1 8,18
M_A_RAS# 8,18
SMB_DATA_RUN 5,20
SMB_CLK_RUN 5,20
+3VRUN
SA0_DIM1
SA1_DIM1
R304 10K
R303 10K
SMBus Address: A0(W)/A1(R)
DIMM_0
Place DIMM_0 near GMCH
M_B_DM[0..7] 8
M_B_DQ[0..63] 8
M_B_DQS[0..7] 8
M_B_DQS#[0..7] 8
M_B_A[0..13] 8,18
CN16
AS0A426-M4S
M_CLK_DDR3 7
M_CLK_DDR3#3 7
M_CKE2
M_CKE3 7,18
M_B_BS#2 8,18
M_B_BS#0 8,18
M_B_WE# 8,18
M_B_CAS# 8,18
M_CS#3 7,18
M_ODT3 7,18
M_ODT2 7,18
M_B_A13
M_B_BS#1 8,18
M_B_RAS# 8,18
M_CS#2 7,18
R316 10K
R315 10K
SMBus Address: A4(W)/A5(R)
DIMM_1
C466 2.2U/10V_0603
C451 0.1U/16V
Place these Caps near So-Dimm0.
C471 2.2U/10V_0603
C427 2.2U/10V_0603
C428 2.2U/10V_0603
C456 2.2U/10V_0603
C442 2.2U/10V_0603
Place these Caps near So-Dimm1.
C441 2.2U/10V_0603
C420 2.2U/10V_0603
C472 2.2U/10V_0603
C440 2.2U/10V_0603
C485 2.2U/10V_0603
Place these Caps near So-Dimm0.
C443 0.1U/16V
C461 0.1U/16V
C457 0.1U/16V
C480 0.1U/16V
Place these Caps near So-Dimm1.
C469 0.1U/16V
C481 0.1U/16V
C446 0.1U/16V
C470 0.1U/16V
Place these Caps near So-Dimm0.
C444 0.1U/16V
C439 0.1U/16V
C473 0.1U/16V
C418 0.1U/16V
C429 0.1U/16V
C455 0.1U/16V
C474 0.1U/16V
C445 0.1U/16V
C843 1000P
C880 1000P
C909 470P
Place these Caps near So-Dimm1.
C452 0.1U/16V
C476 0.1U/16V
C417 0.1U/16V
C430 0.1U/16V
C438 0.1U/16V
C454 0.1U/16V
C475 0.1U/16V
C453 0.1U/16V
C879 1000P
C908 470P
DIMM_VREF
C450 0.1U/16V
C465 2.2U/10V_0603
C416 0.1U/16V
C431 2.2U/10V_0603
0.9V
DDRDIMM_VREF
R313 NC_1K/F
R299 0
R312 NC_1K/F
C468 NC_0.1U/16V
ODIMM_VREF
+3VRUN
C728 0.1U/16V
R258 10K
U36
LM75BIM-3
SMB_THRM_DATA 5,15,30
SMB_THRM_CLK 5,15,30
OVT_DDR2# 30
Address: 49H.
Place Thermal-Sensor near DDR2 module.
SM bus Address : 1001001 (EC)
For LM75BIM-3
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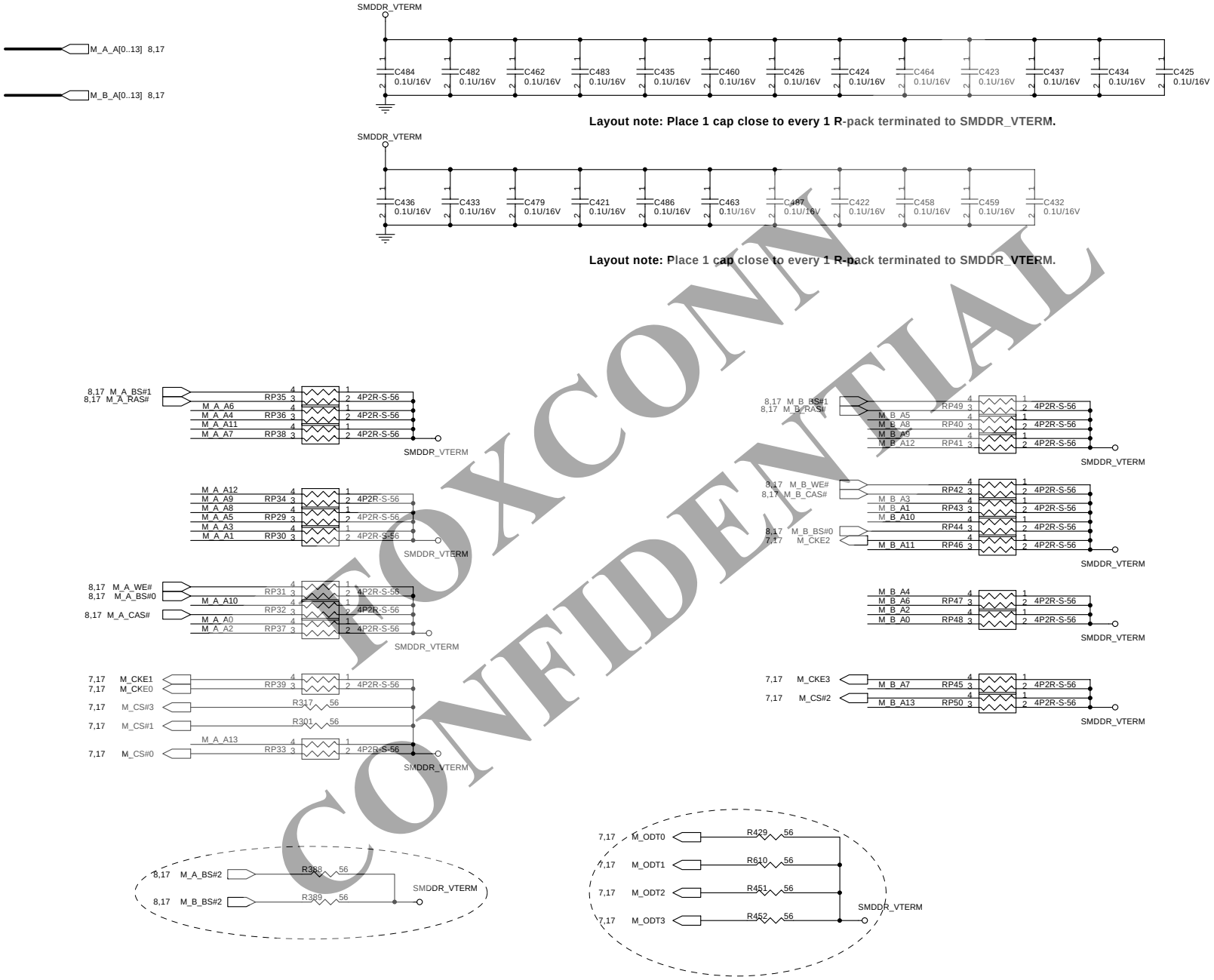

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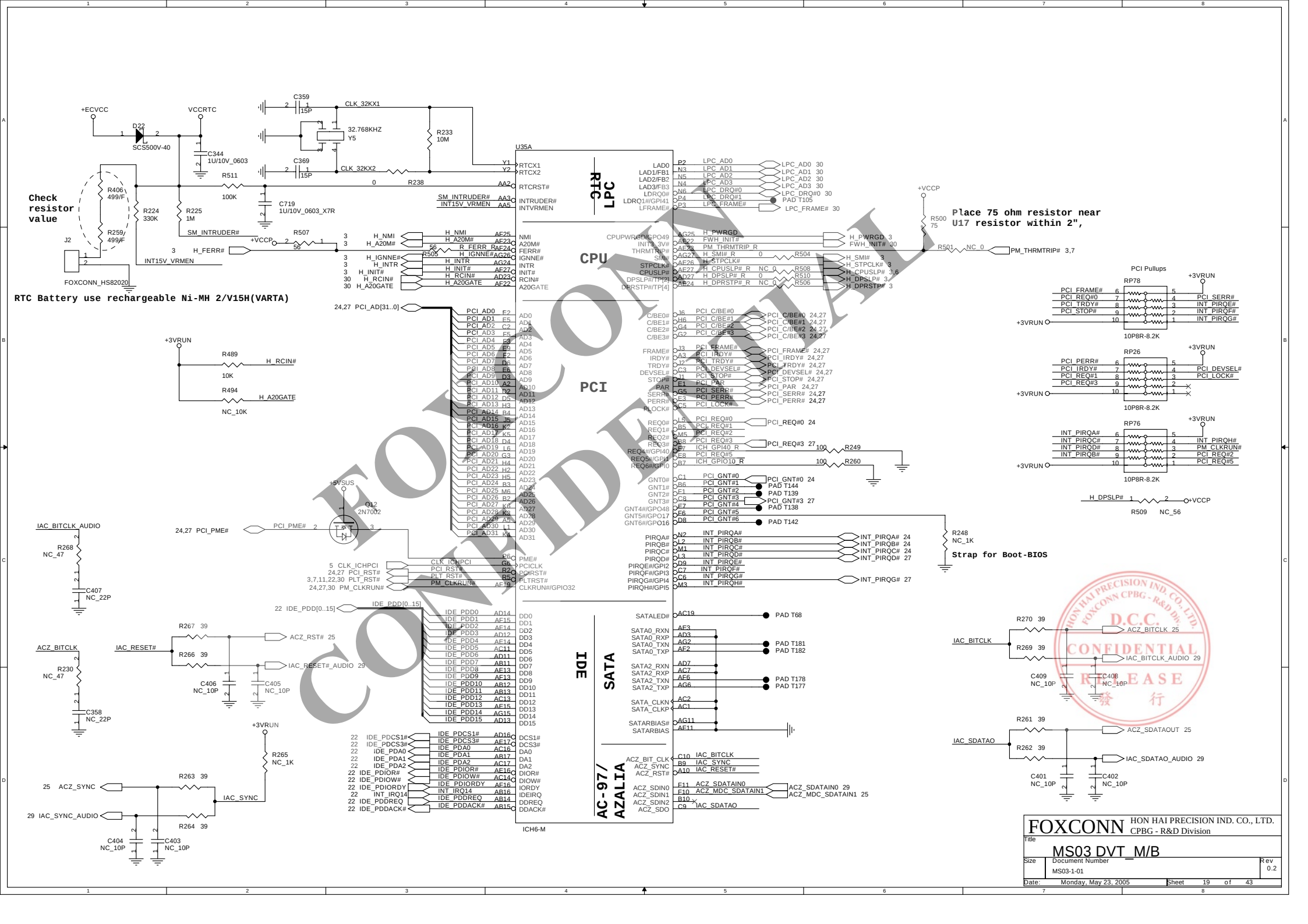
Check resistor value
RTC Battery use rechargeable Ni-MH 2/V15H(VARTA)
Place 75 ohm resistor near U17 resistor within 2",
Strap for Boot-BIOS
U35A
RTC
LPC
CPU
PCI
IDE
SATA
AC-97/ AZALIA
ICH6-M
PCI Pullups
10P8R-8.2K
FOXCONN HS82020
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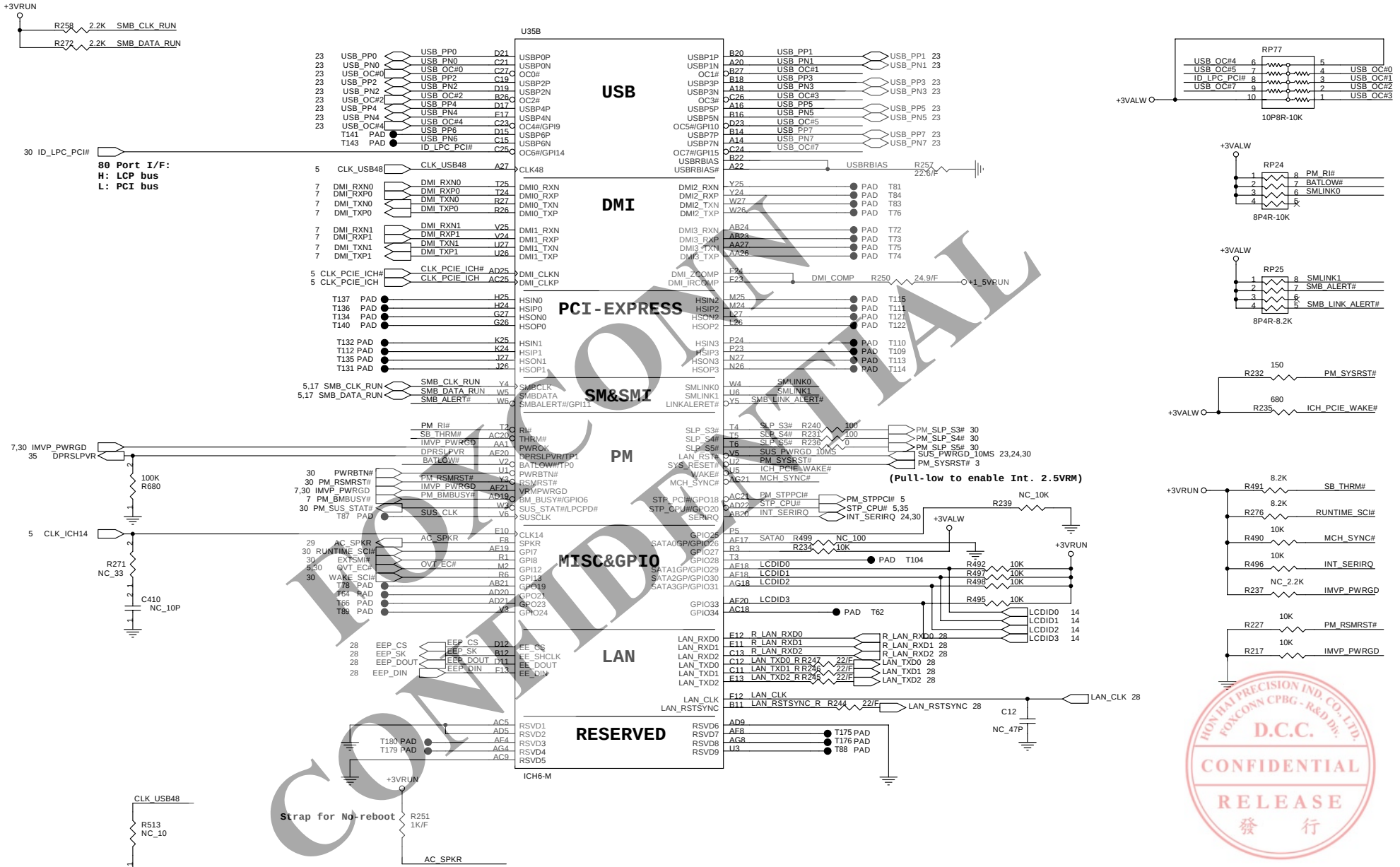

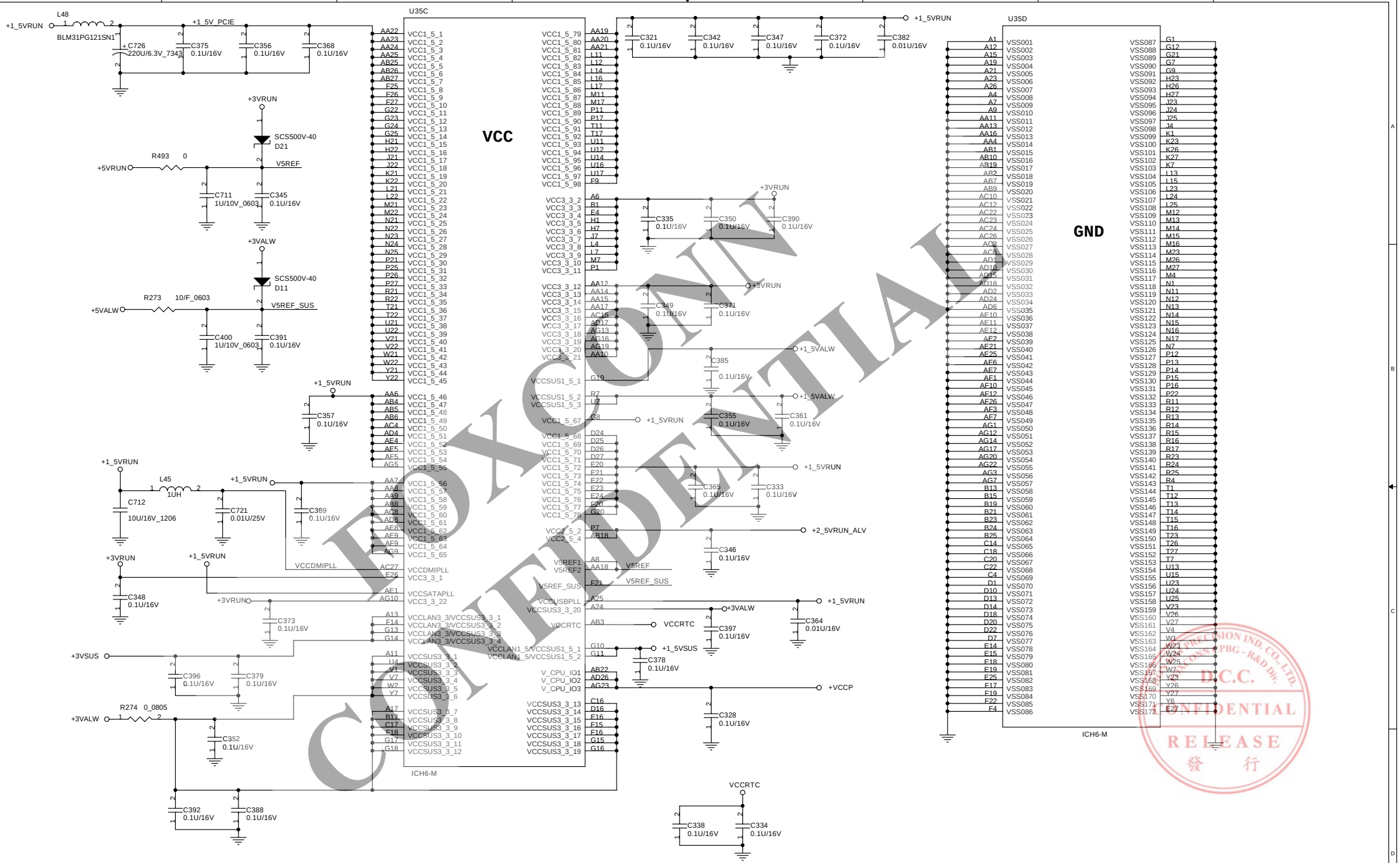

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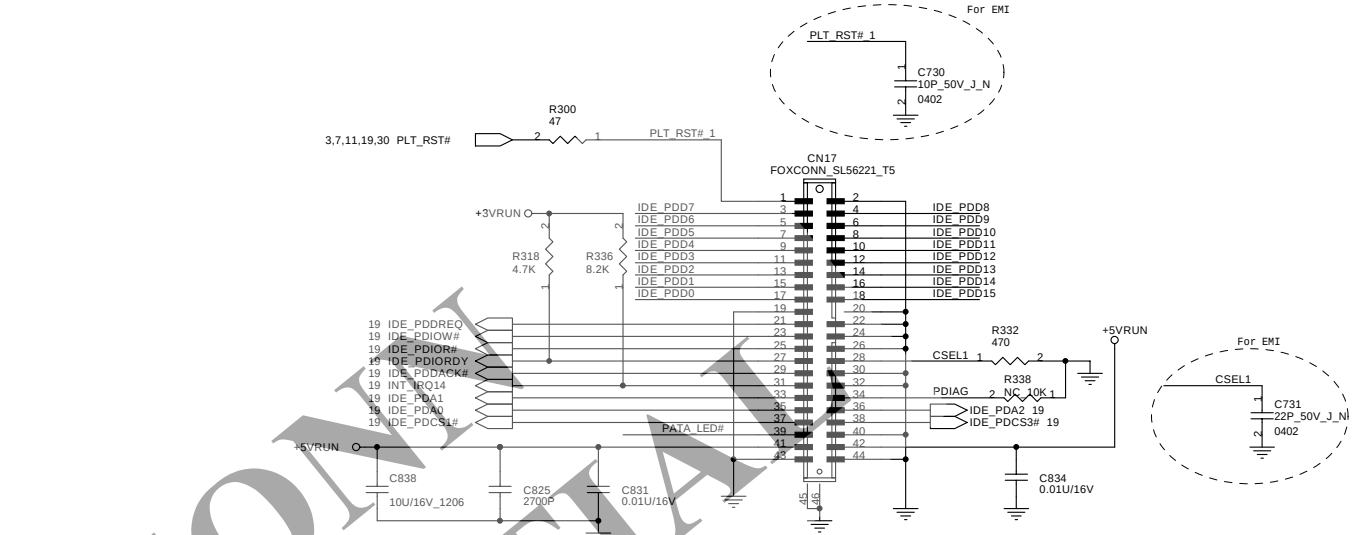
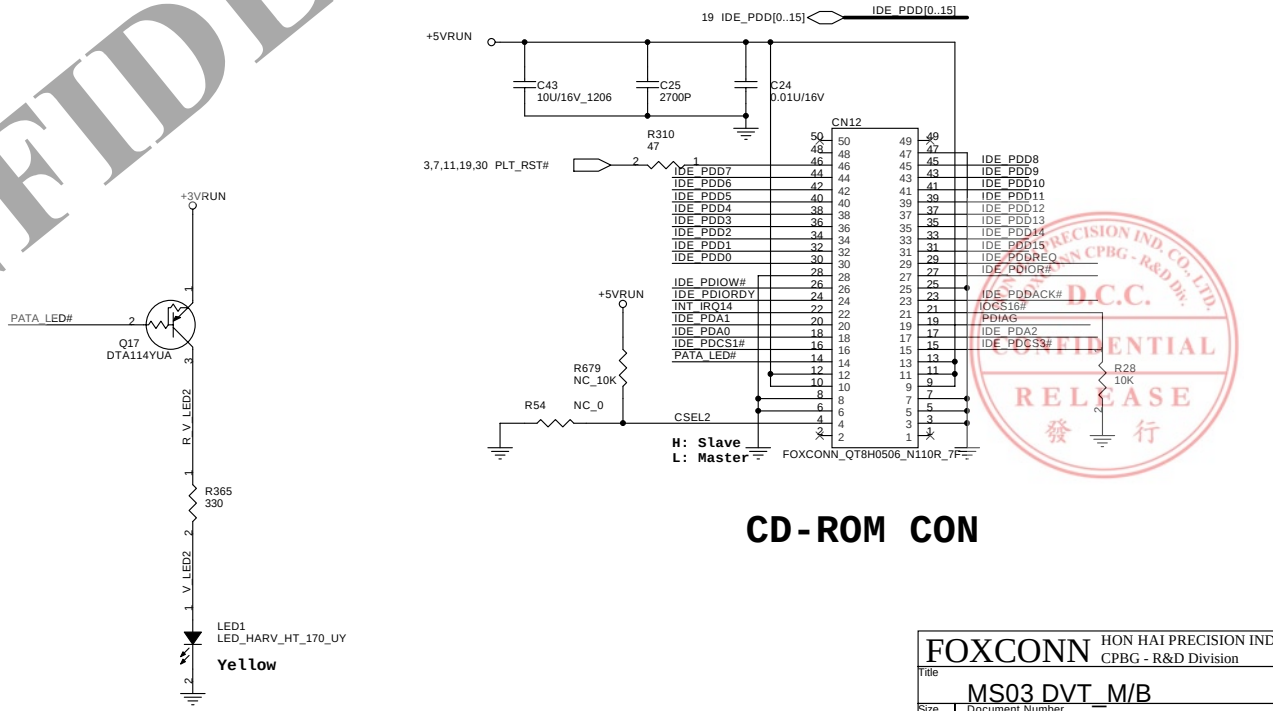


HDD CON



CD-ROM CON

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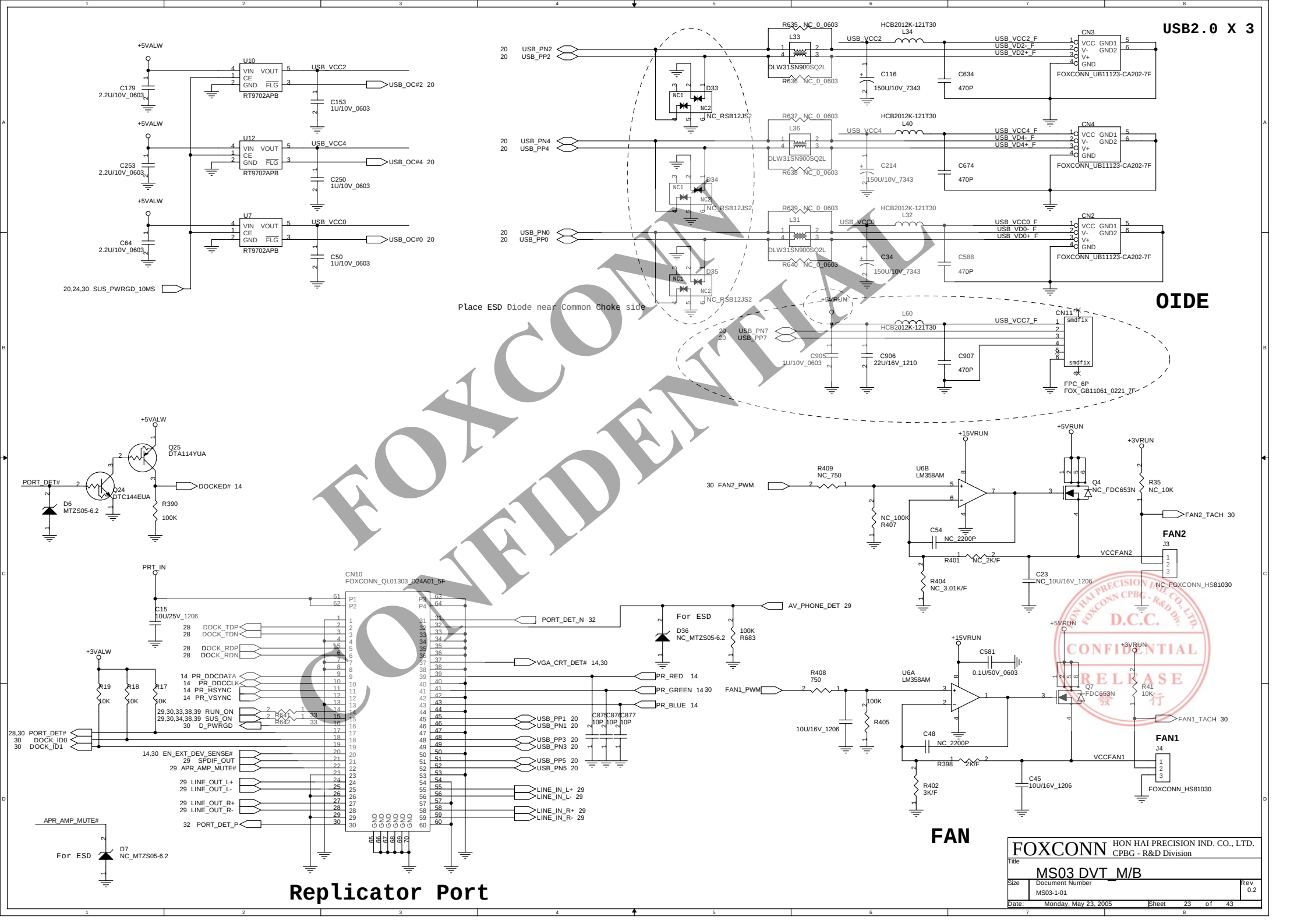

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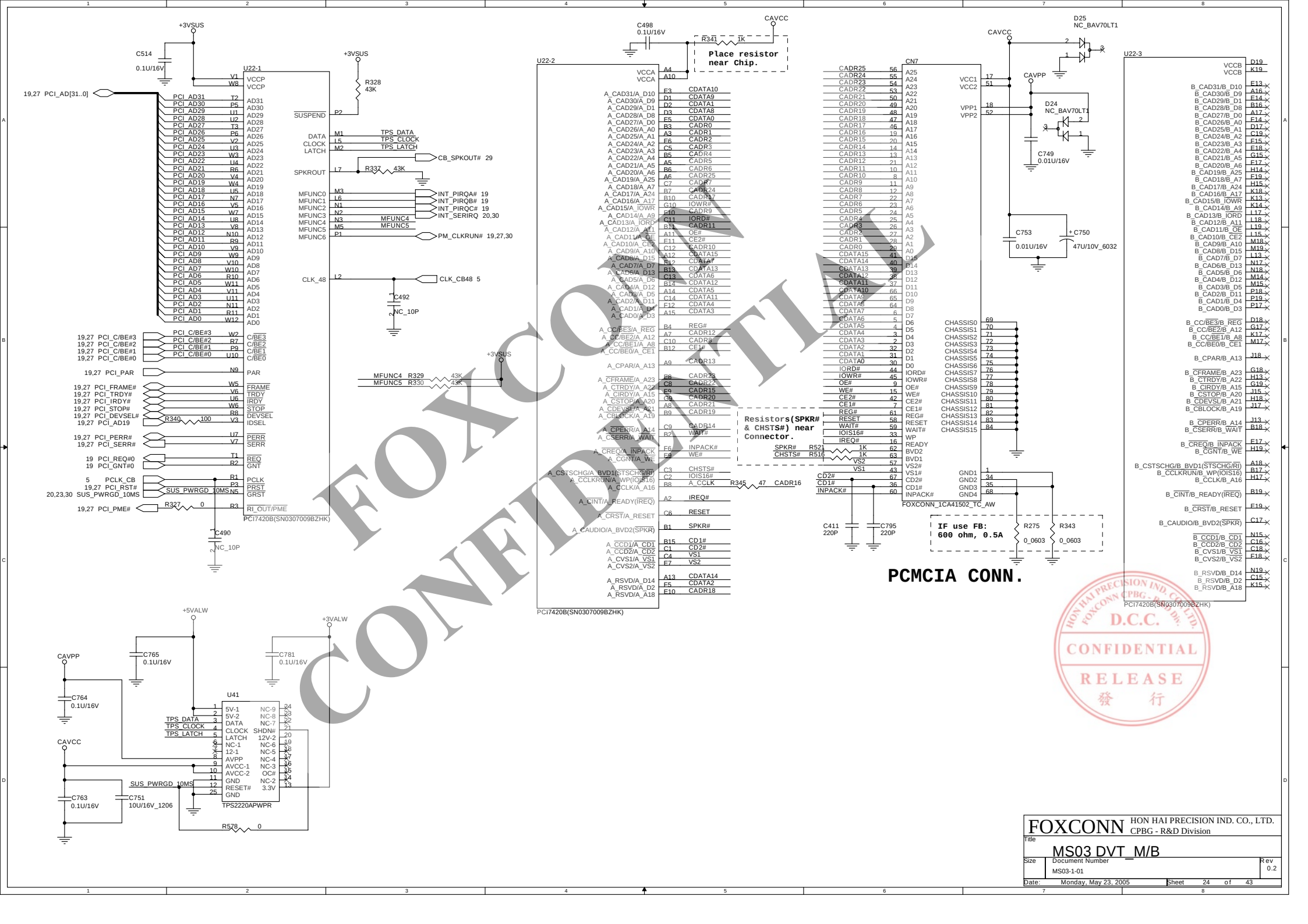

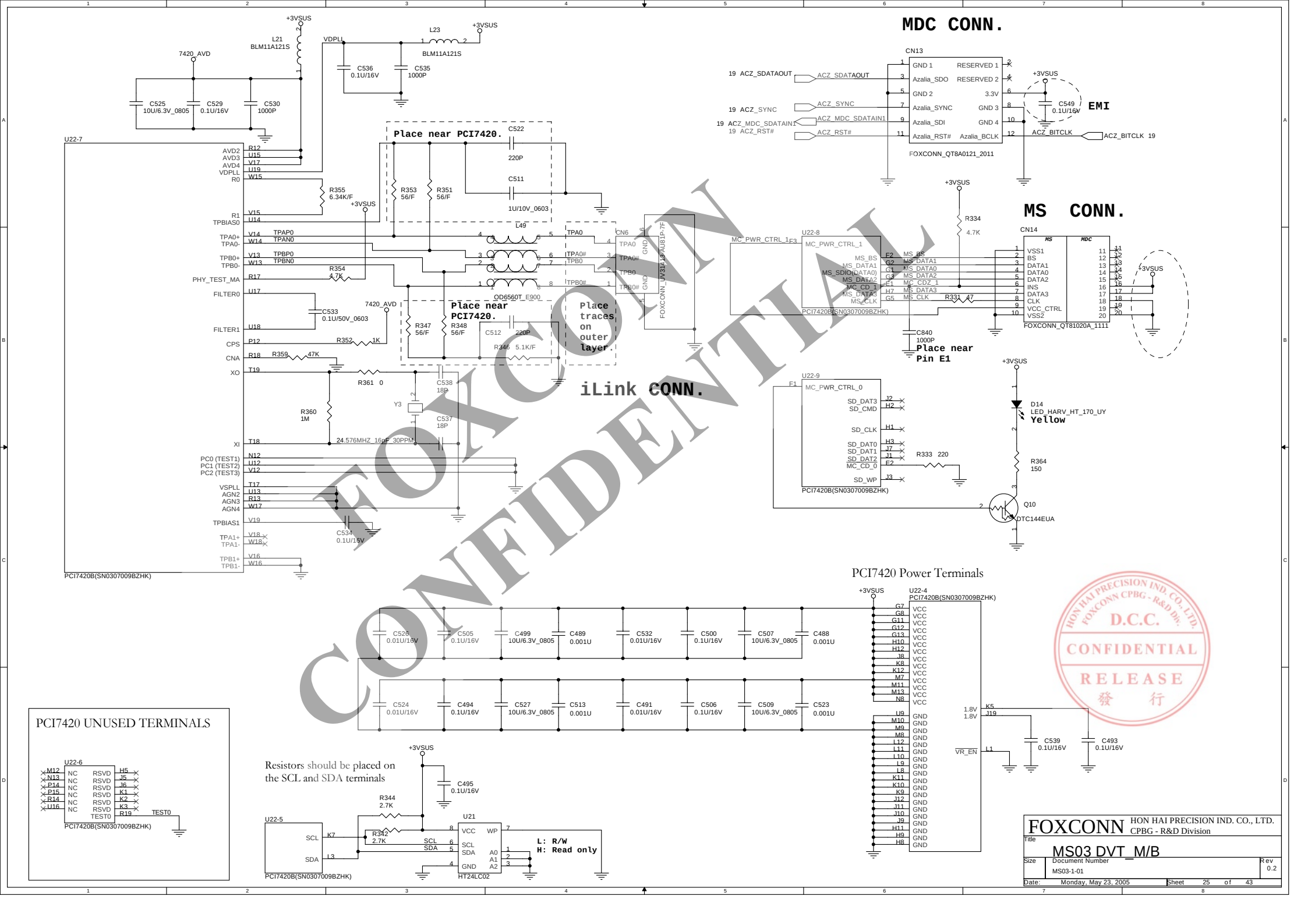
MDC CONN.
MS CONN.
iLink CONN.
EMI
Place near PCI7420.
Place near PCI7420.
Place traces on outer layer.
Place near Pin E1
PCI7420 Power Terminals
PCI7420 UNUSED TERMINALS
Resistors should be placed on the SCL and SDA terminals
L: R/W
H: Read only
FOXCONN_QT8A0121_2011
FOXCONN_QT81020A_1111
PCI7420B(SN0307009BZHK)
HT24LC02
D14
LED_HARV_HT_170_UY
Yellow
Q10
DTC144EUA
24.576MHZ_16pF_30PPM
OD5560T_E900
FOXCONN_UV3148AU81P-7F
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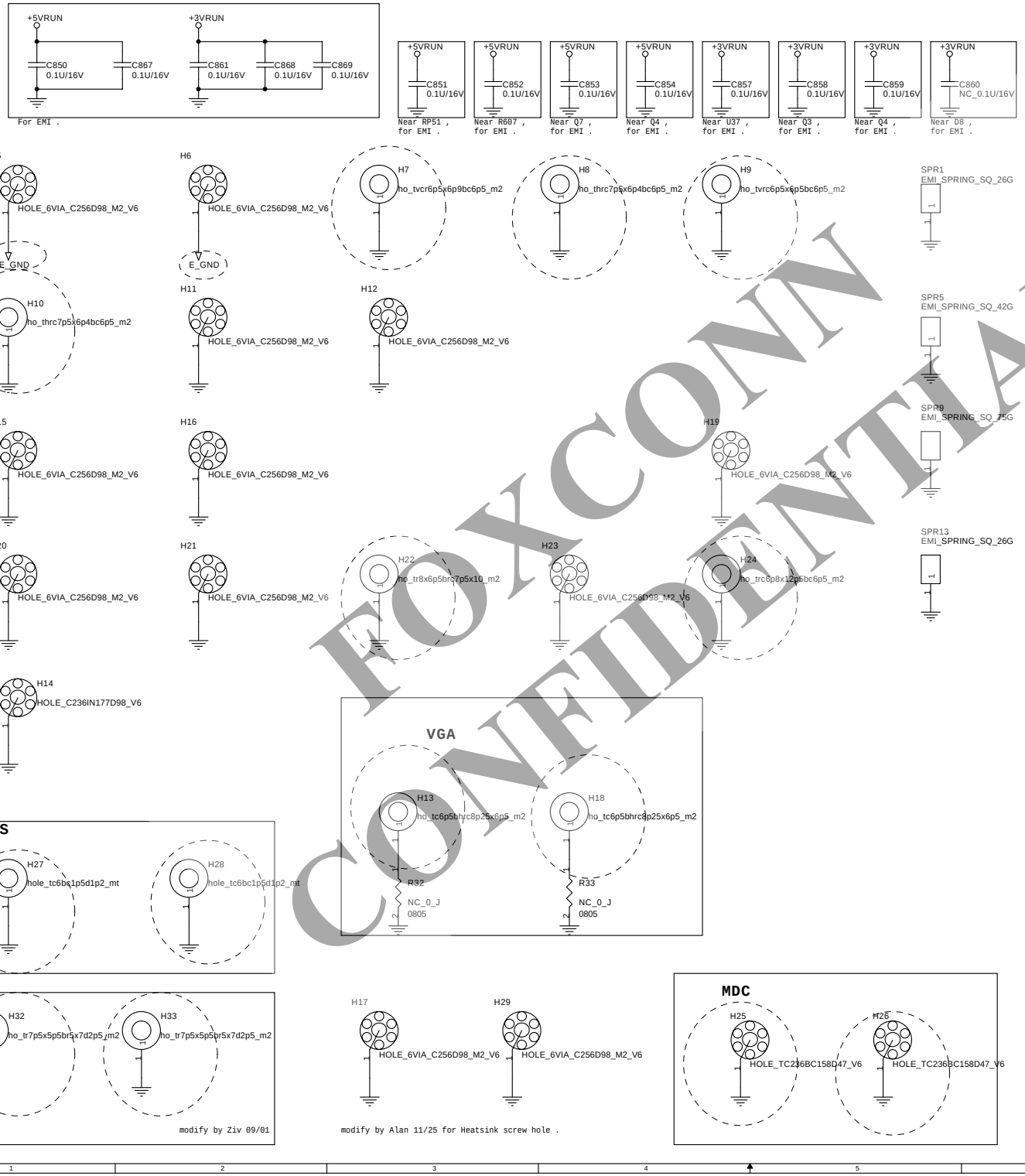

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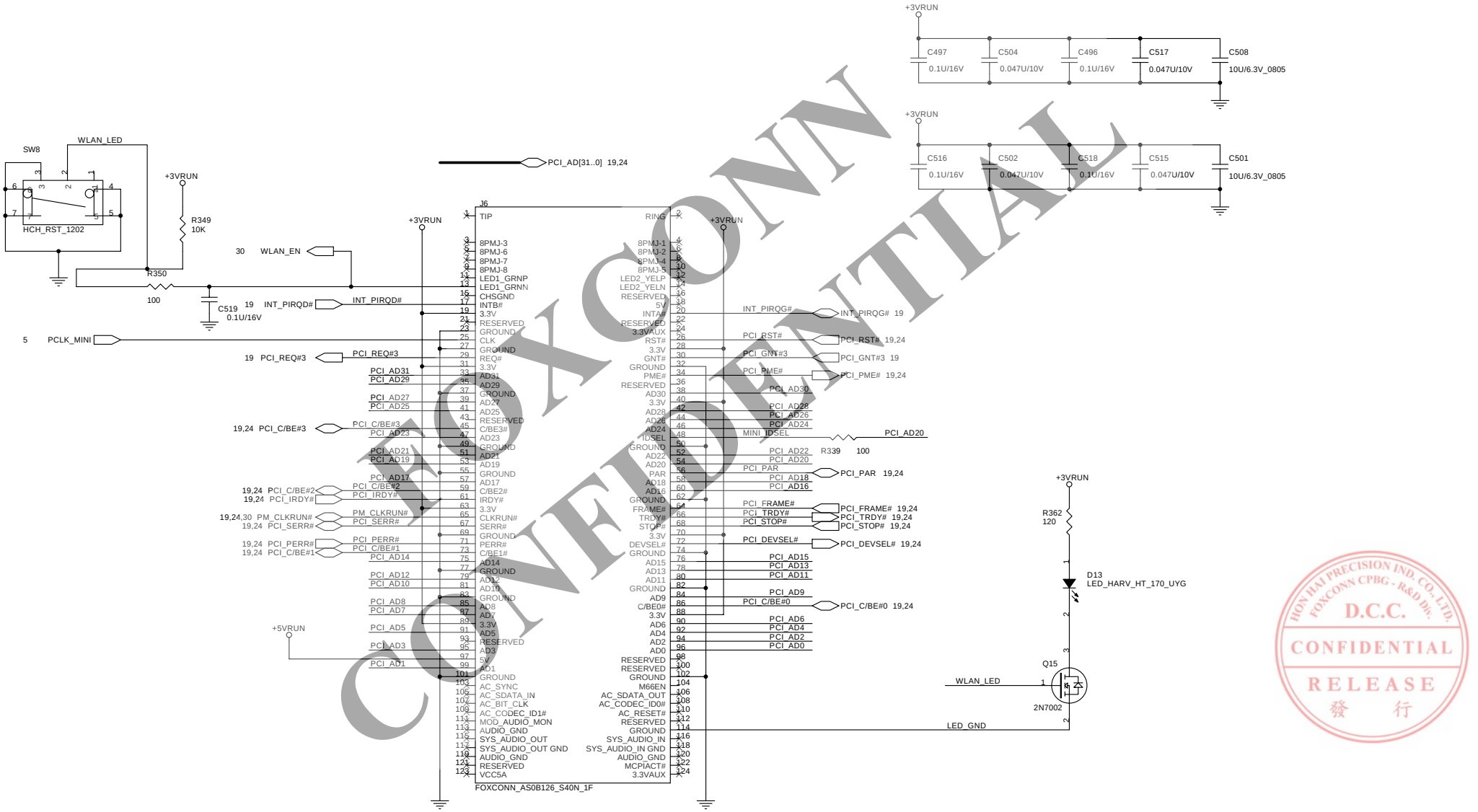

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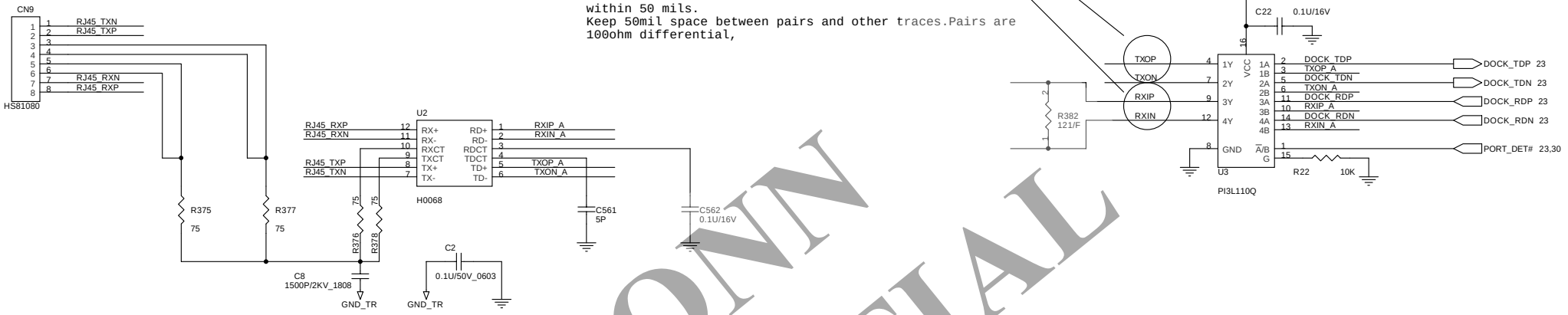
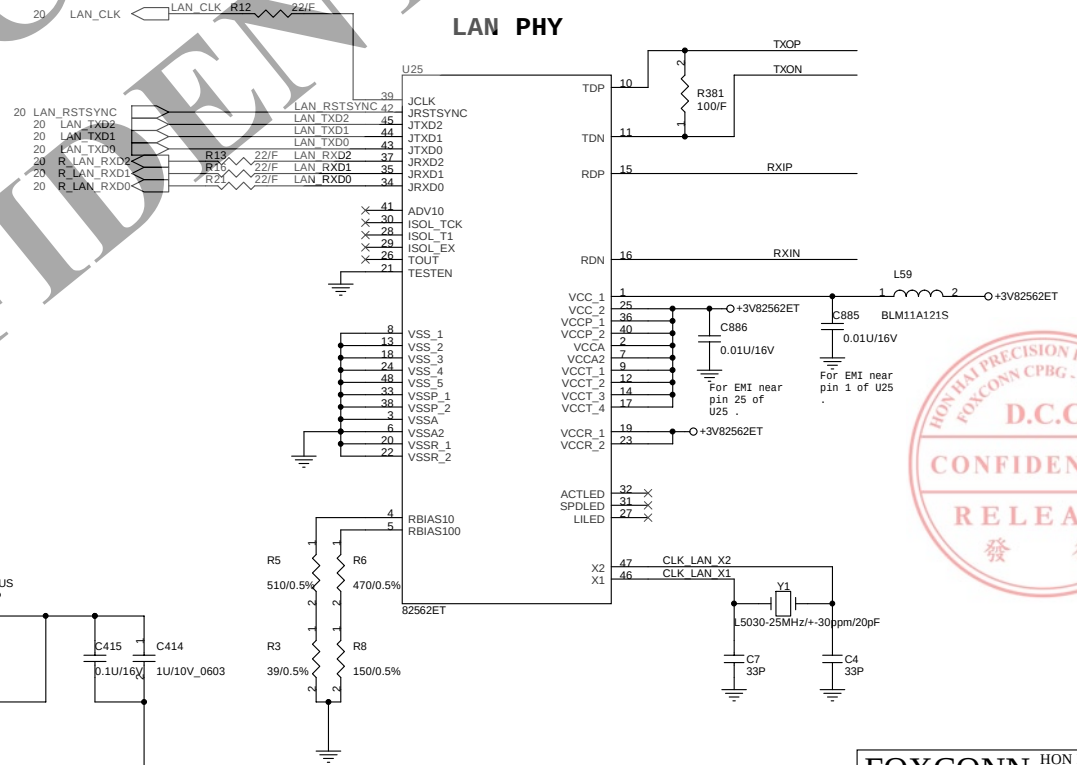
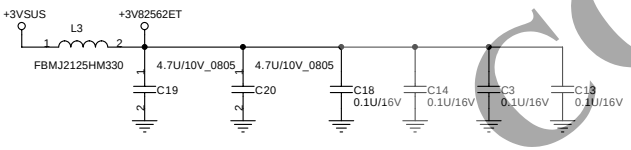
Match trace length

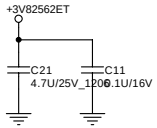
LAYOUT NOTES:
Match total length of chip side Rx and Tx pair traces +/-50mil
Match length of cable side Rx and Tx pair traces +/- 50 mil
Total line TX+ to TX- and RX- and RX+ should be matched within 50 mils.
Keep 50mil space between pairs and other traces.Pairs are 100ohm differential,

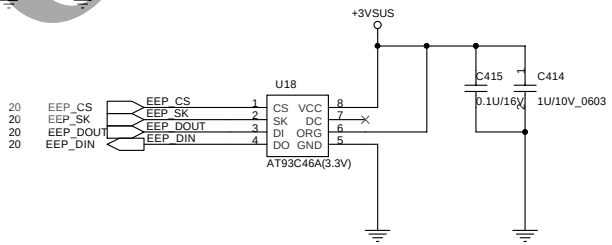


LAN PHY









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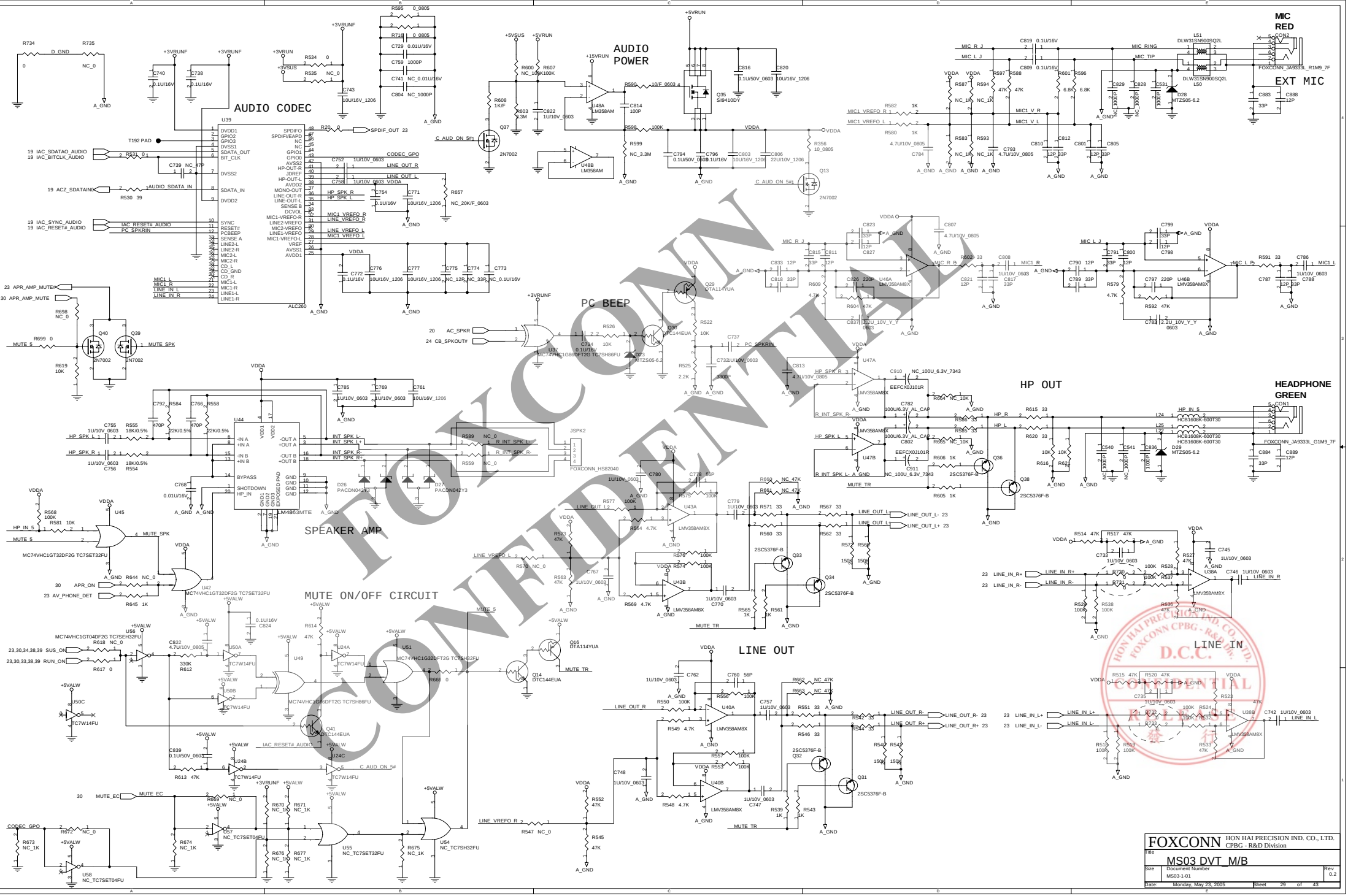

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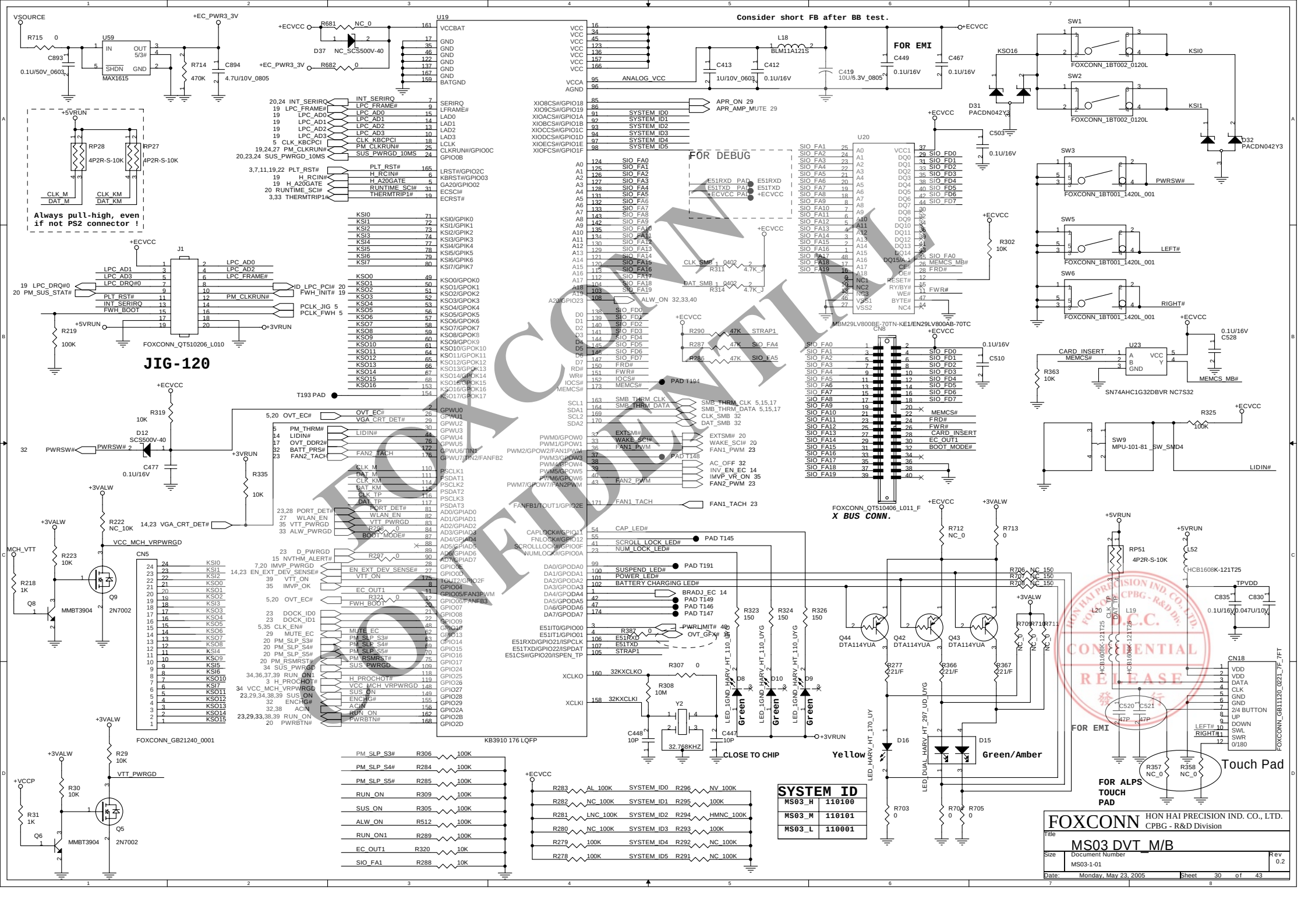

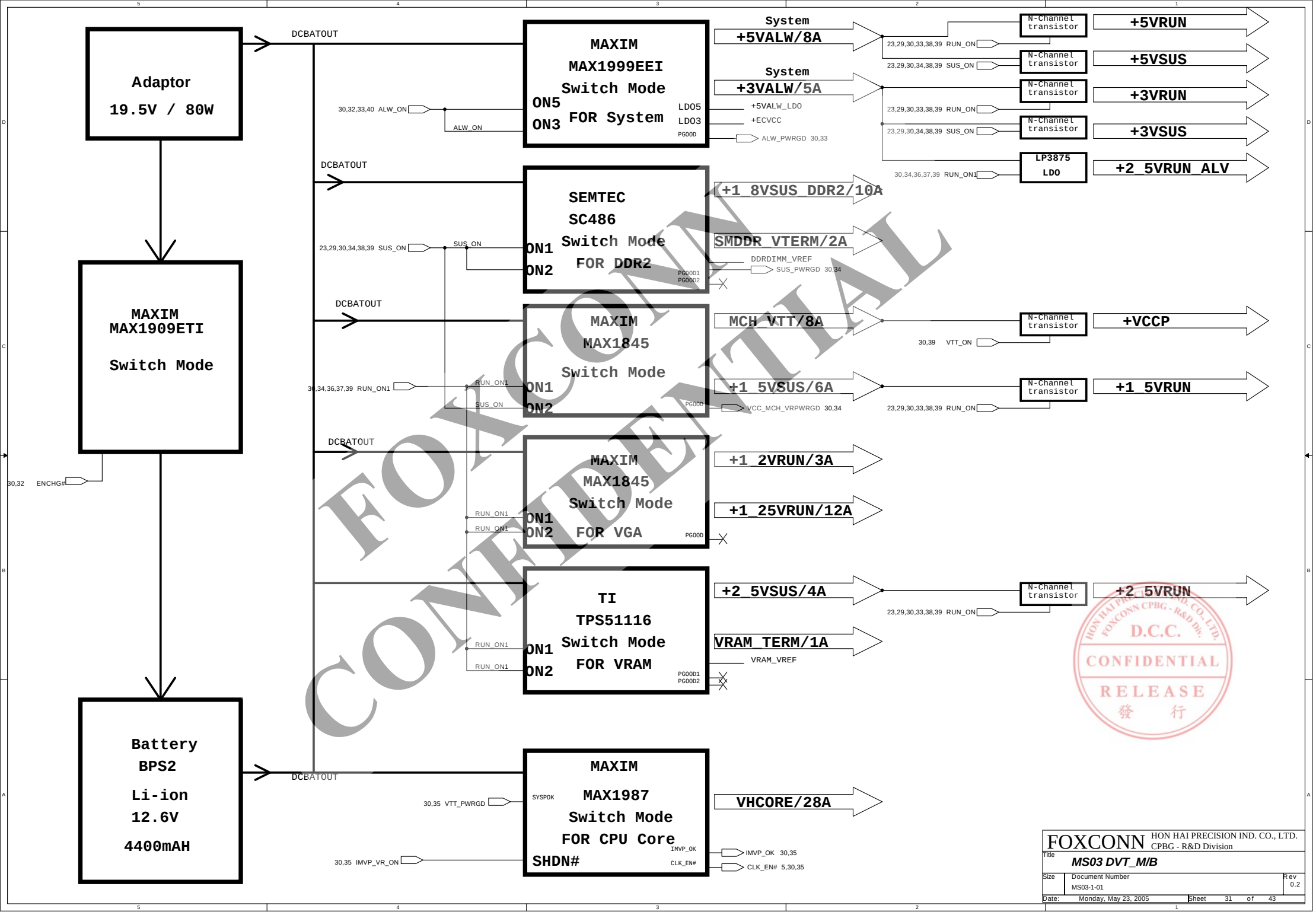

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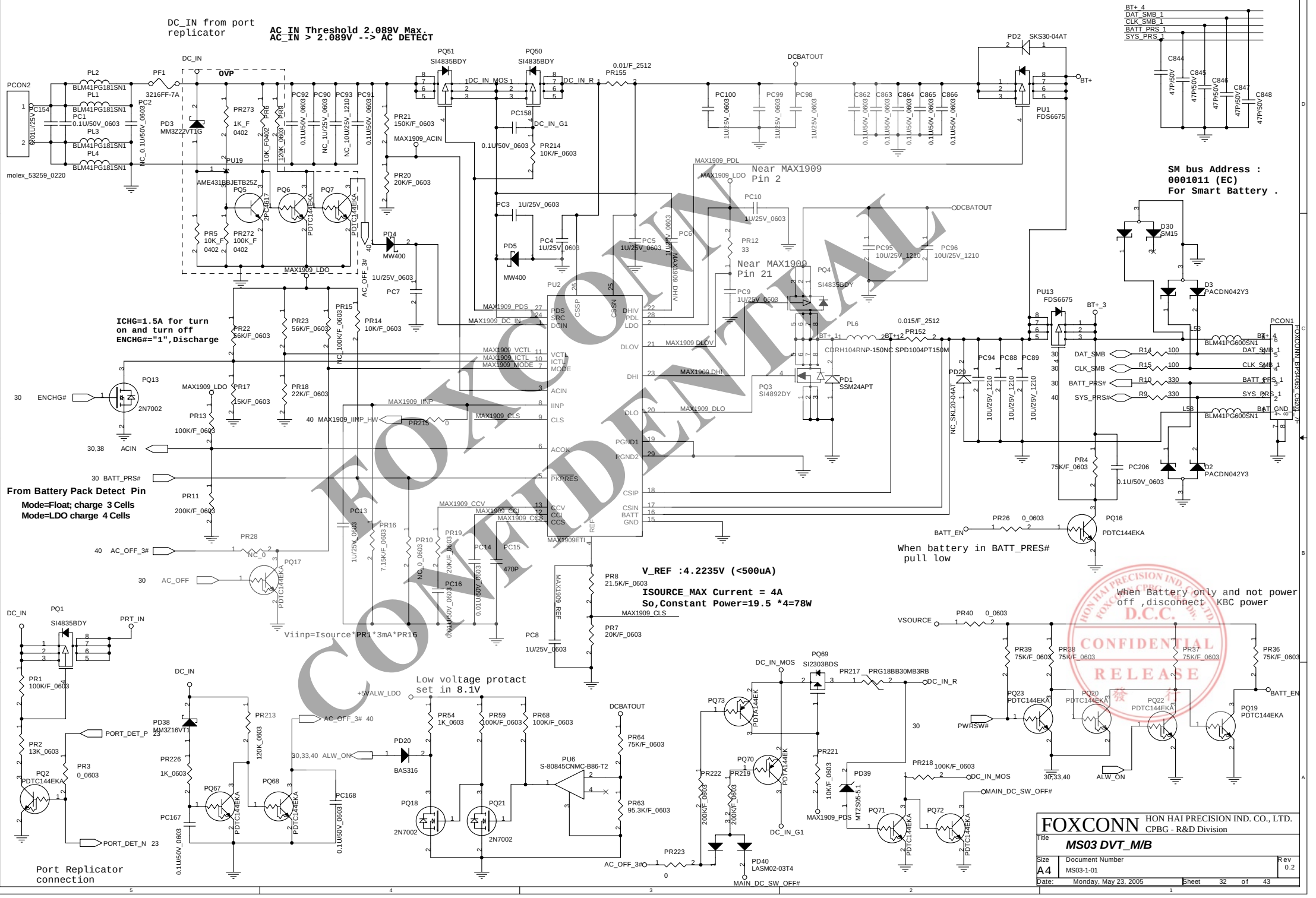
DC_IN from port replicator
AC_IN Threshold 2.089V Max.
AC_IN > 2.089V --> AC DETECT
DC_IN
OVP
PCON2
molex_53259_0220
PF1
3216FF-7A
PD3 MM3Z22VT1G
PU19 AME431BBJETB25Z
PQ5 2PC4617
PQ6 PDTC144EKA
PQ7 PDTC144EKA
PD4 MW400
PD5 MW400
PQ51 SI4835BDY
PQ50 SI4835BDY
DC_IN_MOS
DC_IN_R
DC_IN_G1
0.01/F_2512 PR155
DCBATOUT
PD2 SKS30-04AT
PU1 FDS6675
BT+
BT+ 4
DAT_SMB_1
CLK_SMB_1
BATT_PRS_1
SYS_PRS_1
SM bus Address : 0001011 (EC)
For Smart Battery .
MAX1909_ACIN
MAX1909_LDO
MAX1909_PDL
Near MAX1909 Pin 2
Near MAX1909 Pin 21
MAX1909_PDS
MAX1909_DC_IN
MAX1909_VCTL
MAX1909_ICTL
MAX1909_MODE
MAX1909_IINP
MAX1909_CLS
MAX1909_DLOV
MAX1909_DHI
MAX1909_DLO
MAX1909_CCV
MAX1909_CCI
MAX1909_CCS
MAX1909ETI
ICHG=1.5A for turn on and turn off
ENCHG#="1",Discharge
ENCHG#
2N7002
ACIN
BATT_PRS#
From Battery Pack Detect Pin
Mode=Float; charge 3 Cells
Mode=LDO charge 4 Cells
AC_OFF_3#
AC_OFF
V_REF :4.2235V (<500uA)
ISOURCE_MAX Current = 4A
So,Constant Power=19.5 *4=78W
Viinp=Isource*PR1*3mA*PR16
When battery in BATT_PRES# pull low
When Battery only and not power off ,disconnect KBC power
Low voltage protact set in 8.1V
+5VALW_LDO
ALW_ON
PWRSW#
VSOURCE
BATT_EN
MAIN_DC_SW_OFF#
PORT_DET_P
PORT_DET_N
Port Replicator connection
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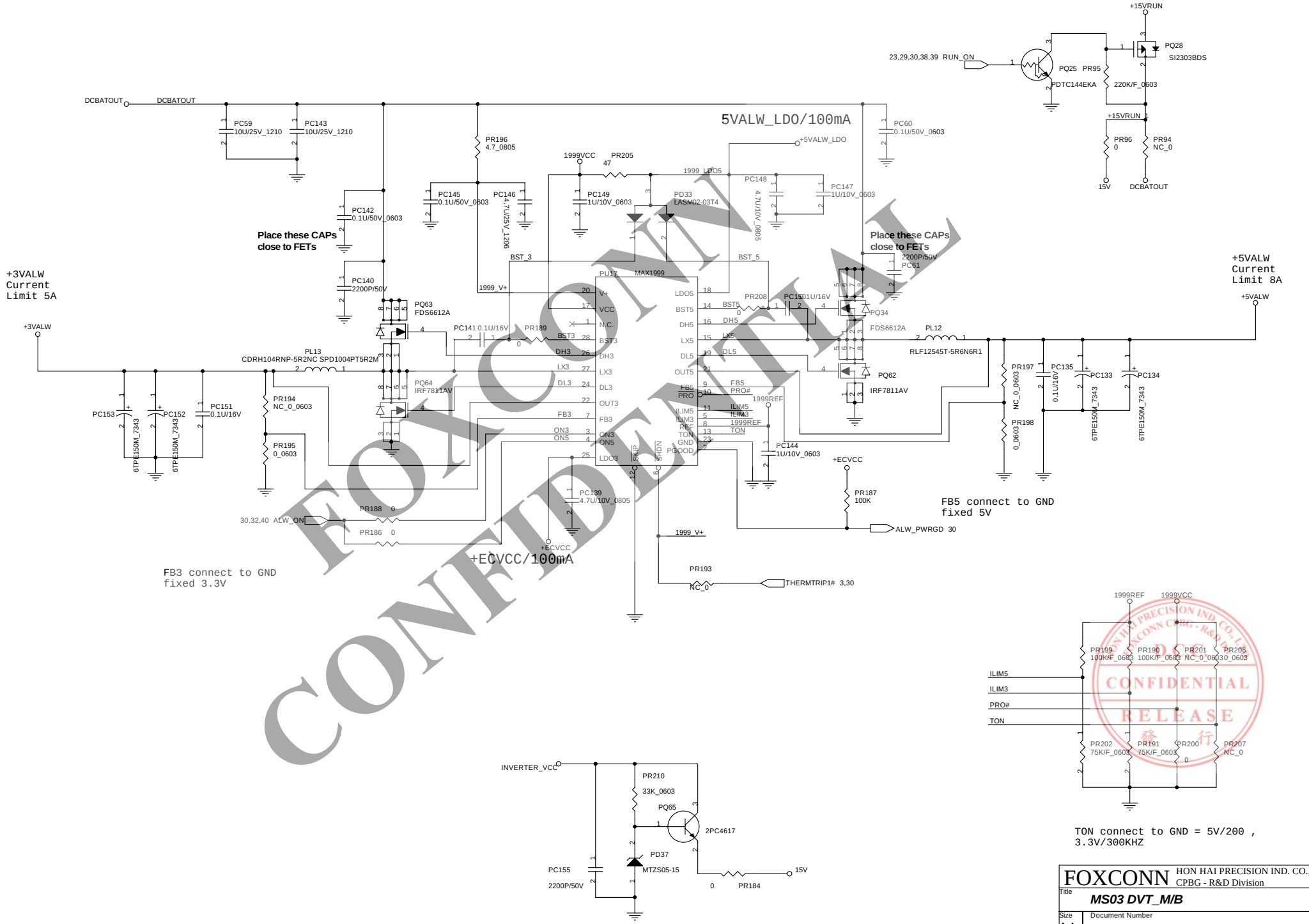

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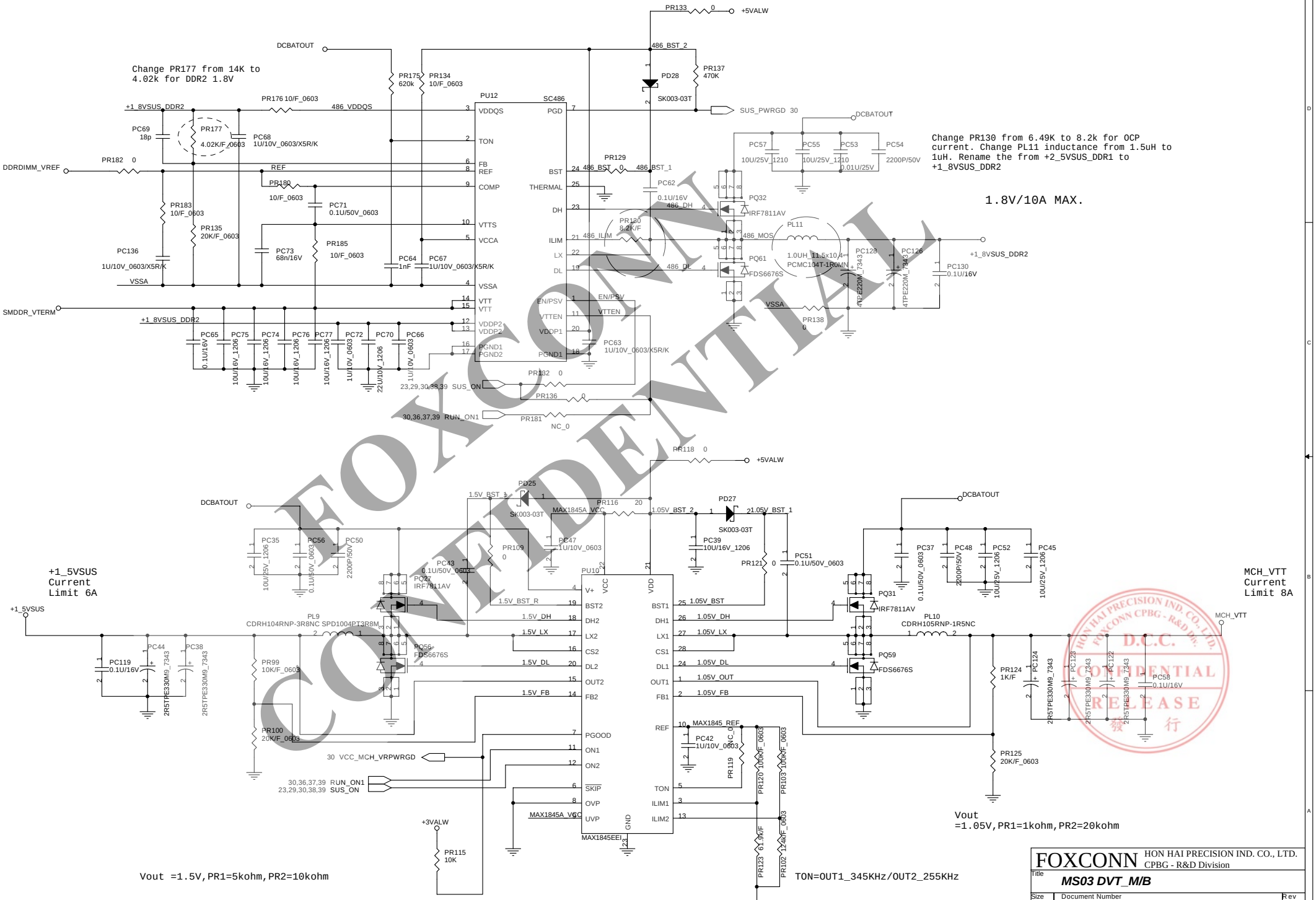

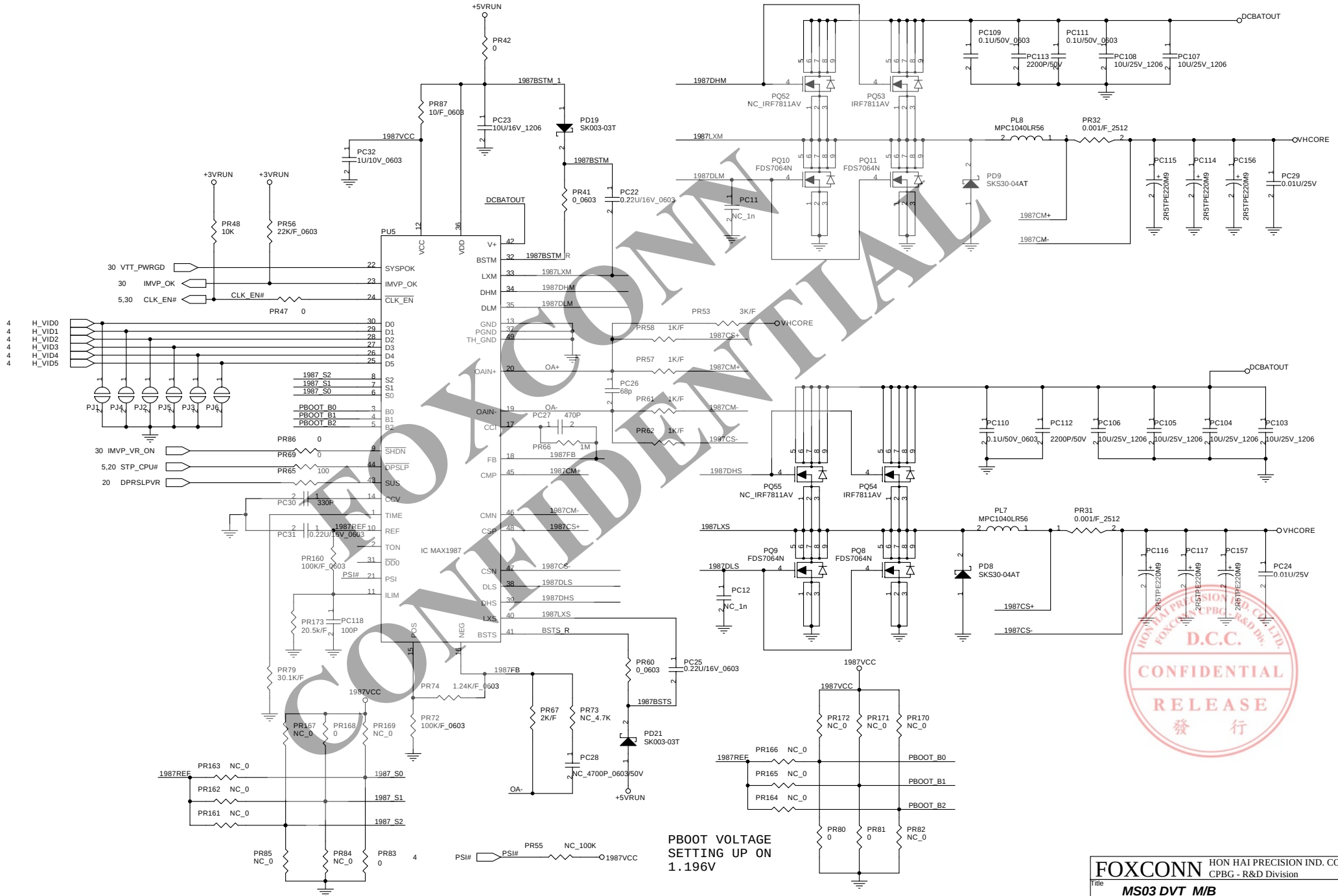

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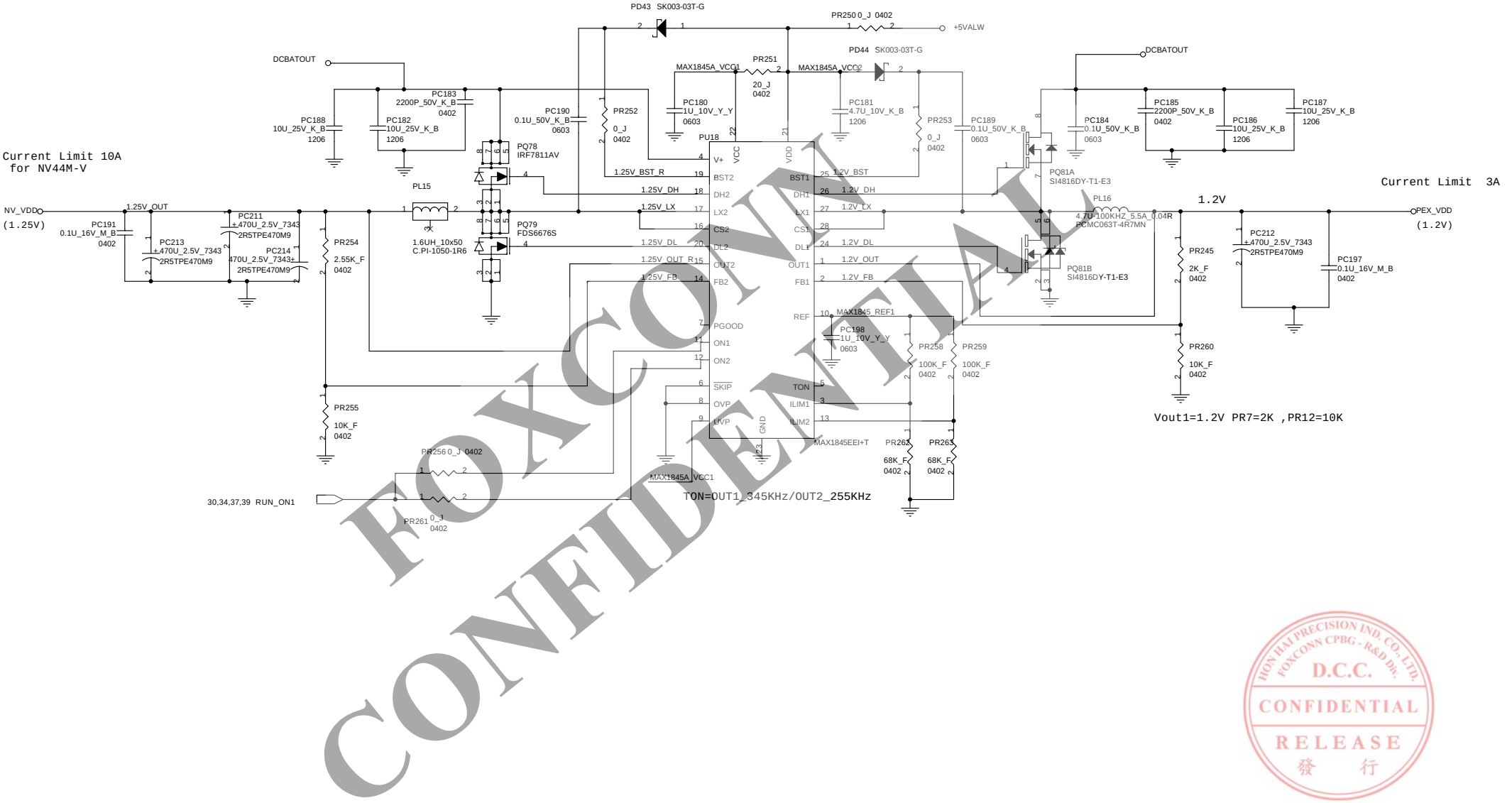

Power daughter board for VGA

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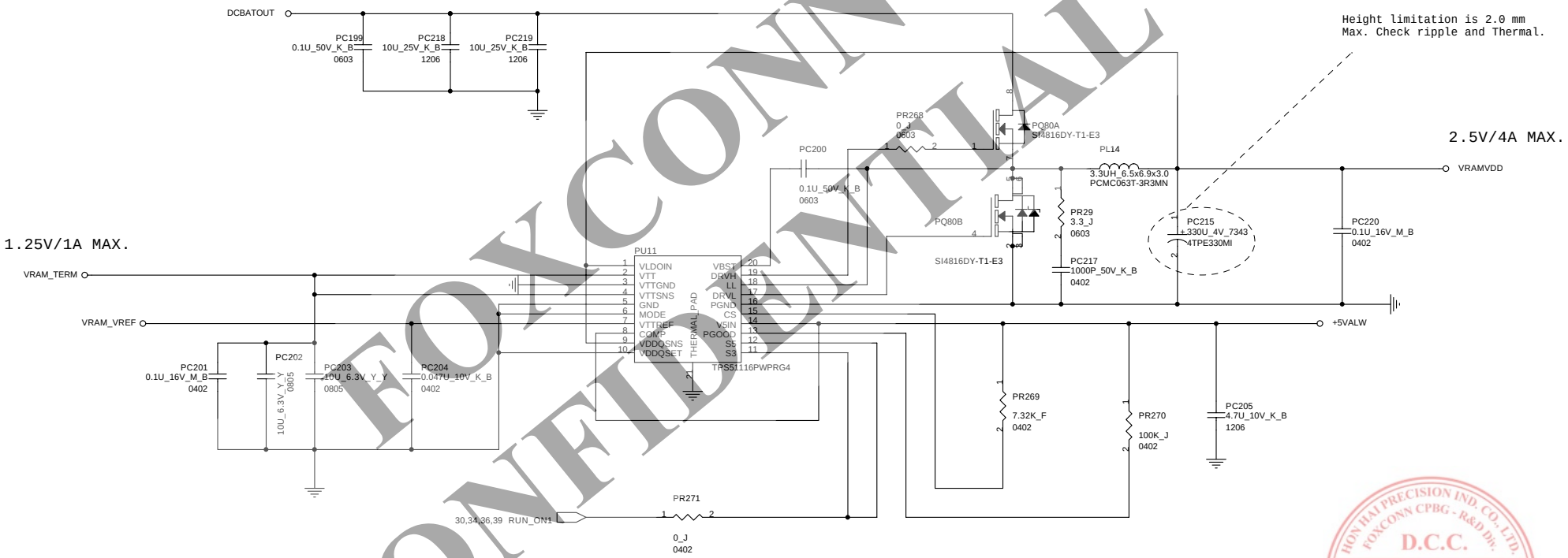

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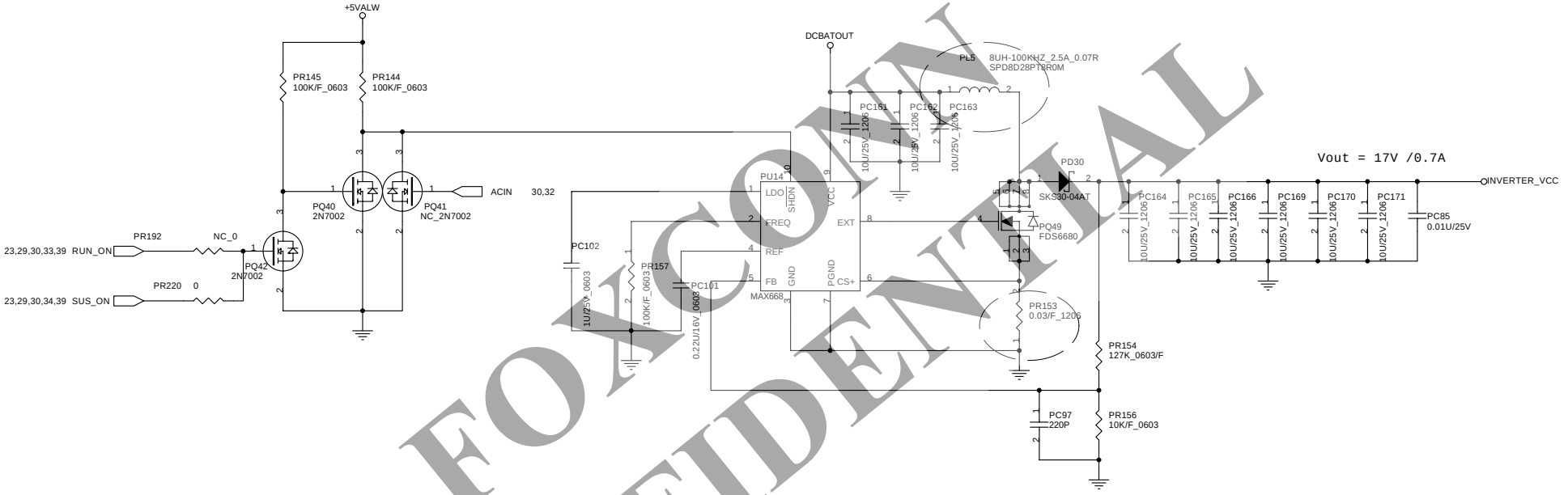
Change PL5 and PR153 for improving the step-up circuit



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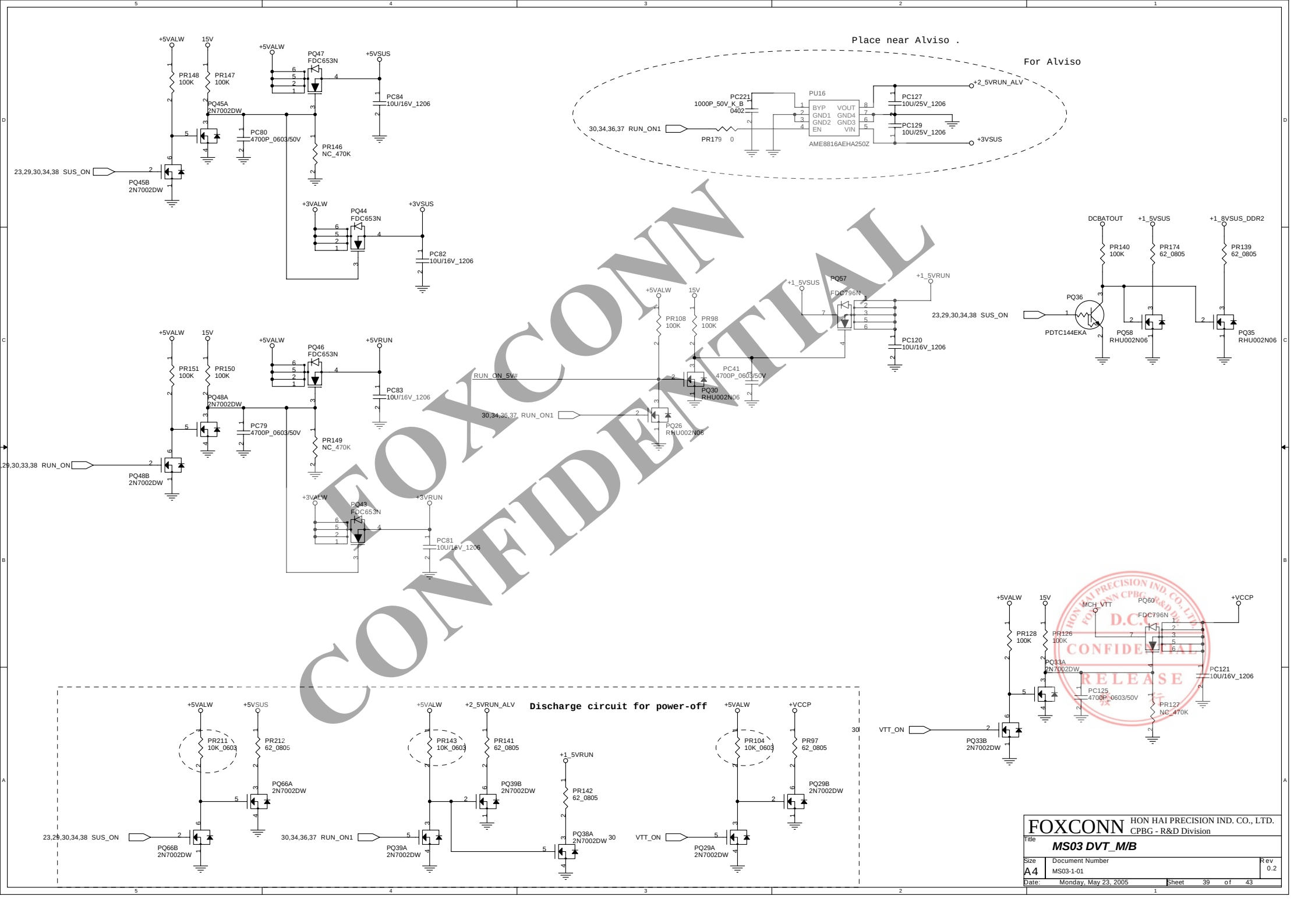
Place near Alviso .
For Alviso
PU16
PC221
1000P_50V_K_B
0402
PR179 0
30,34,36,37 RUN_ON1
BYP VOUT
GND1 GND4
GND2 GND3
EN VIN
AME8816AEHA250Z
PC127
10U/25V_1206
PC129
10U/25V_1206
+2_5VRUN_ALV
+3VSUS
+5VALW
15V
PR148
100K
PR147
100K
PQ45A
2N7002DW
PQ47
FDC653N
+5VSUS
PC84
10U/16V_1206
PC80
4700P_0603/50V
PR146
NC_470K
23,29,30,34,38 SUS_ON
PQ45B
2N7002DW
+3VALW
PQ44
FDC653N
+3VSUS
PC82
10U/16V_1206
+5VRUN
PR151
100K
PR150
100K
PQ48A
2N7002DW
PQ46
FDC653N
PC83
10U/16V_1206
PC79
4700P_0603/50V
PR149
NC_470K
29,30,33,38 RUN_ON
PQ48B
2N7002DW
PQ43
FDC653N
PC81
10U/16V_1206
PR108
100K
PR98
100K
RUN_ON_5V#
PQ30
RHU002N06
PC41
4700P_0603/50V
30,34,36,37 RUN_ON1
PQ26
RHU002N06
+1_5VSUS
PQ57
FDC796N
+1_5VRUN
PC120
10U/16V_1206
23,29,30,34,38 SUS_ON
DCBATOUT
+1_5VSUS
+1_8VSUS_DDR2
PR140
100K
PR174
62_0805
PR139
62_0805
PQ36
PDTC144EKA
PQ58
RHU002N06
PQ35
RHU002N06
Discharge circuit for power-off
PR211
10K_0603
PR212
62_0805
PQ66A
2N7002DW
PQ66B
2N7002DW
23,29,30,34,38 SUS_ON
PR143
10K_0603
PR141
62_0805
PQ39B
2N7002DW
PR142
62_0805
PQ38A
2N7002DW
PQ39A
2N7002DW
30,34,36,37 RUN_ON1
PR104
10K_0603
PR97
62_0805
PQ29B
2N7002DW
PQ29A
2N7002DW
VTT_ON
+VCCP
MCH_VTT
PQ60
FDC796N
PR128
100K
PR126
100K
PQ33A
2N7002DW
PQ33B
2N7002DW
PC125
4700P_0603/50V
PC121
10U/16V_1206
PR127
NC_470K
VTT_ON
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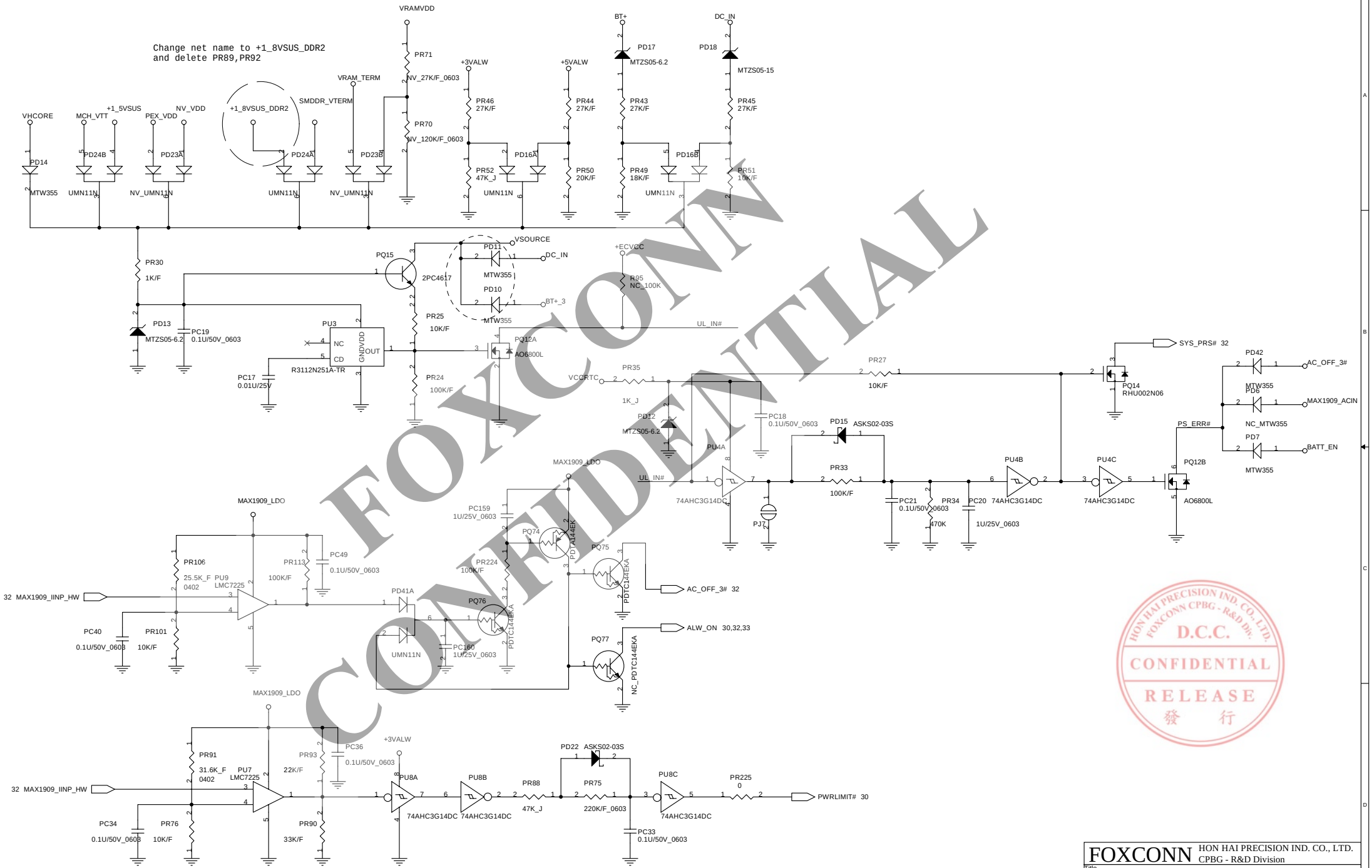

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HISTORY

EVT board start .

(2005/03/31)

P14: Change D1 from NC to mount.

P15: Change R115 from mount to NC.

(2005/04/01)

P40: Rotate PD10, PD11.

(2005/04/12)

P14: Correct LVDS Bus relative of connection (swap Data+/Data-, CLK+/CLK-).

P25: Change CN14 pin16, pin17 from NC to +3VSUS, change CN14 pin18, pin20 from NC to GND.

P30: Delete SW7, Q46, R396, R721.

(2005/04/25)

P12: Add T195 for measure VRAM Timing.

(2005/04/26)

P14: Delete the right side dot of L1.

(2005/04/29)

P14: Change Q22, Q23 from 2N7002 to BSS138.

(2005/05/04)

P14: Change R694, R695, R696, R697 from 2.2K to 0 ohm.

(2005/05/09)

P26: Change H14 part from HOLE_6VIA_C256IN177D98_M2_V6(6.5mm) to HOLE_C236IN177D98_V6(6.0mm).

P15: Change R701 from NC to mount, change R702 from mount to NC.

(2005/05/10)

P32: Change PD3 from P4SSMJ24APT to MM3Z22VT1G.

P32: Change PQ5 from PDTC144EKA to 2PC4617.

P32: Change PR5 from 75K/F_0603 to 10K/F_0402.

P32: Change PR6 from 120K_0603 to 10K/F_0402.

P32: Add PU19 AME, AME431BBJETB25Z.

P32: Add PR272 100K/F_0402.

P32: Add PR273 1K/F_0402.

P36: Change PR262 from 15.8K/F_0402 to 68K/F_0402.

P36: Change PR263 from 40.2K/F_0402 to 68K/F_0402.

P36: Change PL15 footprint from CHOKE_3P_348_409X409_2 to CHOKE_3P_315_394X394_2.

P36: Delete PJ11, PJ12.

P37: Delete PJ13, PJ14.

P39: Delete PJ8, PR178.

P39: Add PC221 1000P_50V_0402.

P39: Change PU16 from LP3875 to AME8816AEHA250Z.

P05: Change R478, R481 from 33ohm to 56ohm for rising edge rate and falling edge rate.

P05: Change C696, C698, C700, C701, C702, C706 from NC to 18pF (EMI solution).

P22: Add C730, C731 (EMI solution).

P22: Connect CN17.20, CN17.32, CN17.44 to GND.

P26: Change SPR7, SPR15 from mount to NC (EMI solution).

(2005/05/11)

P23: Add D33,D34 and D35 for ESD(Not mount).

P36: Change PL15 footprint from CHOKE_3P_348_409X409_2 to L_3P_315_394X394.

(2005/05/13)

P26: Change H25, H26 footprint from ho_tc236bc158d47_v6 to ho_tc236bc158d47_mt.

P26: Change H27, H28 footprint from ho_tc1p5bc6d1p2_m2 to ho_tc1p5bc6d1p2_mb.

(2005/05/16)

P02: Update symbol ahead table, add LNC_ and HMNC_.

P30: Change R280 from mount to NC.

P30: Change R281 from AL_100K to LNC_100K.

P30: Change R293 from NC to mount.

P30: Change R294 from NV_100K to HMNC_100K.

(2005/05/17)

P26: Add R32, R33 NC_0_J (EMI solution).

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(2005/05/18)

P29: Change C783, C837 from 1U/10V_0603 to 2.2U_10V_Y_Y.

P26: Change H27, H28 from ho_tc1p5bc6d1p2_m2(ho_tc1p5bc6d1p2_mb) to hole_tc6bc1p5d1p2_mt(ho_tc6bc1p5d1p2_mt).

P40: Change PR91 from 31.6K to 25.5K, change PR109 from 49.9K to 31.6K.

P29: Delete R611, R622, R628, R629, R647, R648, R649, R650, JSPK1.

P29: Add C910, C911 NC_100U_6.3V_7343.

(2005/05/19)

P29: Add R734,R735 for anti-digital noise interference.

(2005/05/20)

P30: Change R311, R314 from 10K to 4.7K_J.

P29: Change R735 from mount to NC.

P05: Change R481 from 56ohm to 47ohm.

P05: Change R478 from 56ohm to 47ohm.

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Doc. Rev.	ECN No.:	Sch. Page	Rev.Change From	Rev.Change To	Description of Change

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