G41T-R3 V : 1.0A

SCHEMATICS TABLE:

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15	DDR3 DIMM1,2		

REVISION HISTORY:

Rev	Date	Notes
A	2010.03.16	Change from G41T-M4 V:A P06 VRD11 (RT8802) changed R140 from 3.3K-04 to 1.62K-1-04, R148 from 4.7K-04 to 11K-1-04 changed C119,C120,C131 from .1U-04 to 1U-06 changed MF1,MF2,MF5,MF6,MF8,MF9 from MN252-6MS to MN252-9MS changed high side dumpping from 0 to 2.2-04 changed R151 from 820-06 to 806-1-06 added low side dumpping 2.2-04 added CAP from low side to GND removed BC2 P08 Voltage Regulator changed ER52 from 390-1-04 to 430-1-04 changed VCC_DIMM dumpping from 0 to 2.2-04 P10 HW Monitor/Fan removed BC1 P16 DAC VGA RGB added BC102 for ESD added BC103,BC104,BC105,BC106 for VGA noise changed BC11,BC12,BC13 from 3.3P-04 to 4.7P-04 P17 PCIE 16x/1x Slot changed AC coupling from .1U-04 to 0.1U-10VX-04 P18 ICH7_DMI/PCIE/SATA/USB/MISC changed AC coupling from .1U-04 to 0.1U-10VX-04 changed ER59 from 23.7-1-04 to 24.9-1-04 P19 ICH7_HDA/SPI/PCI/RTC/MISC added case open circuit P21 USB add EMI CAP P23 Super I/O (F71808E) deleted TP2,TP3,STP1,STP2 added RJ5,RJ6,RJ7 connected RSTSW- P26 Audio Codec (IDT92HD73C) changed C12,C16,C18,C23,C24,C26 footprint from C0603 to C0805 P27 Audio Interface(3 Port HDA) changed C65 from .22U-04 to 220P-04-O P28 PCIE LAN (JMC260) changed FB16,FB20 footprint from FB0603-SHORT_1 to FB0603 changed FB19 footprint from FB0603 to FB0603-SHORT_1 swaped nets EESK/LINK & LAN_ACTIVE-

IMPORTANT NOTES ABOUT THIS SCHEMATIC

DESIGN NOTE: Example text for the design note to show the note inside the colored box.

1) DESIGN NOTES in grey are information notes.

DESIGN NOTE: Example text for the design note to show the note inside the colored box.

2) DESIGN NOTES in yellow are notes of caution.

3) DESIGN NOTES in red are critical, and must be understood and followed.

PCB STACK:
L1:TOP
L2:PWR
L3:GND
L4:BOTTOM

Elitegroup Computer Systems			
Title: Cover Page			
Size: Custom	Document Number: G41T-R3		Rev: 1.0A
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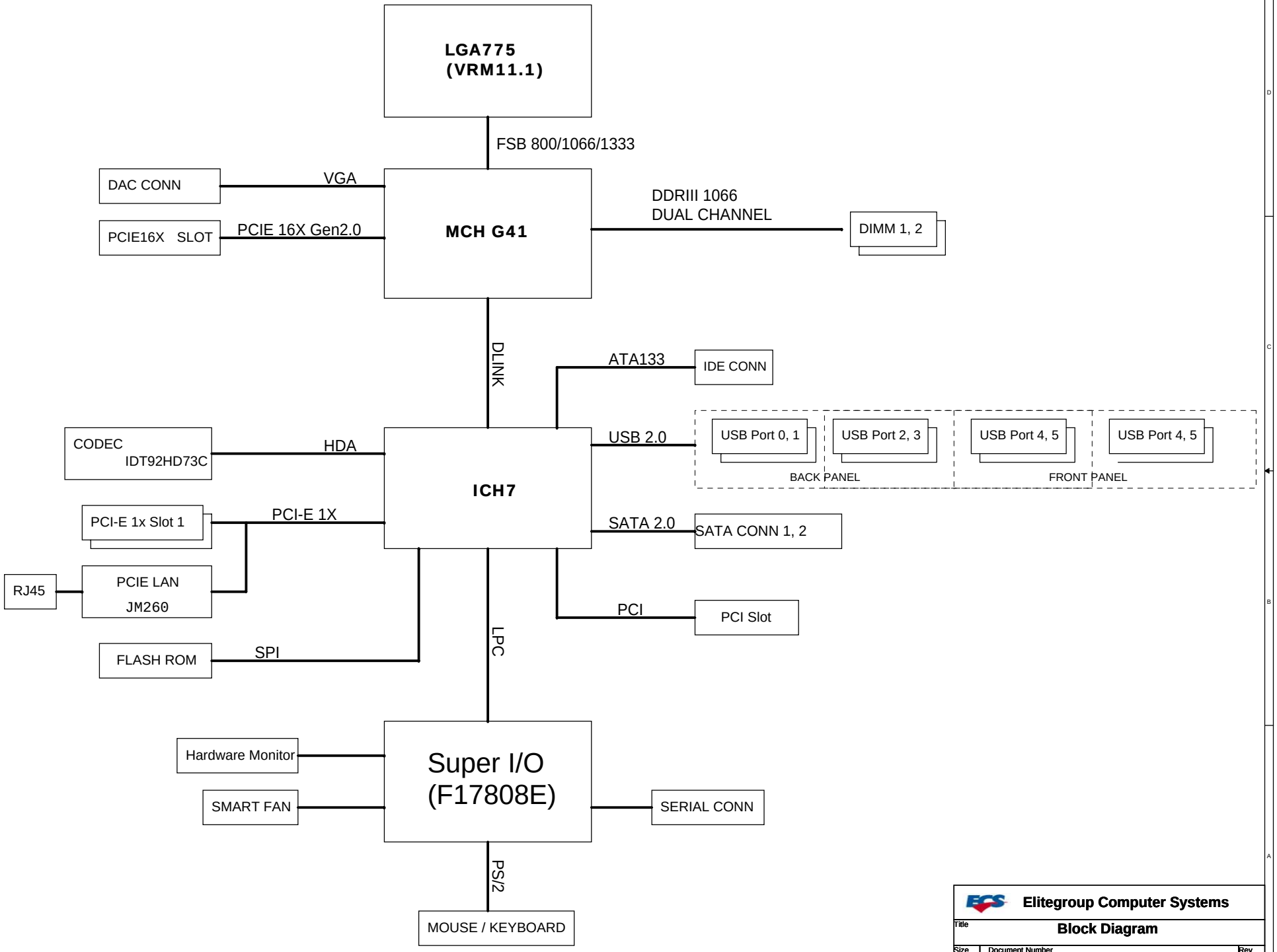

Elitegroup Computer Systems		
Title: Block Diagram		
Size: Custom	Document Number: G41T-R3	Rev: 1.0A
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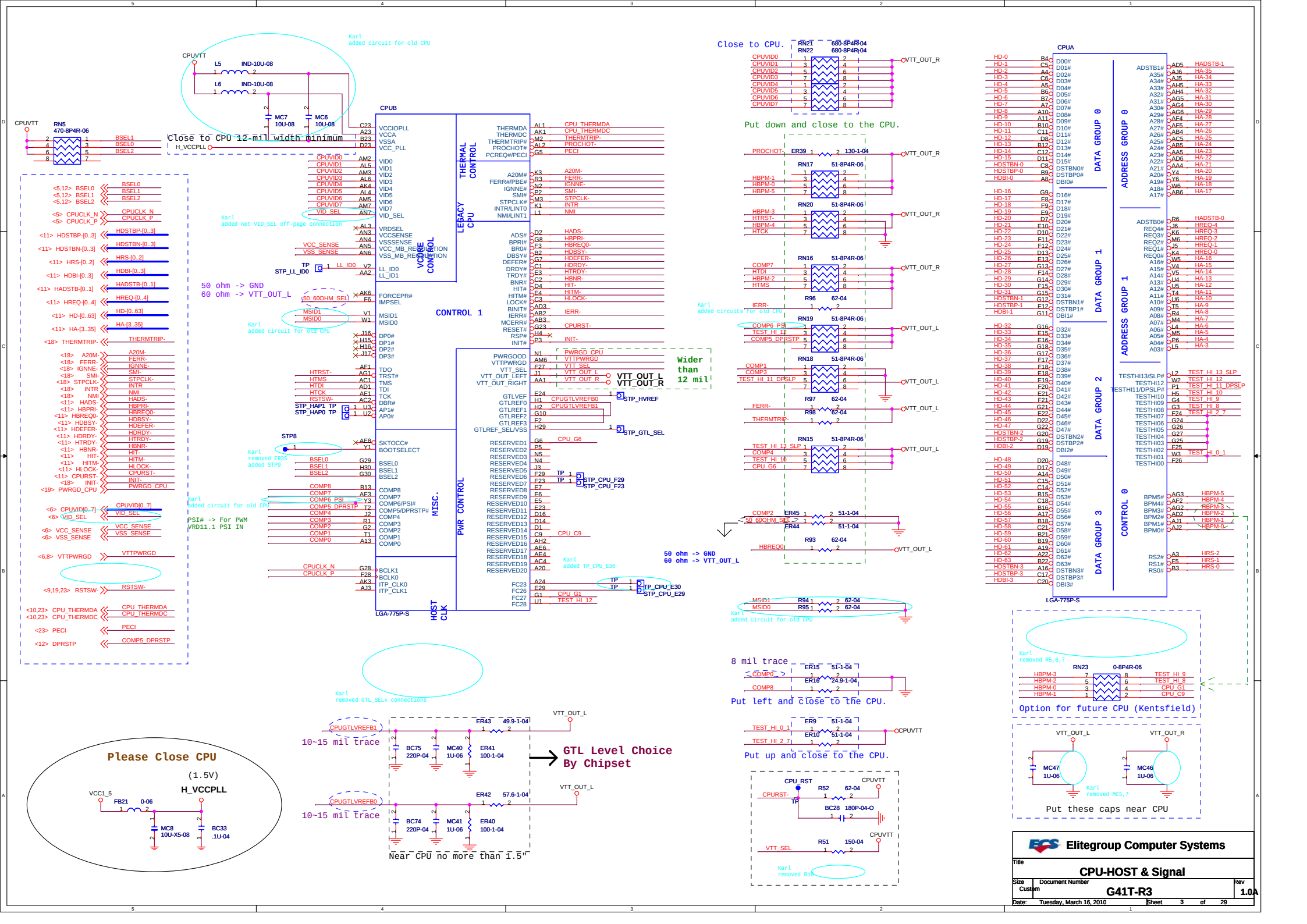

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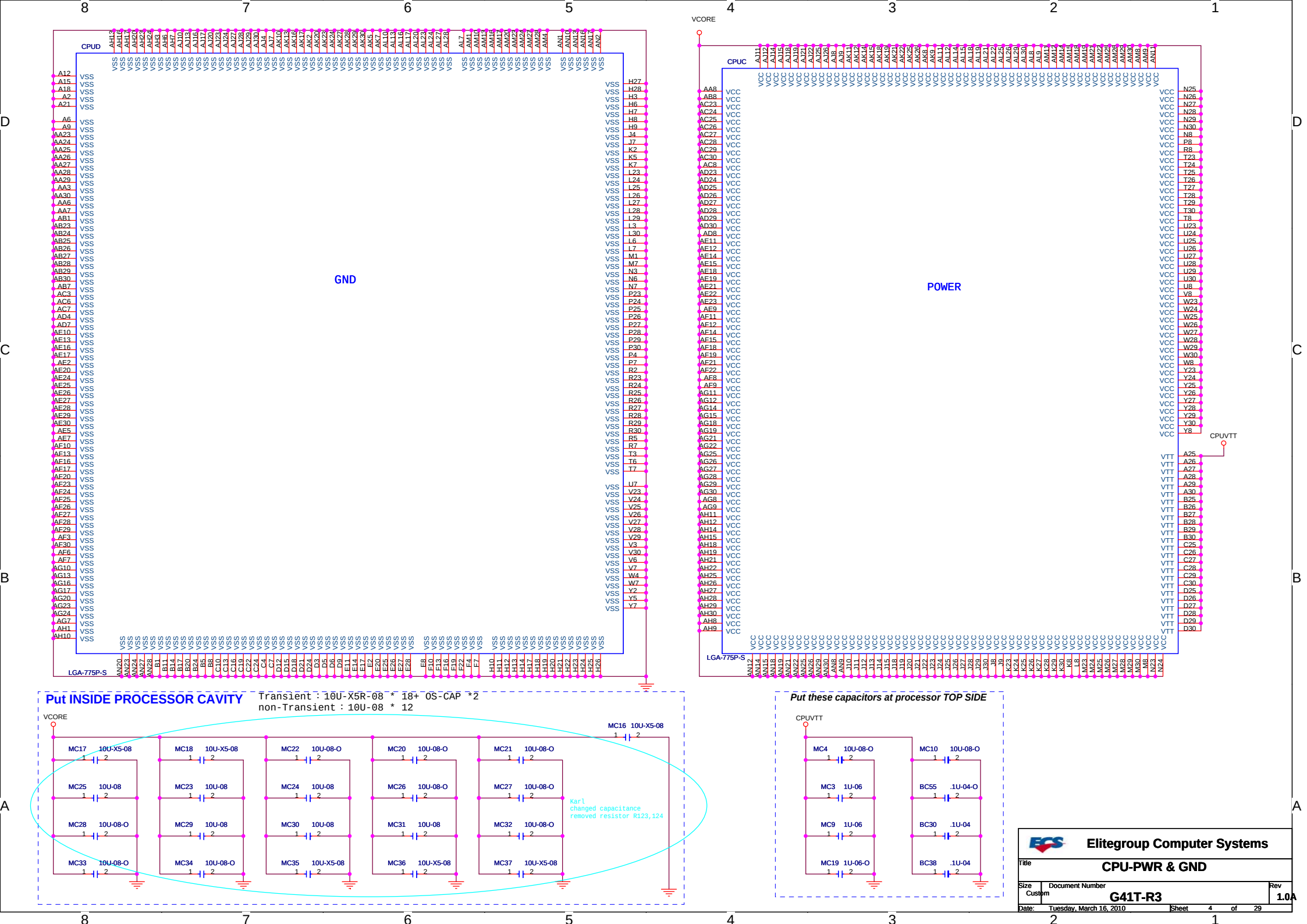
Title	CPU-HOST & Signal	
Size: Custom	Document Number: G41T-R3	Rev: 1.0A
Date: Tuesday, March 16, 2010	Sheet 3 of 29	



Elitegroup Computer Systems		
Title: CPU-PWR & GND		
Size: Custom	Document Number: G41T-R3	Rev: 1.0A
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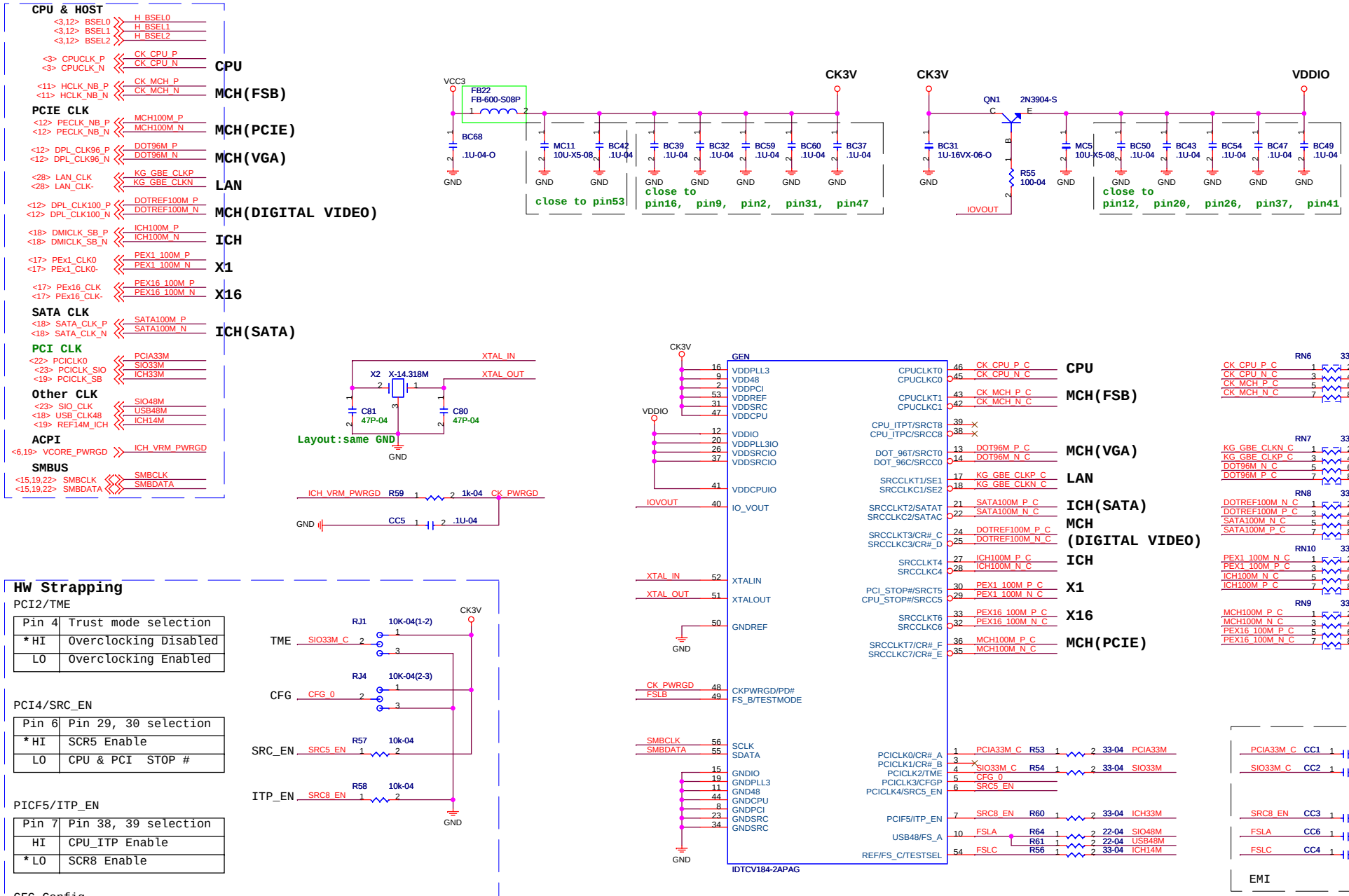
CPU & HOST
<3,12> BSEL0
<3,12> BSEL1
<3,12> BSEL2
H_BSEL0
H_BSEL1
H_BSEL2
<3> CPUCLK_P
<3> CPUCLK_N
CK_CPU_P
CK_CPU_N
CPU
<11> HCLK_NB_P
<11> HCLK_NB_N
CK_MCH_P
CK_MCH_N
MCH(FSB)
PCIE CLK
<12> PECLK_NB_P
<12> PECLK_NB_N
MCH100M_P
MCH100M_N
MCH(PCIE)
<12> DPL_CLK96_P
<12> DPL_CLK96_N
DOT96M_P
DOT96M_N
MCH(VGA)
<28> LAN_CLK
<28> LAN_CLK-
KG_GBE_CLKP
KG_GBE_CLKN
LAN
<12> DPL_CLK100_P
<12> DPL_CLK100_N
DOTREF100M_P
DOTREF100M_N
MCH(DIGITAL VIDEO)
<18> DMICLK_SB_P
<18> DMICLK_SB_N
ICH100M_P
ICH100M_N
ICH
<17> PEx1_CLK0
<17> PEx1_CLK0-
PEX1_100M_P
PEX1_100M_N
X1
<17> PEx16_CLK
<17> PEx16_CLK-
PEX16_100M_P
PEX16_100M_N
X16
SATA CLK
<18> SATA_CLK_P
<18> SATA_CLK_N
SATA100M_P
SATA100M_N
ICH(SATA)
PCI CLK
<22> PCICLK0
<23> PCICLK_SIO
<19> PCICLK_SB
PCIA33M
SIO33M
ICH33M
Other CLK
<23> SIO_CLK
<18> USB_CLK48
<19> REF14M_ICH
SIO48M
USB48M
ICH14M
ACPI
<6,19> VCORE_PWRGD
ICH_VRM_PWRGD
SMBUS
<15,19,22> SMBCLK
<15,19,22> SMBDATA
SMBCLK
SMBDATA
VCC3
FB22
FB-600-S08P
BC68
.1U-04-O
GND
CK3V
MC11
10U-X5-08
BC42
.1U-04
BC39
BC32
BC59
BC60
BC37
close to pin53
close to
pin16, pin9, pin2, pin31, pin47
QN1
2N3904-S
BC31
1U-16VX-06-O
R55
100-04
IOVOUT
VDDIO
MC5
BC50
BC43
BC54
BC47
BC49
close to
pin12, pin20, pin26, pin37, pin41
X2
X-14.318M
C81
47P-04
C80
47P-04
XTAL_IN
XTAL_OUT
Layout:same GND
ICH_VRM_PWRGD
R59
1k-04
CK_PWRGD
CC5
.1U-04
GEN
VDDPLL3
VDD48
VDDPCI
VDDREF
VDDSRC
VDDCPU
VDDIO
VDDPLL3IO
VDDSRCIO
VDDSRCIO
VDDCPUIO
IO_VOUT
XTALIN
XTALOUT
GNDREF
CKPWRGD/PD#
FS_B/TESTMODE
SCLK
SDATA
GNDIO
GNDPLL3
GND48
GNDCPU
GNDPCI
GNDSRC
GNDSRC
IDTCV184-2APAG
CPUCLKT0
CPUCLKC0
CPUCLKT1
CPUCLKC1
CPU_ITPT/SRCT8
CPU_ITPC/SRCC8
DOT_96T/SRCT0
DOT_96C/SRCC0
SRCCLKT1/SE1
SRCCLKC1/SE2
SRCCLKT2/SATAT
SRCCLKC2/SATAC
SRCCLKT3/CR#_C
SRCCLKC3/CR#_D
SRCCLKT4
SRCCLKC4
PCI_STOP#/SRCT5
CPU_STOP#/SRCC5
SRCCLKT6
SRCCLKC6
SRCCLKT7/CR#_F
SRCCLKC7/CR#_E
PCICLK0/CR#_A
PCICLK1/CR#_B
PCICLK2/TME
PCICLK3/CFGP
PCICLK4/SRC5_EN
PCIF5/ITP_EN
USB48/FS_A
REF/FS_C/TESTSEL
CK_CPU_P_C
CK_CPU_N_C
CK_MCH_P_C
CK_MCH_N_C
DOT96M_P_C
DOT96M_N_C
KG_GBE_CLKN_C
KG_GBE_CLKP_C
SATA100M_P_C
SATA100M_N_C
DOTREF100M_P_C
DOTREF100M_N_C
ICH100M_P_C
ICH100M_N_C
PEX1_100M_P_C
PEX1_100M_N_C
PEX16_100M_P_C
PEX16_100M_N_C
MCH100M_P_C
MCH100M_N_C
PCIA33M_C
SIO33M_C
CFG_0
SRC5_EN
SRC8_EN
FSLA
FSLC
R53
R54
R60
R64
R61
R56
33-04
22-04
PCIA33M
SIO33M
ICH33M
SIO48M
USB48M
ICH14M
RN6
RN7
RN8
RN10
RN9
33-8P4R-04
RN4
1K-8P4R-04
H_BSEL2
H_BSEL0
H_BSEL1
FSLC
FSLA
FSLB
CC1
CC2
CC3
CC6
CC4
10P-04-O
EMI
HW Strapping
PCI2/TME
Pin 4 | Trust mode selection
*HI | Overclocking Disabled
LO | Overclocking Enabled
PCI4/SRC_EN
Pin 6 | Pin 29, 30 selection
*HI | SCR5 Enable
LO | CPU & PCI STOP #
PICF5/ITP_EN
Pin 7 | Pin 38, 39 selection
HI | CPU_ITP Enable
*LO | SCR8 Enable
TME
CFG
SRC_EN
ITP_EN
RJ1
RJ4
R57
R58
10K-04(1-2)
10K-04(2-3)
10k-04
CFG Config.
Pin 5 CFGP | Pin 4 TME | SATA Pin 21,22 | PCI | Pin 17,18
*LO | 0 or 1 | PLL4 SRC | PLL4 SRC | CFB table
MID | 0 or 1 | PLL3 | PLL3 | CFB table
HI | 0 | PLL3 | PLL3 | CFB table
HI | 1 | PLL2 (CV184-1) PLL4 (CV184-2) | PLL4 | Pin17=25MHz PLL2 Pin18=1394A PLL3
Frequeny selection
Pin 54, 49, 10 FSLC, B, A | CPU CLOCK (MHZ) | CPU FSB CLOCK
010 | 200.00 | FSB 800
000 | 266.66 | FSB 1066
100 | 333.33 | FSB 1333
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Title
CLK IDTCV184
Size
Custom
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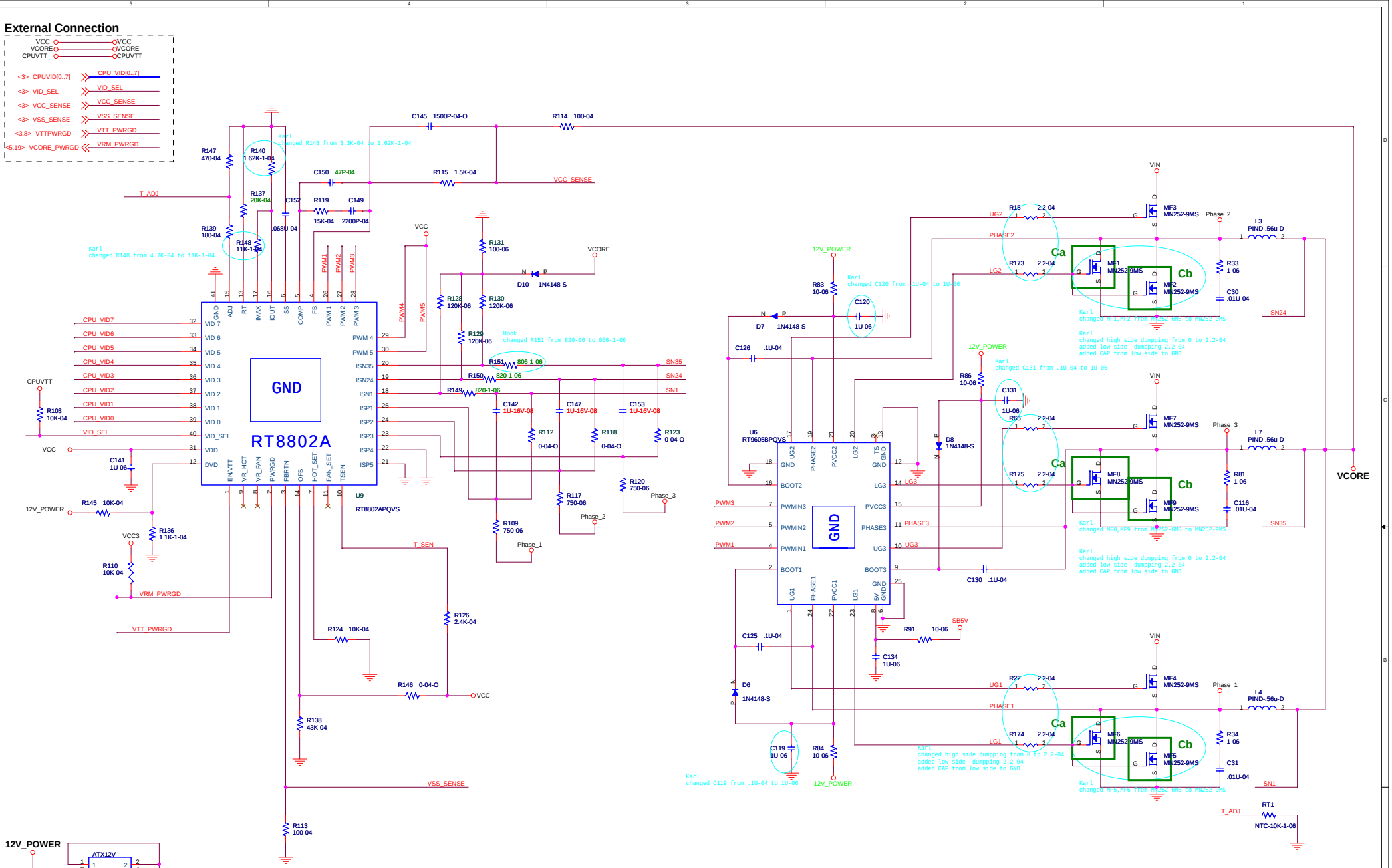
External Connection

VCC, VCORE, CPUVTT

<3> CPUVID[0..7] — CPU_VID[0..7]
<3> VID_SEL — VID_SEL
<3> VCC_SENSE — VCC_SENSE
<3> VSS_SENSE — VSS_SENSE
<3,8> VTTPWRGD — VTT_PWRGD
<5,19> VCORE_PWRGD — VRM_PWRGD



Location	For 95W CPU	For 65W CPU
Ca	MN252-9MS	X
Cb	MN252-9MS	MN252-6MS

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Title: VRD11.1 (uP6206)

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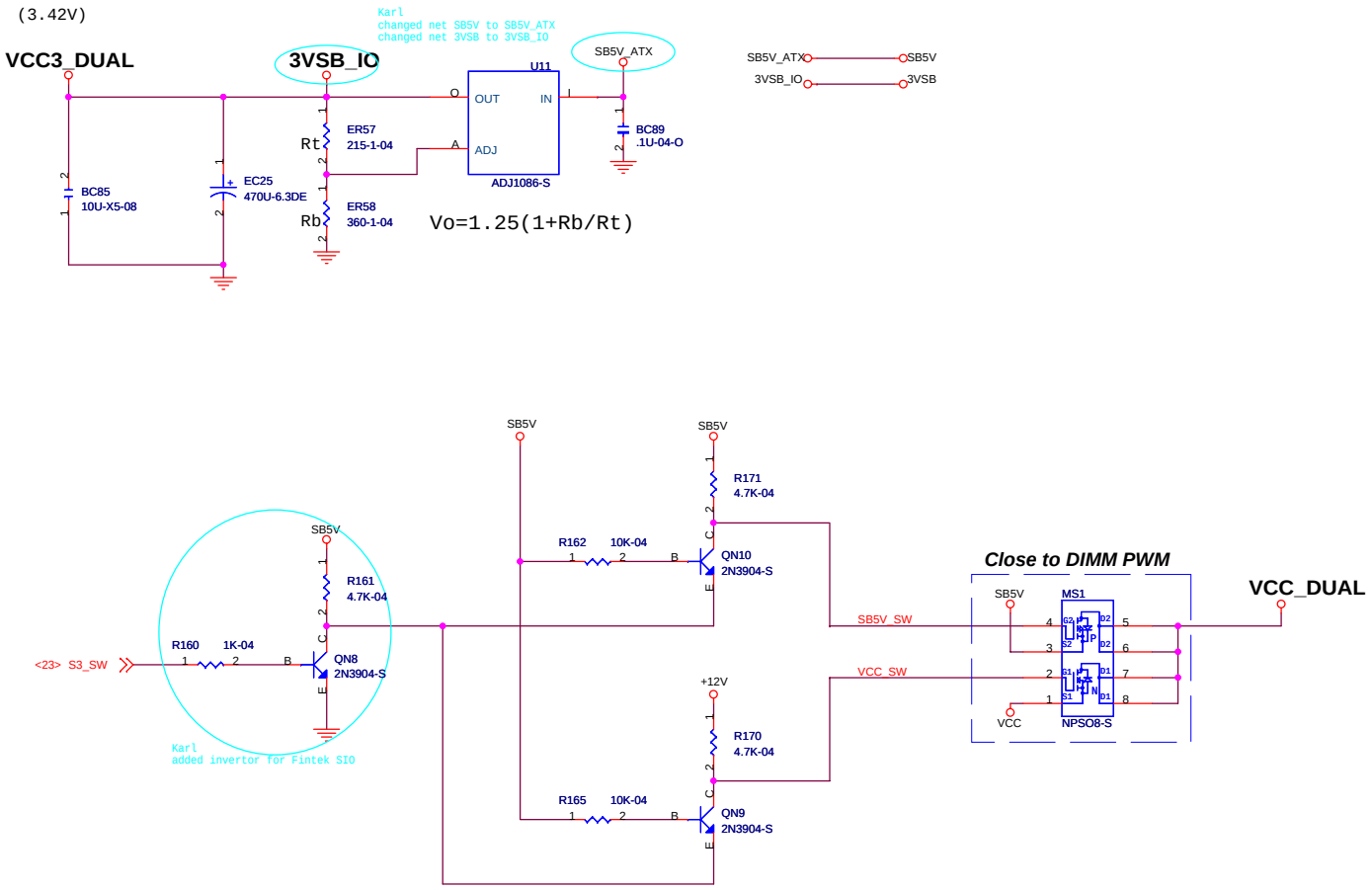

Memory Power Output Control

	VCC_DUAL	ATX_PWOK	SLP_S4-	SLP_S3-	S3_SW
S0	VCC	1	1	1	0
S1	VCC	1	1	1	0
S3	SB5V	0	1	0	1
S4	0	0	0	0	0
S5	0	0	0	0	0

Elitegroup Computer Systems

Title: Dual Power

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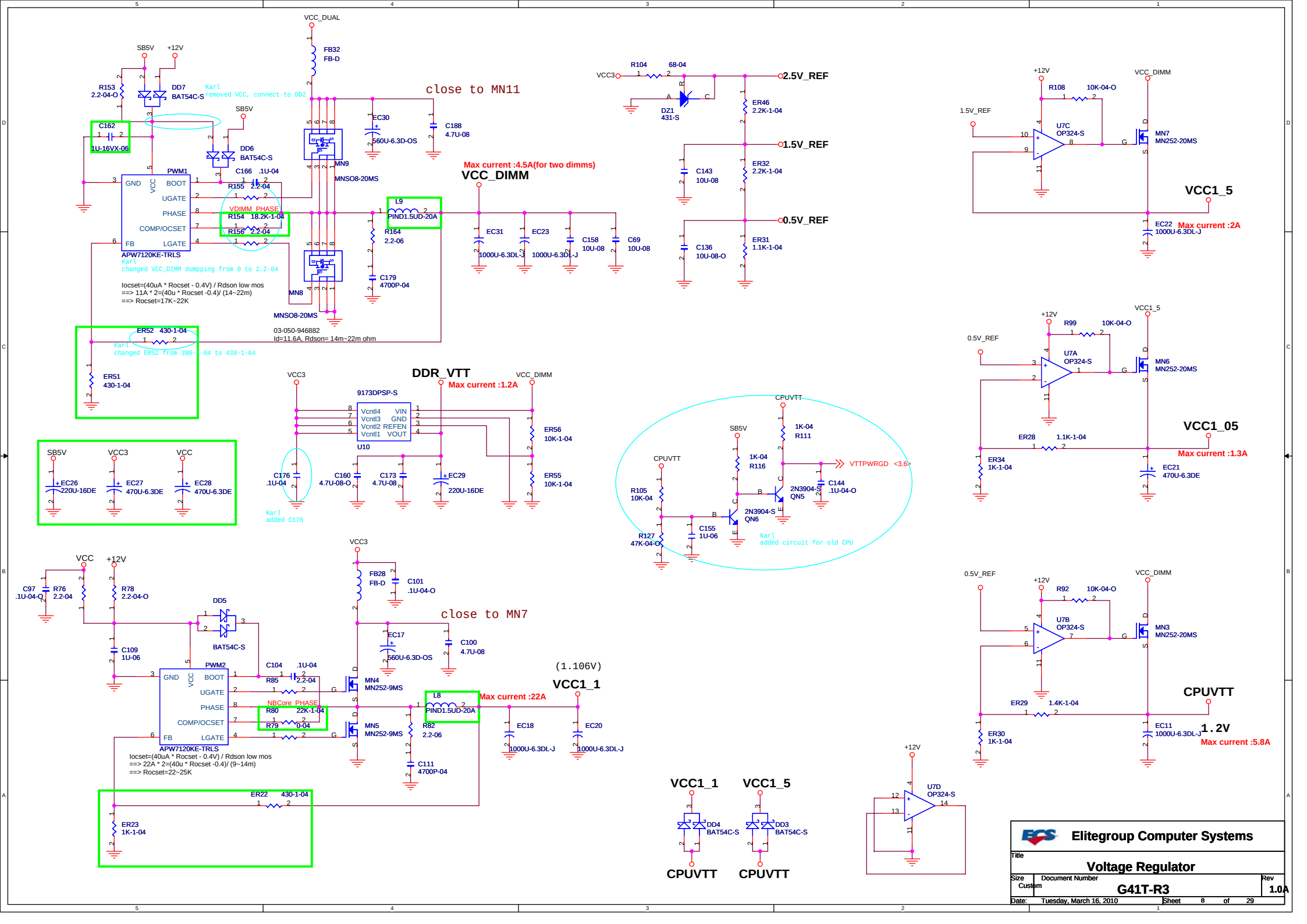
VCC_DUAL
FB32
FB-D
close to MN11
SB5V
+12V
R153
2.2-04-O
DD7
BAT54C-S
Karl
removed VCC, connect to DD2
C162
1U-16VX-06
DD6
BAT54C-S
C166 .1U-04
R155 2.2-04
PWM1
GND
VCC
BOOT
UGATE
PHASE
COMP/OCSET
FB
LGATE
APW7120KE-TRLS
VDIMM_PHASE
R154 18.2K-1-04
R156 2.2-04
MN9
MNSO8-20MS
MN8
MNSO8-20MS
EC30
560U-6.3D-OS
C188
4.7U-08
Max current :4.5A(for two dimms)
VCC_DIMM
L9
PIND1.5UD-20A
R164
2.2-06
C179
4700P-04
EC31
1000U-6.3DL-J
EC23
1000U-6.3DL-J
C158
10U-08
C69
10U-08
Karl
changed VCC_DIMM dumpping from 0 to 2.2-04
Iocset=(40uA * Rocset - 0.4V) / Rdson low mos
==> 11A * 2=(40u * Rocset -0.4)/ (14~22m)
==> Rocset=17K~22K
ER52 430-1-04
Karl
changed ER52 from 390-1-04 to 430-1-04
ER51
430-1-04
03-050-946882
Id=11.6A, Rdson= 14m~22m ohm
VCC3
R104 68-04
DZ1
431-S
2.5V_REF
1.5V_REF
0.5V_REF
ER46
2.2K-1-04
ER32
2.2K-1-04
ER31
1.1K-1-04
C143
10U-08
C136
10U-08-O
+12V
VCC_DIMM
R108 10K-04-O
1.5V_REF
U7C
OP324-S
MN7
MN252-20MS
VCC1_5
EC22
1000U-6.3DL-J
Max current :2A
SB5V
VCC3
VCC
EC26
220U-16DE
EC27
470U-6.3DE
EC28
470U-6.3DE
DDR_VTT
Max current :1.2A
9173DPSP-S
Vcntl4 VIN
Vcntl3 GND
Vcntl2 REFEN
Vcntl1 VOUT
U10
ER56
10K-1-04
ER55
10K-1-04
C176
.1U-04
C160
4.7U-08-O
C173
4.7U-08
EC29
220U-16DE
Karl
added C176
CPUVTT
SB5V
1K-04
R111
1K-04
R116
2N3904-S
QN5
2N3904-S
QN6
R105
10K-04
R127
47K-04-O
C155
1U-06
C144
.1U-04-O
VTTPWRGD <3,6>
Karl
added circuit for old CPU
+12V
R99 10K-04-O
0.5V_REF
U7A
OP324-S
MN6
MN252-20MS
VCC1_5
VCC1_05
Max current :1.3A
ER28 1.1K-1-04
ER34
1K-1-04
EC21
470U-6.3DE
VCC
+12V
C97
.1U-04-O
R76
2.2-04
R78
2.2-04-O
DD5
BAT54C-S
C109
1U-06
PWM2
GND
VCC
BOOT
UGATE
PHASE
COMP/OCSET
FB
LGATE
APW7120KE-TRLS
C104 .1U-04
R85 2.2-04
NBCore_PHASE
R80 22K-1-04
R79 0-04
MN4
MN252-9MS
MN5
MN252-9MS
close to MN7
FB28
FB-D
C101
.1U-04-O
EC17
560U-6.3D-OS
C100
4.7U-08
L8
PIND1.5UD-20A
Max current :22A
(1.106V)
VCC1_1
R82
2.2-06
C111
4700P-04
EC18
1000U-6.3DL-J
EC20
1000U-6.3DL-J
Iocset=(40uA * Rocset - 0.4V) / Rdson low mos
==> 22A * 2=(40u * Rocset -0.4)/ (9~14m)
==> Rocset=22~25K
ER22 430-1-04
ER23
1K-1-04
0.5V_REF
+12V
VCC_DIMM
R92 10K-04-O
U7B
OP324-S
MN3
MN252-20MS
CPUVTT
ER29 1.4K-1-04
ER30
1K-1-04
EC11
1000U-6.3DL-J
1.2V
Max current :5.8A
VCC1_1
VCC1_5
DD4
BAT54C-S
DD3
BAT54C-S
CPUVTT
CPUVTT
+12V
U7D
OP324-S
Elitegroup Computer Systems
Title
Voltage Regulator
Size
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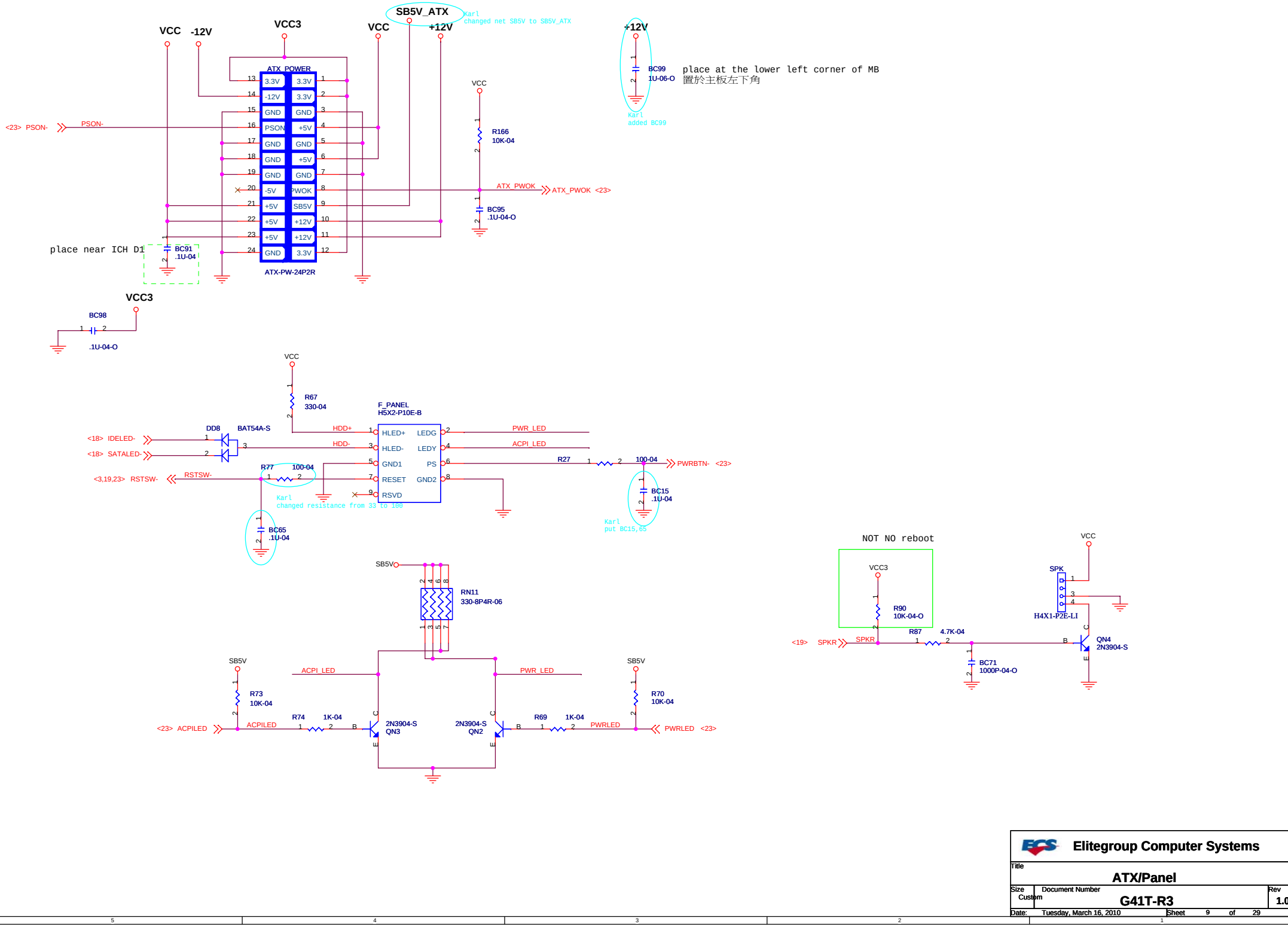

Elitegroup Computer Systems		
Title: ATX/Panel		
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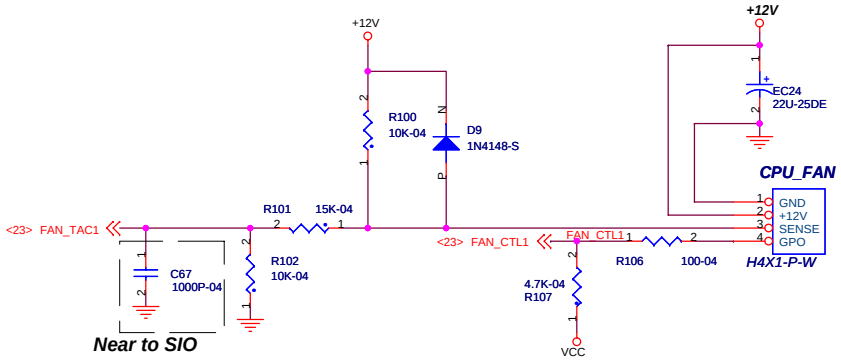
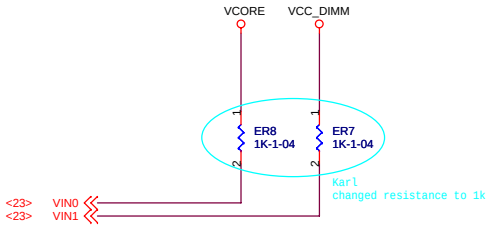


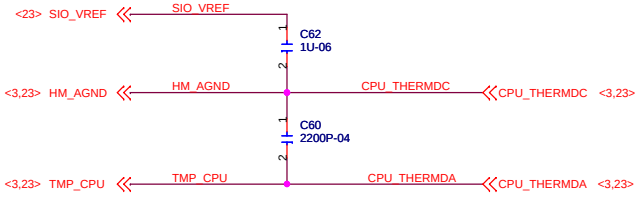
Karl
removed BC1

Voltage Monitor



Temperature Monitor

Choosing method of measuring temperature by either thermistor or diode



Elitegroup Computer Systems

Title: HW Monitor/Fan

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HOST

<3> HDSTBP-[0..3] — HDSTBP-[0..3]
<3> HD-[0..63] — HD-[0..63]
<3> HA-[3..35] — HA-[3..35]
<3> HDSTBN-[0..3] — HDSTBN-[0..3]
<3> HREQ-[0..4] — HREQ-[0..4]
<3> HDBI-[0..3] — HDBI-[0..3]
<3> HRS-[0..2] — HRS-[0..2]
<3> HADSTB-[0..1] — HADSTB-[0..1]

<3> HADS- — HADS-
<3> HTRDY- — HTRDY-
<3> HDRDY- — HDRDY-
<3> HDEFER- — HDEFER-
<3> HITM- — HITM-
<3> HIT- — HIT-
<3> HLOCK- — HLOCK-
<3> HBREQ0- — HBREQ0-
<3> HBNR- — HBNR-
<3> HBPRI- — HBPRI-
<3> HDBSY- — HDBSY-
<3> CPURST- — CPURST-

<5> HCLK_NB_P — HCLK_NB_P
<5> HCLK_NB_N — HCLK_NB_N

<$LOCATION>
NBA
EAGLELAKE_DDR3
SYM_REV = 1.55

HA-3	L36	FSB_AB_3	FSB_DB_0	F44	HD-0
HA-4	L37	FSB_AB_4	FSB_DB_1	C44	HD-1
HA-5	J38	FSB_AB_5	FSB_DB_2	D44	HD-2
HA-6	F40	FSB_AB_6	FSB_DB_3	C41	HD-3
HA-7	H39	FSB_AB_7	FSB_DB_4	F43	HD-4
HA-8	L38	FSB_AB_8	FSB_DB_5	B43	HD-5
HA-9	L43	FSB_AB_9	FSB_DB_6	D40	HD-6
HA-10	N39	FSB_AB_10	FSB_DB_7	B42	HD-7
HA-11	N35	FSB_AB_11	FSB_DB_8	B38	HD-8
HA-12	N37	FSB_AB_12	FSB_DB_9	F38	HD-9
HA-13	J41	FSB_AB_13	FSB_DB_10	A38	HD-10
HA-14	N40	FSB_AB_14	FSB_DB_11	B37	HD-11
HA-15	M45	FSB_AB_15	FSB_DB_12	D38	HD-12
HA-16	R35	FSB_AB_16	FSB_DB_13	C37	HD-13
HA-17	T36	FSB_AB_17	FSB_DB_14	D37	HD-14
HA-18	R36	FSB_AB_18	FSB_DB_15	B36	HD-15
HA-19	R34	FSB_AB_19	FSB_DB_16	E37	HD-16
HA-20	R37	FSB_AB_20	FSB_DB_17	J35	HD-17
HA-21	R39	FSB_AB_21	FSB_DB_18	H35	HD-18
HA-22	U38	FSB_AB_22	FSB_DB_19	F37	HD-19
HA-23	T37	FSB_AB_23	FSB_DB_20	G37	HD-20
HA-24	U34	FSB_AB_24	FSB_DB_21	J33	HD-21
HA-25	U40	FSB_AB_25	FSB_DB_22	L33	HD-22
HA-26	T34	FSB_AB_26	FSB_DB_23	G33	HD-23
HA-27	Y36	FSB_AB_27	FSB_DB_24	L31	HD-24
HA-28	U35	FSB_AB_28	FSB_DB_25	M31	HD-25
HA-29	AA35	FSB_AB_29	FSB_DB_26	M30	HD-26
HA-30	U37	FSB_AB_30	FSB_DB_27	J30	HD-27
HA-31	Y37	FSB_AB_31	FSB_DB_28	G31	HD-28
HA-32	Y34	FSB_AB_32	FSB_DB_29	K30	HD-29
HA-33	Y38	FSB_AB_33	FSB_DB_30	M29	HD-30
HA-34	AA37	FSB_AB_34	FSB_DB_31	G30	HD-31
HA-35	AA36	FSB_AB_35	FSB_DB_32	J29	HD-32
			FSB_DB_33	F29	HD-33
			FSB_DB_34	H29	HD-34
			FSB_DB_35	L25	HD-35
			FSB_DB_36	K26	HD-36
			FSB_DB_37	L29	HD-37
HREQ-0	G38	FSB_REQB_0	FSB_DB_38	J26	HD-38
HREQ-1	K35	FSB_REQB_1	FSB_DB_39	M26	HD-39
HREQ-2	J39	FSB_REQB_2	FSB_DB_40	H26	HD-40
HREQ-3	C43	FSB_REQB_3	FSB_DB_41	F25	HD-41
HREQ-4	G39	FSB_REQB_4	FSB_DB_42	F24	HD-42
			FSB_DB_43	G25	HD-43
HADSTB-0	J40	FSB_ADSTBB_0	FSB_DB_44	H24	HD-44
HADSTB-1	T39	FSB_ADSTBB_1	FSB_DB_45	L24	HD-45
			FSB_DB_46	J24	HD-46
HDSTBP-0	C39	FSB_DSTBPB_0	FSB_DB_47	N24	HD-47
HDSTBN-0	B39	FSB_DSTBNB_0	FSB_DB_48	C28	HD-48
HDBI-0	B40	FSB_DINVB_0	FSB_DB_49	B31	HD-49
HDSTBP-1	K31	FSB_DSTBPB_1	FSB_DB_50	E35	HD-50
HDSTBN-1	J31	FSB_DSTBNB_1	FSB_DB_51	C35	HD-51
HDBI-1	F33	FSB_DINVB_1	FSB_DB_52	B35	HD-52
HDSTBP-2	J25	FSB_DSTBPB_2	FSB_DB_53	D35	HD-53
HDSTBN-2	K25	FSB_DSTBNB_2	FSB_DB_54	D31	HD-54
HDBI-2	F26	FSB_DINVB_2	FSB_DB_55	A34	HD-55
HDSTBP-3	C32	FSB_DSTBPB_3	FSB_DB_56	B32	HD-56
HDSTBN-3	D32	FSB_DSTBNB_3	FSB_DB_57	F31	HD-57
HDBI-3	D30	FSB_DINVB_3	FSB_DB_58	D28	HD-58
			FSB_DB_59	A29	HD-59
HADS-	J42	FSB_ADSB	FSB_DB_60	C30	HD-60
HTRDY-	L40	FSB_TRDYB	FSB_DB_61	B30	HD-61
HDRDY-	J43	FSB_DRDYB	FSB_DB_62	E27	HD-62
HDEFER-	G44	FSB_DEFERB	FSB_DB_63	B28	HD-63
HITM-	K44	FSB_HITMB			
HIT-	H45	FSB_HITB			
HLOCK-	H40	FSB_LOCKB	FSB_SWING	B24	HXSWING
HBREQ0-	L42	FSB_BREQ0B	FSB_RCOMP	A23	HXRCOMP
HBNR-	J44	FSB_BNRB			
HBPRI-	H37	FSB_BPRIB			
HDBSY-	H42	FSB_DBSYB	FSB_DVREF	C22	MCH_GTLREF
HRS-0	G43	FSB_RSB_0	FSB_ACCVREF	B23	
HRS-1	L44	FSB_RSB_1			
HRS-2	G42	FSB_RSB_2			
CPURST-	D27	FSB_CPURSTB			
			HPL_CLKINP	P29	HCLK_NB_P
STP_NB_N1			HPL_CLKINN	P30	HCLK_NB_N
TP	1	RSVD_05 N25			

1 OF 9
AC82G41-A3
<MATERIAL>

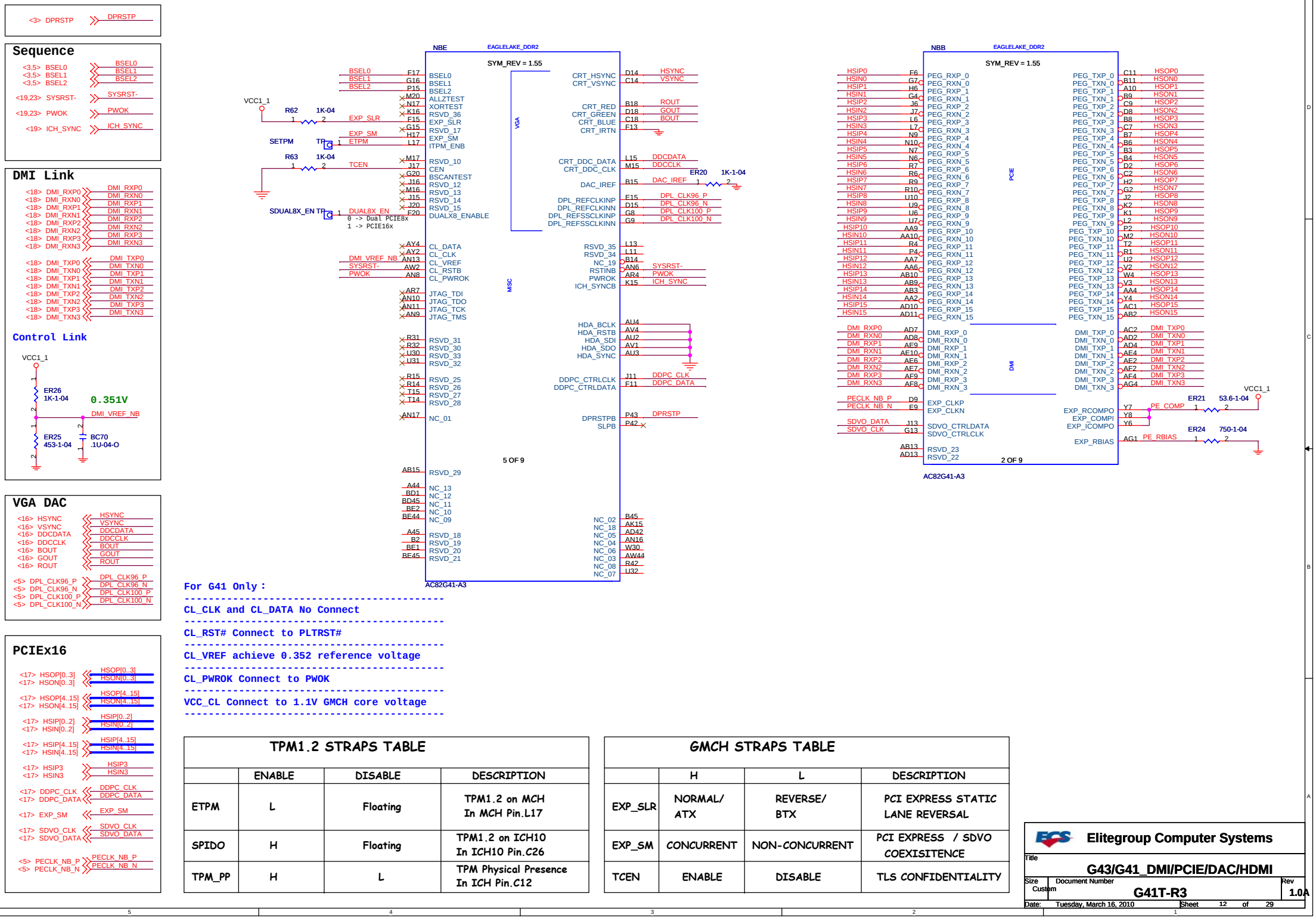
ER19 16.5-1-04 (2, 1)

HDSWING W/S =10/10 mils
HXSWING — 301-1-04 ER12 (2, 1) — CPUVTT
BC53 .1U-04
ER13 100-1-04

MCH_GTLREF W/S =10/7 mils
MCH_GTLREF — 115-1-04 ER11 (2, 1) — CPUVTT
C84 220P-04-O
C86 .1U-04
ER14 200-1-04

Elitegroup Computer Systems

Title	G43/G41_HOST	
Size: Custom	Document Number: G41T-R3	Rev: 1.0A
Date: Tuesday, March 16, 2010	Sheet 11 of 29	



TPM1.2 STRAPS TABLE

	ENABLE	DISABLE	DESCRIPTION
ETPM	L	Floating	TPM1.2 on MCH In MCH Pin.L17
SPIDO	H	Floating	TPM1.2 on ICH10 In ICH10 Pin.C26
TPM_PP	H	L	TPM Physical Presence In ICH Pin.C12

GMCH STRAPS TABLE

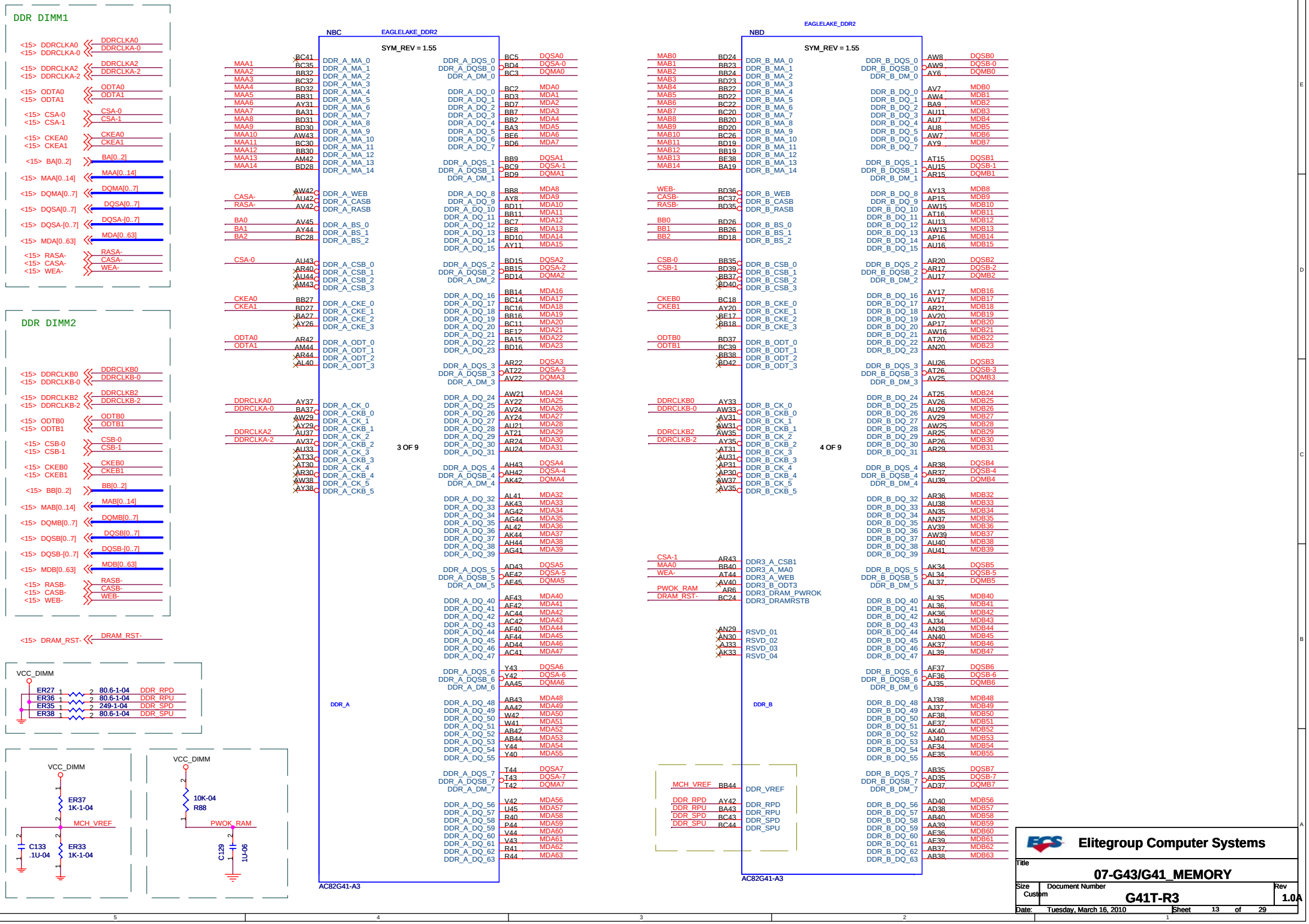
	H	L	DESCRIPTION
EXP_SLR	NORMAL/ ATX	REVERSE/ BTX	PCI EXPRESS STATIC LANE REVERSAL
EXP_SM	CONCURRENT	NON-CONCURRENT	PCI EXPRESS / SDVO COEXISITENCE
TCEN	ENABLE	DISABLE	TLS CONFIDENTIALITY

Elitegroup Computer Systems

Title: G43/G41_DMI/PCIE/DAC/HDMI

Size: Custom | Document Number: G41T-R3 | Rev: 1.0A

Date: Tuesday, March 16, 2010 | Sheet 12 of 29


DDR DIMM1
DDR DIMM2
NBC
EAGLELAKE_DDR2
SYM_REV = 1.55
3 OF 9
DDR_A
AC82G41-A3
NBD
4 OF 9
DDR_B
AC82G41-A3
VCC_DIMM
ER27 80.6-1-04 DDR_RPD
ER36 80.6-1-04 DDR_RPU
ER35 249-1-04 DDR_SPD
ER38 80.6-1-04 DDR_SPU
ER37 1K-1-04
MCH_VREF
C133 .1U-04
ER33 1K-1-04
10K-04 R88
PWOK_RAM
C129 1U-06
MCH_VREF
DDR_RPD
DDR_RPU
DDR_SPD
DDR_SPU
Elitegroup Computer Systems
07-G43/G41_MEMORY
G41T-R3
1.0A
Tuesday, March 16, 2010
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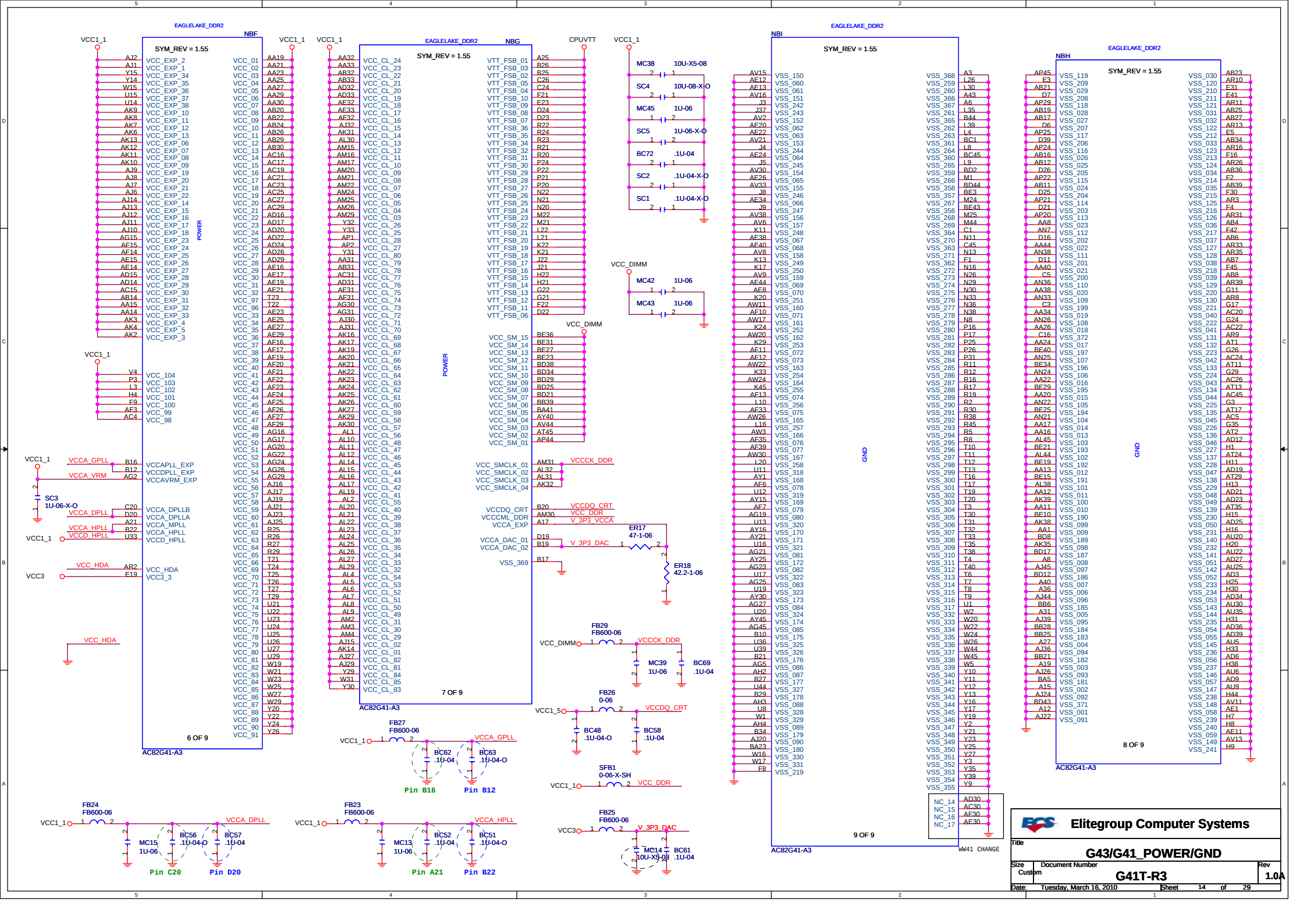
EAGLELAKE_DDR2
NBF
SYM_REV = 1.55
POWER
6 OF 9
AC82G41-A3
NBG
7 OF 9
NBI
GND
9 OF 9
NBH
8 OF 9
Elitegroup Computer Systems
G43/G41_POWER/GND
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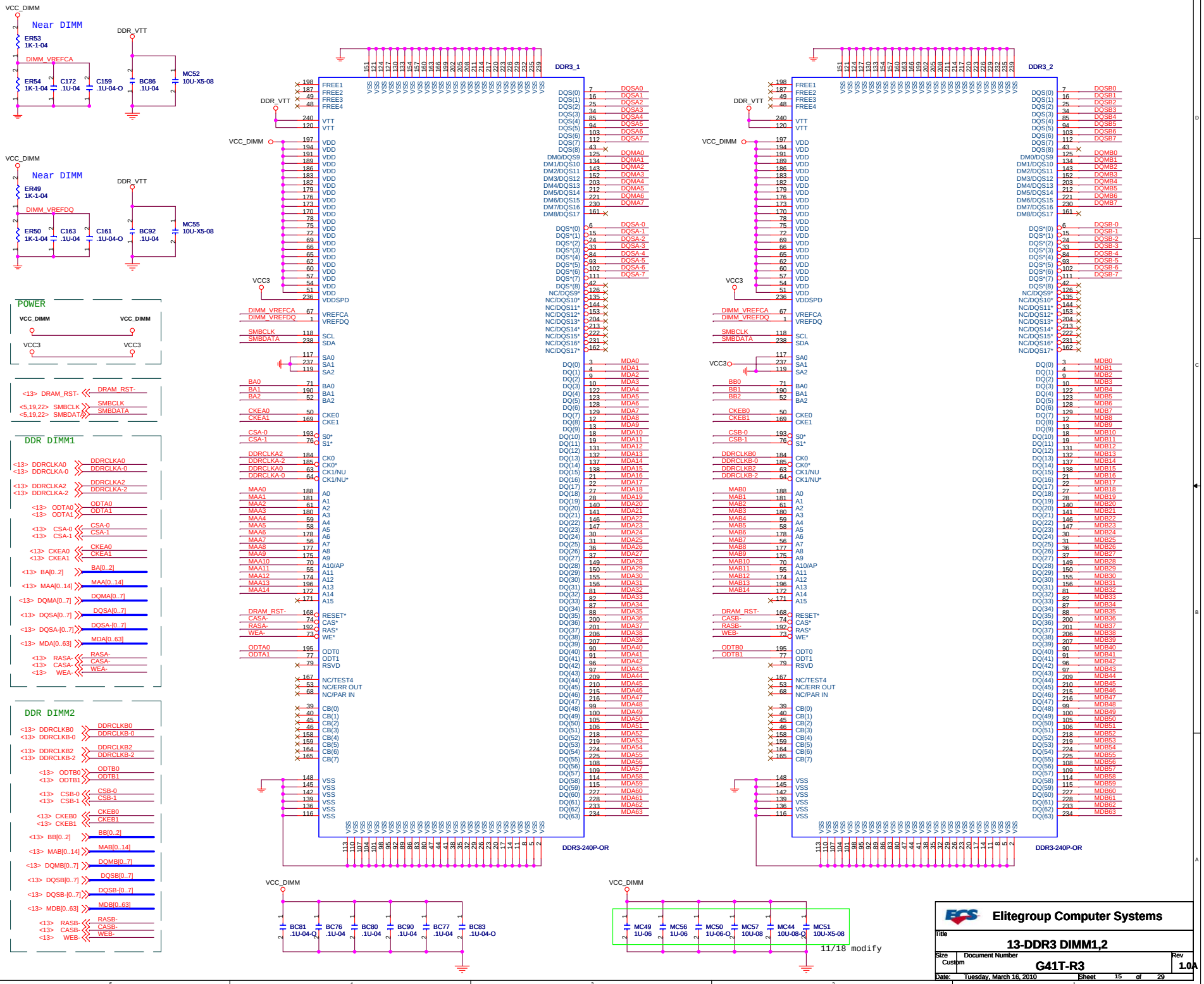
1.0A
Tuesday, March 16, 2010

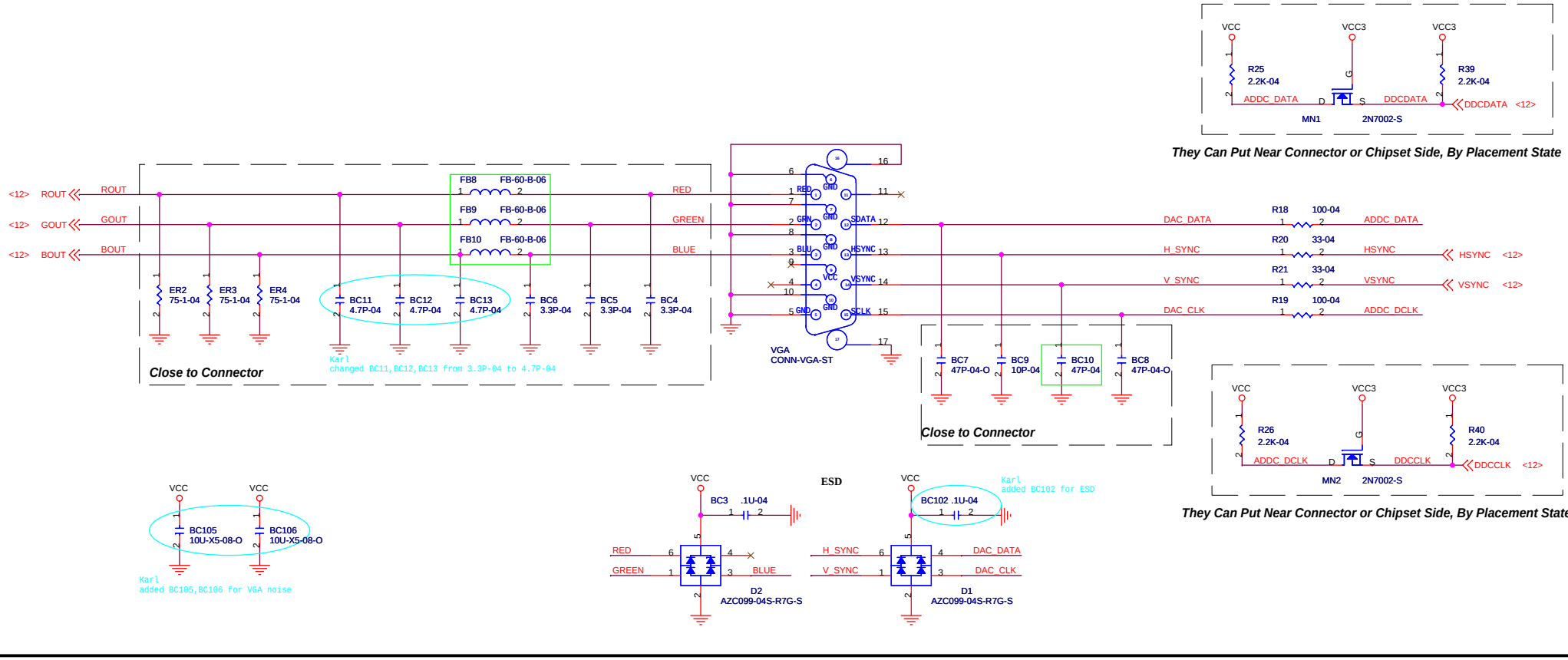


Elitegroup Computer Systems

Title: 13-DDR3 DIMM1,2

Size	Document Number	Rev
Custom	G41T-R3	1.0A

Date: Tuesday, March 16, 2010 Sheet 15 of 29



Elitegroup Computer Systems		
Title: DAC VGA RGB		
Size: Custom	Document Number: G41T-R3	Rev: 1.0A
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POWER

3VSB — 3VSB

VCC3 — VCC3

+12V — +12V

PCI-E Comon Signal

<19,28> PCIE_WAKE- — PCIE_WAKE-

<23,28> PCIE_RST- — PCIE_RST-

PCI-E x16

<5> PEx16_CLK — PEx16_CLK

<5> PEx16_CLK- — PEx16_CLK-

<12> HSIP3 — HSIP3

<12> HSIN3 — HSIN3

<12> HSIP[0..2] — HSIP[0..2]

<12> HSIN[0..2] — HSIN[0..2]

<12> HSIP[4..15] — HSIP[4..15]

<12> HSIN[4..15] — HSIN[4..15]

<12> HSOP[0..3] — HSOP[0..3]

<12> HSON[0..3] — HSON[0..3]

<12> HSOP[4..15] — HSOP[4..15]

<12> HSON[4..15] — HSON[4..15]

<12> SDVO_CLK — PE_PRSNT_1x

<12> SDVO_DATA — PE_PRSNT_4x

<12> EXP_SM — PE_PRSNT_8x

<12> DDPC_DATA — PE_PRSNT_16x

<12> DDPC_CLK — PE_PRSNT

PCI-E x1 Slot A

<18> PE0_OUT — PE0_OUT

<18> PE0_OUT- — PE0_OUT-

<18> PE0_IN — PE0_IN

<18> PE0_IN- — PE0_IN-

<5> PEx1_CLK0 — PEx1_CLK0

<5> PEx1_CLK0- — PEx1_CLK0-

PCIEX16

B1	12V_A	PRSNT1*	A1
B2	12V_B	12V_C	A2
B3	12V_D	12V_E	A3
B4	GND1	GND2	A4
B5	SMCLK	JTAG2	A5
B6	SMDAT	JTAG3	A6
B7	GND3	JTAG4	A7
B8	3.3V_A	JTAG5	A8
B9	JTAG1	3.3V_B	A9
B10	3.3VAUX	3.3V_C	A10
B11	WAKE#	PWRGD	A11
	KEY		
B12	RSVD_A	GND4	A12
B13	GND5	REFCLK_+_H	A13
B14	HSOP0_H	REFCLK_-_L	A14
B15	HSON0_L	GND6	A15
B16	GND7	HSIP0_H	A16
B17	PRSNT2#1	HSIN0_L	A17
B18	GND8	GND9	A18
B19	HSOP1_H	RSVD_B	A19
B20	HSON1_L	GND10	A20
B21	GND11	HSIP1_H	A21
B22	GND12	HSIN1_L	A22
B23	HSOP2_H	GND13	A23
B24	HSON2_L	GND14	A24
B25	GND15	HSIP2_H	A25
B26	GND16	HSIN2_L	A26
B27	HSOP3_H	GND17	A27
B28	HSON3_L	GND18	A28
B29	GND19	HSIP3_H	A29
B30	RSVD_C	HSIN3_L	A30
B31	PRSNT2#2	GND20	A31
B32	GND21	RSVD_D	A32
B33	HSOP4_H	RSVD_E	A33
B34	HSON4_L	GND22	A34
B35	GND23	HSIP4_H	A35
B36	GND24	HSIN4_L	A36
B37	HSOP5_H	GND25	A37
B38	HSON5_L	GND26	A38
B39	GND27	HSIP5_H	A39
B40	GND28	HSIN5_L	A40
B41	HSOP6_H	GND29	A41
B42	HSON6_L	GND30	A42
B43	GND31	HSIP6_H	A43
B44	GND32	HSIN6_L	A44
B45	HSOP7_H	GND33	A45
B46	HSON7_L	GND34	A46
B47	GND35	HSIP7_H	A47
B48	PRSNT2#3	HSIN7_L	A48
B49	GND36	GND37	A49
B50	HSOP8_H	RSVD_F	A50
B51	HSON8_L	GND38	A51
B52	GND39	HSIP8_H	A52
B53	GND40	HSIN8_L	A53
B54	HSOP9_H	GND41	A54
B55	HSON9_L	GND42	A55
B56	GND43	HSIP9_H	A56
B57	GND44	HSIN9_L	A57
B58	HSOP10_H	GND45	A58
B59	HSON10_L	GND46	A59
B60	GND47	HSIP10_H	A60
B61	GND48	HSIN10_L	A61
B62	HSOP11_H	GND49	A62
B63	HSON11_L	GND50	A63
B64	GND51	HSIP11_H	A64
B65	GND52	HSIN11_L	A65
B66	HSOP12_H	GND53	A66
B67	HSON12_L	GND54	A67
B68	GND55	HSIP12_H	A68
B69	GND56	HSIN12_L	A69
B70	HSOP13_H	GND57	A70
B71	HSON13_L	GND58	A71
B72	GND59	HSIP13_H	A72
B73	GND60	HSIN13_L	A73
B74	HSOP14_H	GND61	A74
B75	HSON14_L	GND62	A75
B76	GND63	HSIP14_H	A76
B77	GND64	HSIN14_L	A77
B78	HSOP15_H	GND65	A78
B79	HSON15_L	GND66	A79
B80	GND67	HSIP15_H	A80
B81	PRSNT2#4	HSIN15_L	A81
B82	RSVD_G	GND68	A82

PCIEX16-OR

PE_PRSNT — PCIE_WAKE- — PCIE_RST- — PEx16_CLK — PEx16_CLK- — HSIP0 — HSIN0 — HSIP1 — HSIN1 — HSIP2 — HSIN2 — HSIP3 — HSIN3 — HSIP4 — HSIN4 — HSIP5 — HSIN5 — HSIP6 — HSIN6 — HSIP7 — HSIN7 — HSIP8 — HSIN8 — HSIP9 — HSIN9 — HSIP10 — HSIN10 — HSIP11 — HSIN11 — HSIP12 — HSIN12 — HSIP13 — HSIN13 — HSIP14 — HSIN14 — HSIP15 — HSIN15

C_HSOP0 — C_HSON0 — PE_PRSNT_1x — C_HSOP1 — C_HSON1 — C_HSOP2 — C_HSON2 — C_HSOP3 — C_HSON3 — PE_PRSNT_4x — C_HSOP4 — C_HSON4 — C_HSOP5 — C_HSON5 — C_HSOP6 — C_HSON6 — C_HSOP7 — C_HSON7 — PE_PRSNT_8x — C_HSOP8 — C_HSON8 — C_HSOP9 — C_HSON9 — C_HSOP10 — C_HSON10 — C_HSOP11 — C_HSON11 — C_HSOP12 — C_HSON12 — C_HSOP13 — C_HSON13 — C_HSOP14 — C_HSON14 — C_HSOP15 — C_HSON15 — PE_PRSNT_16x

R68 0-04-O

C_HSOP0 — C90 0.1U-10VX-04 — HSOP0

C_HSOP1 — C91 0.1U-10VX-04 — HSOP1

C_HSOP2 — C93 0.1U-10VX-04 — HSOP2

C_HSOP3 — C95 0.1U-10VX-04 — HSOP3

C_HSOP4 — C98 0.1U-10VX-04 — HSOP4

C_HSOP5 — C103 0.1U-10VX-04 — HSOP5

C_HSOP6 — C107 0.1U-10VX-04 — HSOP6

C_HSOP7 — C115 0.1U-10VX-04 — HSOP7

C_HSOP8 — C123 0.1U-10VX-04 — HSOP8

C_HSOP9 — C127 0.1U-10VX-04 — HSOP9

C_HSOP10 — C135 0.1U-10VX-04 — HSOP10

C_HSOP11 — C138 0.1U-10VX-04 — HSOP11

C_HSOP12 — C139 0.1U-10VX-04 — HSOP12

C_HSOP13 — C148 0.1U-10VX-04 — HSOP13

C_HSOP14 — C154 0.1U-10VX-04 — HSOP14

C_HSOP15 — C157 0.1U-10VX-04 — HSOP15

C_HSON0 — C89 0.1U-10VX-04 — HSON0

C_HSON1 — C92 0.1U-10VX-04 — HSON1

C_HSON2 — C94 0.1U-10VX-04 — HSON2

C_HSON3 — C96 0.1U-10VX-04 — HSON3

C_HSON4 — C99 0.1U-10VX-04 — HSON4

C_HSON5 — C102 0.1U-10VX-04 — HSON5

C_HSON6 — C106 0.1U-10VX-04 — HSON6

C_HSON7 — C113 0.1U-10VX-04 — HSON7

C_HSON8 — C124 0.1U-10VX-04 — HSON8

C_HSON9 — C128 0.1U-10VX-04 — HSON9

C_HSON10 — C132 0.1U-10VX-04 — HSON10

C_HSON11 — C137 0.1U-10VX-04 — HSON11

C_HSON12 — C140 0.1U-10VX-04 — HSON12

C_HSON13 — C146 0.1U-10VX-04 — HSON13

C_HSON14 — C151 0.1U-10VX-04 — HSON14

C_HSON15 — C156 0.1U-10VX-04 — HSON15

Karl
changed AC coupling from .1U-04 to 0.1U-10VX-04

PCIE

PCI_EXPRESS_X1

B1	+12V_B1	PRSNT1*	A1
B2	+12V_B2	+12V_A2	A2
B3	+12V_B3	+12V_A3	A3
B4	GND_B4	GND_A4	A4
B5	SMCLK	TCK	A5
B6	SMDAT	TDI	A6
B7	GND_B7	TDO	A7
B8	+3.3V_B8	TMS	A8
B9	TRST*	+3.3V_A9	A9
B10	+3.3V_AUX	+3.3V_A10	A10
B11	WAKE*	PERST*	A11
B12	RSVD_B12	GND_A12	A12
B13	GND_B13	REFCLK+	A13
B14	PETP0	REFCLK-	A14
B15	PETN0	GND_A15	A15
B16	GND_B16	PERP0	A16
B17	PRSNT2_B17*	PERN0	A17
B18	GND_B18	GND_A18	A18

X1 CONNECTOR

PCIEX1-W

3VSB — PCIE_WAKE-

+12V — PCIE_RST-

VCC3 — BC27 .1U-04-O

PE0_OUT — C87 0.1U-10VX-04 — PE0

PE0_OUT- — C88 0.1U-10VX-04 — PE0-

PEx1_CLK0 — PEx1_CLK0-

PE0_IN — PE0_IN-

Please place the caps close to PCI-E Slot.

3VSB — BC44 .1U-04-O — BC46 .1U-04-O — BC82 .1U-04

+12V — BC35 .1U-04-O — BC34 .1U-04 — BC36 .1U-04 — BC29 .1U-04-O

VCC3 — BC67 .1U-04-O — BC40 .1U-04-O — BC64 .1U-04 — BC45 .1U-04

Please place the caps close to PCI-E Slot.

+12V — EC10 470U-16DE

EMI

+12V — BC41 .1U-04-O — VCC3

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Title: PCIE 16x/1x Slot

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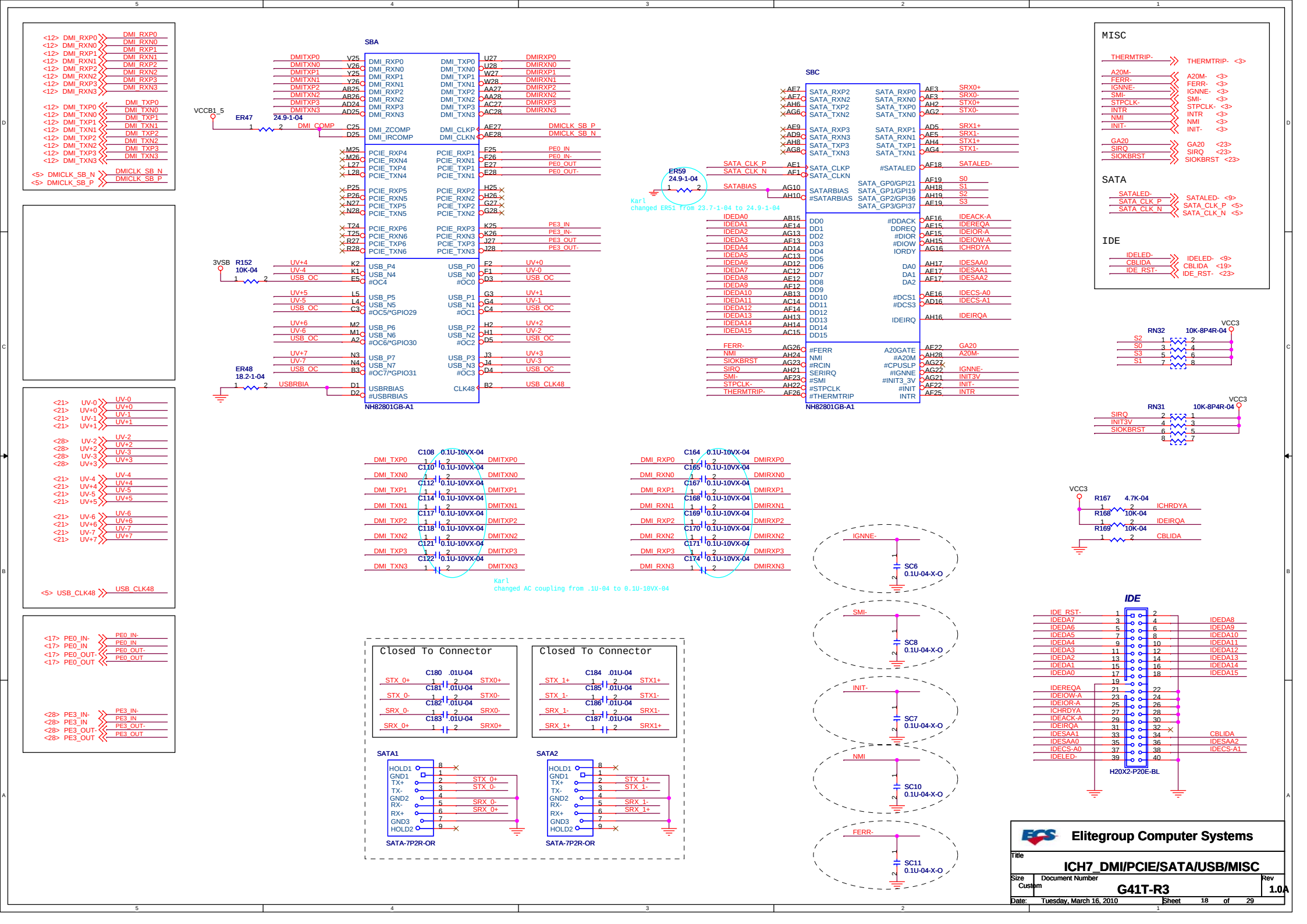
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SBC
NH82801GB-A1
NH82801GB-A1
MISC
SATA
IDE
Closed To Connector
Closed To Connector
SATA1
SATA2
SATA-7P2R-OR
SATA-7P2R-OR
IDE
H20X2-P20E-BL
Elitegroup Computer Systems
ICH7_DMI/PCIE/SATA/USB/MISC
G41T-R3
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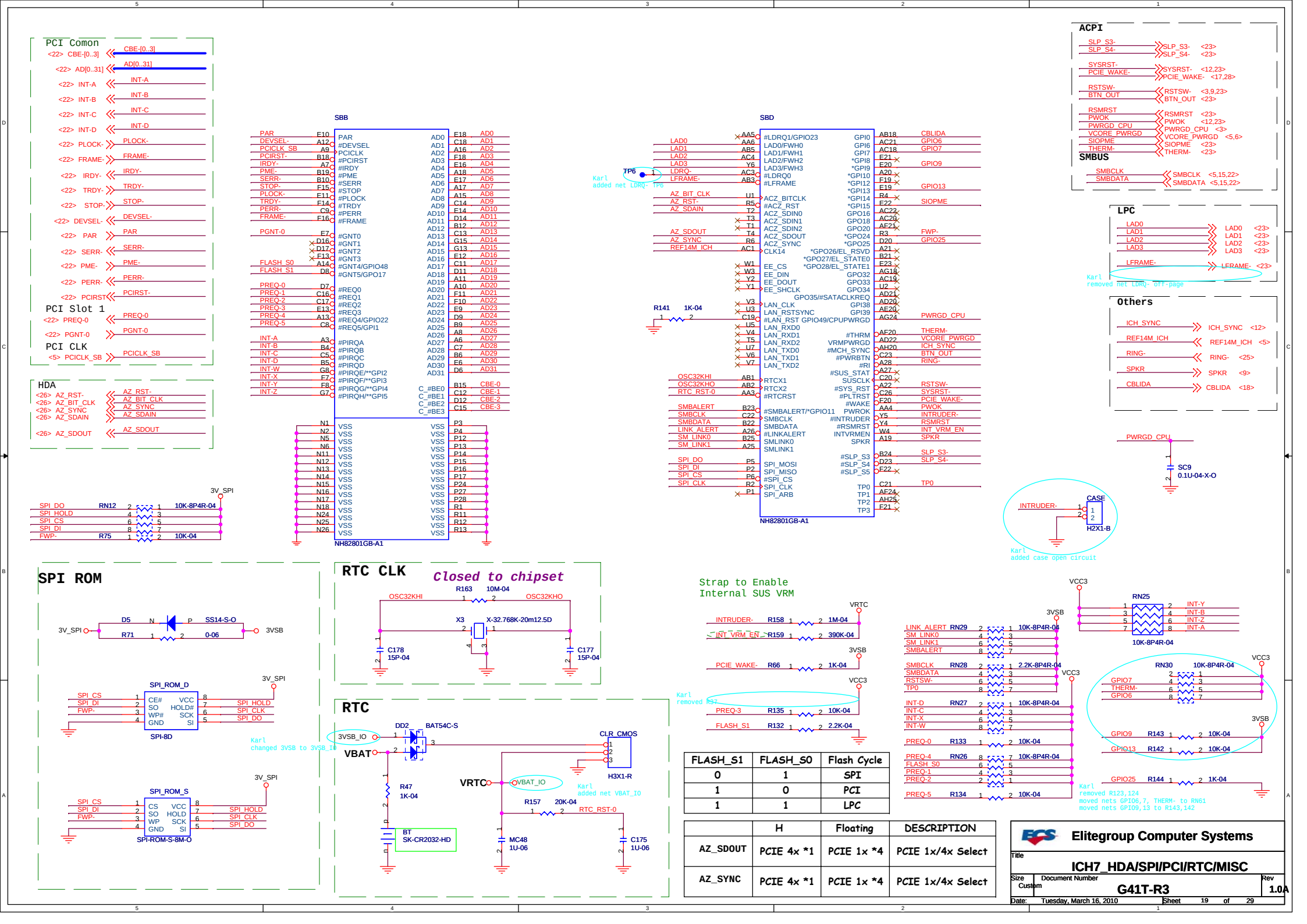
PCI Comon
PCI Slot 1
PCI CLK
HDA
SBB
SBD
ACPI
SMBUS
LPC
Others
SPI ROM
RTC CLK
Closed to chipset
RTC
Strap to Enable
Internal SUS VRM
NH82801GB-A1
SPI-8D
SPI-ROM-S-8M-O
H2X1-B
H3X1-R
BT
SK-CR2032-HD
FLASH_S1 FLASH_S0 Flash Cycle
0 1 SPI
1 0 PCI
1 1 LPC
H Floating DESCRIPTION
AZ_SDOUT PCIE 4x *1 PCIE 1x *4 PCIE 1x/4x Select
AZ_SYNC PCIE 4x *1 PCIE 1x *4 PCIE 1x/4x Select
Elitegroup Computer Systems
ICH7_HDA/SPI/PCI/RTC/MISC
G41T-R3
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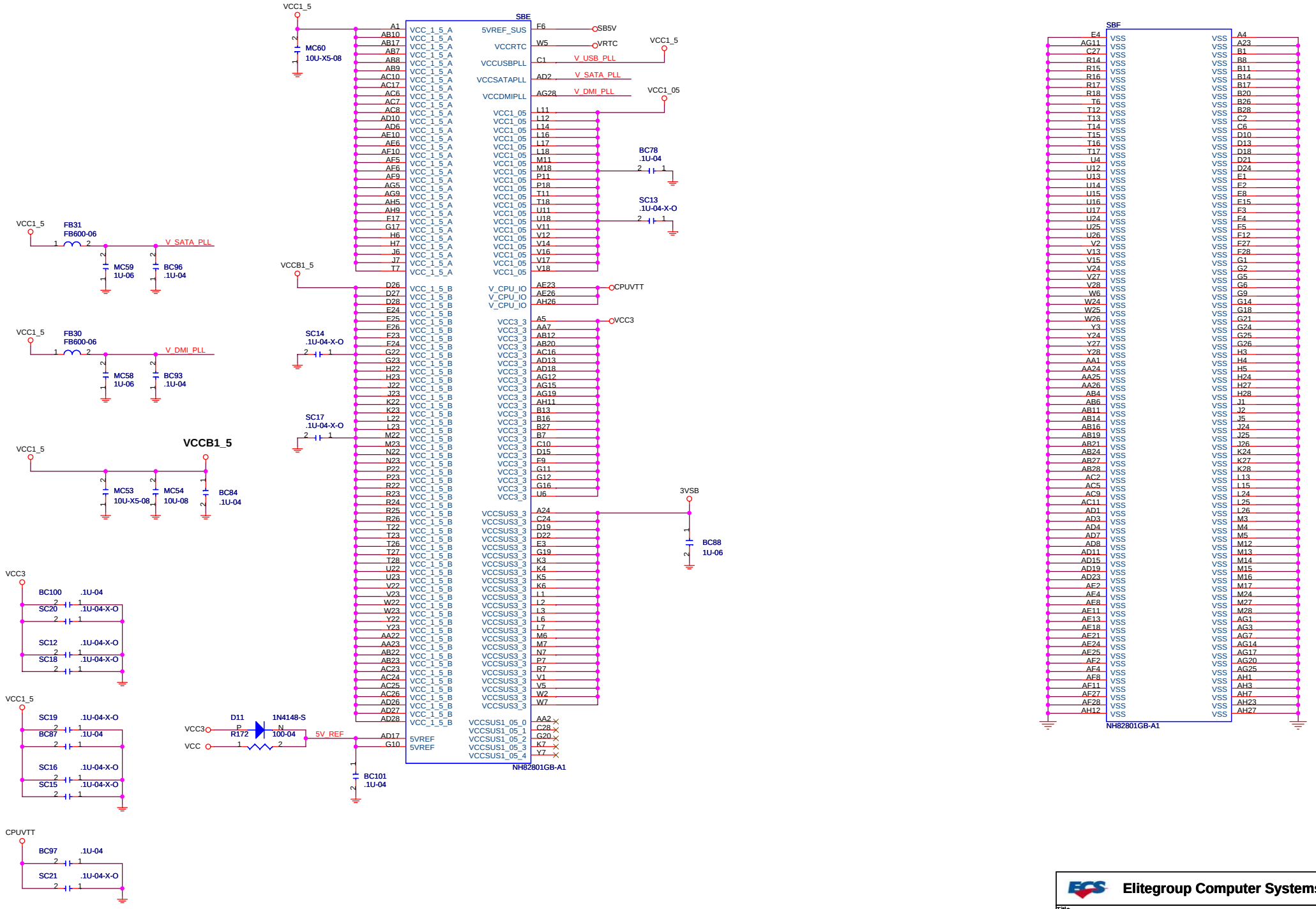

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Title: ICH7_POWER/GND

Size: Custom | Document Number: G41T-R3 | Rev: 1.0A

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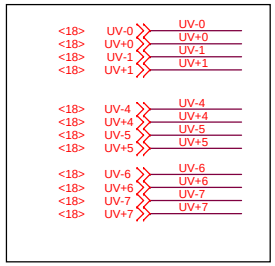
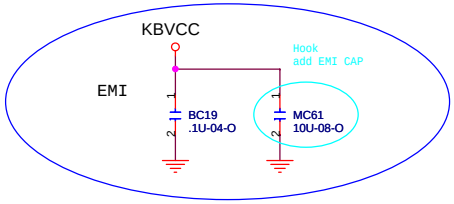
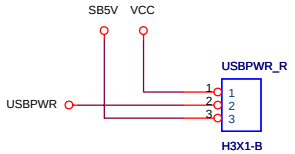
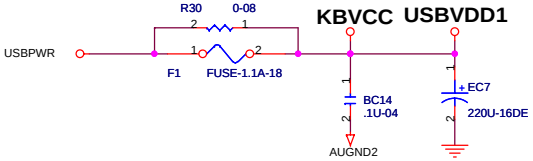
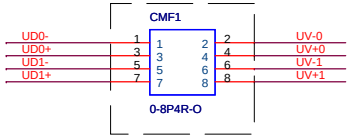




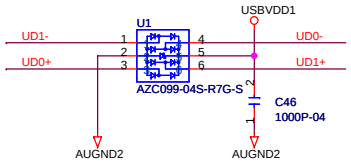


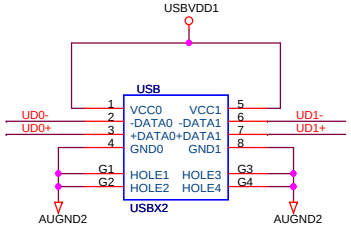
Jumper Short(1-2)

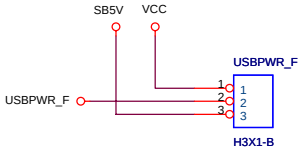




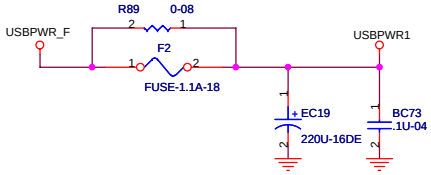
Close to USB connector

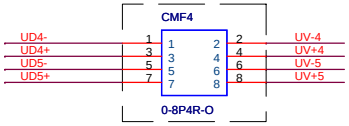




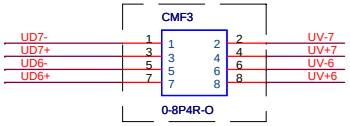


Jumper Short(1-2)

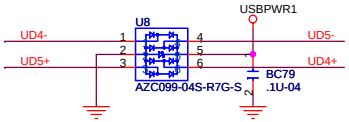


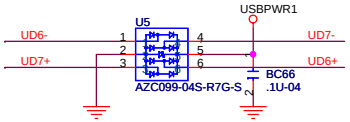


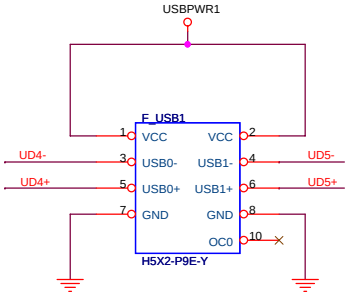
Close to header(F_USB1)

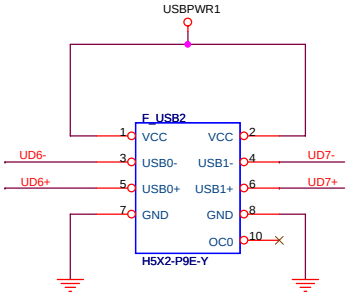


Close to header(F_USB2)









Elitegroup Computer Systems

Title: USB		
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PCI Comon

<19> CBE-[0..3] — CBE-[0..3]
<19> AD[0..31] — AD[0..31]
<19> INT-A — INT-A
<19> INT-B — INT-B
<19> INT-C — INT-C
<19> INT-D — INT-D
<19> IRDY- — IRDY-
<19> SERR- — SERR-
<19> DEVSEL- — DEVSEL-
<19> FRAME- — FRAME-
<19> TRDY- — TRDY-
<19> STOP- — STOP-
<19> PAR — PAR
<19> PME- — PME-
<19> PLOCK- — PLOCK-
<19> PERR- — PERR-
<5,15,19> SMBCLK — SMBCLK
<5,15,19> SMBDATA — SMBDATA
<19> PCIRST- — PCIRST-

PCI Slot 1

<19> PREQ-0 — PREQ-0
<19> PGNT-0 — PGNT-0
<5> PCICLK0 — PCICLK0

PCI Slot 1

PCI

B1	-12V	TRST	A1
B2	TCK	+12V	A2
B3	GND	TMS	A3 — TMS
B4	TDO	TDI	A4 — TDI
B5	+5V	+5V	A5
B6	+5V	INTA	A6 — INT-A
B7	INTB	INTC	A7 — INT-C
B8	INTD	+5V	A8
B9	PRSNT1	NC1	A9
B10	NC3	+5V	A10
B11	PRSNT2	NC2	A11
B12	GND	GND	A12
B13	GND	GND	A13
B14	NC4	3VAUX	A14 — 3VSB
B15	GND	RST	A15 — PCIRST-
B16	CLK	+5V	A16 — PCICLK0
B17	GND	GNT	A17 — PGNT-0
B18	REQ	GND	A18 — PREQ-0
B19	+5V	PME	A19 — PME-
B20	AD31	AD30	A20 — AD30
B21	AD29	+3.3V	A21 — AD31
B22	GND	AD28	A22 — AD28
B23	AD27	AD26	A23 — AD27
B24	AD25	GND	A24 — AD25
B25	+3.3V	AD24	A25 — AD24
B26	C/BE3	IDSEL	A26 — IDSEL_20 — R72 1 330-04 2
B27	AD23	+3.3V	A27 — AD23
B28	GND	AD22	A28 — AD22
B29	AD21	AD20	A29 — AD21
B30	AD19	GND	A30 — AD19
B31	+3.3V	AD18	A31 — AD18
B32	AD17	AD16	A32 — AD17
B33	C/BE2	+3.3V	A33 — CBE-2
B34	GND	FRAME	A34 — FRAME-
B35	IRDY	GND	A35 — IRDY-
B36	+3.3V	TRDY	A36 — TRDY-
B37	DEVSEL	GND	A37 — DEVSEL-
B38	GND	STOP	A38 — STOP-
B39	LOCK	+3.3V	A39 — PLOCK-
B40	PERR	SDONE	A40 — PERR-
B41	+3.3V	SBO	A41 — SMBCLK, SMBDATA
B42	SERR	GND	A42 — SERR-
B43	+3.3V	PAR	A43 — PAR
B44	C/BE1	AD15	A44 — CBE-1
B45	AD14	+3.3V	A45 — AD14
B46	GND	AD13	A46 — AD13
B47	AD12	AD11	A47 — AD12
B48	AD10	GND	A48 — AD10
B49	GND	AD9	A49 — AD9
B52	AD8	C/BE0	A52 — CBE-0
B53	AD7	+3.3V	A53 — AD7
B54	+3.3V	AD6	A54 — AD6
B55	AD5	AD4	A55 — AD5
B56	AD3	GND	A56 — AD3
B57	GND	AD2	A57 — AD2
B58	AD1	AD0	A58 — AD0
B59	+5V	+5V	A59
B60	ACK64	REQ64	A60 — PREQ64-1
B61	+5V	+5V	A61
B62	+5V	+5V	A62

PCI-W

IDSEL=AD20
INT[A,B,C,D]

RN14 — 10K-8P4R-04: SERR-, PERR-, PLOCK-, STOP-
RN13 — 10K-8P4R-04: FRAME-, IRDY-, TRDY-, DEVSEL-
RN24 — 10K-8P4R-04: PREQ64-1, PACK64-1, TDI, TMS

EC14 1000U-6.3DL-J
EC16 470U-6.3DE

Please place close to PCI1 and PCI2 Slot.

Close PCI Slot.

EMI — PCICLK0 — C82 100P-04-O

每個 PCI 插槽 pin B62 各放一顆 — C85 .1U-04-O
每個 PCI 插槽 pin A33 各放一顆 — C105 .1U-04
每個 PCI 插槽 pin A14 各放一顆 — C83 .1U-04

Elitegroup Computer Systems

Title	PCI Slot	
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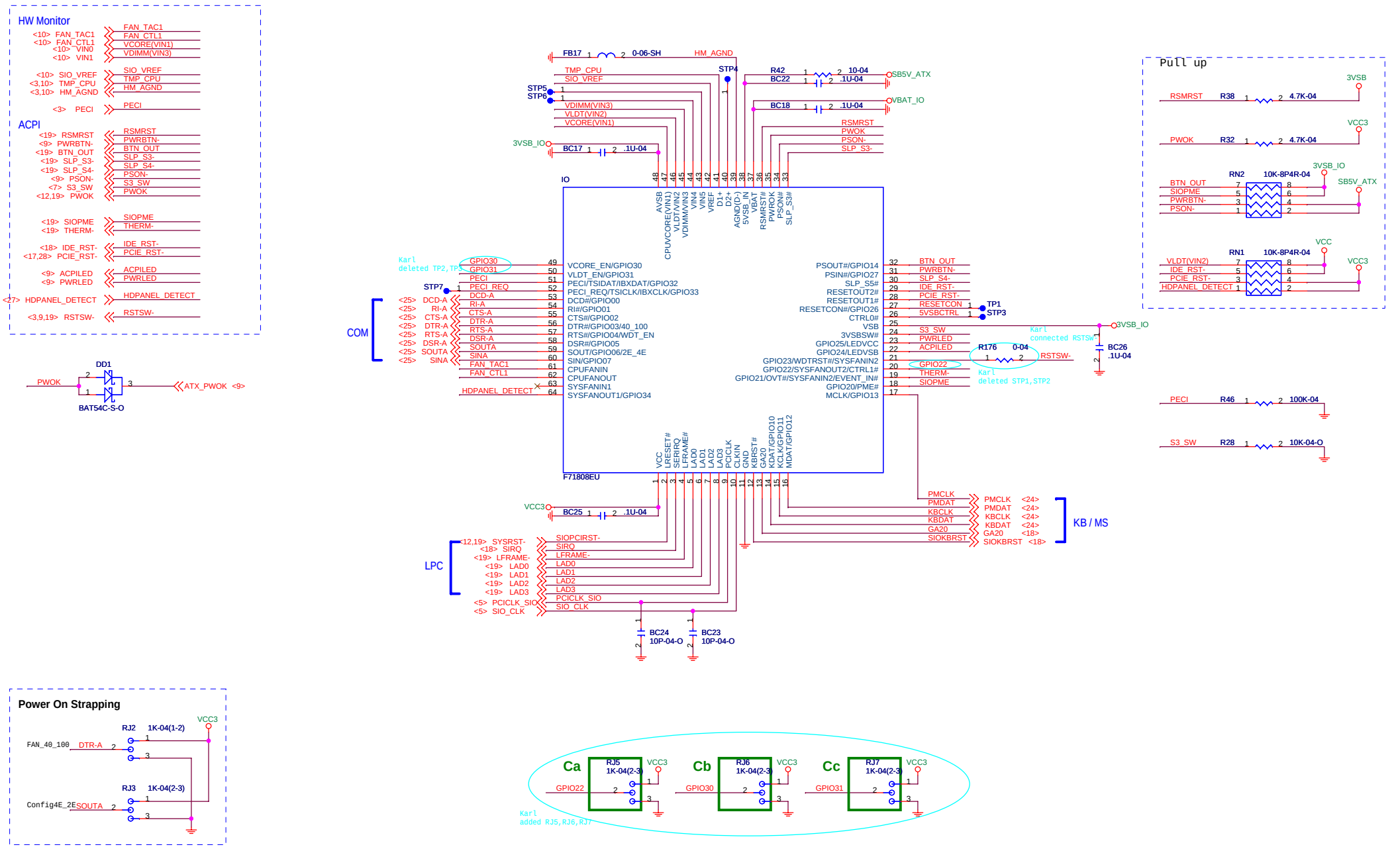

Power On Strapping Options

PIN NO.	Symbol	value	Description
Pin 56	FAN_40_100	1	FAN power on speed is the last programmed value
		0	FAN power on speed is 100%
Pin 59	Config4E_2E	1	Configuration Register I/O port is 4E. (Default)
		0	Configuration Register I/O port is 2E.

Location	For 95W CPU	For 65W CPU
Ca	1K-04(2-3) LOW	1K-04(1-2) High
Cb	reserved	reserved
Cc	reserved	reserved

Elitegroup Computer Systems

Title: Super I/O (IT8758)

Size: Custom | Document Number: G41T-R3 | Rev: 1.0A

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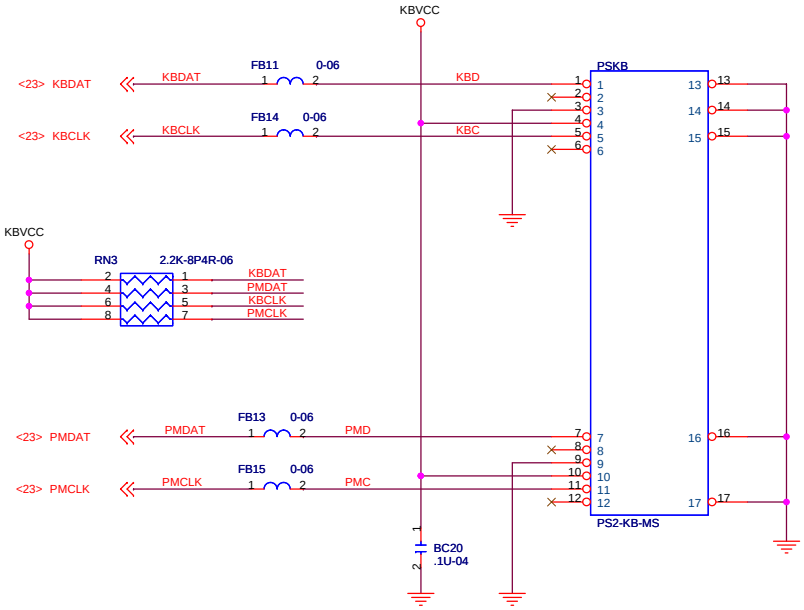

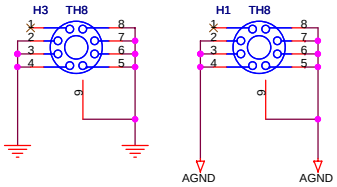

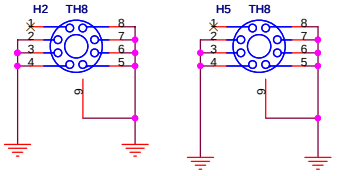

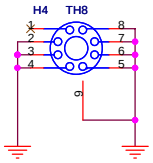

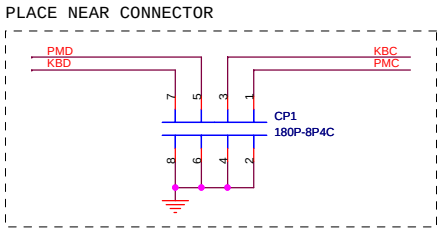

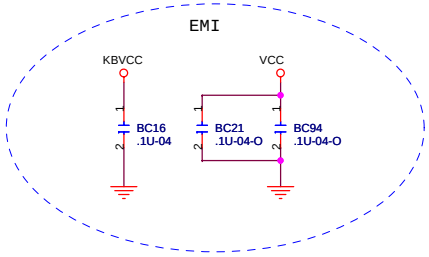

Elitegroup Computer Systems

Title: KB/MS/FDD/HOLE

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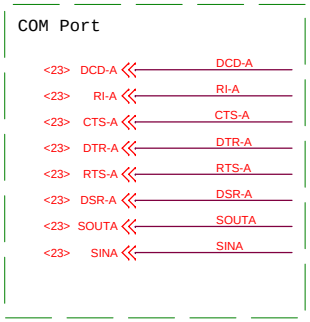
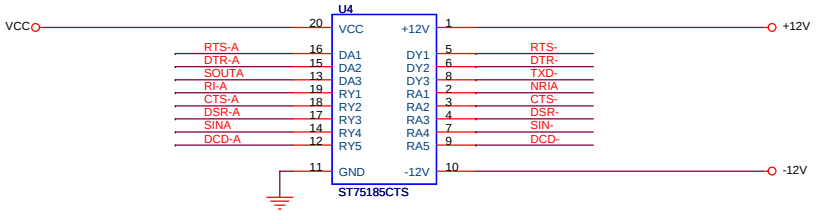
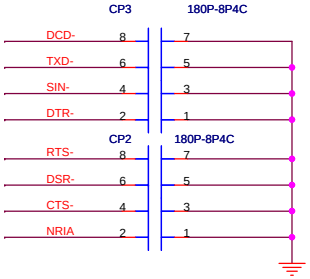
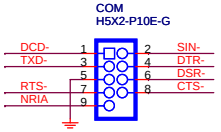








For 104

SPI_ROM_D(104)

SPI-ROM-D-8M

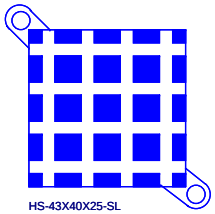
CLR_CMOS1(1-2)

JP-R

USBPWR_R(1-2)

JP-Y

NB(104)



HS-43X40X25-SL

If need 13mm hight :
HS-FCBGA-L38H13-S

For 103

X3(wire)

JP-WI-P6.25

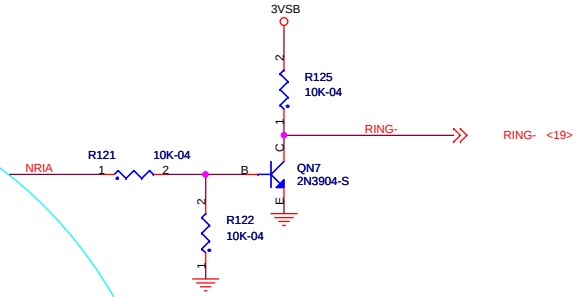
BT(104)

CR2032

USBPWR_F(1-2)

JP-Y

Karl
changed ref name from X1 to X3



PCB Impedance control

Impedance (ohm)	Trace Width (mil) (S/W/S)	Trace Length (inch)	Pre-preg
60	5 (20/5/20)	6	2116
50	4 (50/4/50)	6	1080
42	6 (50/6/50)	6	1080

1)Circuit type 1

Layer 1:TOP

Layer 2:PWR

Layer 3:GND

Layer 4:BOTTOM

Karl
removed LPT

Elitegroup Computer Systems

Title: COM / Others

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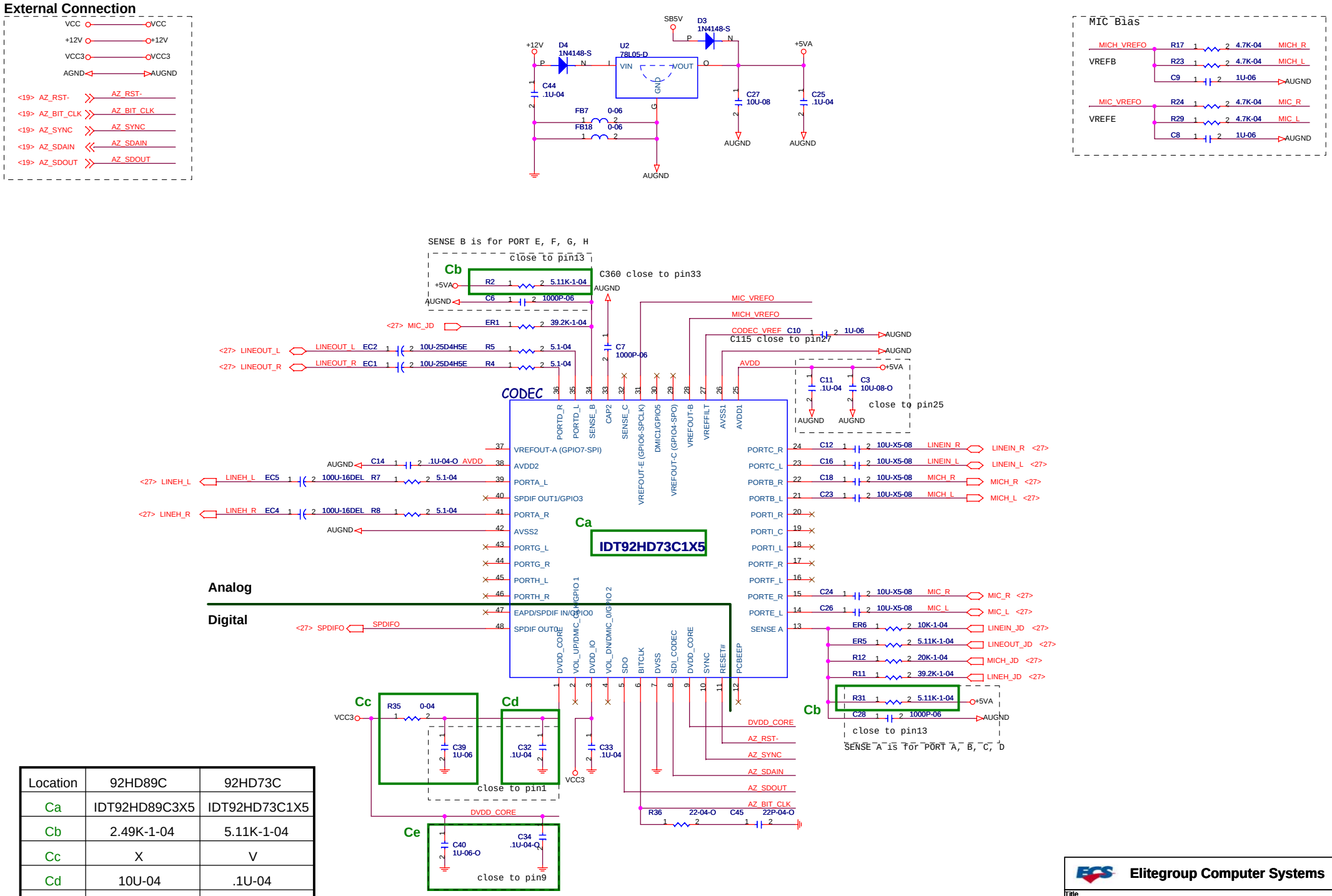

Location	92HD89C	92HD73C
Ca	IDT92HD89C3X5	IDT92HD73C1X5
Cb	2.49K-1-04	5.11K-1-04
Cc	X	V
Cd	10U-04	.1U-04
Ce	V	X

Elitegroup Computer Systems

Title: Audio Codec (IDT92HD73C)

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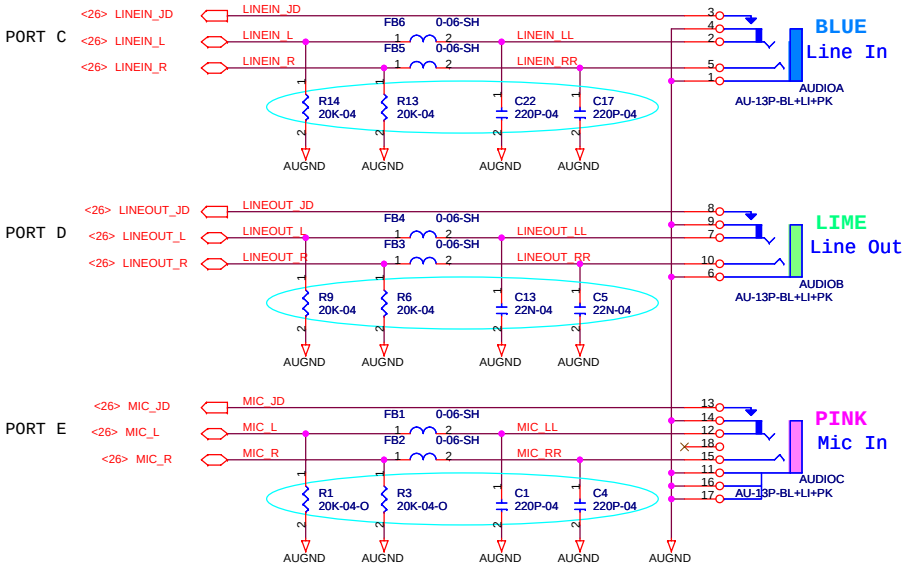
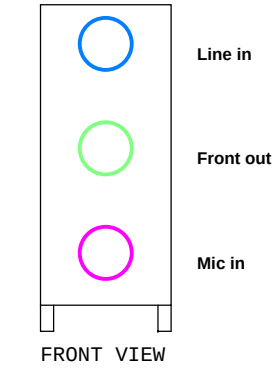
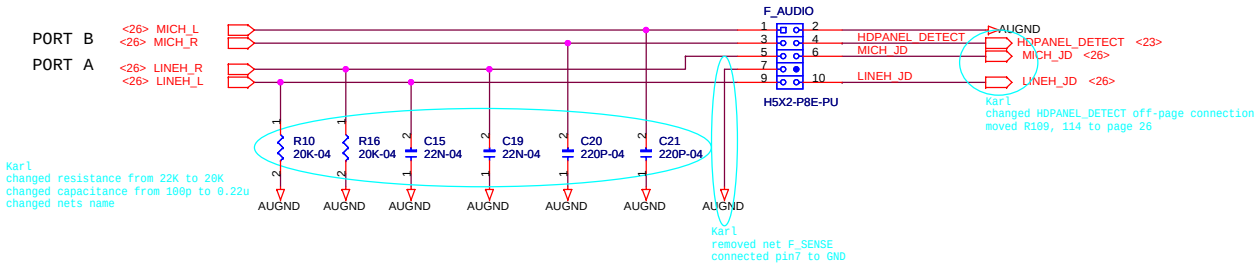
REAR-AUDIO

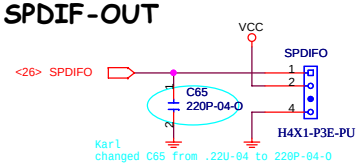




FRONT-AUDIO



SPDIF-OUT



Elitegroup Computer Systems		
Title: Audio Interface(3 Port HDA)		
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Date: Tuesday, March 16, 2010	Sheet 27 of 29	

External Connection

3VSB, VCC3, AUGND2

<18> PE3_OUT / <18> PE3_OUT- → LAN_HSIP / LAN_HSIN

<5> LAN_CLK / <5> LAN_CLK- → CK_PE_LANP / CK_PE_LANN

<18> PE3_IN / <18> PE3_IN- → LAN_HSOP / LAN_HSON

<17,19> PCIE_WAKE- → PCIE_WAKE_UP-

<17,23> PCIE_RST- → PCIE_LAN_RST-

<18> UV-2 / UV+2 / UV-3 / UV+3 → UV-2 / UV+2 / UV-3 / UV+3

新手提醒:
LAN_HSOP/N請接到SB的PCIE RX端
LAN_HSIP/N請接到SB的PCIE TX端
LAN_HSIP/N在SB的PCIE TX端要記得放AC coupling cap

for EMI

3VSB — C54 1000P-04-O — AUGND2

LXTAL1 / LXTAL2

R43 1M-04-O; X1 X-25M; C42 27P-04; C41 27P-04

LAN — JMC250B / JMC260 (LQFP 64)

3VSB, 3VSB

EESK/LINK, LAN_ACTIVE-, DVDD12, MDI0+, MDI0-, AVDD12, MDI1+, MDI1-, AVDD33, MDI2+, MDI2-, AVDD12, MDI3+, MDI3-

LED0, LED1, VDD, GND, VIP_1, VIN_1, AVDD12, VIP_2, VIN_2, GND, AVDD33, VIP_3(NC), VIN_3(NC), AVDD12(NC), VIP_4(NC), VIN_4(NC)

GND, NC31, NC30, SMB_SDA, TESTN, VDDIO, VDD, NC25, NC24, PHP_RESUMEN, SMB_SCL/LED2, CREQN, MPD, WAKEN, RSTN, AVDDTX

SMB_SDA → 3VSB, DVDD12; SMB_SCL → TP5; MPD → TP4; PCIE_WAKE_UP-; PCIE_LAN_RST-; AVDD12

REXT, VDDX33, XIN, XOUT, GND, LX, FB12, VDDREG, CLKN, CLKP, AVDDH, RXP, RXN, GND, TXN, TXP

R41 12K-1-06; AVDD33; LXTAL1; LXTAL2; LX; FB12; 3VSB

HSOP C73 .1U-16VX7-04 LAN_HSOP; HSON C72 .1U-16VX7-04 LAN_HSON

HSIN C71 .1U-16VX7-04 LAN_HSIN; HSIP C70 .1U-16VX7-04 LAN_HSIP

AVDD33; CK_PE_LANP; CK_PE_LANN

C48 10U-X5-08; C78 .1U-04

Close to pin 8

EEPROM

3VSB; C61 .1U-04

EEP: A0, A1, A2, GND, VCC, WP, SCL, SDA — CAT24C08WI-G

R49 4.7K-04; R50 4.7K-04; SMB_SCL; SMB_SDA

Enable D3E(2)

VCC3; R48 100K-04; MPD; C76 .1U-04

ESD for LAN

3VSB

SU2: MDI1+, MDI0+, MDI0-, MDI1- — AZC099-04S-R7G-S-X; C43 1000P-04

SU1: MDI3+, MDI2+, MDI2-, MDI3- — AZC099-04S-R7G-S-X; C29 1000P-04

ESD for USB

CMF2: UV+2, UV-2, UV+3, UV-3, UD+2, UD-2, UD+3, UD-3 — 0-8P4R-O

USBVDD1

U3: UD-2, UD+3, UD-3, UD+2 — AZC099-04S-R7G-S; C58 1000P-04

AUGND2, AUGND2

close to connector

Power Regulator

LX — L2 IND-4.7U — FB12 — AVDD12 — FB20 0-06 — DVDD12

C66 10U-X5-08; C35 .1U-04

Decoupling

DVDD12: C77 .1U-04, C38 .1U-04, C74 10U-X5-08 — close to pin 26, pin 17, pin 17

AVDD12: C75 .1U-04, C37 .1U-04, C64 .1U-04, C57 10U-16V-08 — close to pin 51, pin 62, pin 55, pin 55

3VSB: C79 .1U-04, C52 .1U-04, C68 .1U-04, C50 10U-X5-08 — close to pin 27, pin 45, pin 38, pin 38

AVDD33: C36 .1U-04, C49 .1U-04, C47 .1U-04, C51 10U-16V-08 — close to pin 2, pin 11, pin 59, pin 59

EESK/LINK / LAN_ACTIVE-

EESK/LINK — R45 330-04 — LINK

LAN_ACTIVE- — R37 330-04 — ACTIVE

AUGND2 — FB19 0-06-SH — FB12 0-06-SH

AVDD33 — FB16 0-06 — 3VSB

USBLAN

USBVDD1

USBLAN: VCC, -DATA1, +DATA1, GND, H_USB3, H_USB4, L_TCT, MDI0+, MDI0-, MDI1+, MDI1-, MDI2+, MDI2-, MDI3+, MDI3-, L_RCT, TCT(P01), TX1+, TX1-, TX2+, TX2-, TX3+, TX3-, TX4+, TX4-, RCT(P10), GLED(P11), OLED(P12), YLED(P13), VCC(P14), H_LAN1, H_LAN2, H_LAN3, H_LAN4, VCC, -DATA0, +DATA0, GND, H_USB1, H_USB2

UD-3, UD+3, UD-2, UD+2, AUGND2, 3VSB, LINK, ACTIVE

C53 .1U-04-O; R44 0-04; C56 .1U-04-O; C55 .01U-04; C59 .1U-04-O; C63 .1U-04-O

USBX2-LAN-100

Link: Green on
Active: Yellow blinking

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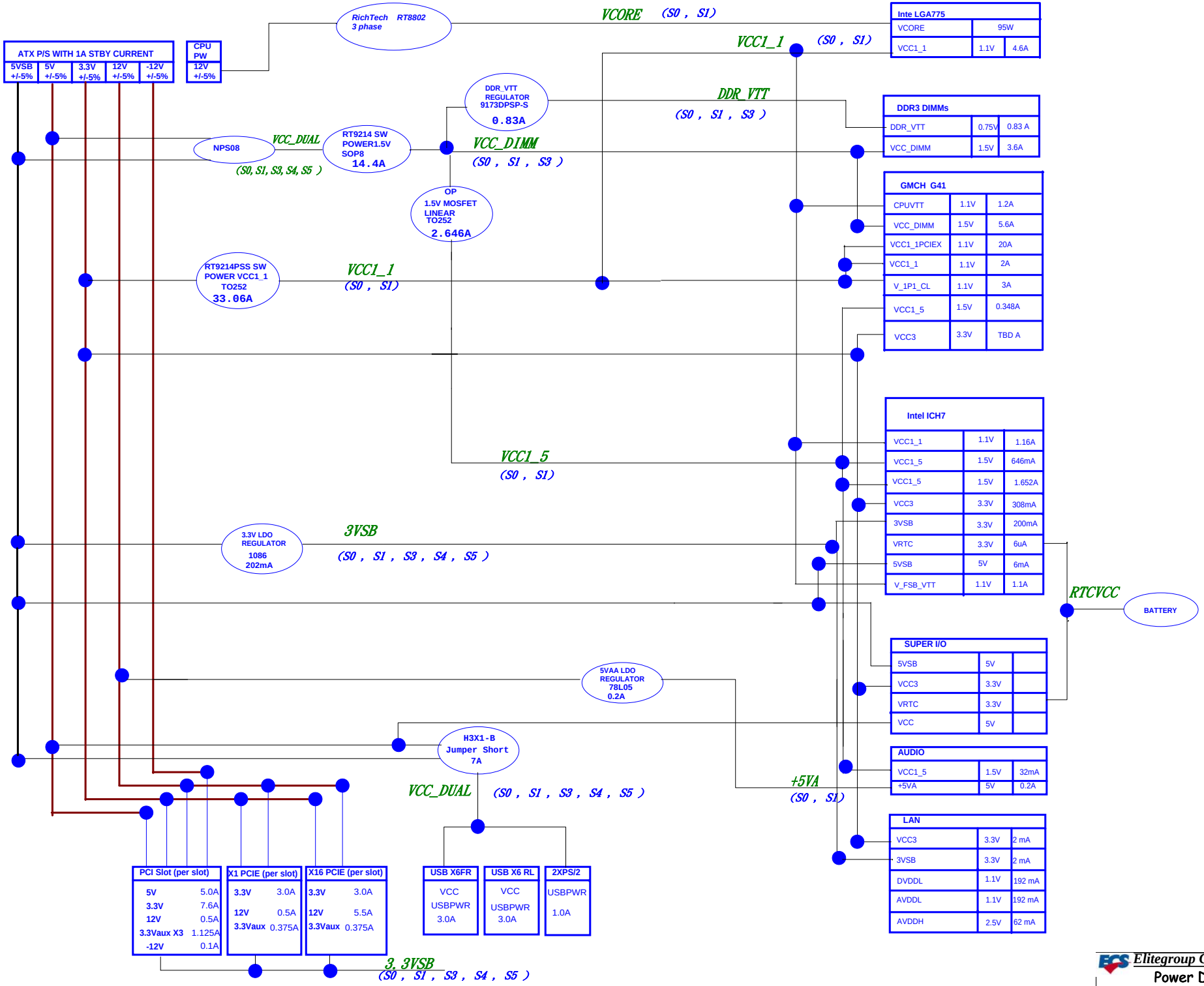

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Power Delivery Chart

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