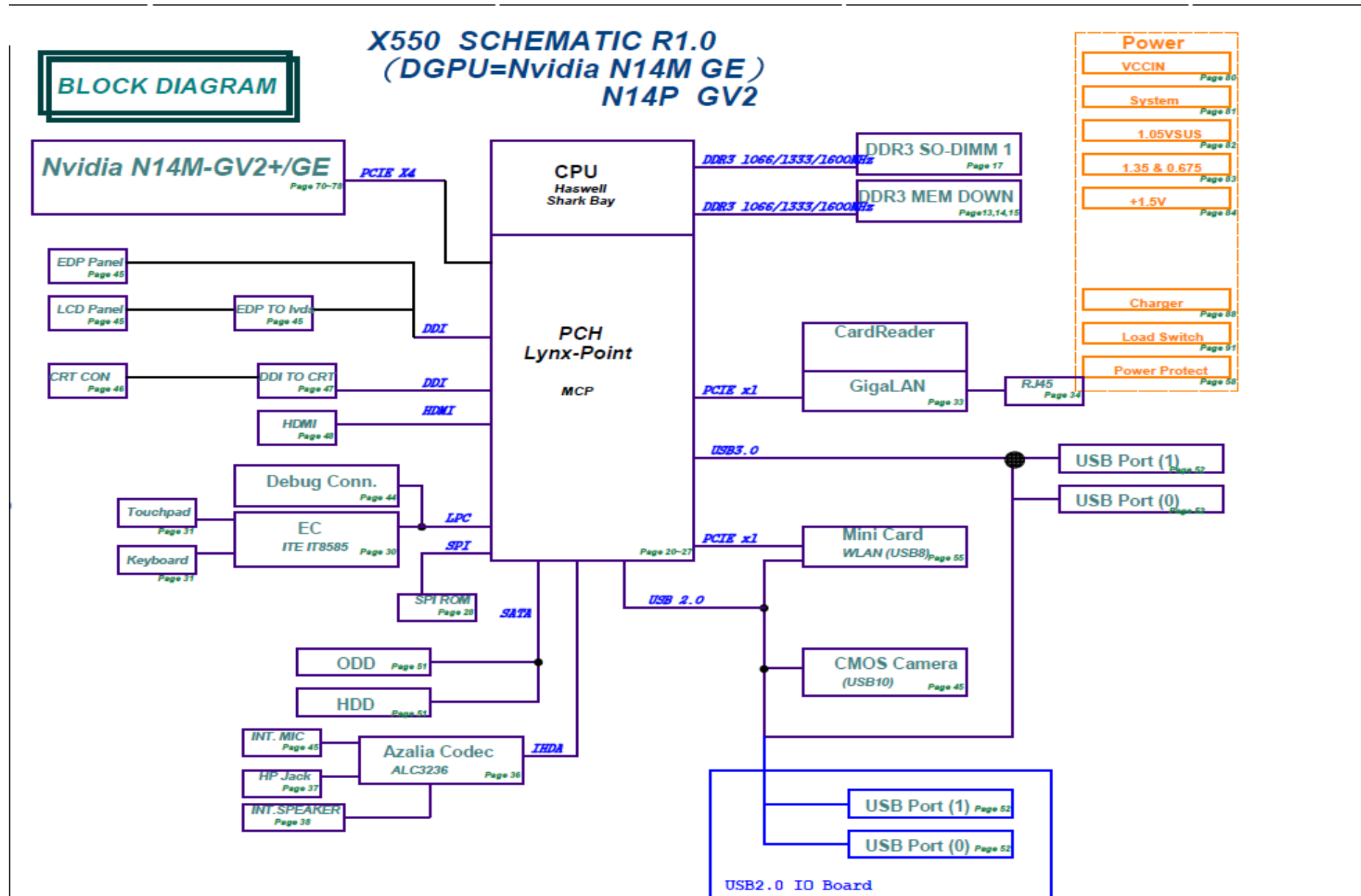
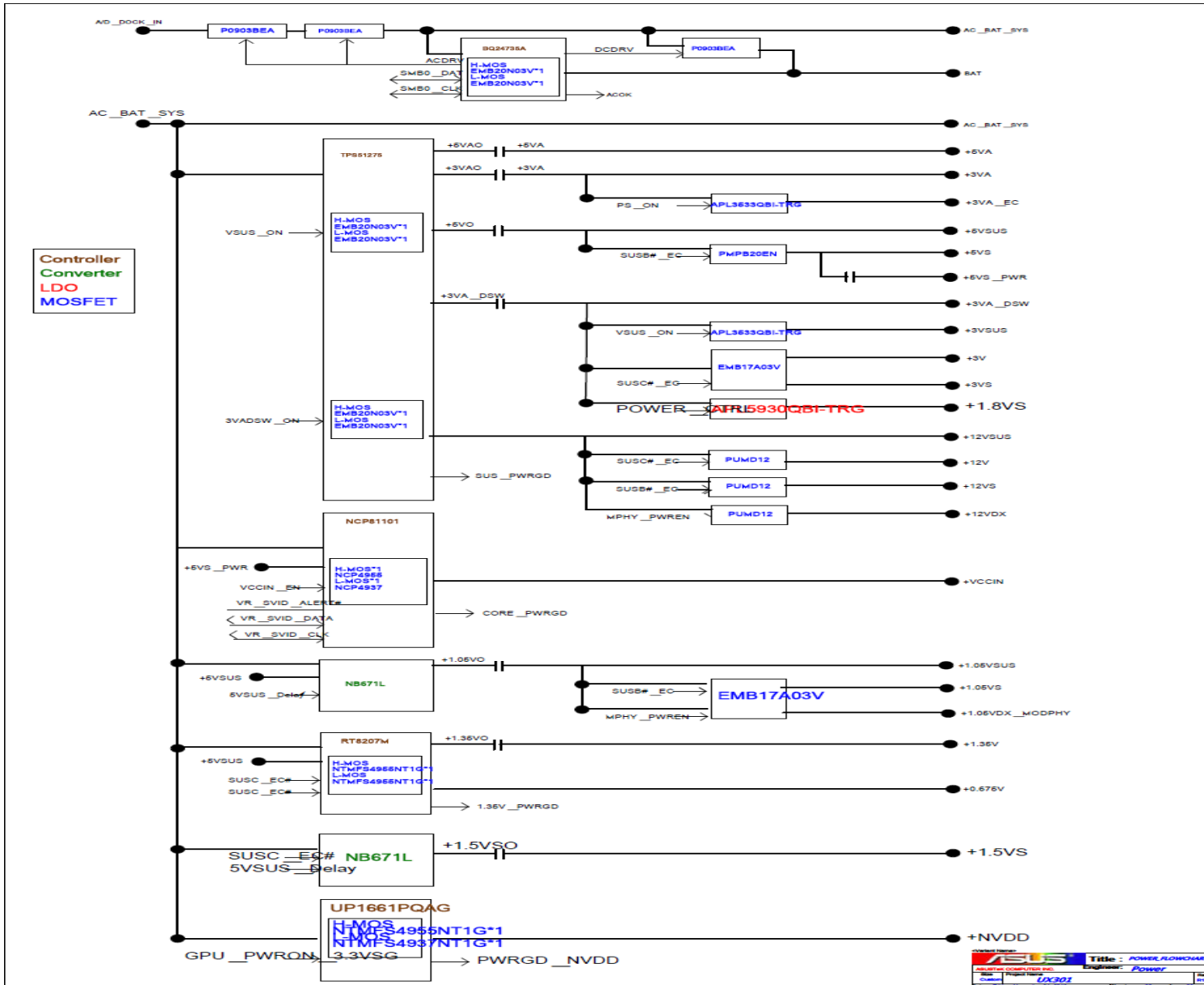


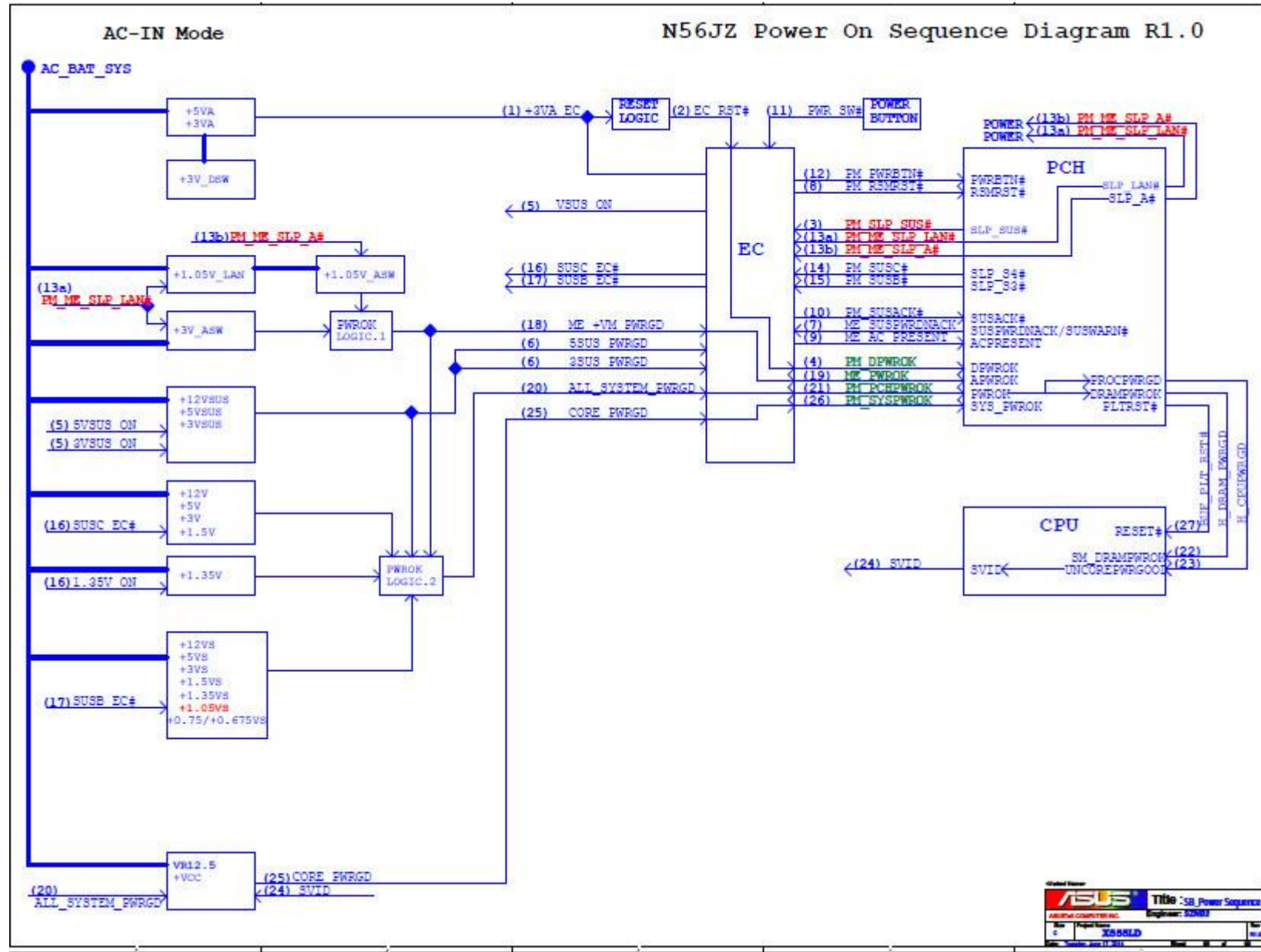
## BLOCK DIAGRAM



# POWER FLOW



# POWER ON SEQUENCE

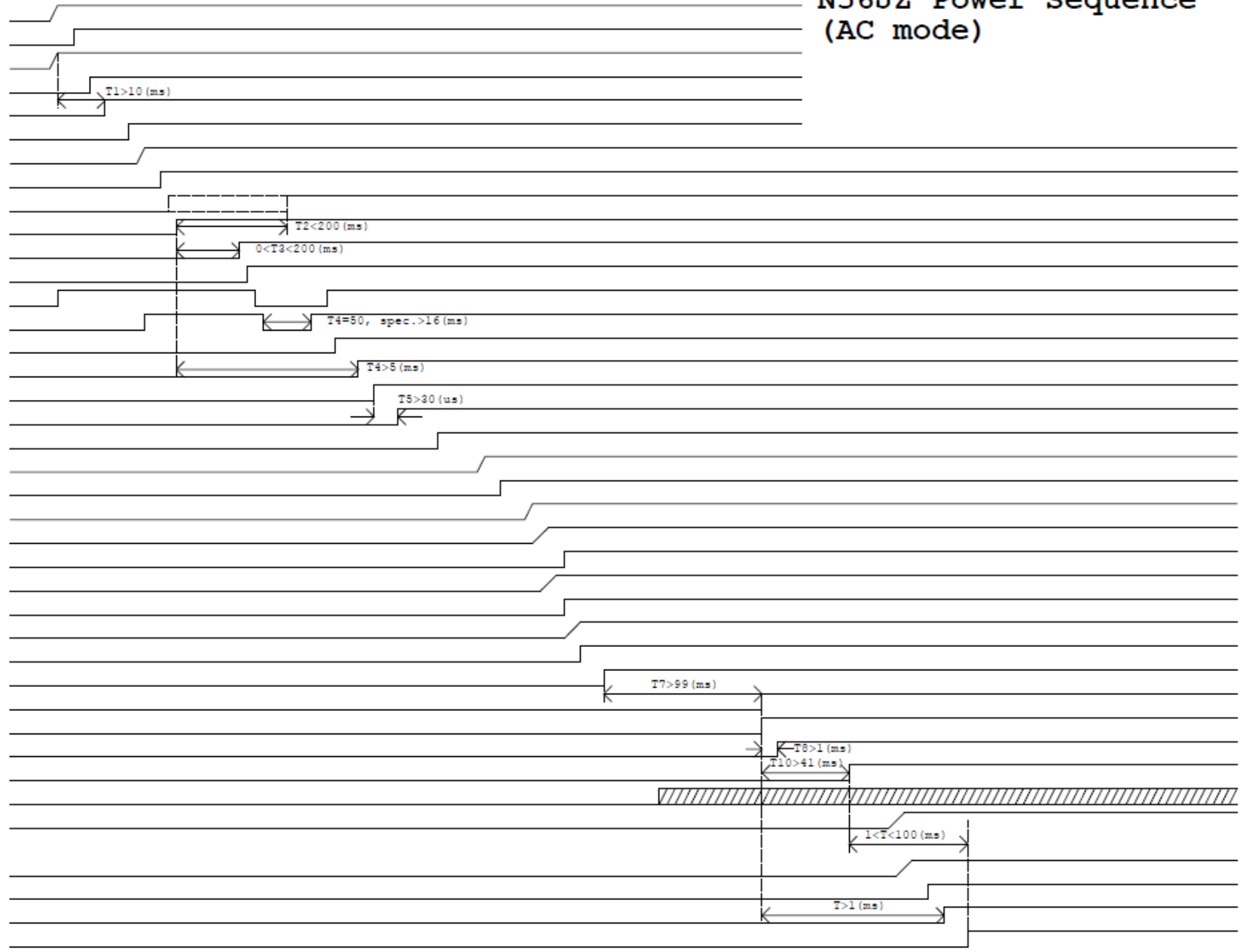


# AC POWER ON SEQUENCE

AC-IN Mode

N56JZ Power Sequence  
(AC mode)

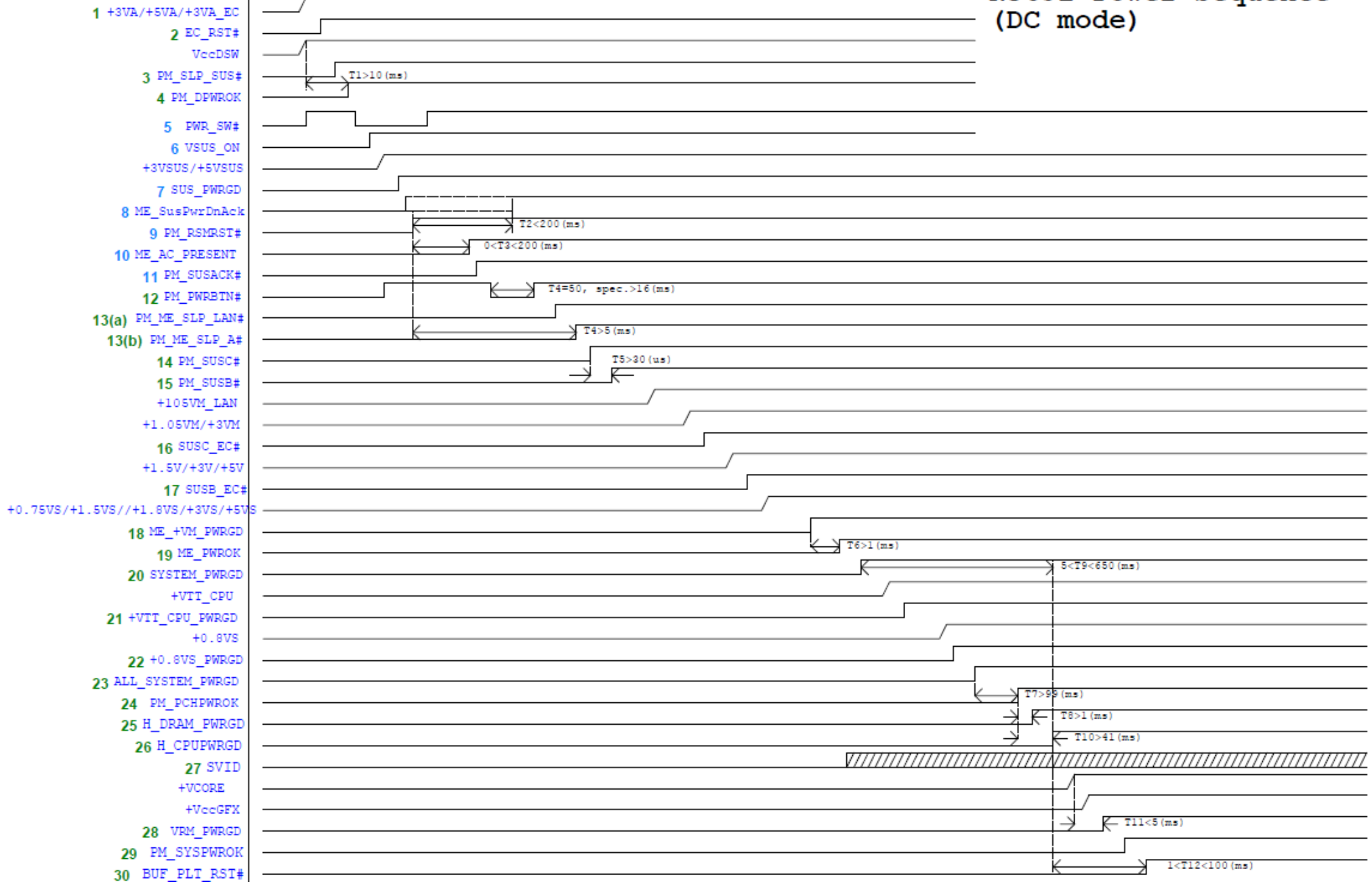
- 1 +3VA/+5VA/+3VA\_EC
- 2 EC\_RST#  
VccDSW
- 3 PM\_SLP\_SUS#
- 4 PM\_DPWROK
- 5 VSUS\_ON  
+3VSUS/+5VSUS  
SUS\_PWRGD
- 7 ME\_SusPwrDnAck
- 8 PM\_RSMRST#
- 9 ME\_AC\_PRESENT
- 10 PM\_SUSACK#
- 11 PWR\_SW#
- 12 PM\_PWRBTN#
- 13(a) PM\_ME\_SLP\_LAN#
- 13(b) PM\_ME\_SLP\_A#
- 14 PM\_SUSC#
- 15 PM\_SUSB#
- 16 S USC\_EC#  
+1.5V/+3V/+5V
- 17 S USB\_EC#  
+0.8V/+0.75V/+1.5V//+1.8V/+3V/+5V  
+PEX\_VDD/+1.5V/+1.8V/+3V/+NVDD
- 20 SYSTEM\_FWRGD  
+VTT\_CPU
- 21 +VTT\_CPU\_FWRGD  
+0.8V
- 22 +0.8V\_FWRGD
- 23 ALL\_SYSTEM\_FWRGD
- 24 PM\_PCHPWROK
- 25 PM\_SYSPWROK
- 26 H\_DRAM\_FWRGD
- 27 H\_CPUPWROK
- 28 SVID  
+V CORE
- +VccGFX
- 29 VRM\_FWRGD
- 30 SUS\_SATA#
- 31 BUF\_PLT\_RST#



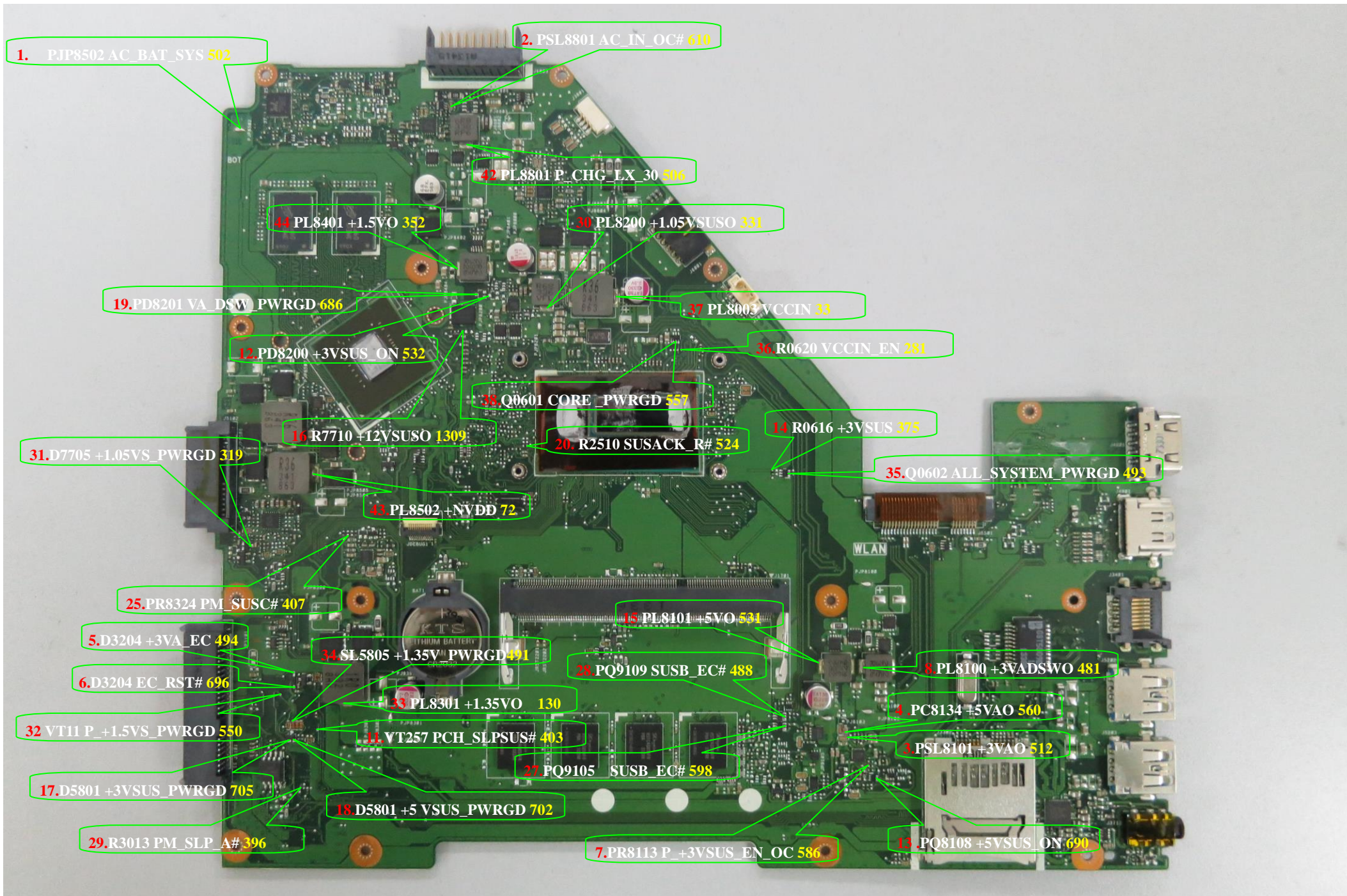
# DC POWER ON SEQUENCE

DC-IN Mode

N56JZ Power Sequence  
(DC mode)



# Signal Measure Point-Bottom



# Signal Measure Point-Top

