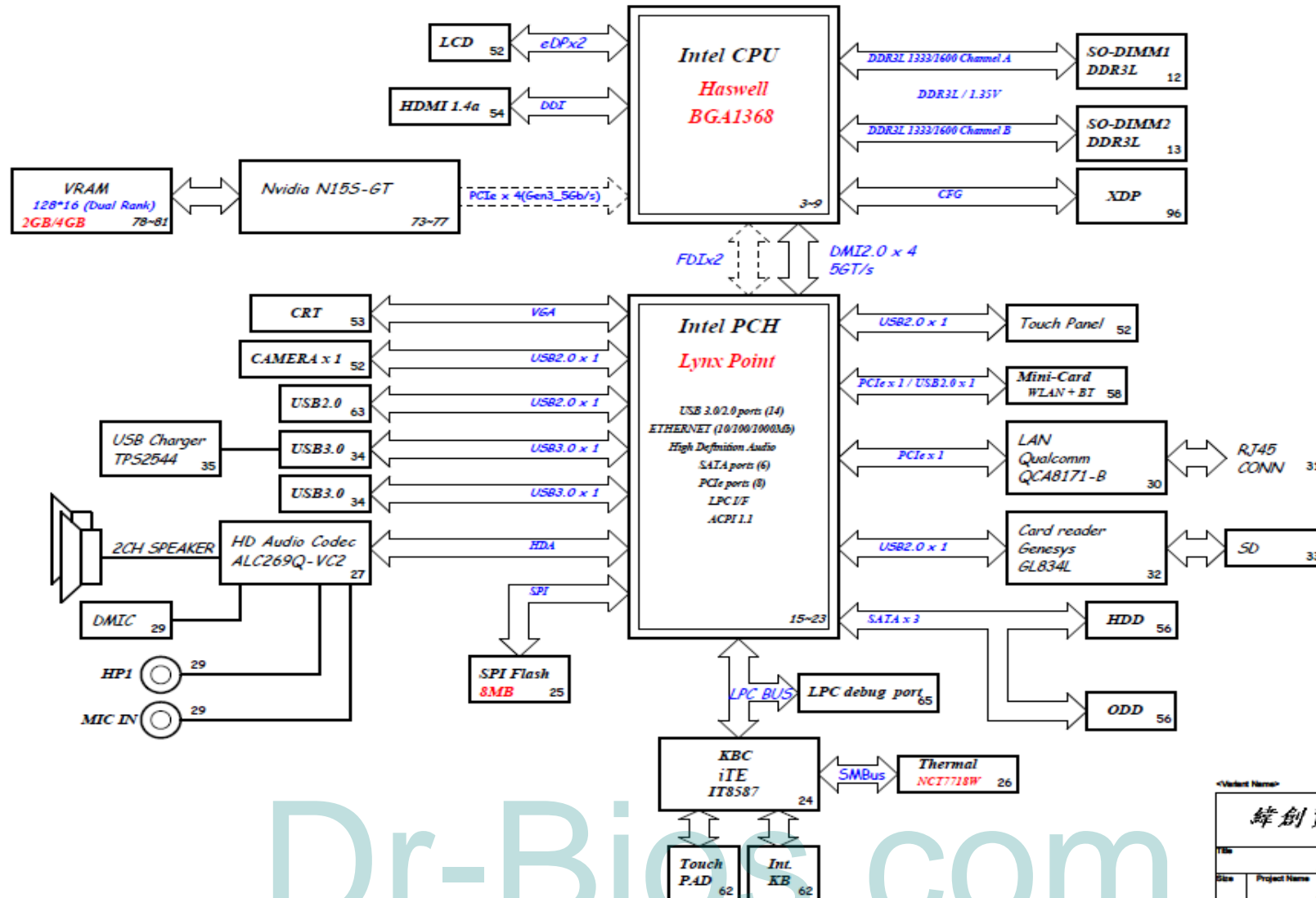


BLOCK DIAGRAM

SV41JN -HW Block Diagram

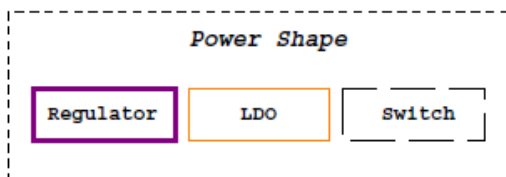
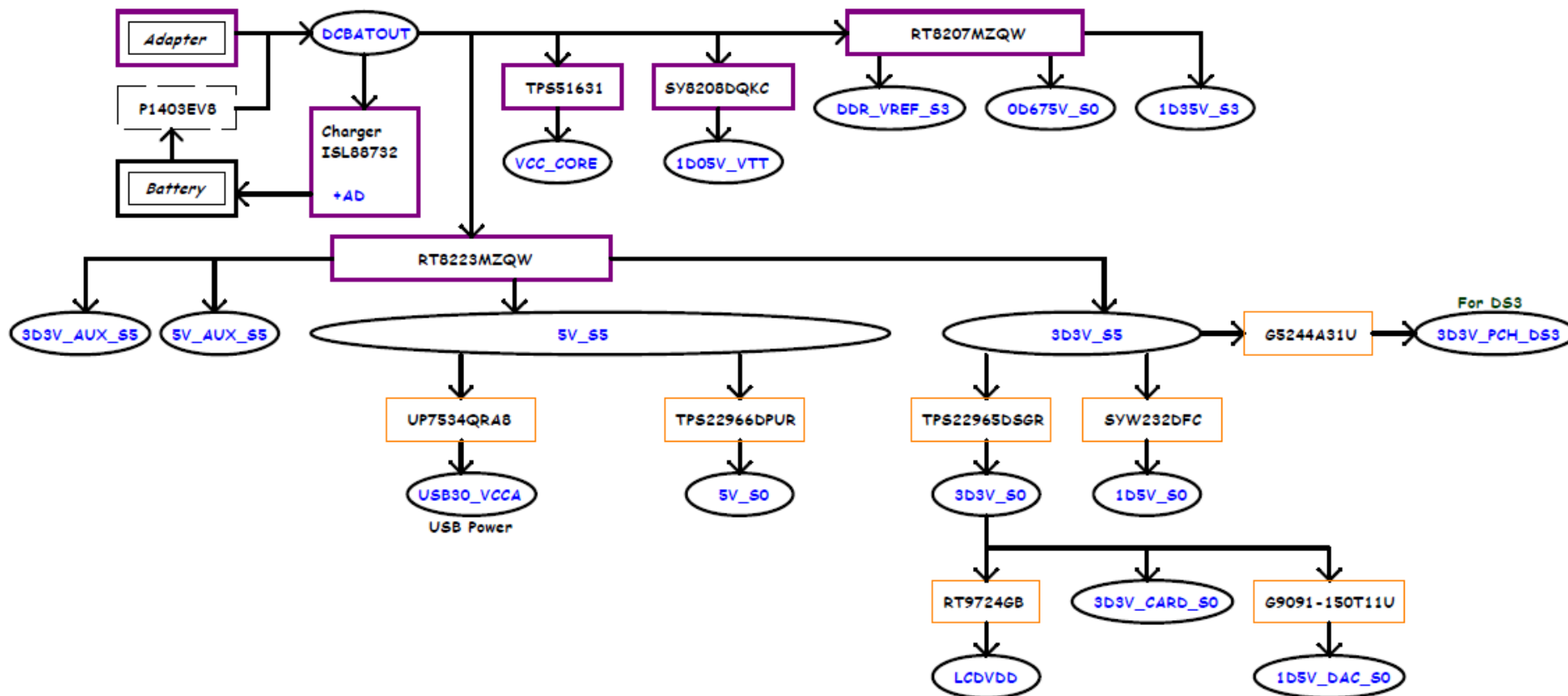
Project code : 4PD01P010001
 PCB number : 13320-SA
 Revision : SA



CHARGER	
ISL88732	44
INPUTS	OUTPUTS
DCBATOUT	BT+
SYSTEM DC/DC	
RT8223	45
INPUTS	OUTPUTS
DCBATOUT	5V_AUX_S5 303V_AUX_S5 5V_S5 303V_S5
CPU DC/DC	
TPS51631	46,47
INPUTS	OUTPUTS
DCBATOUT	VCC_CORE
SYSTEM DC/DC	
SY8208b	48
INPUTS	OUTPUTS
DCBATOUT	1005V_VTT
SYSTEM DC/DC	
RT8207LGQW	49
INPUTS	OUTPUTS
DCBATOUT	105V_S3 0075V_S0 DDR_VREF_S3

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POWER FLOW

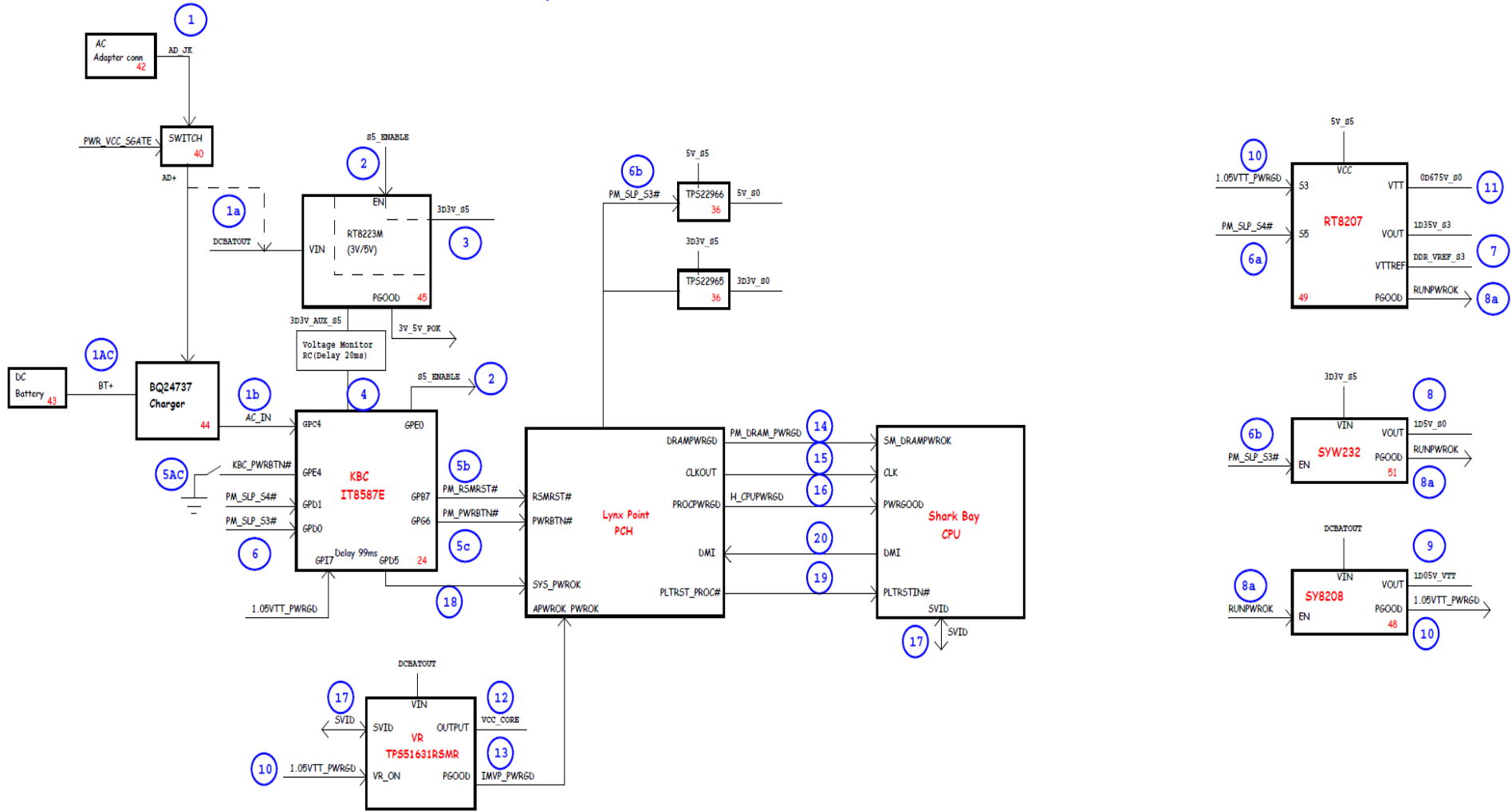


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21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichin, Taipei Hsien 221, Taiwan, R.O.C.	
File: Power block Diagram	
Size: Project Name	Rev: SA
SV41JN	

POWER ON SEQUENCE

SHARK BAY POWER UP SEQUENCE DIAGRAM



Signal Measure Point-Bottom



Signal Measure Point-Top



2.PQ4406 AC IN# 598

15 R913 1D05V_VTT 135

17.PG4608 0D576V_S0 132

9.R9604 PM_PWRBTN# 531

10.RP4634 PM_SLP_S4 514

14.C5805 1D5V_S0 386

12.R503 DDR_VREF_S3 435

26 R1707 DMI_RCOMP_R 771

21.RN1804 CLKOUT_DP_N 268

19.R3601 IMVP_PWRGD 423

8.D1701 PM_RSMRET# 791

7.R2404 KBC_PWRBTN# 986