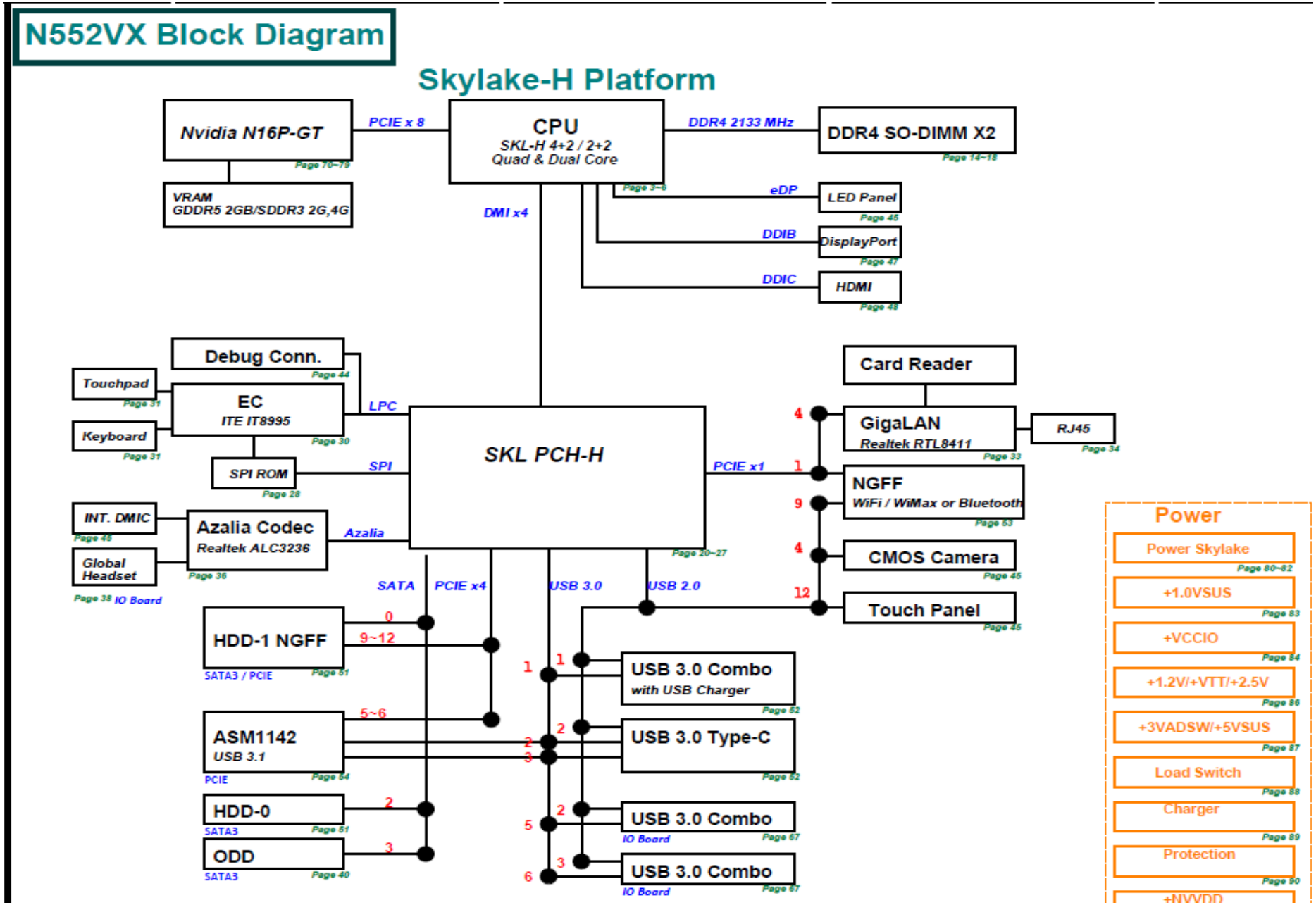
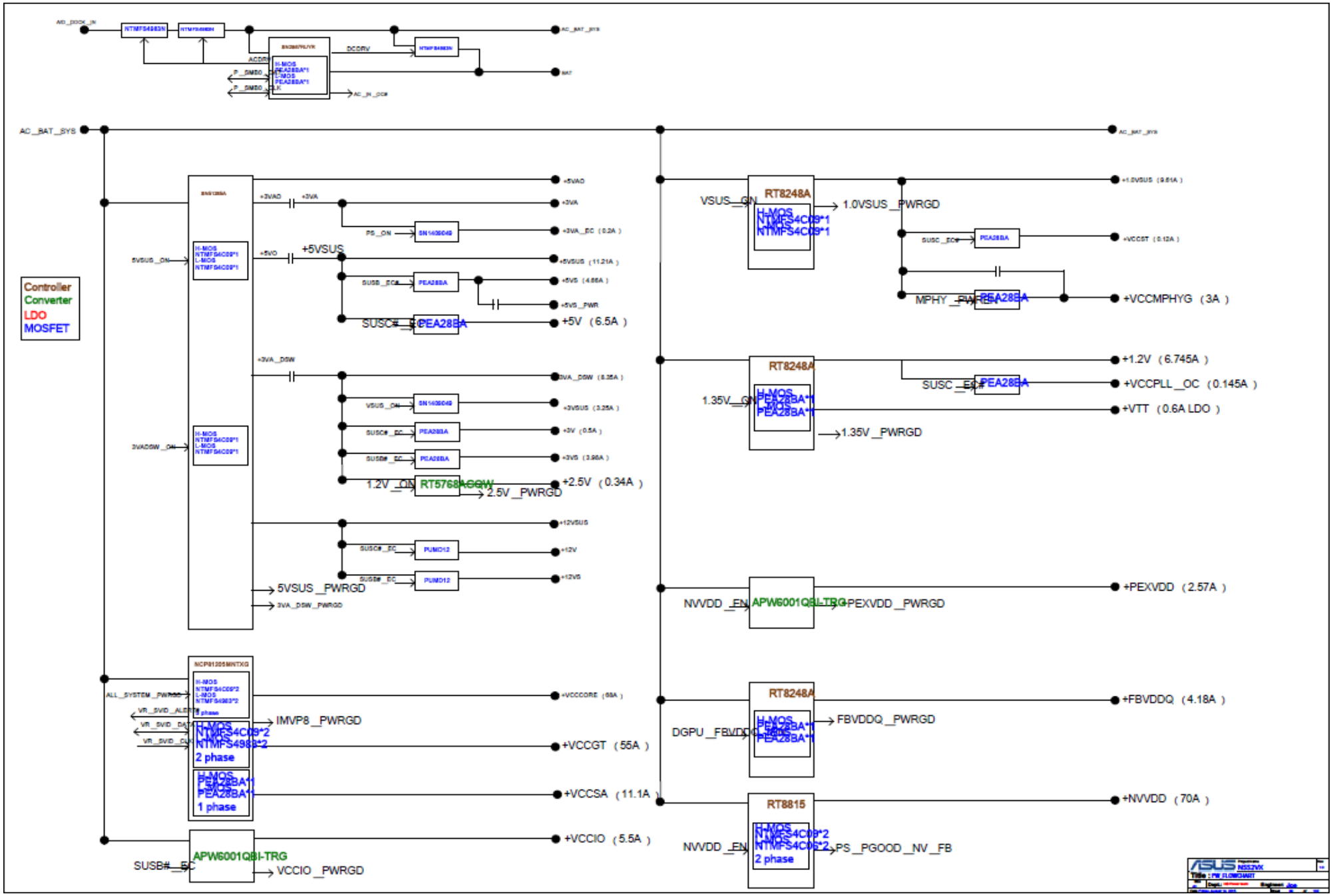
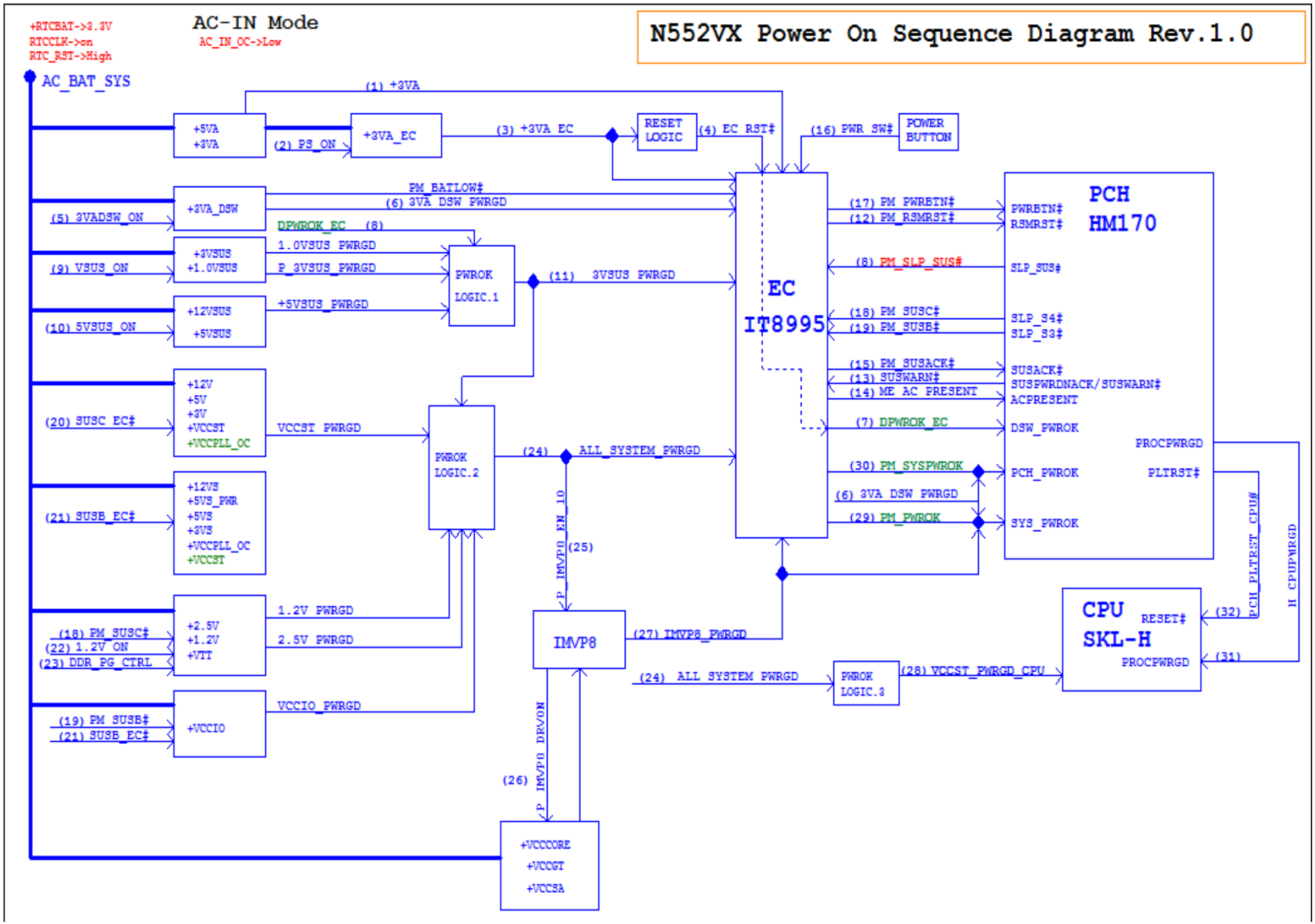
BLOCK DIAGRAM



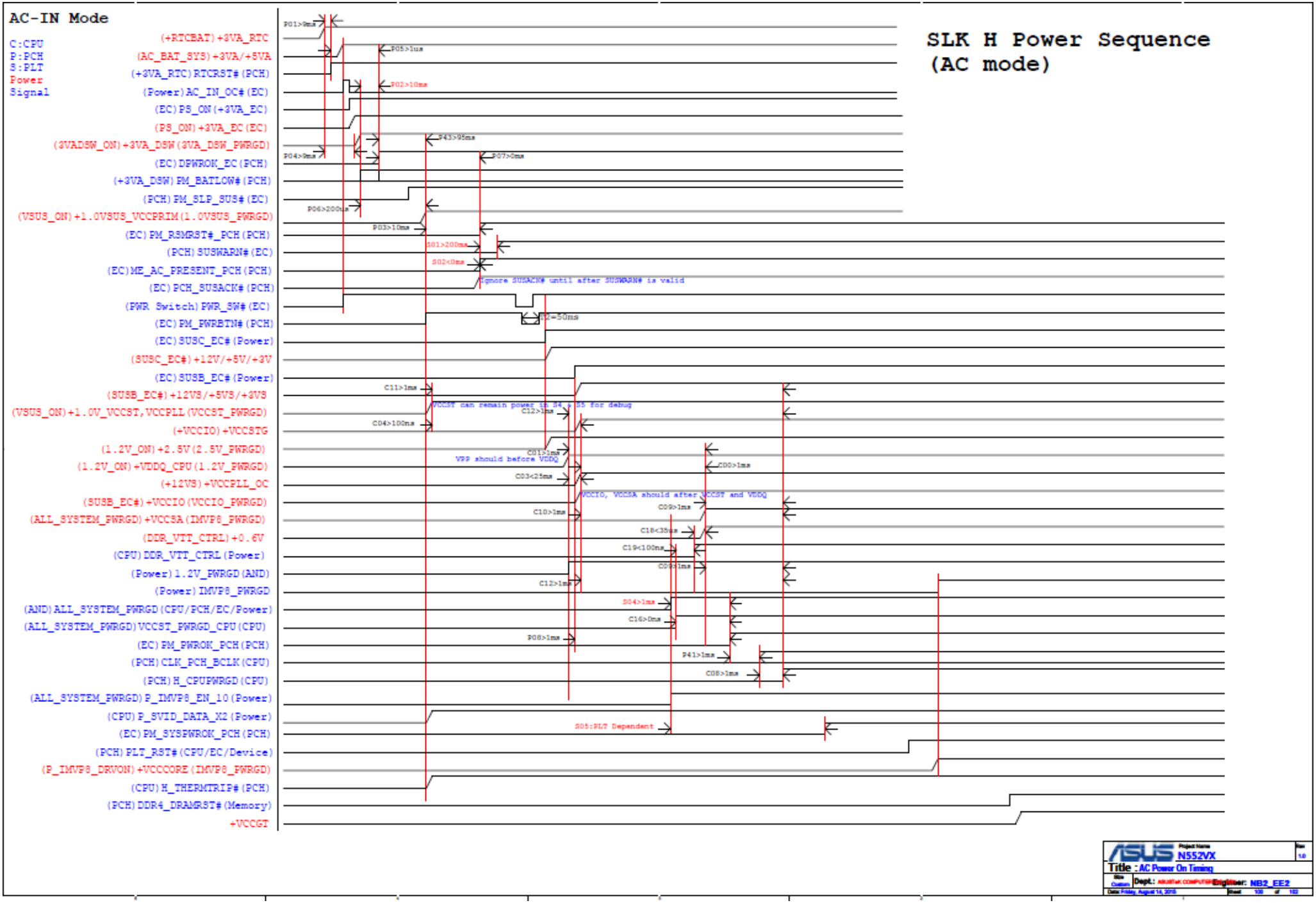
POWER FLOW


Controller
Converter
LDO
MOSFET
AC_BAT_SYS
+5VALW
+3VALW
+3VA_EC (0.2A)
+5VSUS (11.21A)
+5VS (4.66A)
+5V_PWR
+5V (6.5A)
+3VA_DSW (0.35A)
+3VSUS (3.25A)
+3V (0.5A)
+3VS (3.96A)
+2.5V (0.34A)
+12VSUS
+12V
+12VS
5VSUS_PWRGD
3VA_DSW_PWRGD
2.5V_PWRGD
RT8248A
1.0VSUS_PWRGD
+1.0VSUS (9.81A)
+VCCST (0.12A)
+VCCMPHYG (3A)
1.35V_PWRGD
+1.2V (6.745A)
+VCCPLL_OC (0.145A)
+VTT (0.6A LDO)
APW6001QBI-TRG
+PEXVDD (2.57A)
PEXVDD_PWRGD
+FBVDDQ (4.18A)
FBVDDQ_PWRGD
RT8815
+NVVDD (70A)
PS_PGOOD_NV_FB
IMVP8_PWRGD
+VCCGT (55A)
+VCCSA (11.1A)
+VCCIO (5.5A)
VCCIO_PWRGD
2 phase
1 phase

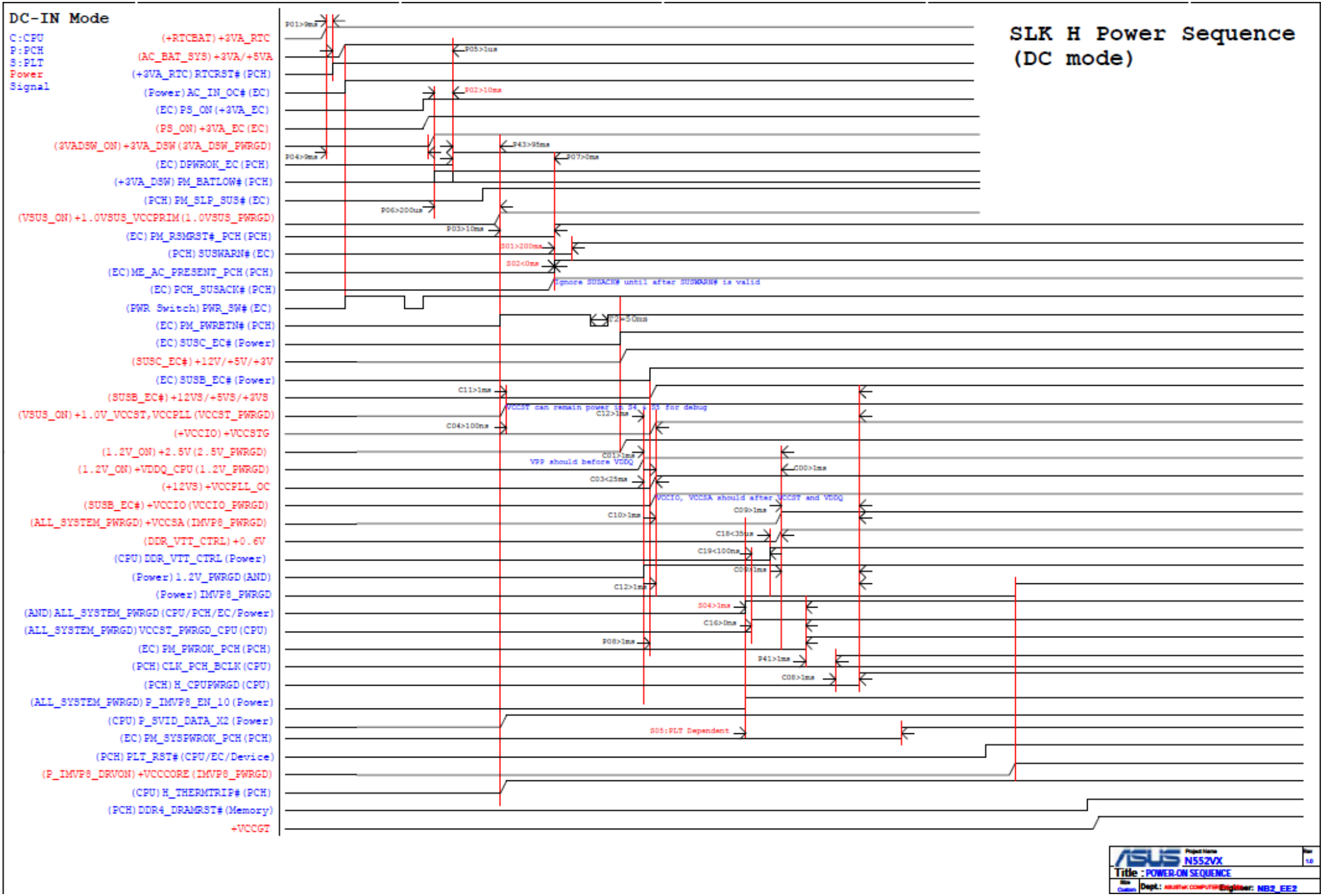
POWER ON SEQUENCE



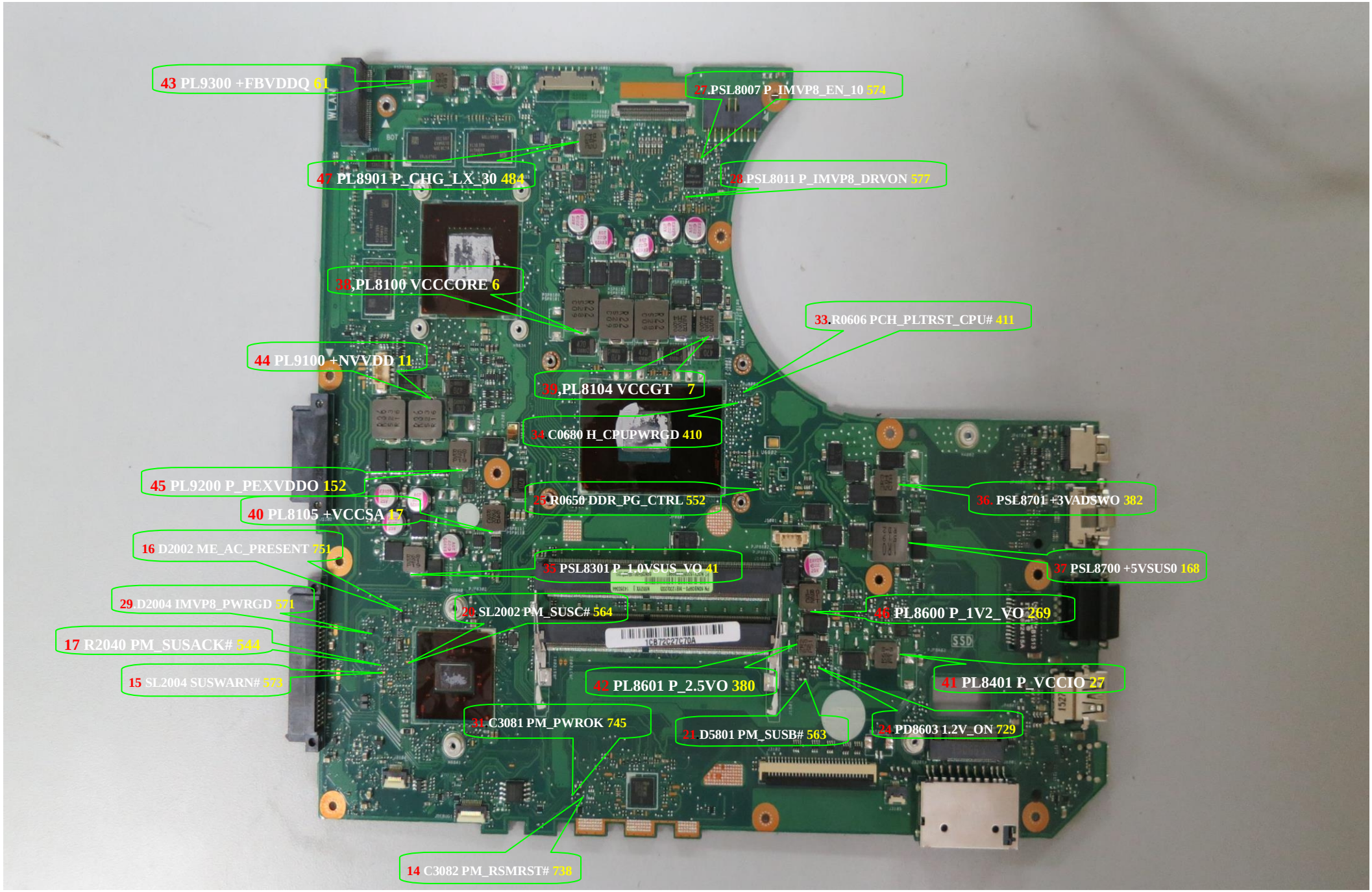
AC_IN POWER ON SEQUENCE



DC_IN POWER ON SEQUENCE



Signal Measure Point-Bottom



Signal Measure Point-Top