

ZZZ



PCB DAZ@
DAZ10G00100
LA-A401P/LS-A401P

ZZZ1



PCB DA1@
DA8000WX010
MB LA-A401P REV1

ZZZ2



PCB DA2@
DA4001Q4010
AUDIO/LS-A401P REV1

S1



for WF

ZSJVV_MB_FRAME 1 PC A false

EC10G000200

@

S4



for GLONASS

ZSJVV_GPS_FRAME 1 PC A false

EC10G000100

@

Compal Confidential

Schematics Document

ZSJVV

LA-A401P

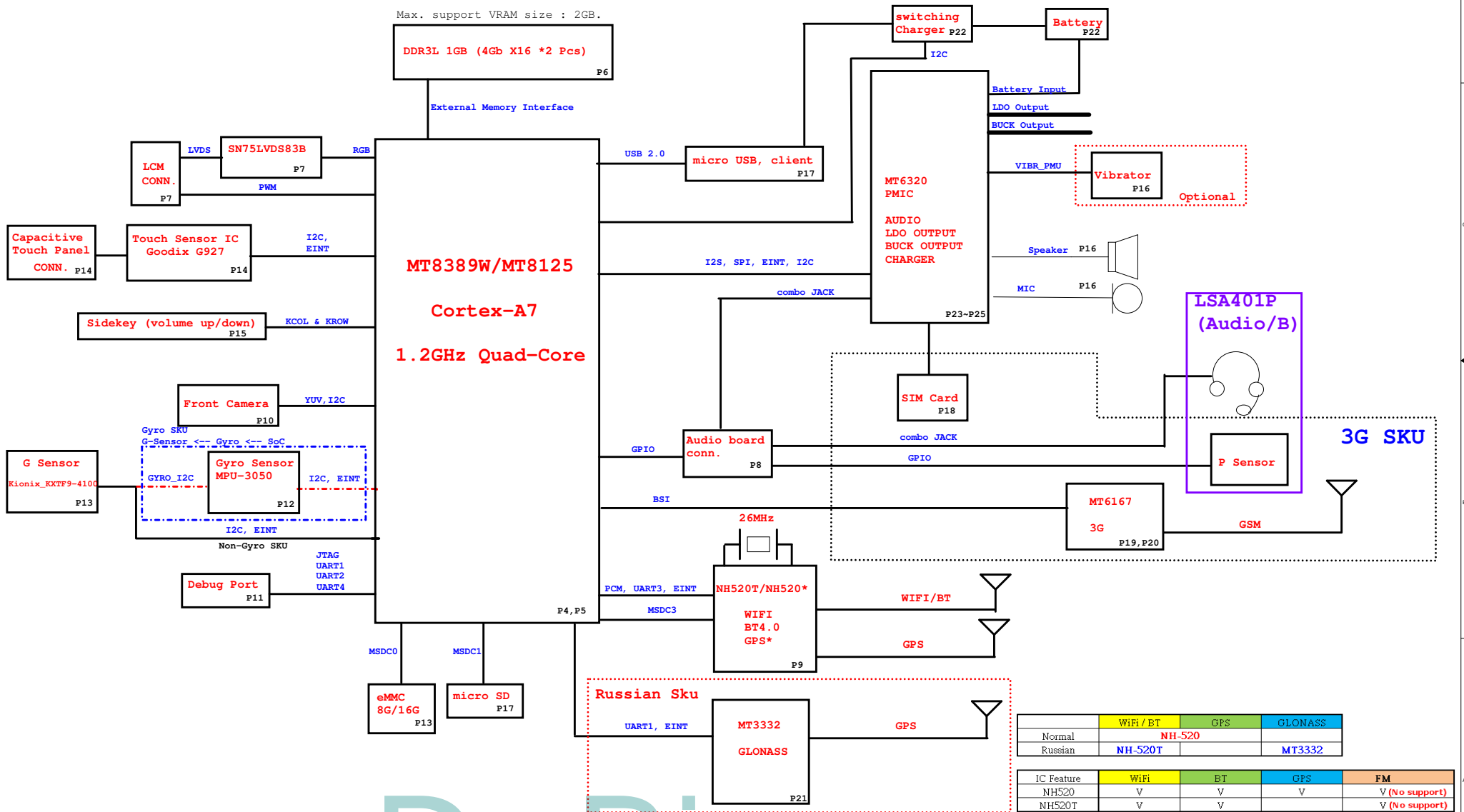
2013-06-13

REV:1.0

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Issued Date	2012/11/09	Deciphered Date	2014/11/09	Cover Page		
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				ZSJVV-LA-A401P		
				Date: Thursday, June 13, 2013	Sheet 1	of 28

giordano2 block diagram



	WIFI / BT	GPS	GLONASS
Normal		NH-520	
Russian	NH-520T		MT3332

IC Feature	WIFI	BT	GPS	FM
NH520	v		v	v (No support)
NH520T	v	v		v (No support)

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Voltage Rails

MT6320 Power Plane	Function	Power Level	IDLE	Sleep mode
VCORE_PMU	VCORE switching output	0.75 ~ 1.3	ON	Low voltage
VPROC_PMU	VPROC switching output	0.75 ~ 1.3	ON	Low voltage
VTCXO_1_PMU	LDO output for TCXO	2.8V	ON	OFF
VCAMA_PMU	LDO output for camera analog	2.8V	ON	Gating by SW
VSRAM_PMU	LDO output used for 1.2V SRAM	1.2V	ON	Low voltage
VDD28_6583	LDO output used for 2.8V IO	2.8V	ON	Gating by SW
VGP2_PMU	LDO output for camera 1.8V	1.8V	ON	Gating by SW
VEMC_3V3_PMU	LDO output for eMMC&P-sensor	3.3V	ON	Gating by SW
VMCH_PMU	LDO output for SD card	3.3V	ON	Gating by SW
VGP5_PMU	LDO output for Touch panel	2.8V	ON	Gating by SW
DDR3VCCIO	LDO output for DDR3L	1.35V	ON	Gating by SW
VDD33_6583	LCD & Bridge power	3.3V	ON	Gating by SW
VDD18_6583	LDO output used for 1.8V IO	1.8V	ON	Gating by SW
VRF18_PMU	LDO output for RF_MT6167	1.8V	ON	Gating by SW

Main board ID

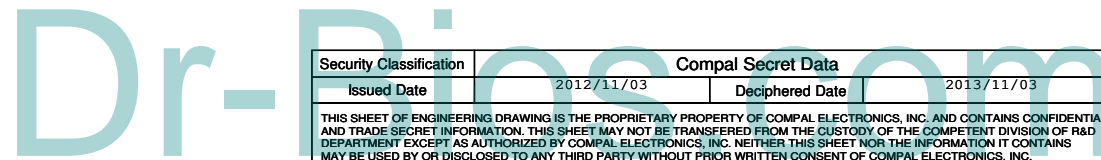
MB_ID0	MB_ID1	
0	0	EVT
0	1	DVT
1	0	PVT
1	1	MP

I2C address

Device	Address(7 bit)	Address(8bit)	
		Write	Read
Gyro (MPU-3050)	0x68	0xD0	0xD1
G-sensor (KXTIK-1004)	0x0F	0x1E	0x1F
G-sensor(STK8313)	0x22	0x44	0x45
Camera (OV7675)	0x21	0x42	0x43
Charge (BQ24196)	0x6B	0xD6	0xD7
Touch screen (GT927)	0x5D	0xBA	0xBB
PMU(MT6320)			

BOM structure

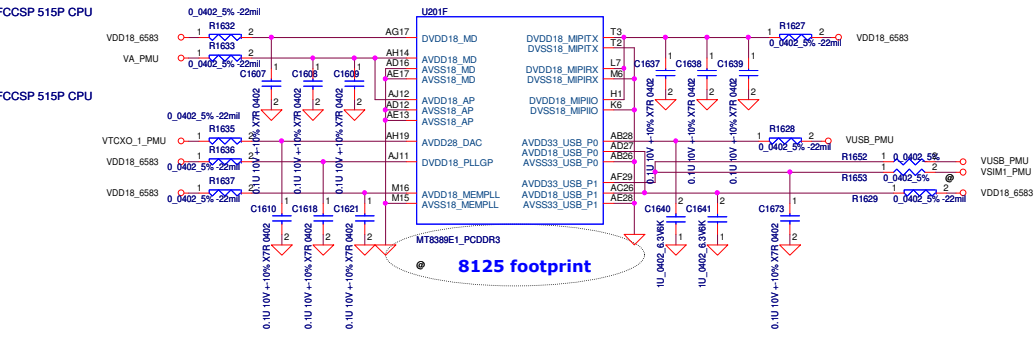
Name	Function
3G@	3G only
EMC@	for EMC/ESD/RF request
G_SENSOR@	G_SENSOR
KIONIX@	G-sensor for Kionix
STK@	G-sensor for Sensortek
GYRO@	GYRO
NAN 256Mx16@	NANYA 256M(DDR3L)
HYN 256Mx16@	HYNIX 256M(DDR3L)
WIFI_ONLY@	WIFI ONLY
LVDS_TI@	LVDS for TI
LVDS_ITE@	LVDS for ITE
NH520@	AW-NH520 for GPS
NH520_EMC@	GPS(NH520) EMC
GLONASS@	NH520T+GLONASS
P_SENSOR@	P-sensor
HYN 8GB@	HYNIX 8G(EMMC)
HYN 16GB@	HYNIX 16G(EMMC)
KIN 8GB@	Kingston 8G(EMMC)
KIN 16GB@	Kingston 16G(EMMC)
SAM 8GB@	Samsung 8G(EMMC)
SAM 16GB@	Samsung 16G(EMMC)
CONN@	Connector
ME@	CLIP
DAZ@	PCB
VIB@	Vibrator



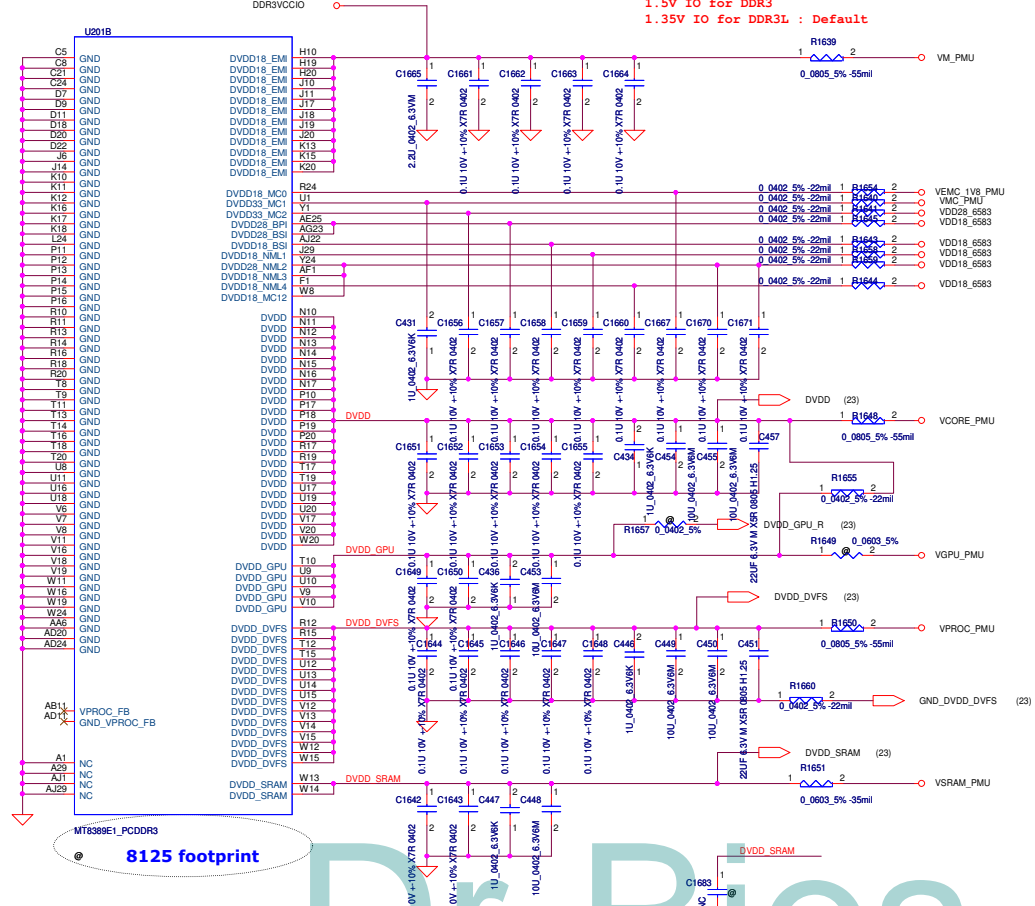
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Date:	K I I I X p # A e \ (* #) ' (*)	Page	3	of	28

U201 3G@ SA00006SG00 S IC MT8389WK/A 1.2G FCCSP 515P CPU

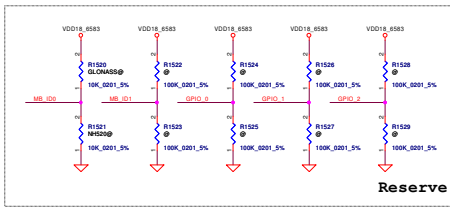
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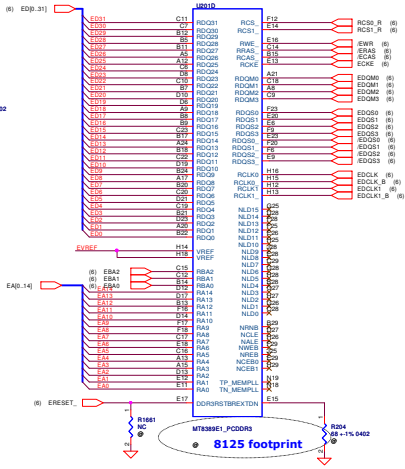
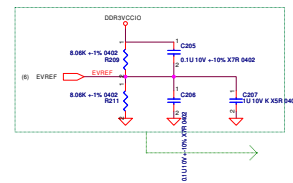
1.8V IO for DDR
 1.2V IO for DDR2
 1.5V IO for DDR3
 1.35V IO for DDR3L : Default



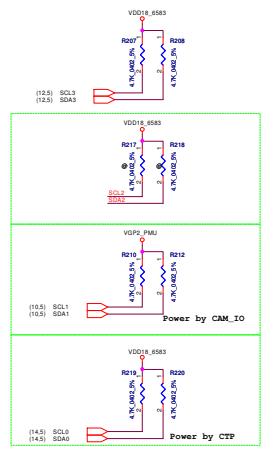
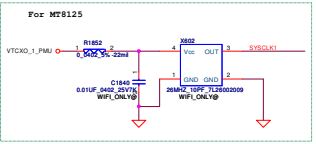
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Reserve



8125 footprint

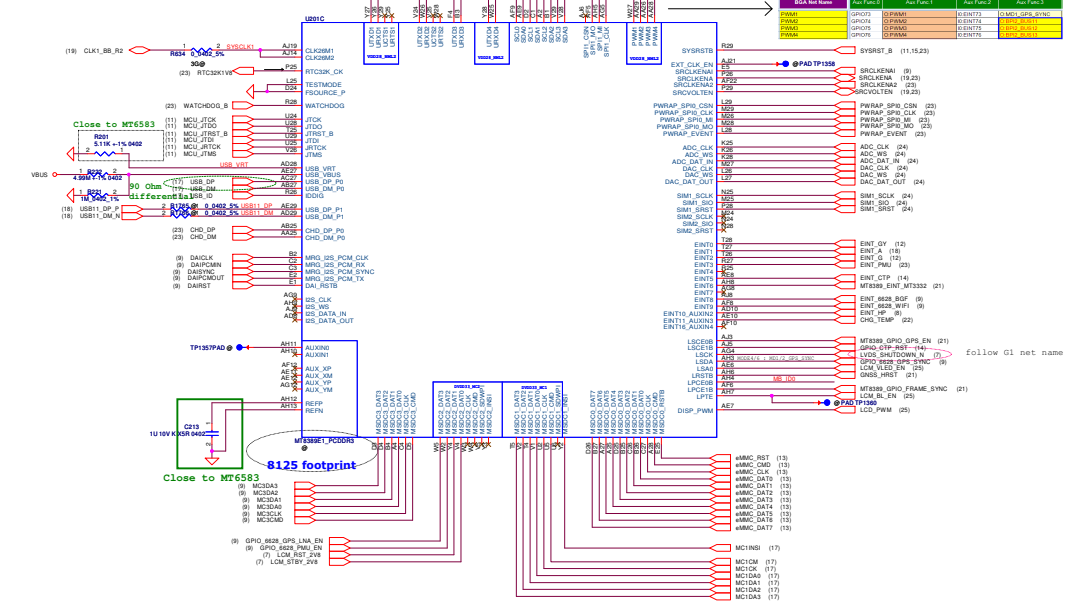


```

<TESTMODE>
Connect to VDD18 : Enter Test Mode
Connect to GND : Normal mode

<FSDORCE_P>
Connect to VDR (2V0) : w/1 FSPOSE program
Connect to GND : w/0 FSPOSE program

```



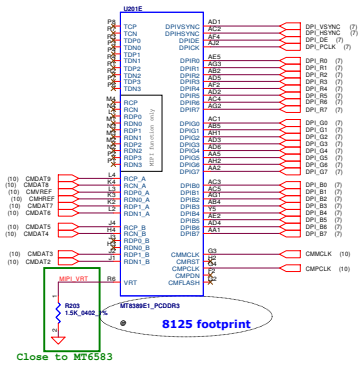
```

#10T0 : GYRO-Sensor
#10T1 : SIM
#10T2 : -Sensor
#10T3 : PM0 HS330
#10T4 : CTP
#10T5 : MT6532
#10T6 : MT6628 SGP
#10T7 : MT6628 WIFL
#10T8 : HEADSET

#120 : CTP
#12C1 : Sub Camera
#12C3 : G/YRO sensor
#12C4 (PMIC) : Charger IC
#12C5 (PMIC) :
#12C6 (PMIC) :

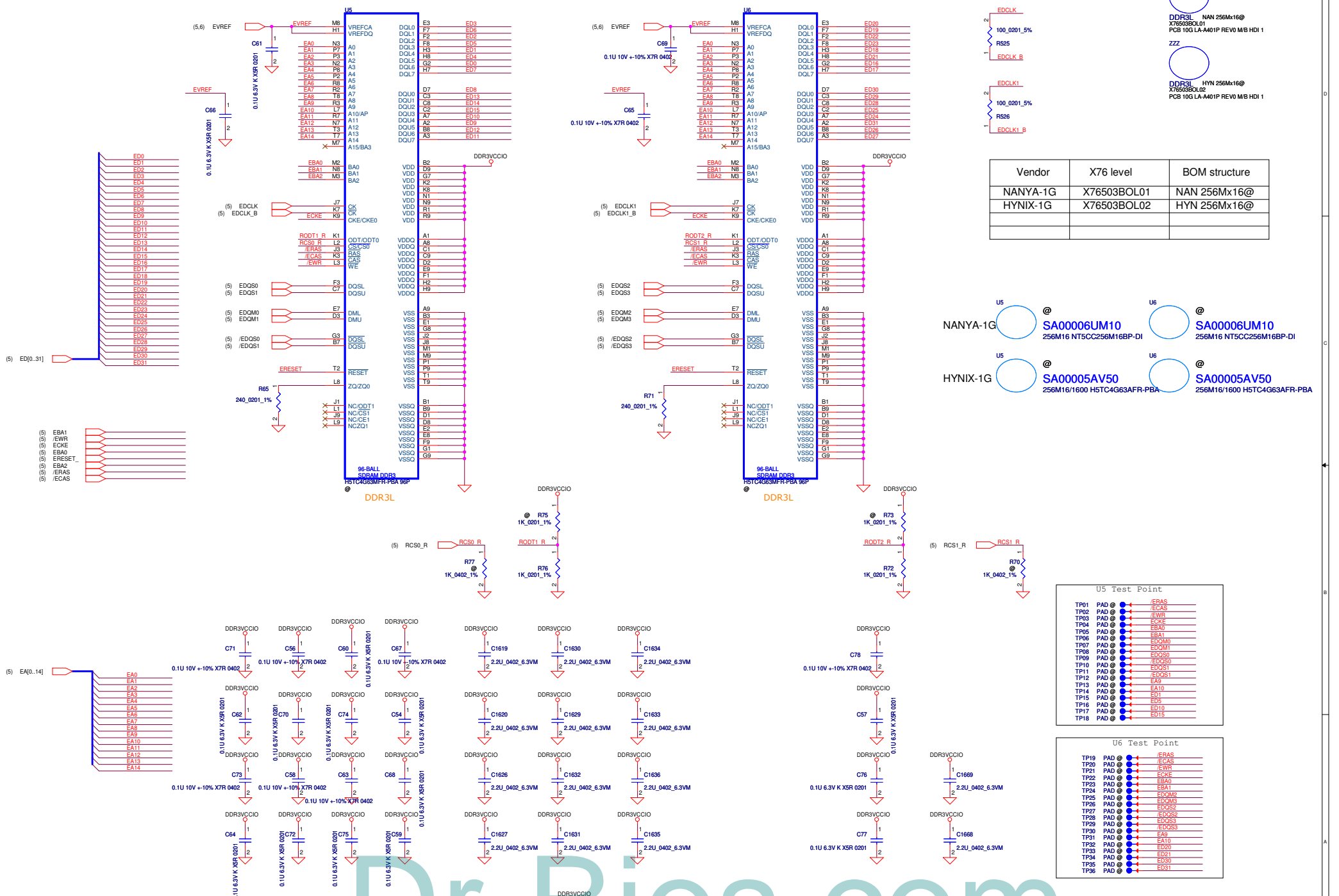
#128 :
#128D0 : eMMC
#128D1 : SD Card
#128D2 : GP0 (2.8V)
#128D3 : MT6628

```

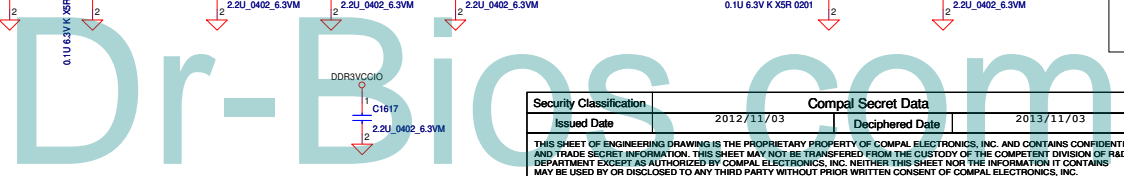
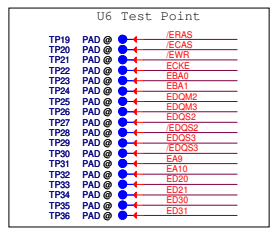
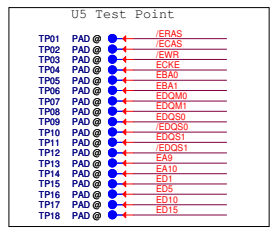


8125 footprint

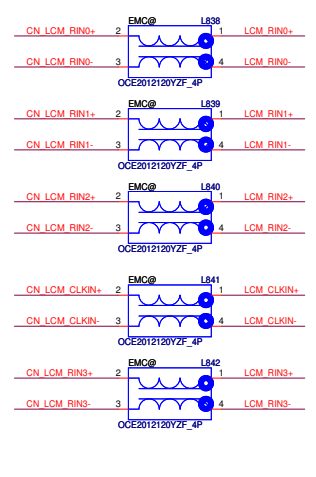
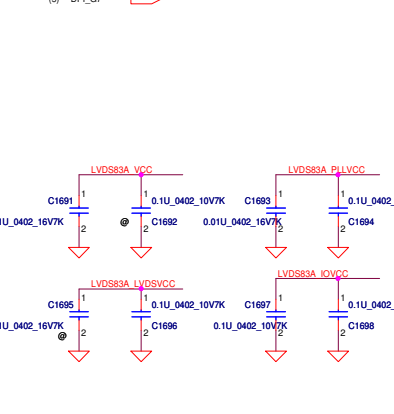
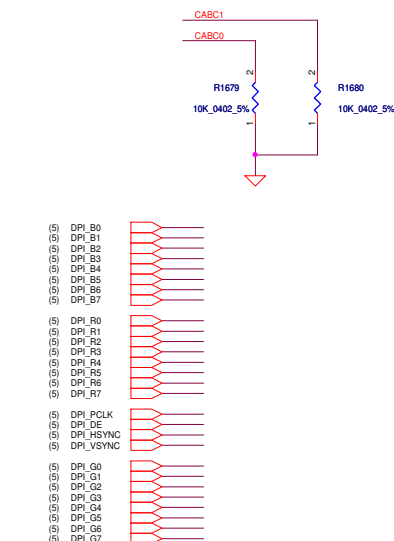
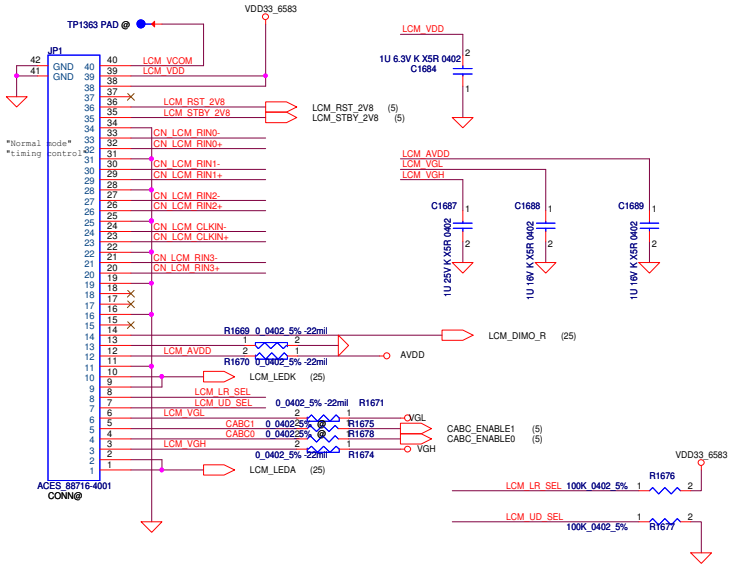
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Issued Date	2012/11/03	Disciplined Date	2013/11/03
Item	MT8377 - Baseband		Rev
Doc No	ZSJVV-LA-A401P		Rev
Rev	1	Rev	1



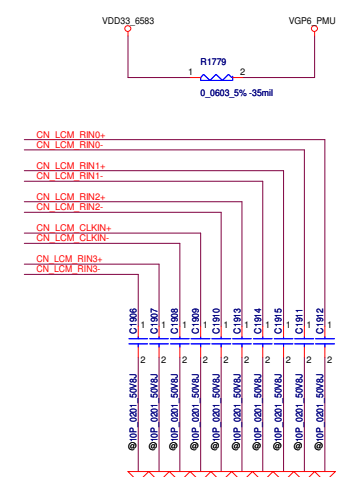
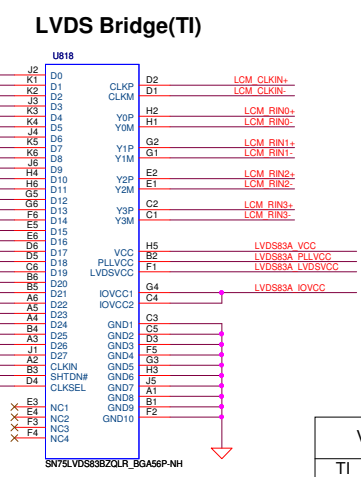
Vendor	X76 level	BOM structure
NANYA-1G	X76503BOL01	NAN 256Mx16@
HYNIX-1G	X76503BOL02	HYN 256Mx16@



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Date:	K	U	U	U	U	U	6 of 28



LVDS Bridge(ITE) @ SA00006UP00
 IC IT6211VG BGA 56P TRANSMITTER



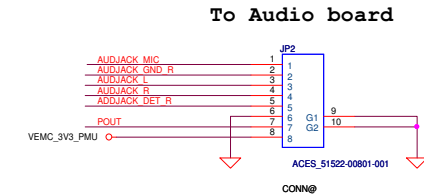
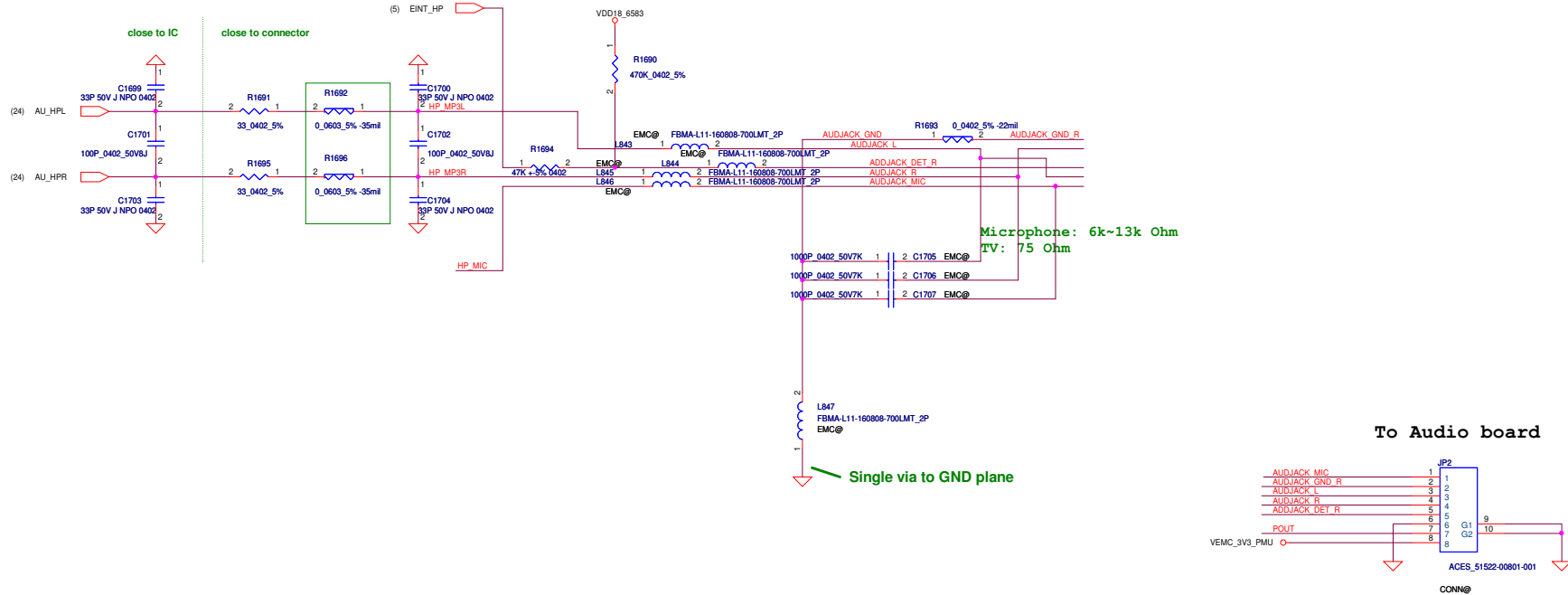
Vendor	X76 level	BOM structure
TI	X76503BOL21	LVDS_TI@
ITE	X76503BOL22	LVDS_ITE@

for SN75LVDS83BZQLR

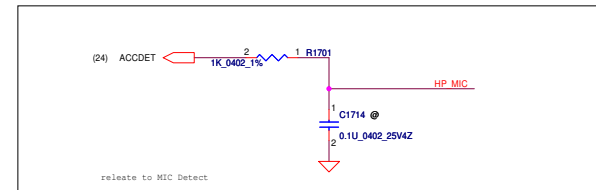
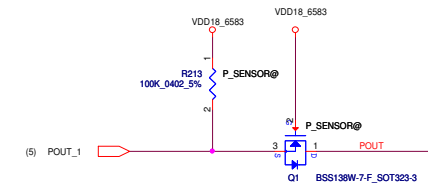
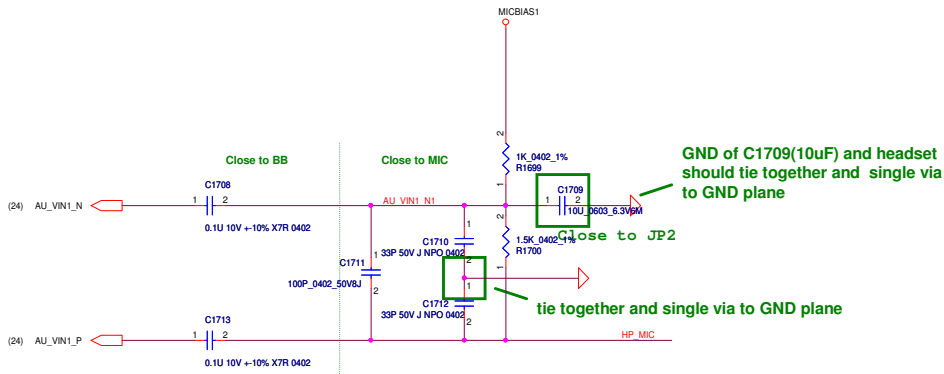
for IT6211VG

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Date:	K 1111 2012.11.03		7 of 28	

Earphone RECEIVER

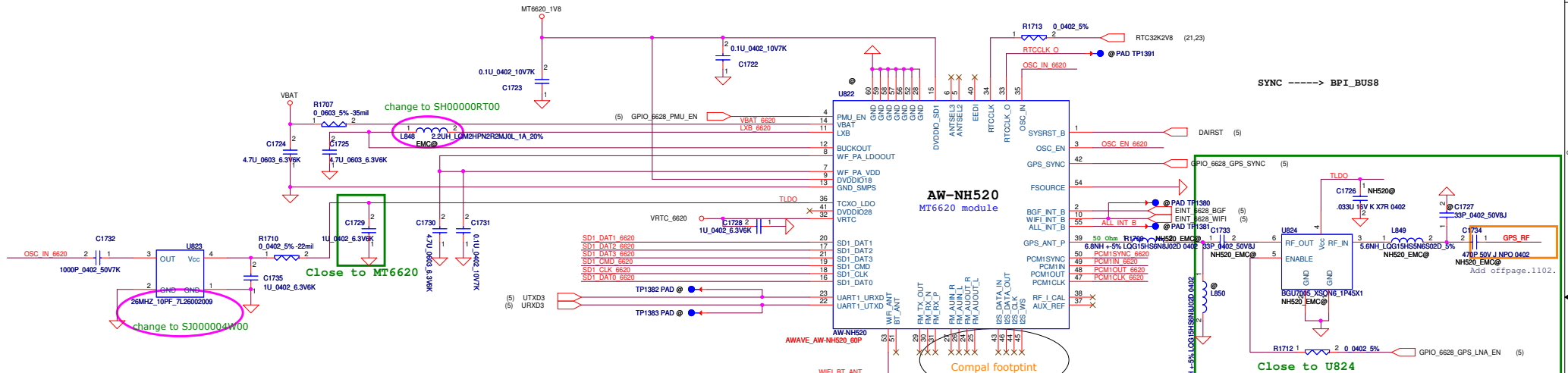
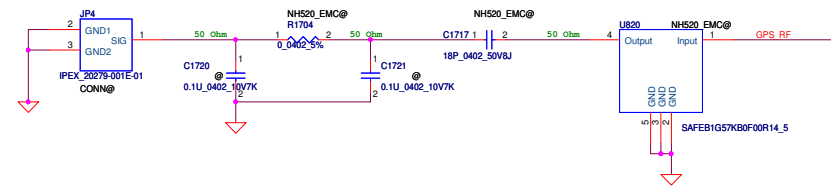
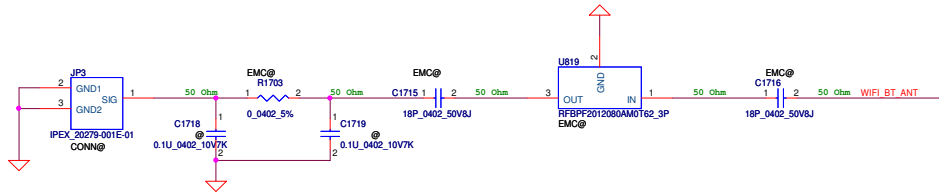


Earphone MICPHONE



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Size	C	Document Number	ZSJVV-LA-A401P	Rev
Date:	X 1111 2012 11 03 14:00:00	Page	8	of 28

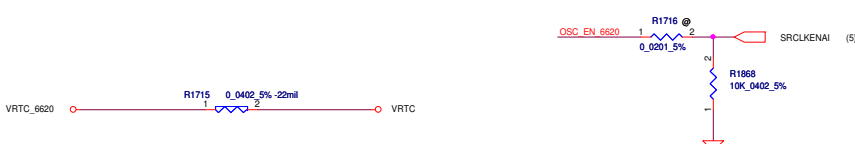


SYNC -----> BPI_BUS8

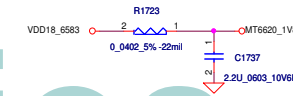
Close to U824
IF GPS have to pass AGPS IOT, recommend to reserve an external GPS LNA between U824 pin3 and U822 pin39

AW-NH520
MT6620 module

Compal footprint



- SD1_CMD_6620 0.0201 5% 1 B721 2 MC3CMD (5)
- SD1_DAT3_6620 0.0201 5% 1 B718 2 MC3DA3 (5)
- SD1_DAT2_6620 0.0201 5% 1 B719 2 MC3DA2 (5)
- SD1_DAT1_6620 0.0201 5% 1 B722 2 MC3DA1 (5)
- SD1_DATA_6620 0.0201 5% 1 B723 2 MC3DA0 (5)
- SD1_CLK_6620 0.0201 5% 1 B724 2 MC3CLK (5)
- PCMI_SYNC_6620 0.0201 5% 1 B725 2 DAICLK (5)
- PCMI_OUT_6620 0.0201 5% 1 B726 2 DAISYNC (5)
- PCMI_IN_6620 0.0201 5% 1 B727 2 DAIPCMIN (5)
- PCMI_OUT_6620 0.0201 5% 1 B728 2 DAIPCMOUT (5)

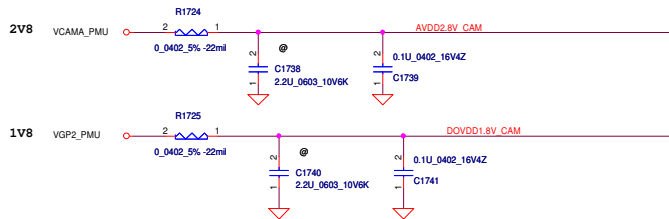


	XTEST	EEDI	ANTSEL_3	Default
clock setting	2.8V TCXO or OSC	0	0	Default
	1.8V TCXO or OSC	0	1	0
	XTAL	0	0	1
	external clock mode	0	1	1

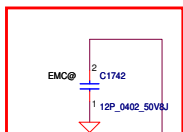
	XTEST	ANTSEL_2	ANTSEL_1	Default
WiFi host interface	WiFi :SDIO1	0	0	Default
	WiFi :SDIO2	0	0	1
	WiFi :SPI	0	1	0
	WiFi :reserved	0	1	1

	XTEST	ANTSEL_0	Default
BT/common host interface	UART1	0	Default
	SDIO2	0	1

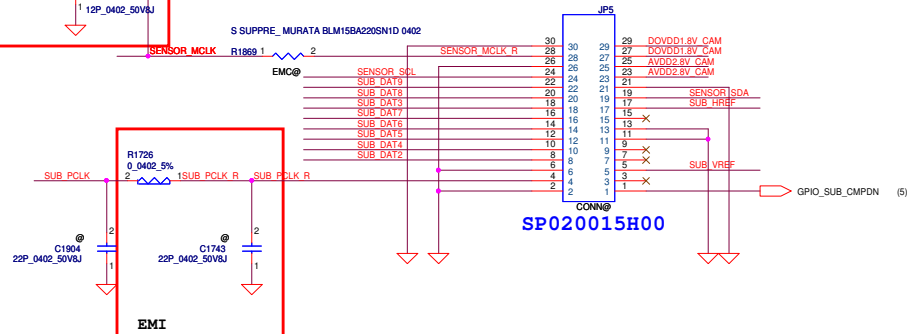
Sub Camera



Acer reserve



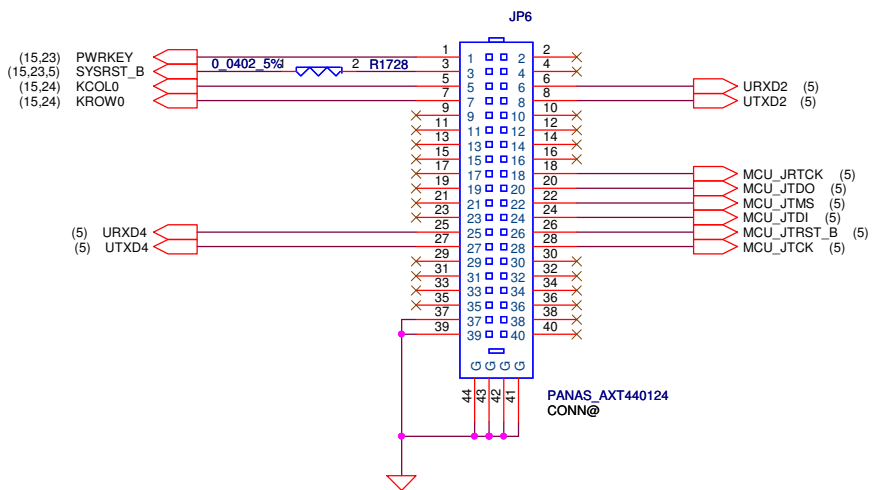
0.3M connector



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Title		Camera	
Size	Document Number	Rev	(%)
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Date:	K:\11149281.e\c\01\12\11\10 of 28		

Common Debug

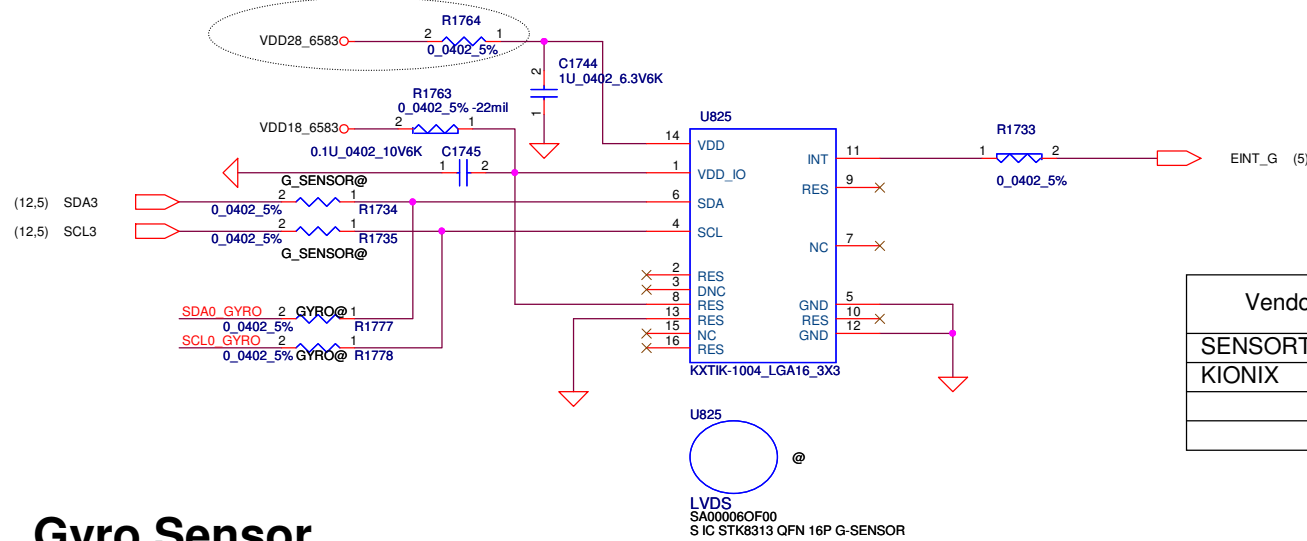


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Issued Date	2012/11/03	Deciphered Date	2013/11/03	Title SD Card,Debug,USB
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for co-lay STK8313,R1764 need change to 20 ohm

G-Sensor

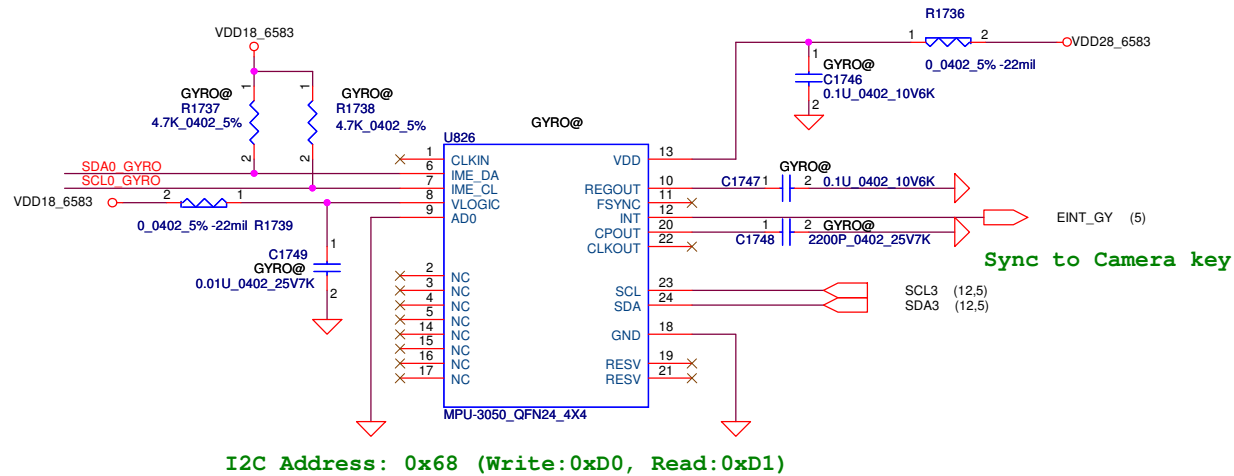


ZZZ
STK@
G SENSOR
X76503BOL11
ALT. GROUP PARTS G-SENSOR SITRONIX ZSJVV

ZZZ
KIONIX@
G SENSOR
X76503BOL12
T. GROUP PARTS G-SENSOR KIONIX ZSJVV

Vendor	X76 level	BOM structure
SENSORTEK	X76503BOL11	STK@
KIONIX	X76503BOL12	KIONIX@

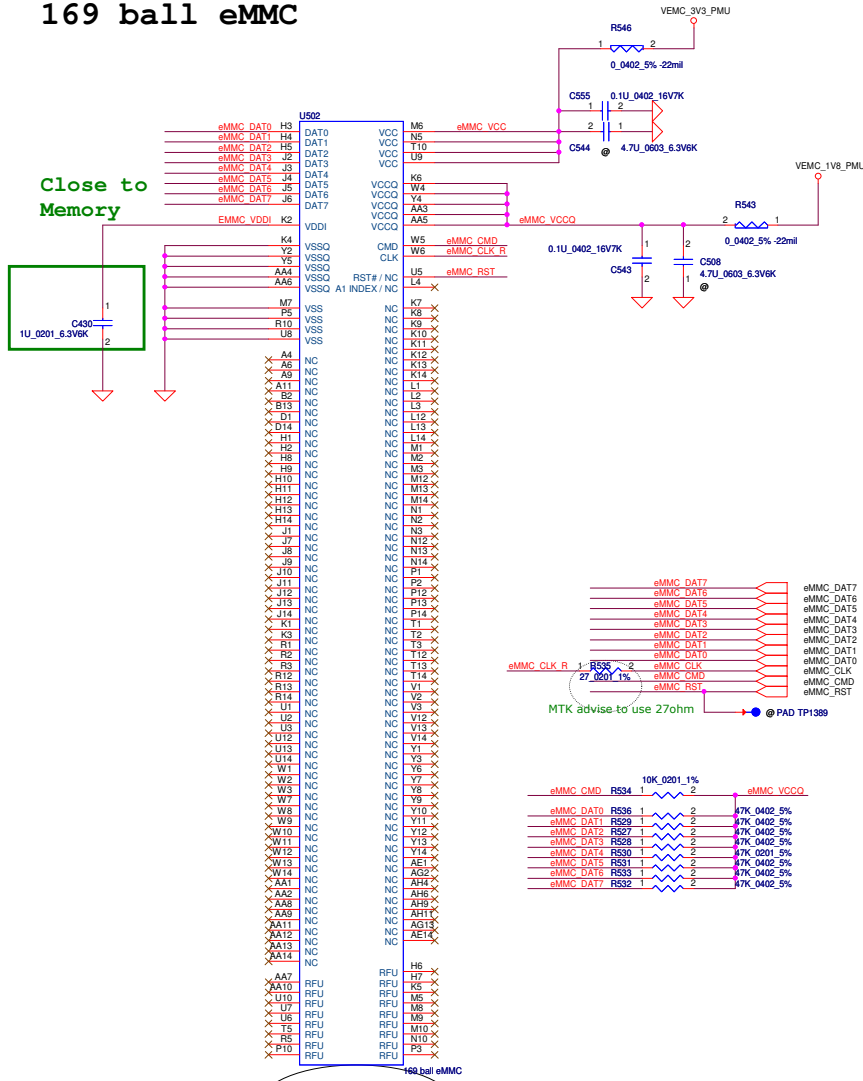
Gyro Sensor



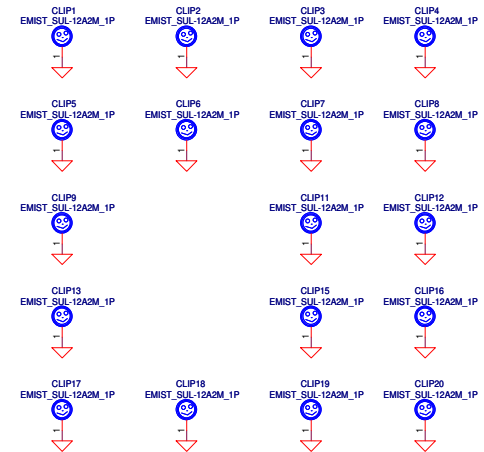
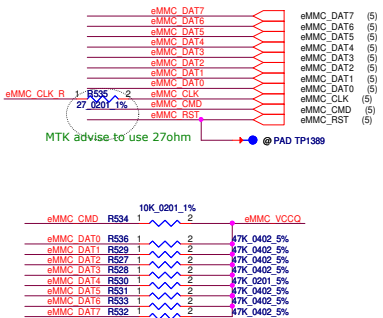
I2C Address: 0x68 (Write:0xD0, Read:0xD1)

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				Date:	K l i j Xp#A e\ (^*#)' (*-----) J \k 12 of 28

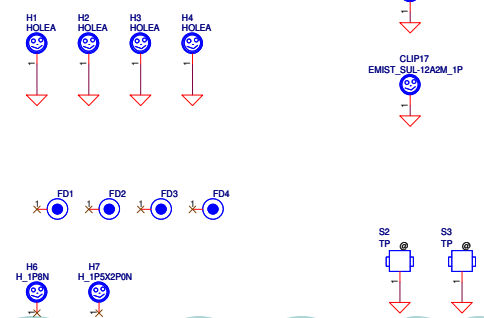
169 ball eMMC



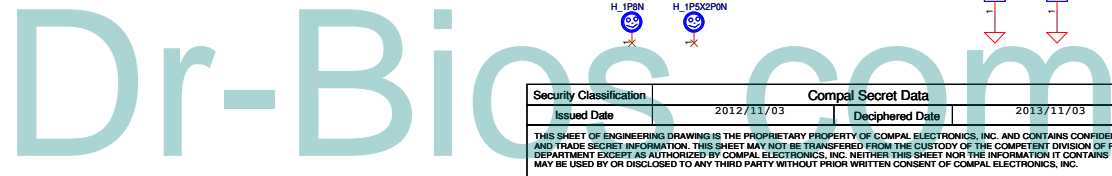
- U502 KIN 8GB@
SA00006MF30
Kingston 8G KE4CN3K6A
- U502 KIN 16GB@
SA00006MG30
Kingston 16G KE4CN4K6A
- U502 HYN 8GB@
SA00006PW10
HYNIX 8G H26M42002GMR
- U502 HYN 16GB@
SA00006VJ10
HYNIX 16G H26M52002EQR
- U502 SAM 8GB@
SA00006VE10
Samsung KLM8G1WE4A-A001
- U502 SAM 16GB@
SA00006Z610
Samsung KLMAG2WE4A-A001



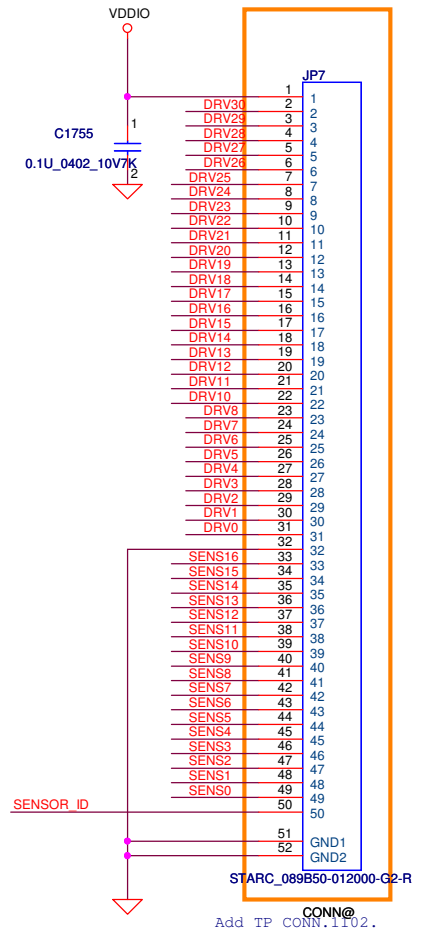
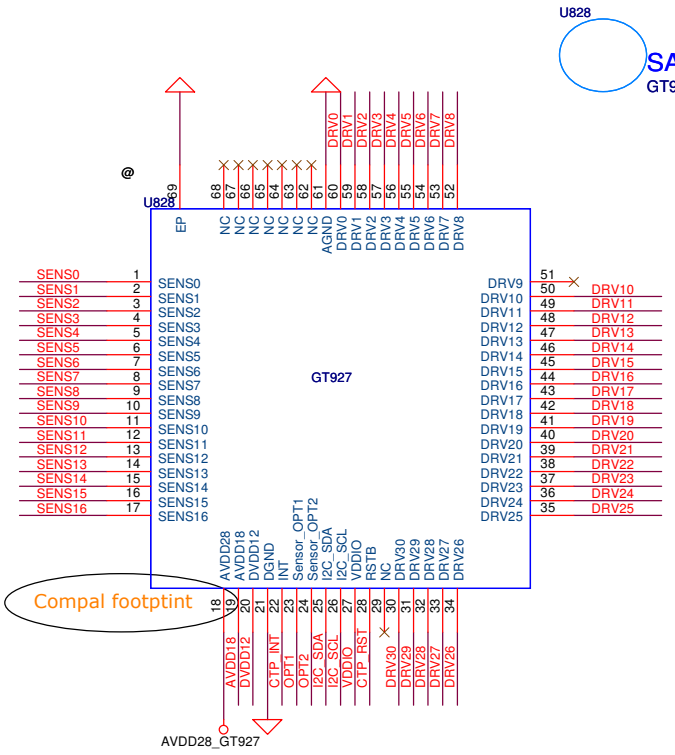
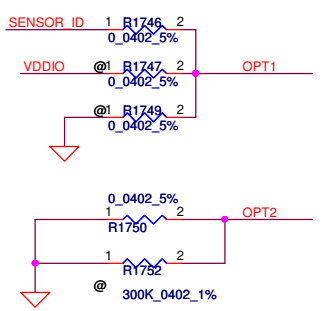
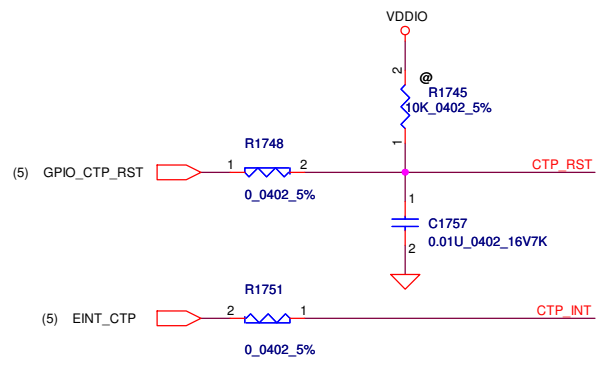
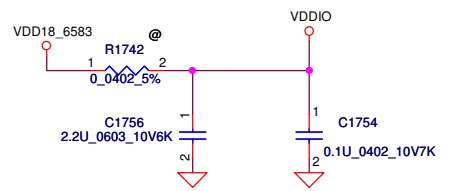
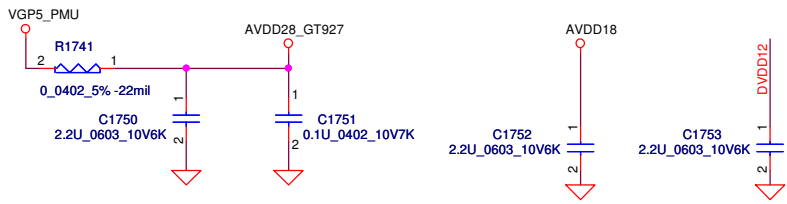
SCREW HOLE



SDIN4E2-16G-T_TFBGA_169P
@ ompal footptint



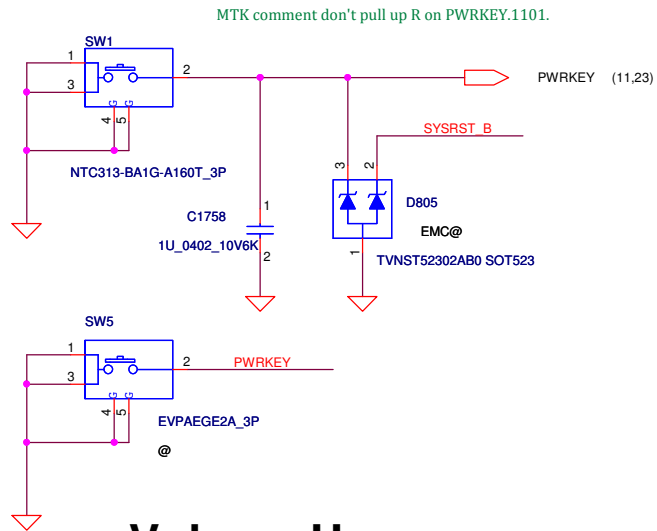
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File	eMMC, SCREW HOLE		Size	Document Number
C	ZSJVV-LA-A401P		Rev	(%)
Date:	K L I U T 2013 11 13 13 of 28			



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				Rev (%)
				Date: K l i j Xp#A e\(^*#)' (* J \k k 14 of 28

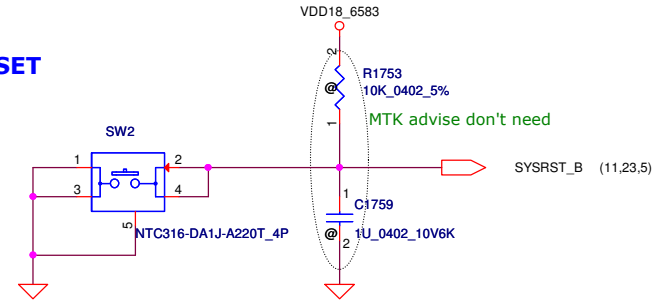
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Power-on Button

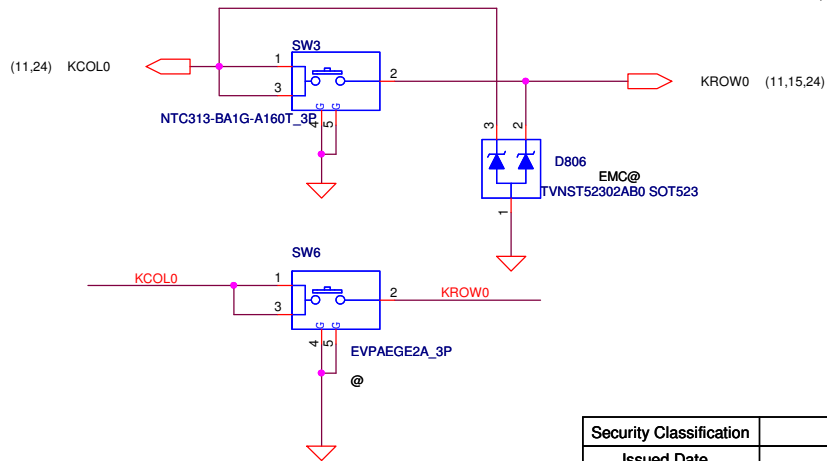


Compal GND MTK GND

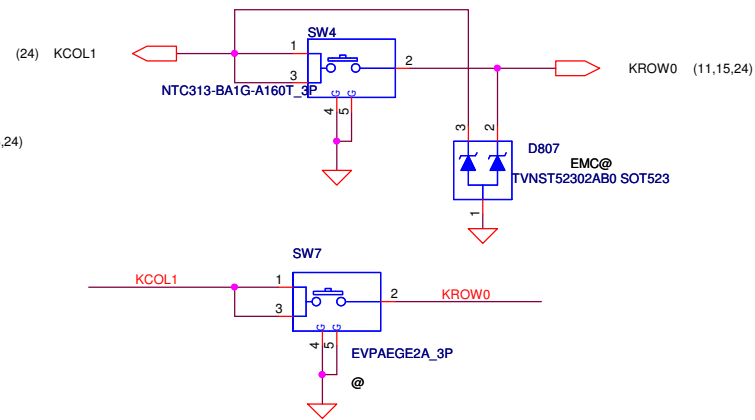
RESET



Volume Up (Download Key)

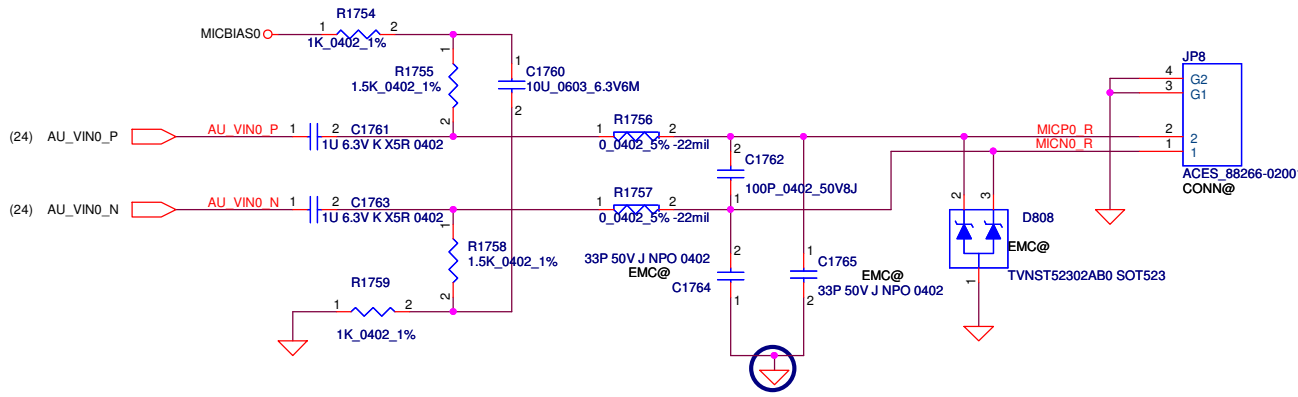


Volume Down

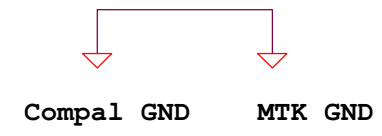


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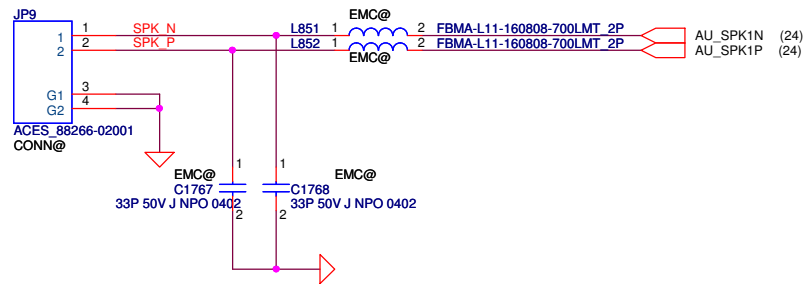
MIC



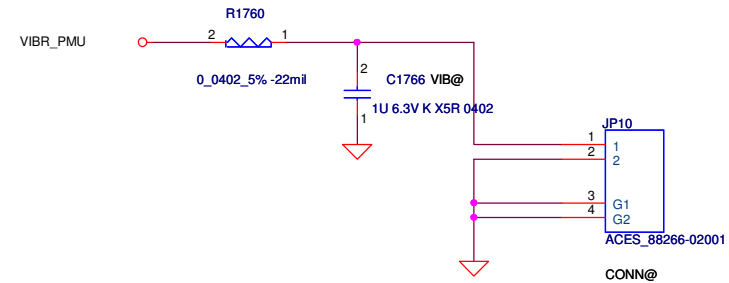
Modify MIC CONN SCH.1102.



SPAKER



Vibrator



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				ZSJVV -LA-A401P
				Date: K l i j X p # A e \ (* #) ' (*) J \ \ k 16 of 28

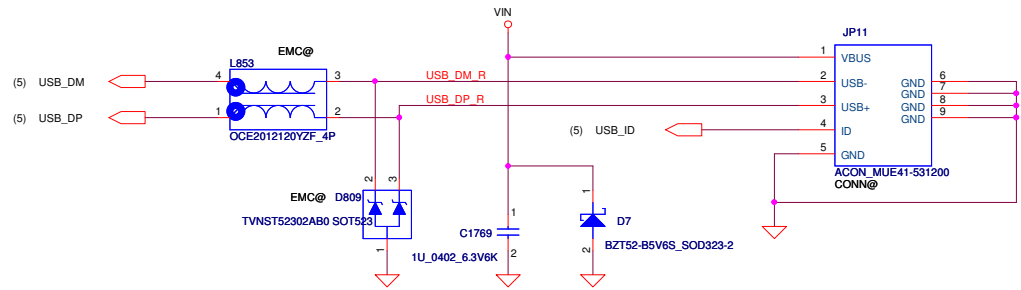
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Micro-USB

If mini-A connector insert => CID < 0V => Low
 If mini-B connector insert => CID > 1.2V => High
 IDPULLUP pin is replaced by 1.2V power source.

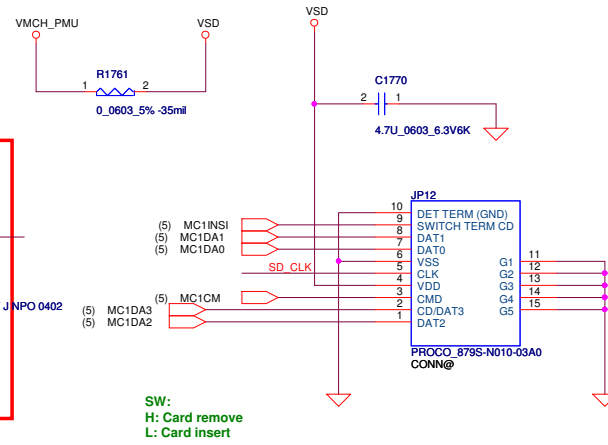
FOR RF request

TP1364PAD@ - USB_DM_R
 TP1365PAD@ - USB_DP_R



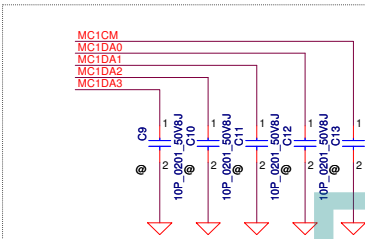
Compal GND MTK GND

SD card



EMI

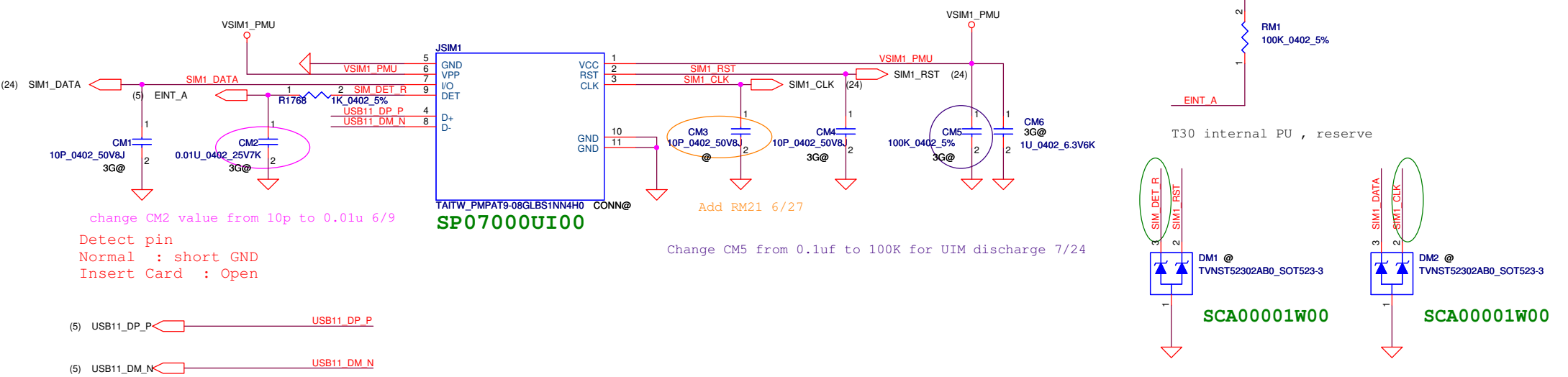
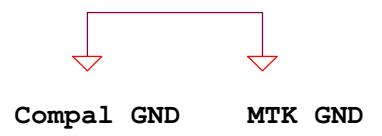
SW:
 H: Card remove
 L: Card insert



for RF request
 close U201 side

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Issued Date	2012/11/03	Deciphered Date	2013/11/03	Title Connector-2(SD card/G-se)
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Date: K L J L X p / A L e (' #) C				17 of 28

SIM CARD



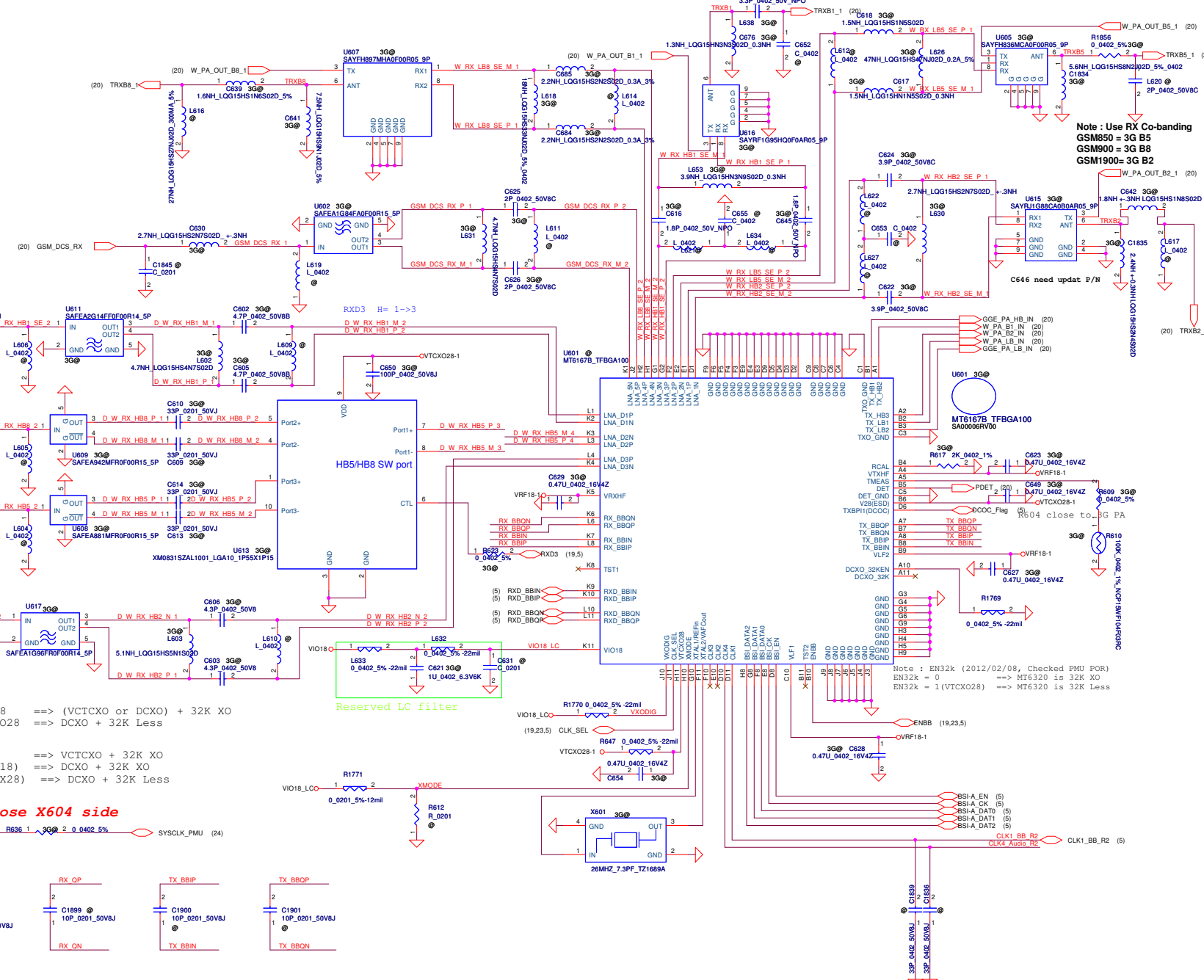
change CM2 value from 10p to 0.01u 6/9
 Detect pin
 Normal : short GND
 Insert Card : Open

Change CM5 from 0.1uf to 100K for UIM discharge 7/24

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			Document Number	Rev (%)
			ZSJVV-LA-A401P	
Date: K l i j Xp#A e\(^*#)' (*-----)J \k k 18 of 28				

WS1940-1	SKY13380	CTRL1	CTRL2	CTL	XM0831SZ
B2	switch path	FXD1	FXD2	FXD3	switch path
B8	ANT-RF1	L	L	X	RF1~RF2
B1	ANT-RF3	H	L	X	X
B5	ANT-RF4	H	H	H	RF1~RF3

SP4T Control Truth Table_SKY13380
 Vct1 Vct2
 RF1 L L
 RF2 L H
 RF3 H L
 RF4 H H

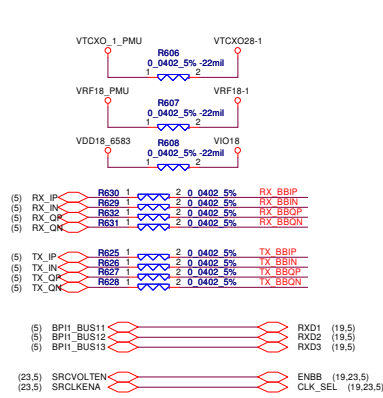
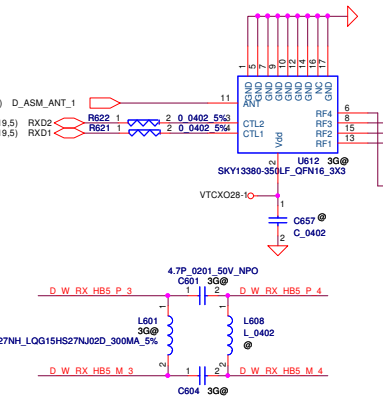
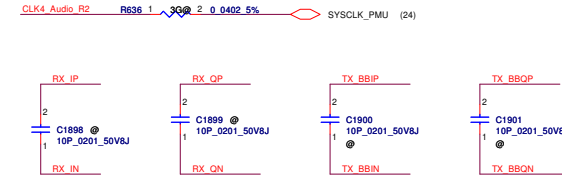


Note : Use RX Co-banding
 GSM850 = 3G B5
 GSM900 = 3G B8
 GSM1900 = 3G B2

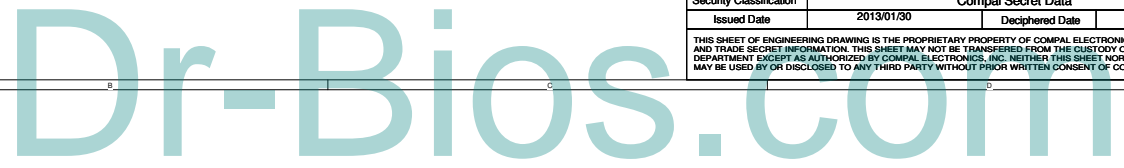
Note : VXODIG
 VXODIG = VIO18 ==> (VTCXO or DCXO) + 32K XO
 VXODIG = VTCXO28 ==> DCXO + 32K Less

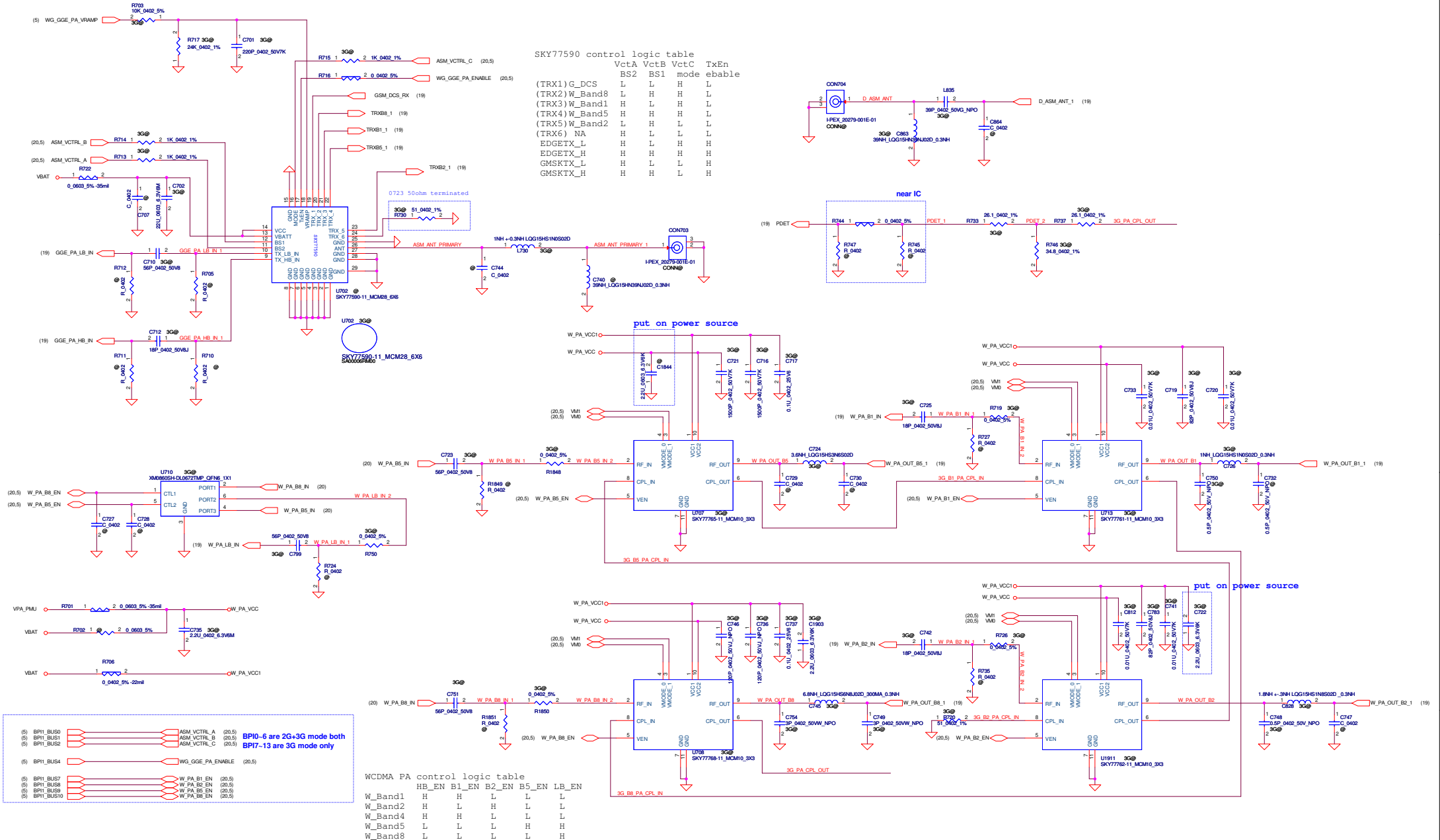
Note : Xmode
 Xmode = 0 ==> VTCXO + 32K XO
 Xmode = 1(VIO18) ==> DCXO + 32K XO
 Xmode = 1(VTCX28) ==> DCXO + 32K Less

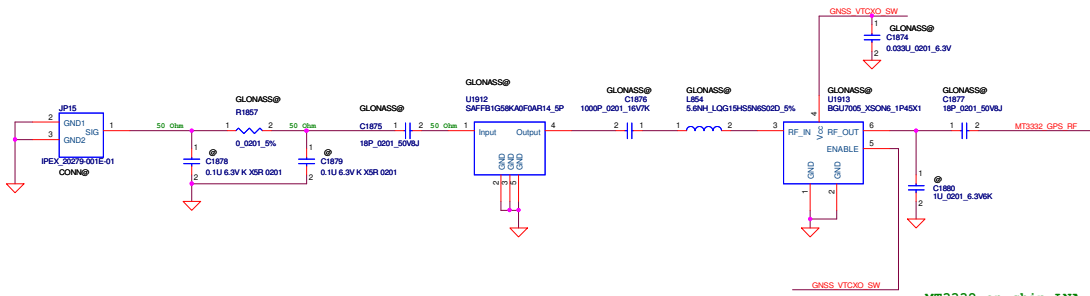
Place close X604 side



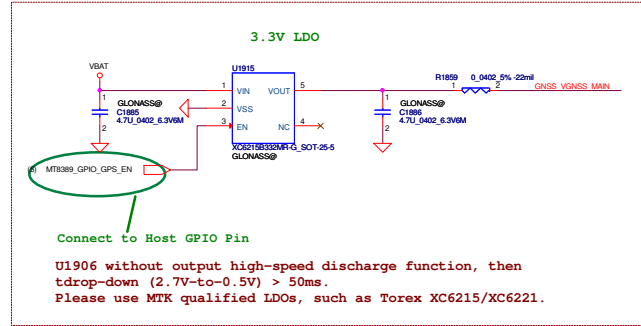
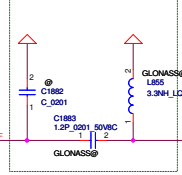
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			ZSJVV-LA-A401P	(%)
			Date: Thursday, June 13, 2013	Sheet 19 of 28







MT3332 on-chip LNA Matching



SA0006M400
S IC MT3332N QFN 48P GPS

MT3332 QFN-48pins

C1891 is close to pin11

C1893 is close to pin11

pin14 connect to C1894 GND net first, then connect to reference GND

RTC Voltage (2.8V)

Connect GNSS_HOST_VRTC to always alive voltage source, and keep the voltage swing of GNSS_HOST_32K RTC clock same as GNSS_HOST_VRTC.

1.8V / 2.8V IO Voltage Selection
2.8V IO : U1907 pin30 - 42 need change to GNSS_VTLDO
1.8V IO : U1907 pin30 - 42 (IO voltage is SMPS_IV8)

Reserved for GPS HRST from host, connect to Host (MT62xx) GPIO pin

Reserved for improving GNSS Hot-Start performance.

Reference Frequency Selection
16.365MHz TCXO : R1864 = NC, PIN_41 = NC
26MHz TCXO : R1864 = 10K, PIN_41 = NC
26MHz XTAL : R1864 = 10K, PIN_41 contact 10K

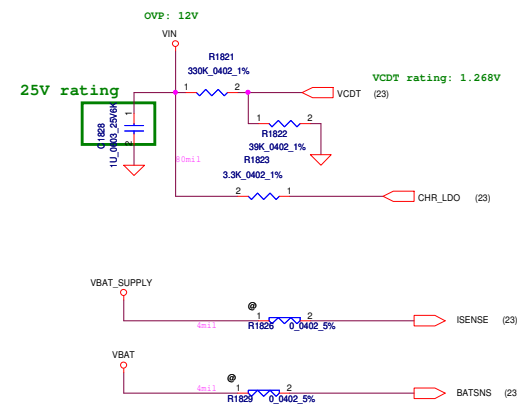
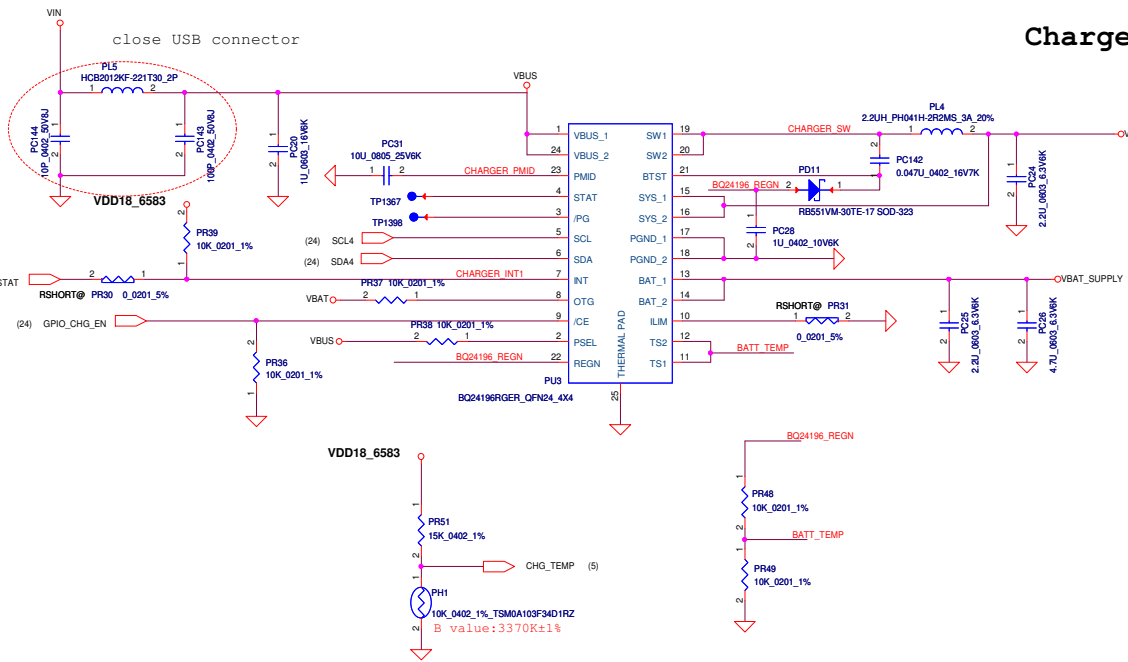
As EINT to Host

Connect to HOST UART (TX/RX) interface

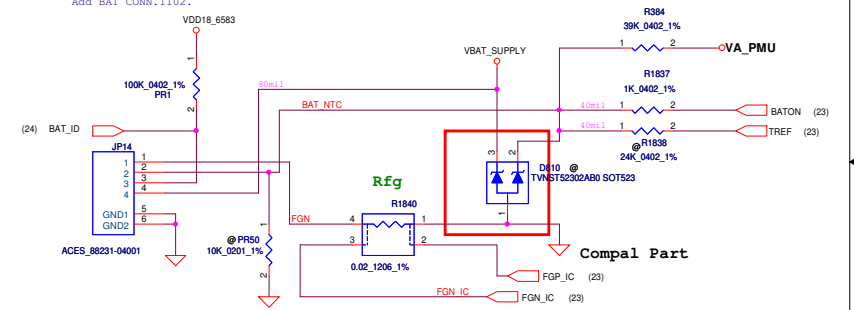
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Charger



BATTERY CONNECTOR

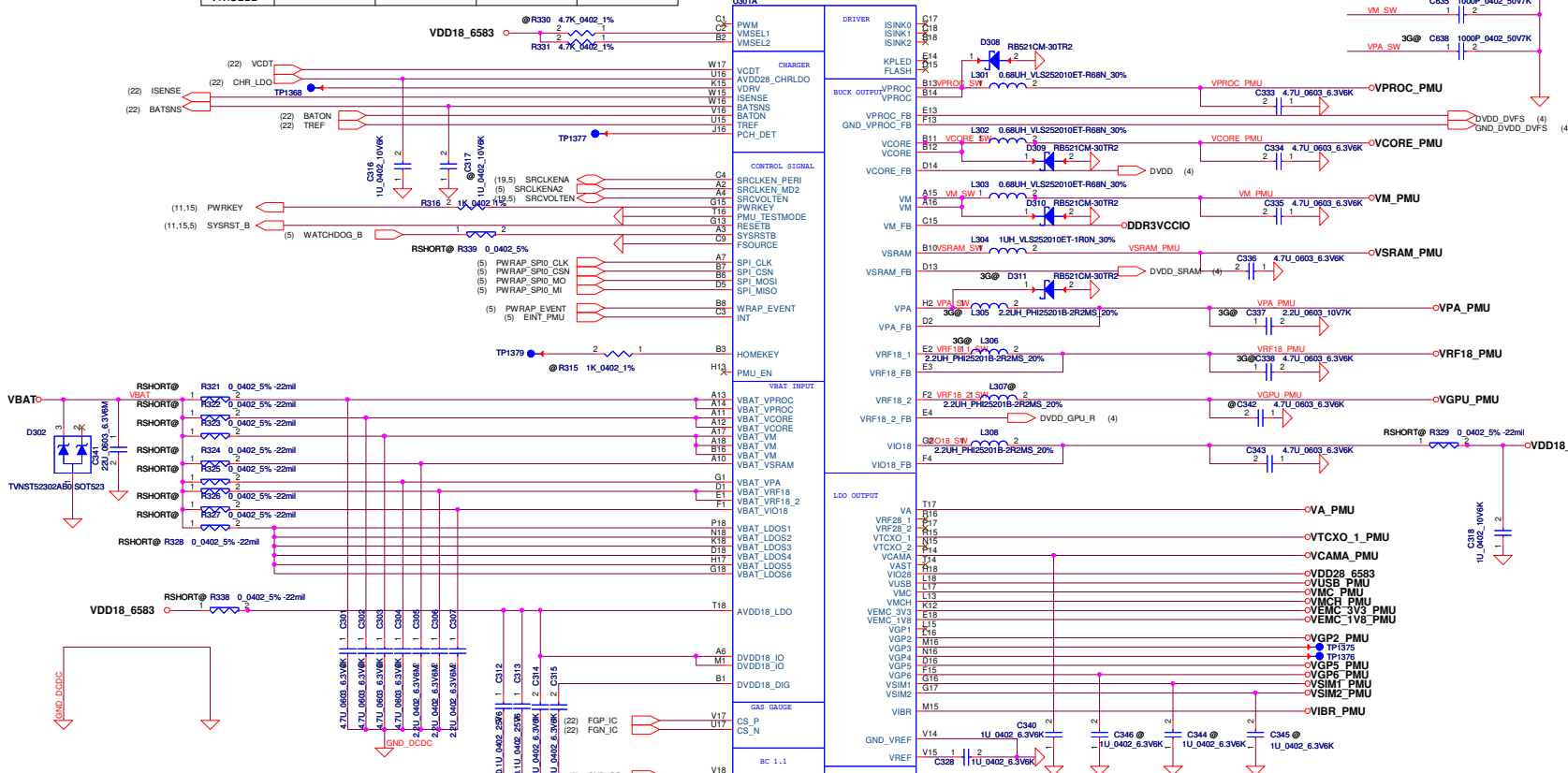


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Size	C	Document Number	LA-031P	Rev
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PMIC

	LPDDR2/1.2V	DDR3U/1.25V	DDR3L/1.35V	DDR3/1.5V
VMSEL2	L	L	H	H
VMSEL1	L	H	L	H



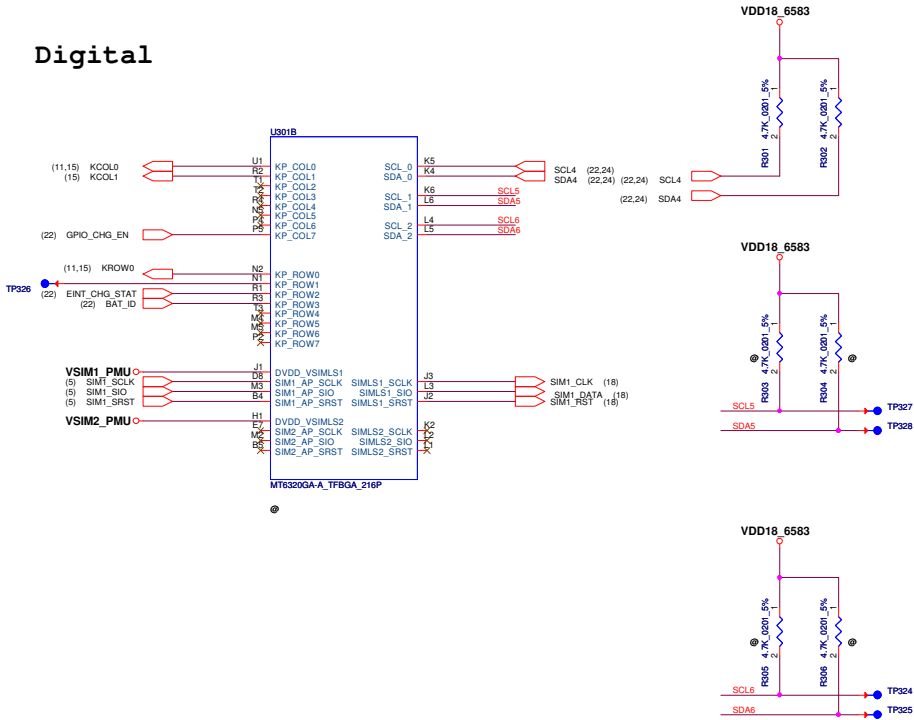
	Symbol	Application	Vout (V)	Iout (mA)	Cap Value (uF)
Buck	VPROC	CPU	0.7-1.25 (DC/DC)	2000	42
	VCORE	MDSYS/Infra	0.7-1.25 (DC/DC)	1200	30
	VM	VM	1.2/1.25/1.35/1.5	1100	10
	VSRAM	Memory	0.7-1.25 (DC/DC)	600	10
	VPA	3GPA	0.5-3.4 (100mV/step)	600	2.2+2.2
	VRF18	1st RF	1.825	450	4.7
	VRF18_2	2nd RF	1.825	450	10
	VIO18	GPU OD	1.05~1.25 (50mV/step)	600	4.7
Analog LDO	VA		1.8/2.5	100	2.2
	VRF28_1	MDSYS	2.85	200	2.2
	VRF28_2	General	1.8/2.85	200	2.2
	VTCXO_1	MDSYS	2.8	40	1
	VTCXO_2	MDSYS	1.8/2.8	40	1
	VCAMA	VCAMA	1.5/1.8/2.5/2.8	200	2.2
Digital LDO	VIO28		2.8	400	2.2
	VAST	MT6168	0.9/1.0/1.1/1.2	300	2.2
	VUSB		3.3	200	1
	VMC	T-Card	1.8/3.3	200	1
	VMCH	T-Card	3.0/3.3	800	4.7
	VMC_3V3	eMMC (Core)	3.0/3.3	800	4.7
	VMCH_1V8	eMMC	1.2/1.3/1.5/1.8/2.5/2.8/3.0/3.3	200	2.2
	VGP1	VCAMD	1.2/1.3/1.5/1.8/2.5/2.8/3.0/3.3	400	2.2
	VGP2	VCAM_IO	1.2/1.3/1.5/1.8/2.5/2.8/3.0/3.3	200	1
	VGP3	VCAM_AF	1.2/1.3/1.5/1.8/2.5/2.8/3.0/3.3	200	1
	VGP4	CTP/CMMB	1.2/1.3/1.5/1.8/2.5/2.8/3.0/3.3	200	1
	VGP5	CTP/CMMB	1.2/1.3/1.5/1.8/2.5/2.8/3.0/3.3	200	1
	VGP6	CTP/CMMB	1.2/1.3/1.5/1.8/2.5/2.8/3.0/3.3	200	1
	VSIM1	VSIM1	1.2/1.3/1.5/1.8/2.5/2.8/3.0/3.3	200	1
VSIM2	VSIM2	1.2/1.3/1.5/1.8/2.5/2.8/3.0/3.3	200	1	
Vibrator	VTBR	Vibrator	1.2/1.3/1.5/1.8/2.5/2.8/3.0/3.3	200	1
	VRTC	VRTC Block	2.8	2	1~22

Security Classification	2012/11/03	Compal Secret Data	2013/11/03	Title	MT6329 PMIC
Issued Date	2012/11/03	Deciphered Date	2013/11/03	Doc Number	QJ AMWCS98+ (G)

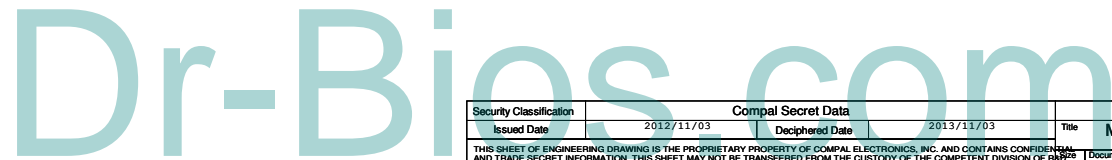
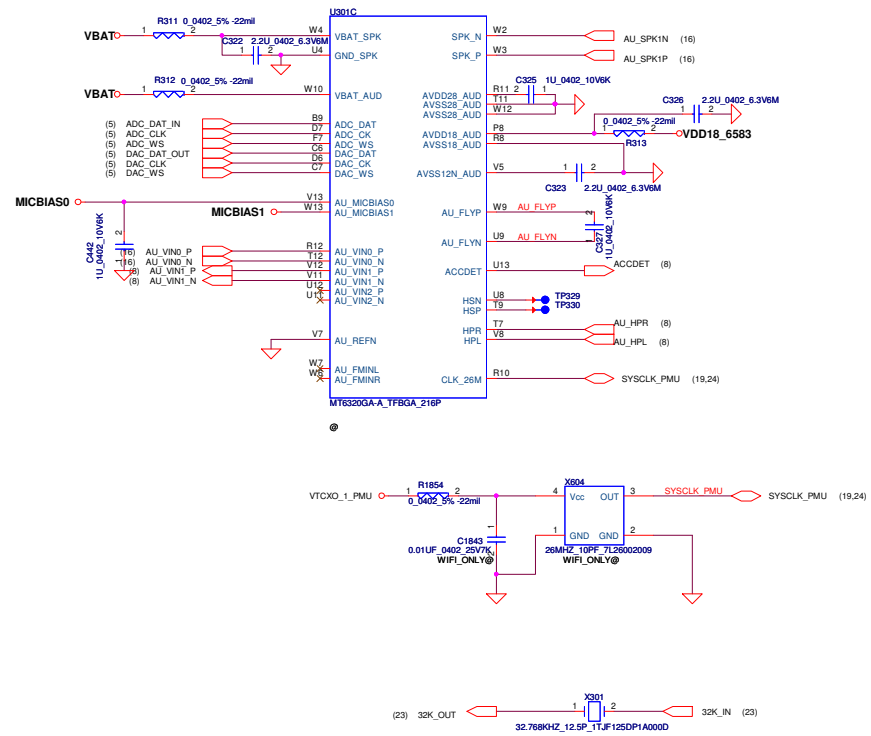
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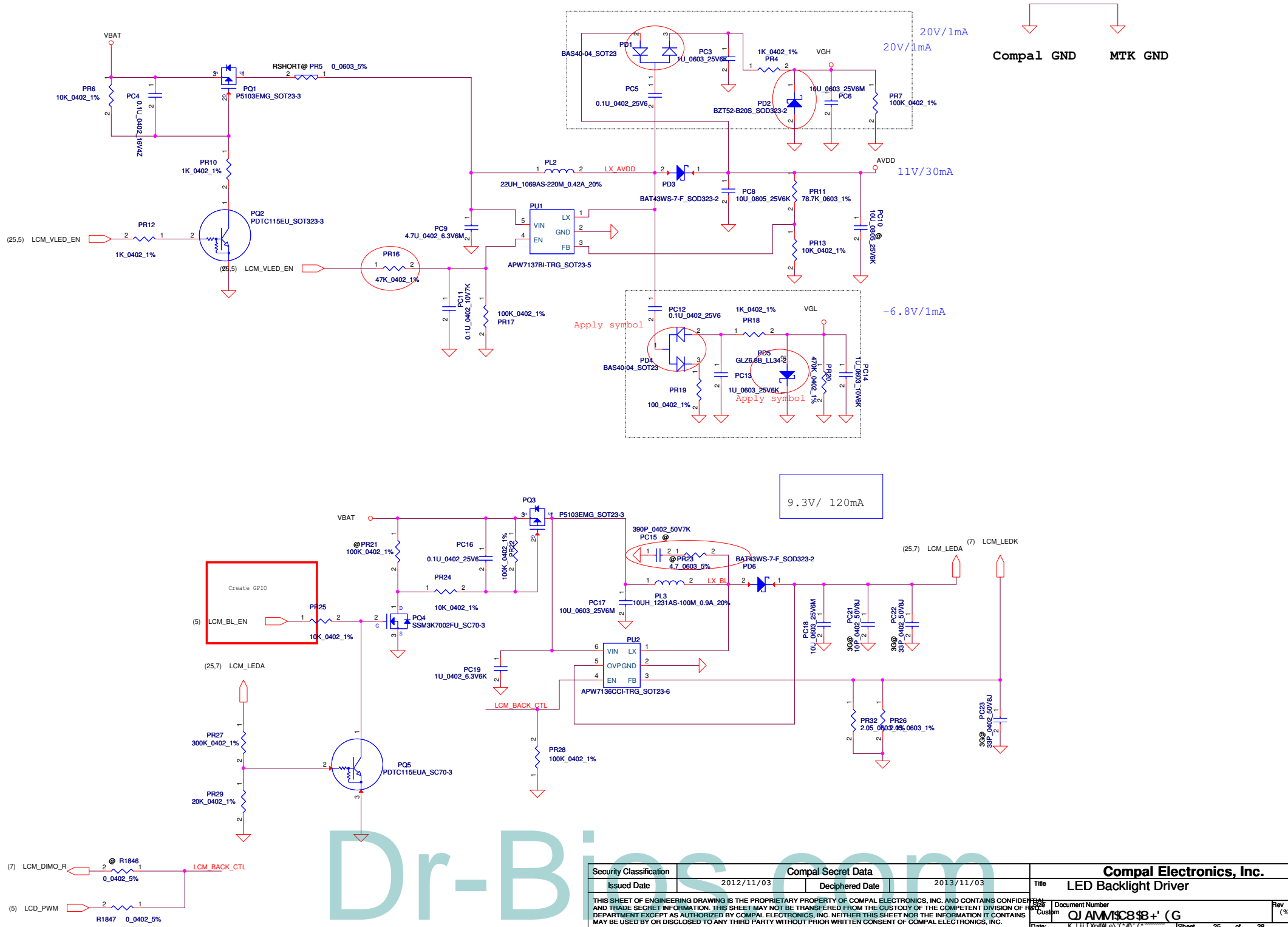
Digital



Audio

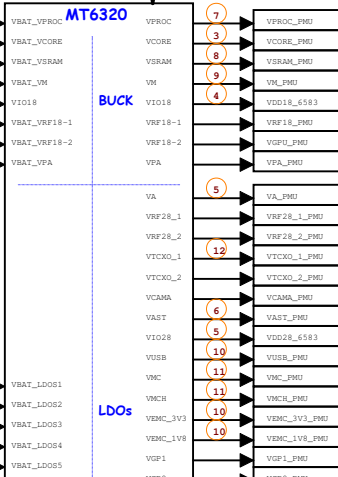
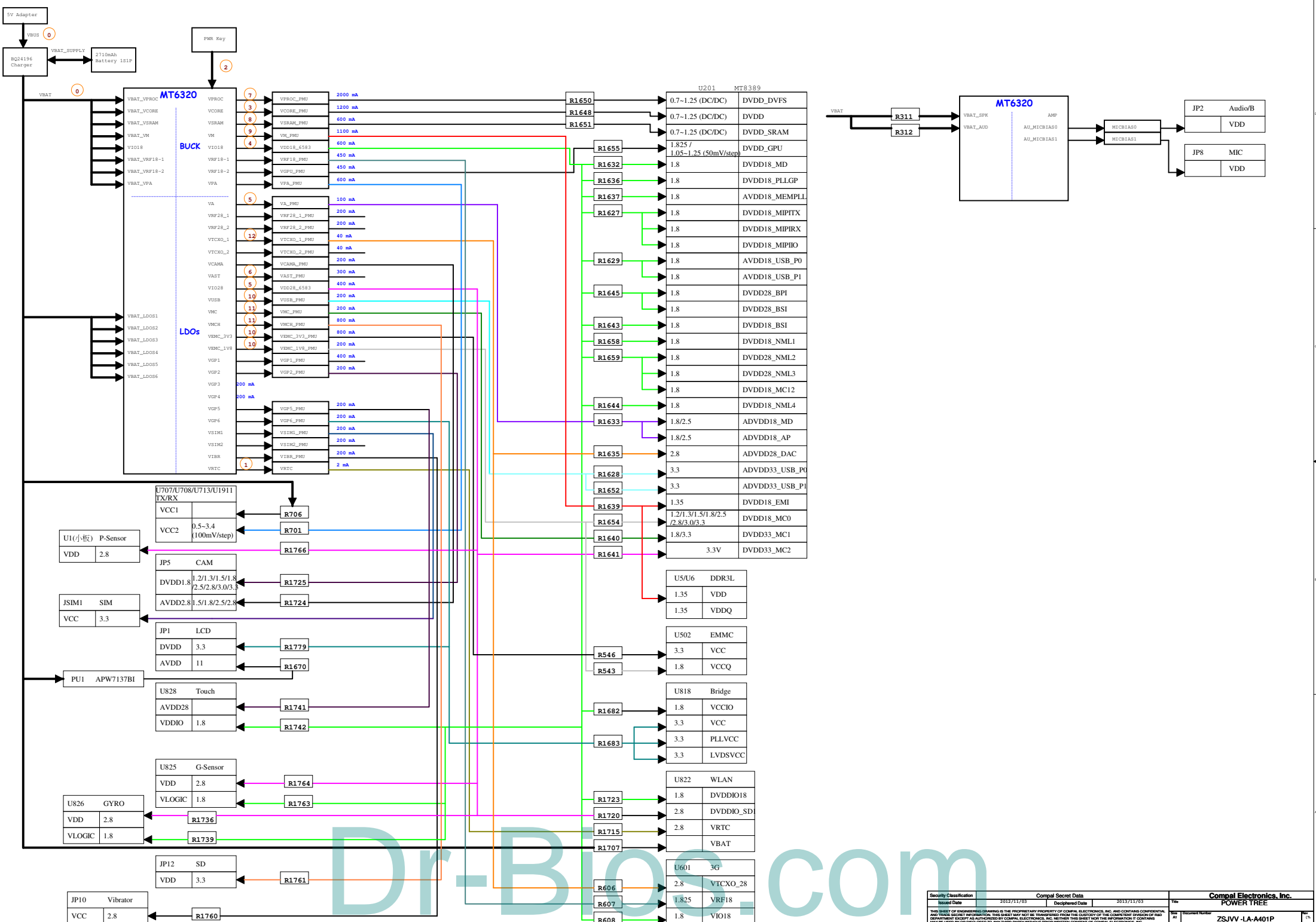


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		OJ AMMCS88+ (G)	(5)
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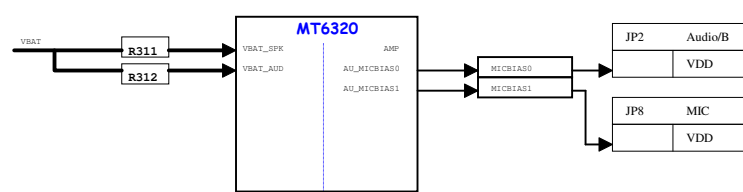
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Title		LED Backlight Driver	
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Pin	Current	Regulator	Output
VPROC_FPM0	2000 mA	R1650	0.7-1.25 (DC/DC)
VCORE_FPM0	1200 mA	R1648	0.7-1.25 (DC/DC)
VSRAM_FPM0	600 mA	R1651	0.7-1.25 (DC/DC)
VM_FPM0	1100 mA		
VDD18_6583	600 mA		
VRF18_FPM0	450 mA		
VRF18_2_FPM0	450 mA		
VFA_FPM0	600 mA		
VA_FPM0	100 mA		
VRF28_1_FPM0	200 mA		
VRF28_2_FPM0	200 mA		
VTCXO_1_FPM0	40 mA		
VTCXO_2_FPM0	40 mA		
VCAMA_FPM0	200 mA		
VAST_FPM0	300 mA		
VDD28_6583	400 mA		
VUSB_FPM0	200 mA		
VMC_FPM0	200 mA		
VWCH_FPM0	800 mA		
VEMC_3V3_FPM0	800 mA		
VEMC_1V8_FPM0	200 mA		
VGP1_FPM0	400 mA		
VGP2_FPM0	200 mA		
VGP3_FPM0	200 mA		
VGP4_FPM0	200 mA		
VGP5_FPM0	200 mA		
VGP6_FPM0	200 mA		
VSI1M1_FPM0	200 mA		
VSI1M2_FPM0	200 mA		
VIBR_FPM0	200 mA		
VRTC	2 mA		

Regulator	Output
R1650	0.7-1.25 (DC/DC)
R1648	0.7-1.25 (DC/DC)
R1651	0.7-1.25 (DC/DC)
R1655	1.825 / 1.05-1.25 (50mV/step)
R1632	1.8
R1636	1.8
R1637	1.8
R1627	1.8
R1629	1.8
R1645	1.8
R1643	1.8
R1658	1.8
R1659	1.8
R1644	1.8
R1633	1.8/2.5
R1635	2.8
R1628	3.3
R1652	3.3
R1639	1.35
R1654	1.2/1.3/1.5/1.8/2.5 / 2.8/3.0/3.3
R1640	1.8/3.3
R1641	3.3V
R1725	1.2/1.3/1.5/1.8 / 2.5/2.8/3.0/3.3
R1724	1.5/1.8/2.5/2.8
R1779	3.3
R1670	1.1
R1741	1.8
R1742	1.8
R1764	2.8
R1763	1.8
R1736	2.8
R1739	1.8
R1761	3.3
R1760	2.8
R606	2.8
R607	1.825
R608	1.8



2013/3/11
 Update PD11 , D308 , D309 , D310 , D311 , L301 ,
 L302 , L303 , L304 , L307 , C647 , C646 , C635 ,
 C638 compal P/N
 Change to un-pop : PR50 , R1838 , R314 ,R315
 Change to pop : R1855

2013/3/13
 Add C318

2013/3/14
 Del R332,R348

2013/4/1
 add R1847
 reserve 0-ohm for panel design

2013/4/10
 reserve C346 for VGP6 PMU
 PR51 change to 15K_0402_1%
 PH1 change to 10K_0402_1%

2013/4/15
 Add PL5 , PC143 ,PC144 for EMI request
 Add PC21 , PC22 ,PC23 for RF request

2013/4/22
 Change L307 and C342 to reserve

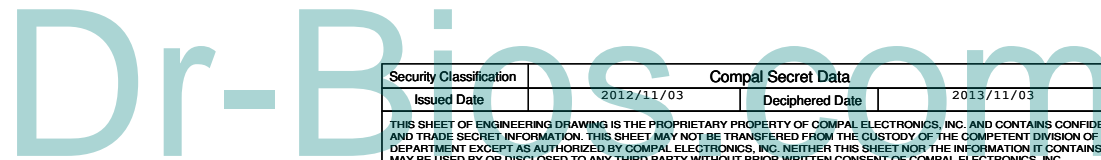
2013/5/6
 Change L306 and C338 BOM structure to 3G@

2013/5/6
 Change PC21,PC22 and PC23 BOM structure to 3G@
 PC21 change from 22P to 10P
 PC22 and PC23 change 68P to 33P

2013/5/8
 Change L305 and C337 BOM structure to 3G@

2013/5/8
 Change D311 and C638 BOM structure to 3G@

2013/5/20
 Change PR30 , PR31 ,R339 ,R321 , R322 , R323
 R324 , R325 , R326 , R327 , R328, R329 , R338 ,
 R1855 to R-SHORT



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2013/2/25
P04. NC parts change to 0.luF_0402_NC
R1648-R1651 to 0_0603
R1639 to 0_0805
P05. R634 from P19 to P05
update X602 symbol
del no functin net :
U201C_AF10, U201D_C29-J25, U201E_G2
P06. DEL TP1392PAD-TP1395PAD
P07. DEL R1672,R1673,net cabc_enable1/0
C1687-C1689 change to 1U_16V
C1691-C1698 change to 0.1U_10V%0.01U_16V
C1692,C1695 to @
P08. DEL R1702,net ADC1_ACCESSORY
JP2_PIN_8 change to VEMC_3V3_PMU
JP2_PIN_6 change to GND
P09. R1711,R1716-R1719,R1721,R1722 to 0_0201
P11. DEL R1731,R1732, net UTXD1/URXD1_DEBUG
R1729,R1730 to short
R1728,R1727 del @
P12. R1764 to short
U825 VDD28_6583 change to VDD18_6583
P13. DEL R1740, net VDD28_6583
H7 change to H_1P5X2P0N
P14. R1743,R1744 to short
P15. SW3 PIN.3 to KC0L0
SW4 PIN.3 to KC0L1
P16. JP10 PIN.2 to GND
P17. ADD D7
ADD C9-C12 for RF request
P18. DEL RM2
R1765,R1766 move to U201 side
Update 3G schematic:
1.change size from 0402 to 0201 of R633,C631,R612,R1771
2.add shunt cap in RX matching circuit.
3.Remove R645,U614,C644,R643
Change C656,C643 bom structure to @
4.remove R613
5.change C610,C609,C614,C613 footprint from 0402 to 0201
6. change C601,C604 footprint from 0402 to 0201
(need update P/N)
7 .Del R634 (change to P5)

2013/2/26
Update block diagram,Notes list
P05. DEL net URXD2,UTXD2,BP11_BUSS5/6,VBIA5
R1765 pin2 contact USB11_DP_P
R1766 pin2 contact USB11_DM_N
P06. C64 , C70 , C 68 change to 0201
P08. JP2_PIN2. del GND
P09. Del net MT6620_EEDI,MT6620_SDI01
RTCCLK_6620,SYSRST_B_6620,PMU_EN_6620
GPS_SYNC_6620
P12. ADD R1764 for co-lay STK8313
Update 3G schematic:
1.Update C601,C604 P/N to SE000010K00
2.C754,C749,C748,C750: @ to 3G@
3.Change C733 to 0.01u
4.add remind as below in C1844 and C722

2013/2/27
P04. R1639 to 0805 short pad
P05. R1765,R1766 change to 3G@
P10. JPS mirror horizontally
P13. PAD1-3 change to S2-4
P17. ADD C13
C9-C12 change to @

2013/3/1
P04. change R1639 footprint from 0805-s to 0805
R1648,R1650 change to 0603 short pad
update PWR schematic
update 3G schematic
Due to RX trace "交錯線"
Change W_RX_HB1_SE_M_1 to U616.8
W_RX_HB1_SE_P_1 to U616.1

2013/3/1 A
P05. U201_AH4,AF6 change to MB_ID0/1
U201_AJ3,AH5,AG5 change to GPIO_0-3
ADD R1520-R1529,net CHG_TEMP
P11. DEL R1727
update PWR schematic

2013/3/1_B
P04. NC parts change to 0805 short pad
P21. update PU3 footprint
P24. DEL PR3

2013/3/2
update 3G schematic
P19. U613.7 contact to D_W_RX_HB5_P_3
U613.8 contact to D_W_RX_HB5_M_3
update PWR schematic
P22. GND_DCCD contact to GND

2013/3/3
update PWR schematic

2013/3/4
P03. update MB_ID table
fix DRC error

2013/3/5
update 3G schematic.
Del TP1396,TP1397
update PWR schemaitc
fix DRC error

2013/3/6
P05. net MB_ID1 change to U201_AA28
net GPIO_0 change to U201_AA29

2013/3/7
P07. ADD R1684 for IT6211VG
ADD R1675,R1678, net CAB_C_ENABLE0/1
P09. U822_PIN15 change to net MT6620_1V8
remove R1720, C1736
Add P21. for GPS MT3332 circuit
update 3G schematic
P19. Add C1896-C1901
del R633,R619,R620,R611

2013/3/8
P13. DEL CLIP10,14, S4
Update 3G schematic
U601.A11 ----> NC

2013/3/8A
P09. ADD R1713
Update MT3332 schematic
remove R1858,R1861,R1863,R1865
R1866,R1868

2013/3/9a
Update 3G schematic
modify C1898,C1899 net name

2013/3/11
Add P26 POWER_PIR
P21. Update L856 LIB & P/N
update PWR schematic

2013/3/12
update block diagram
add power tree page

2013/3/13
ADD NH520@,NH520_EMC@
modify RAM BOM structure
P01 Add S4 for GLONASS frame
P05. Del TP1373
P07 R1675,R1678 to @
ADD C1772,C1773 for RF request
P09. update U823 symbol
P13. R535 to 27ohm(MTK advice)
P15. R1753,C1759 to @ (MTK advice)
P17. ADD TP1364,TP1365 for RF request
update 3G schematic
delete C643,C656,TP601,TP602

2013/3/14
P05. ADD TP1366,TP1369
P21. C1887,C1902-->SE00000WN00
C1888,C1889,C1891,C1893-->SE00000UC00
C1890-->SE00000V600
update 3G schematic
Add C1903 for U708.

2013/3/18
L838,L839,L840,L841,L842,L853
change to SM070001R00

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