

# R2 UMA(11.6")

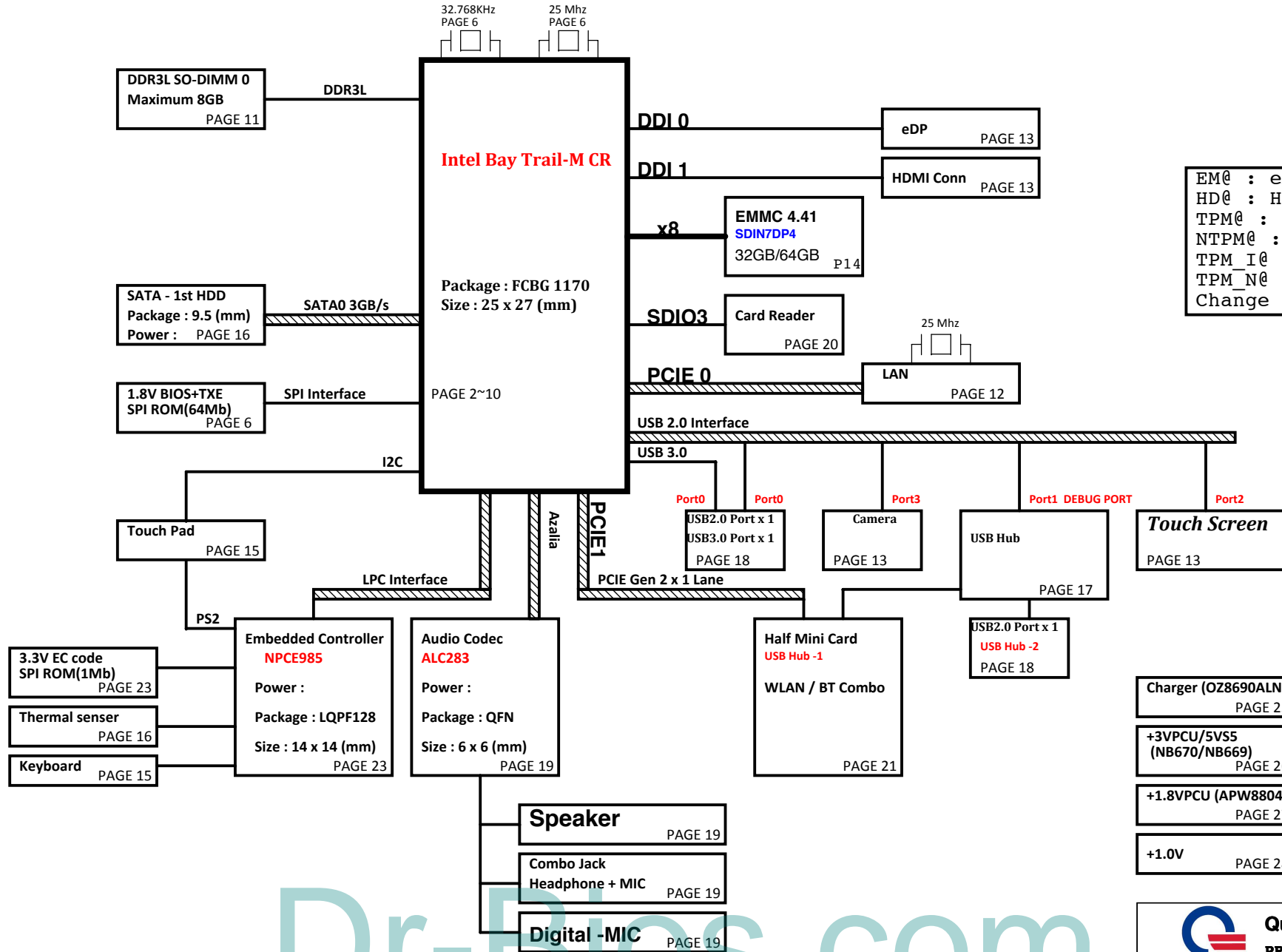
# 01

## Intel Bay Trail-M Platform Block Diagram

PCB 6L STACK UP

LAYER 1 : TOP  
 LAYER 2 : SGND  
 LAYER 3 : IN1(High)  
 LAYER 4 : IN2  
 LAYER 5 : SVCC  
 LAYER 6 : BOT

EM@ : eMMC  
 HD@ : HDD  
 TPM@ : TPM  
 NTPM@ : Non-TPM  
 TPM\_I@ : 新唐  
 TPM\_N@ : 英飛凌  
 Change CPU & EMMC P/N

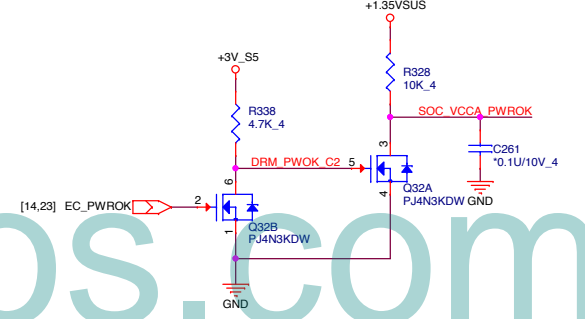
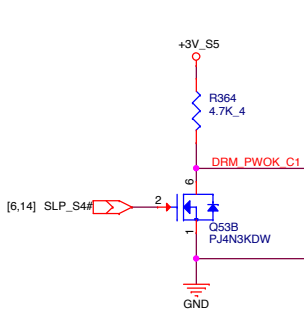
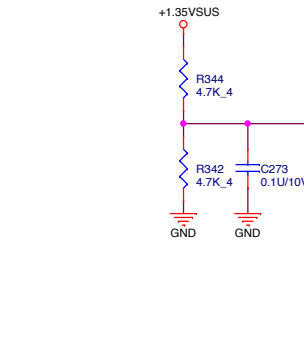
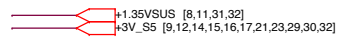
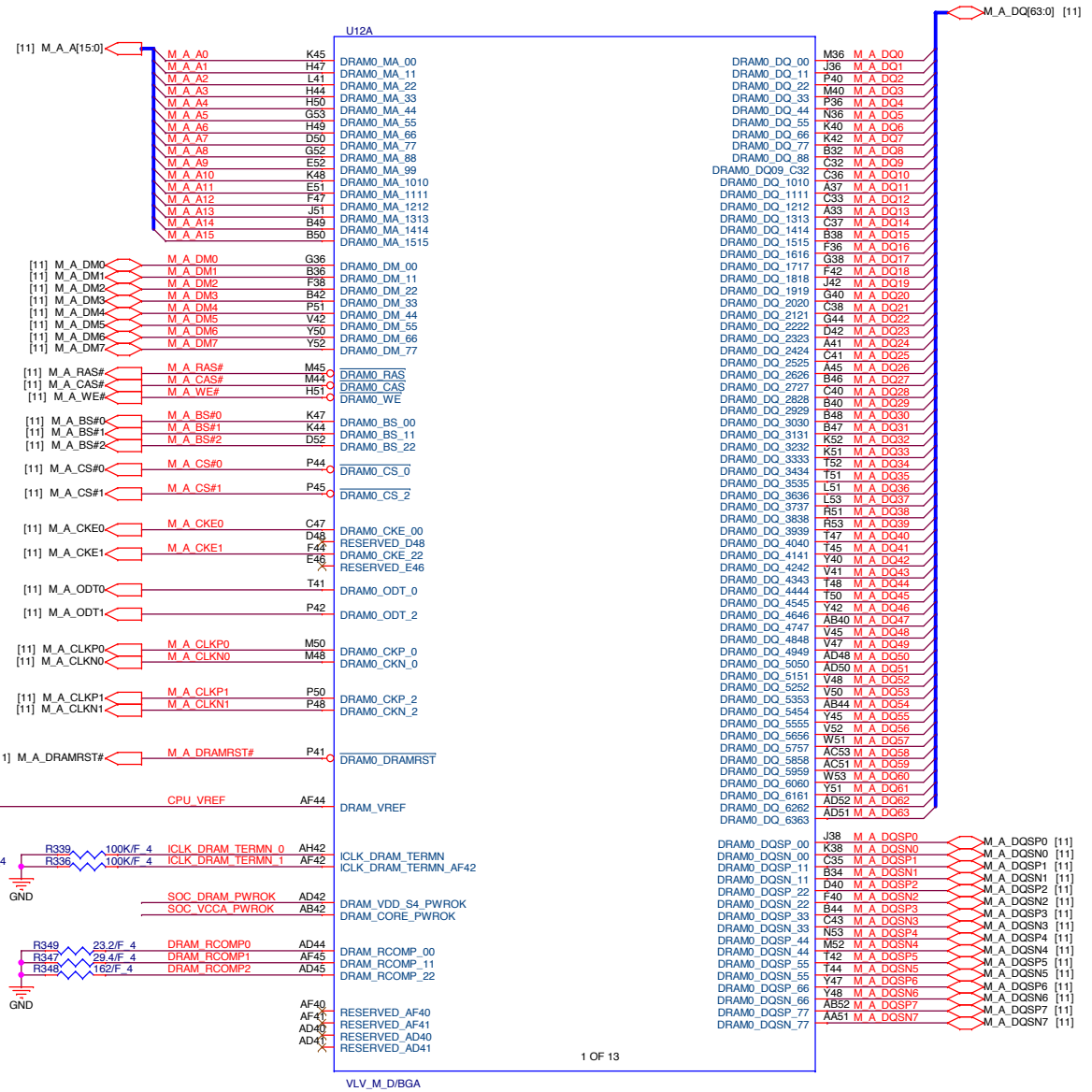


Dr-Bios.com

**Quanta Computer Inc.**  
 PROJECT : ZHK

Size Document Number Rev 1A  
**Intel Block Diagram**

Date: Monday, June 30, 2014 Sheet 1 of 33



Dr-Bios.com

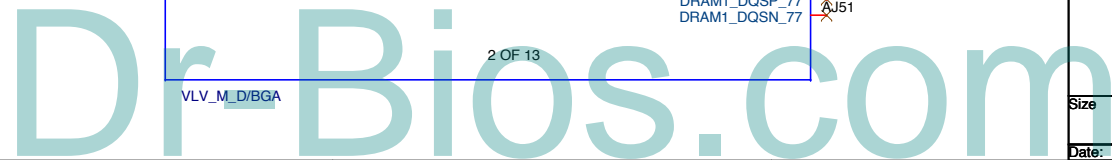
U12B


~~AY45~~ DRAM1\_MA\_00  
~~BB47~~ DRAM1\_MA\_11  
~~AW41~~ DRAM1\_MA\_22  
~~BB44~~ DRAM1\_MA\_33  
~~BB50~~ DRAM1\_MA\_44  
~~BC53~~ DRAM1\_MA\_55  
~~BB49~~ DRAM1\_MA\_66  
~~BF50~~ DRAM1\_MA\_77  
~~BC52~~ DRAM1\_MA\_88  
~~BE52~~ DRAM1\_MA\_99  
~~AY48~~ DRAM1\_MA\_1010  
~~BE51~~ DRAM1\_MA\_1111  
~~BD47~~ DRAM1\_MA\_1212  
~~BA51~~ DRAM1\_MA\_1313  
~~BH49~~ DRAM1\_MA\_1414  
~~BH50~~ DRAM1\_MA\_1515  
  
~~BD38~~ DRAM1\_DM\_00  
~~BH36~~ DRAM1\_DM\_11  
~~BC36~~ DRAM1\_DM\_22  
~~BH42~~ DRAM1\_DM\_33  
~~AT51~~ DRAM1\_DM\_44  
~~AM42~~ DRAM1\_DM\_55  
~~AK50~~ DRAM1\_DM\_66  
~~AK52~~ DRAM1\_DM\_77  
  
~~AV45~~ DRAM1\_RAS  
~~AV49~~ DRAM1\_CAS  
~~BB51~~ DRAM1\_WE  
  
~~AY47~~ DRAM1\_BS\_00  
~~AY44~~ DRAM1\_BS\_11  
~~BF52~~ DRAM1\_BS\_22  
  
~~AT44~~ DRAM1\_CS\_0  
~~AT45~~ DRAM1\_CS\_2  
  
~~BG47~~ DRAM1\_CKE\_00  
~~BE46~~ RESERVED\_BE46  
~~BD44~~ DRAM1\_CKE\_22  
~~BF48~~ RESERVED\_BF48  
  
~~AP41~~ DRAM1\_ODT\_0  
~~AT42~~ DRAM1\_ODT\_2  
  
~~AV50~~ DRAM1\_CKP\_0  
~~AV48~~ DRAM1\_CKN\_0  
  
~~AT50~~ DRAM1\_CKP\_2  
~~AT48~~ DRAM1\_CKN\_2  
  
~~AT41~~ DRAM1\_DRAMRST

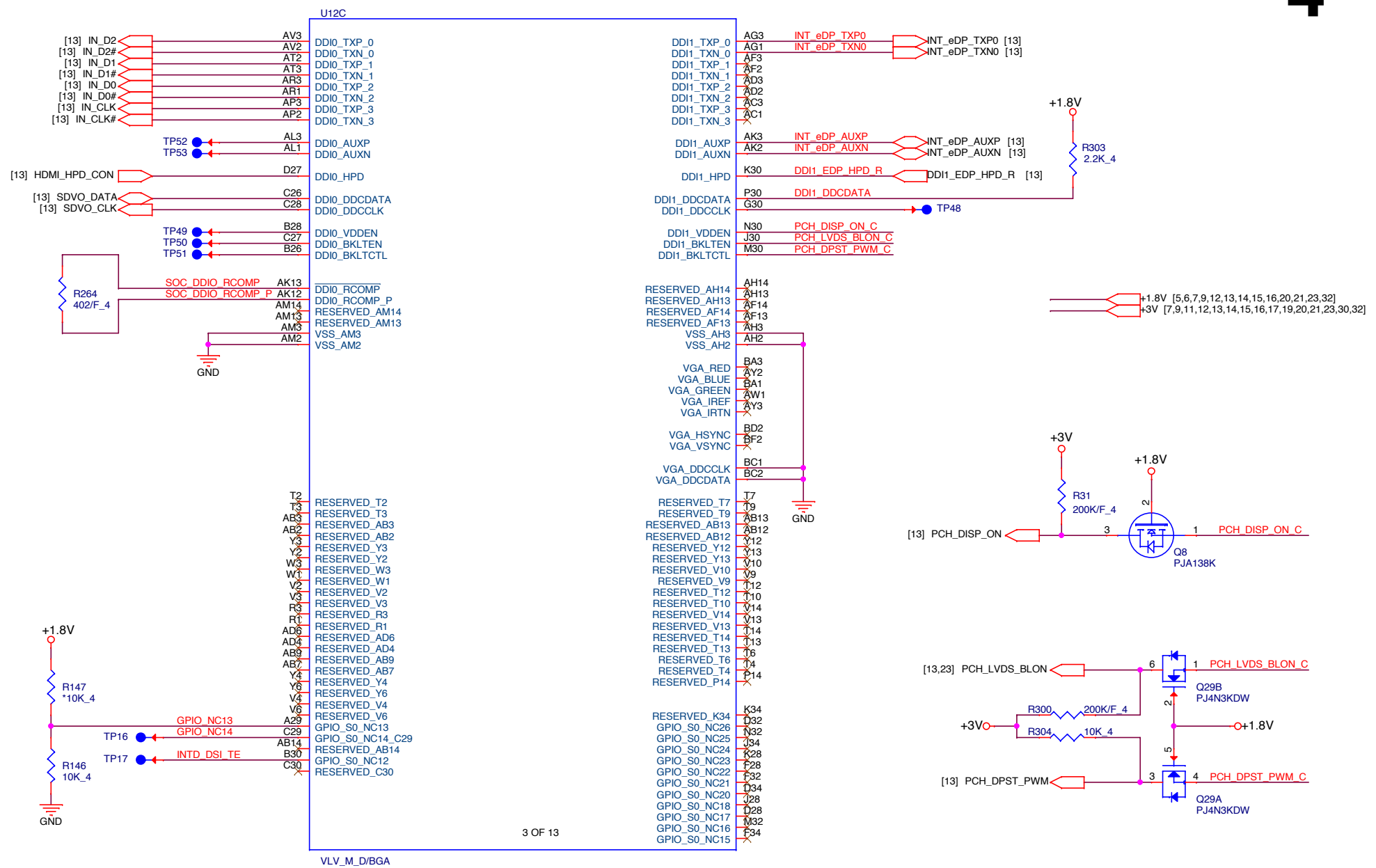
~~DRAM1\_DQ\_00~~ BG38  
~~DRAM1\_DQ\_11~~ BC40  
~~DRAM1\_DQ\_22~~ BA42  
~~DRAM1\_DQ\_33~~ BD42  
~~DRAM1\_DQ\_44~~ BC38  
~~DRAM1\_DQ\_55~~ BD36  
~~DRAM1\_DQ\_66~~ BF42  
~~DRAM1\_DQ\_77~~ BC44  
~~DRAM1\_DQ\_88~~ BH32  
~~DRAM1\_DQ\_99~~ BG32  
~~DRAM1\_DQ\_1010~~ BG36  
~~DRAM1\_DQ\_1111~~ BJ37  
~~DRAM1\_DQ\_1212~~ BG33  
~~DRAM1\_DQ\_1313~~ BJ33  
~~DRAM1\_DQ\_1414~~ BG37  
~~DRAM1\_DQ\_1515~~ BH38  
~~DRAM1\_DQ\_1616~~ AU36  
~~DRAM1\_DQ\_1717~~ AT36  
~~DRAM1\_DQ\_1818~~ AV40  
~~DRAM1\_DQ\_1919~~ AT40  
~~DRAM1\_DQ\_2020~~ BA36  
~~DRAM1\_DQ\_2121~~ AV36  
~~DRAM1\_DQ\_2222~~ AY42  
~~DRAM1\_DQ\_2323~~ AY40  
~~DRAM1\_DQ\_2424~~ BJ41  
~~DRAM1\_DQ\_2525~~ BG41  
~~DRAM1\_DQ\_2626~~ BJ45  
~~DRAM1\_DQ\_2727~~ BH46  
~~DRAM1\_DQ\_2828~~ BG40  
~~DRAM1\_DQ\_2929~~ BH40  
~~DRAM1\_DQ\_3030~~ BH48  
~~DRAM1\_DQ\_3131~~ BH47  
~~DRAM1\_DQ\_3232~~ AY52  
~~DRAM1\_DQ\_3333~~ AY51  
~~DRAM1\_DQ\_3434~~ AP52  
~~DRAM1\_DQ\_3535~~ AP51  
~~DRAM1\_DQ\_3636~~ AV51  
~~DRAM1\_DQ\_3737~~ AV53  
~~DRAM1\_DQ\_3838~~ AR51  
~~DRAM1\_DQ\_3939~~ AR53  
~~DRAM1\_DQ\_4040~~ AP47  
~~DRAM1\_DQ\_4141~~ AP45  
~~DRAM1\_DQ\_4242~~ AK40  
~~DRAM1\_DQ\_4343~~ AM41  
~~DRAM1\_DQ\_4444~~ AP48  
~~DRAM1\_DQ\_4545~~ AP50  
~~DRAM1\_DQ\_4646~~ AK42  
~~DRAM1\_DQ\_4747~~ AH40  
~~DRAM1\_DQ\_4848~~ AM45  
~~DRAM1\_DQ\_4949~~ AM47  
~~DRAM1\_DQ\_5050~~ AF48  
~~DRAM1\_DQ\_5151~~ AF50  
~~DRAM1\_DQ\_5252~~ AM48  
~~DRAM1\_DQ\_5353~~ AH44  
~~DRAM1\_DQ\_5454~~ AK45  
~~DRAM1\_DQ\_5555~~ AM52  
~~DRAM1\_DQ\_5656~~ AL51  
~~DRAM1\_DQ\_5757~~ AG53  
~~DRAM1\_DQ\_5858~~ AG51  
~~DRAM1\_DQ\_5959~~ AL53  
~~DRAM1\_DQ\_6060~~ AK51  
~~DRAM1\_DQ\_6161~~ AF52  
~~DRAM1\_DQ\_6262~~ AF51  
~~DRAM1\_DQ\_6363~~ AF51

~~DRAM1\_DQSP\_00~~ BF40  
~~DRAM1\_DQSN\_00~~ BD40  
~~DRAM1\_DQSP\_11~~ BG35  
~~DRAM1\_DQSN\_11~~ BH34  
~~DRAM1\_DQSP\_22~~ BA38  
~~DRAM1\_DQSN\_22~~ AY38  
~~DRAM1\_DQSP\_33~~ BH44  
~~DRAM1\_DQSN\_33~~ BG43  
~~DRAM1\_DQSP\_44~~ AU53  
~~DRAM1\_DQSN\_44~~ AV52  
~~DRAM1\_DQSP\_55~~ AP42  
~~DRAM1\_DQSN\_55~~ AP44  
~~DRAM1\_DQSP\_66~~ AK47  
~~DRAM1\_DQSN\_66~~ AK48  
~~DRAM1\_DQSP\_77~~ AH52  
~~DRAM1\_DQSN\_77~~ AJ51

VLV\_M\_D/BGA



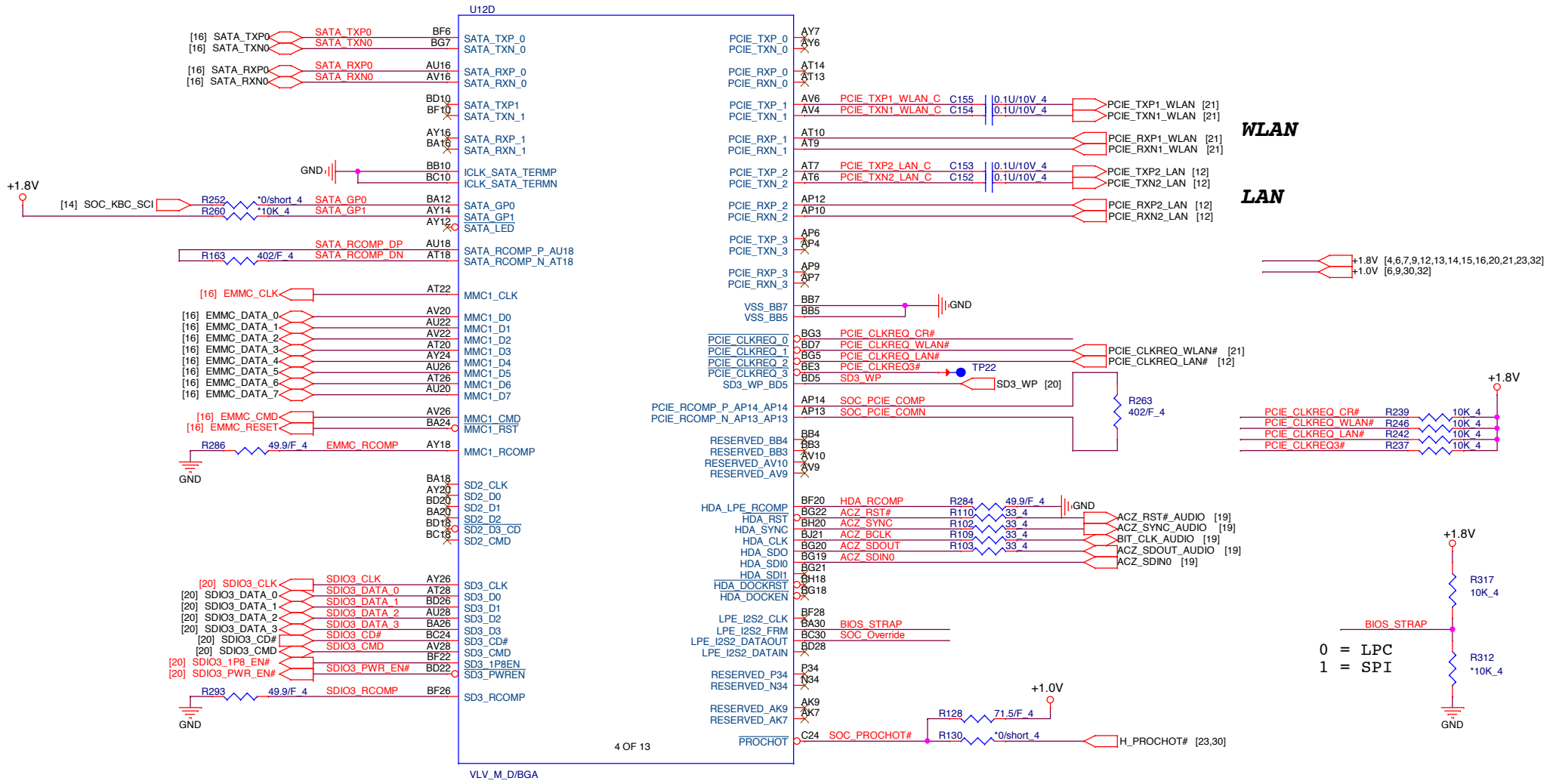
 <b>Quanta Computer Inc.</b> <b>PROJECT : ZHK</b>		Rev
		1A
Size	Document Number	
Valley 2/9 (DDR8)		
Date:	Monday, June 30, 2014	Sheet 3 of 33



3 OF 13

**Quanta Computer Inc.**  
**PROJECT : ZHK**

Size	Document Number	Rev
	<b>Valley 3/9 (Display)</b>	1A
Date:	Monday, June 30, 2014	Sheet 4 of 33



WLAN

LAN

+1.8V [4,6,7,9,12,13,14,15,16,20,21,23,32]  
 +1.0V [6,9,30,32]

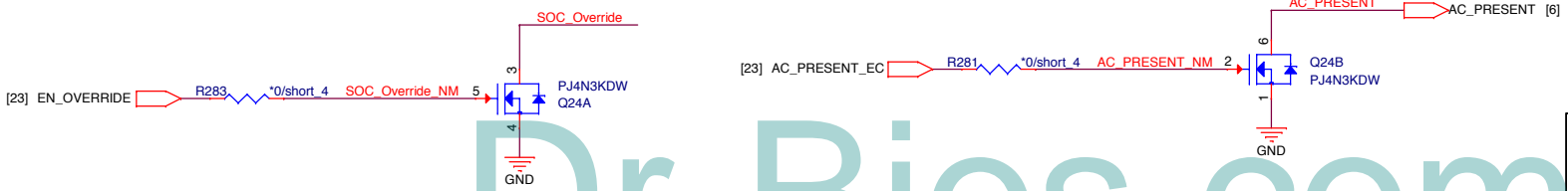
PCIE\_CLKREQ\_CR# R239 10K 4  
 PCIE\_CLKREQ\_WLAN# R246 10K 4  
 PCIE\_CLKREQ\_LAN# R242 10K 4  
 PCIE\_CLKREQ3# R237 10K 4

+1.8V  
 R317 10K\_4  
 BIOS\_STRAP  
 R312 \*10K\_4  
 GND

0 = LPC  
 1 = SPI

Security Flash Descriptors  
 0 = Override  
 1 = Normal Operation

AC Present: This input pin indicates when the platform is plugged into AC power.

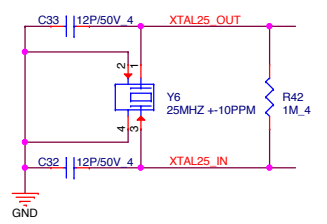


Dr-Bios.com

**Quanta Computer Inc.**  
**PROJECT : ZHK**

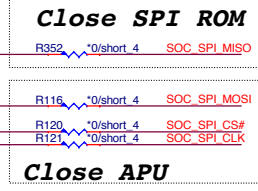
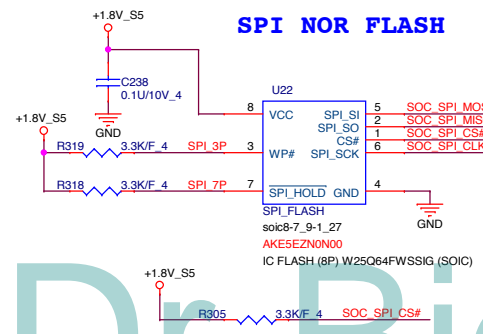
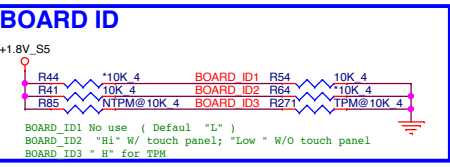
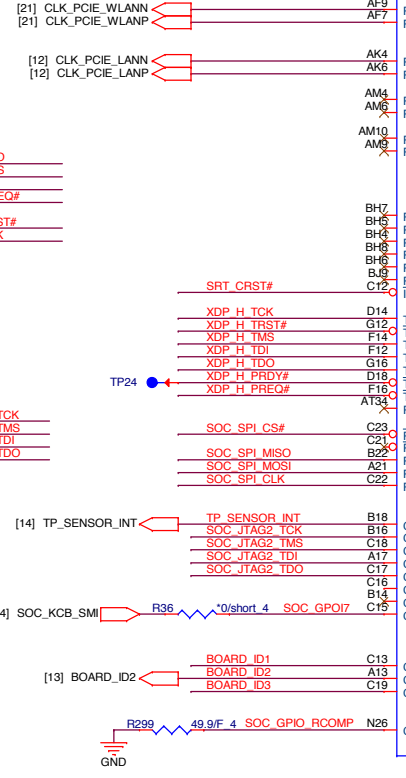
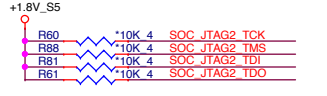
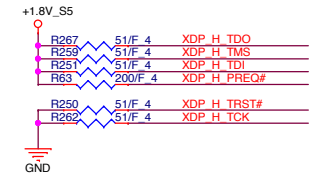
Size	Document Number	Rev
	Valley 4/9 (SD/PCIE/SATA)	1A
Date:	Monday, June 30, 2014	Sheet 5 of 33

+1.8V\_S5 [7,9,14,32]  
+1.8V [4,5,7,9,12,13,14,15,16,20,21,23,32]  
+5V\_S5 [18,26,28,29,30,31,32]  
+3VPCU [8,13,15,16,19,23,25,26,27,32]



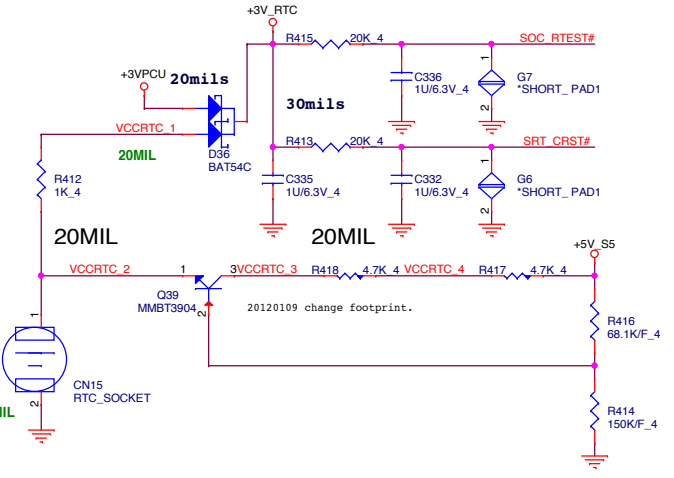
WLAN clk

LAN clk

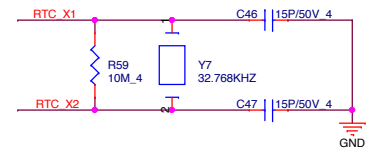


ML1220 Coin type  
AHL03001424 FDK (SAY) 15mAh  
AHL03017100 Panasonic (MAT) 17mAh

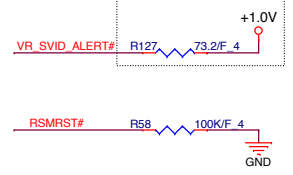
RTC Circuitry(RTC)



RTC Clock 32.768KHz



Close APU



Quanta Computer Inc. PROJECT : ZHK  
Size Document Number Valley 5/9 (SP/GPIO/CLK) Rev 1A  
Date: Monday, June 30, 2014 Sheet 6 of 33

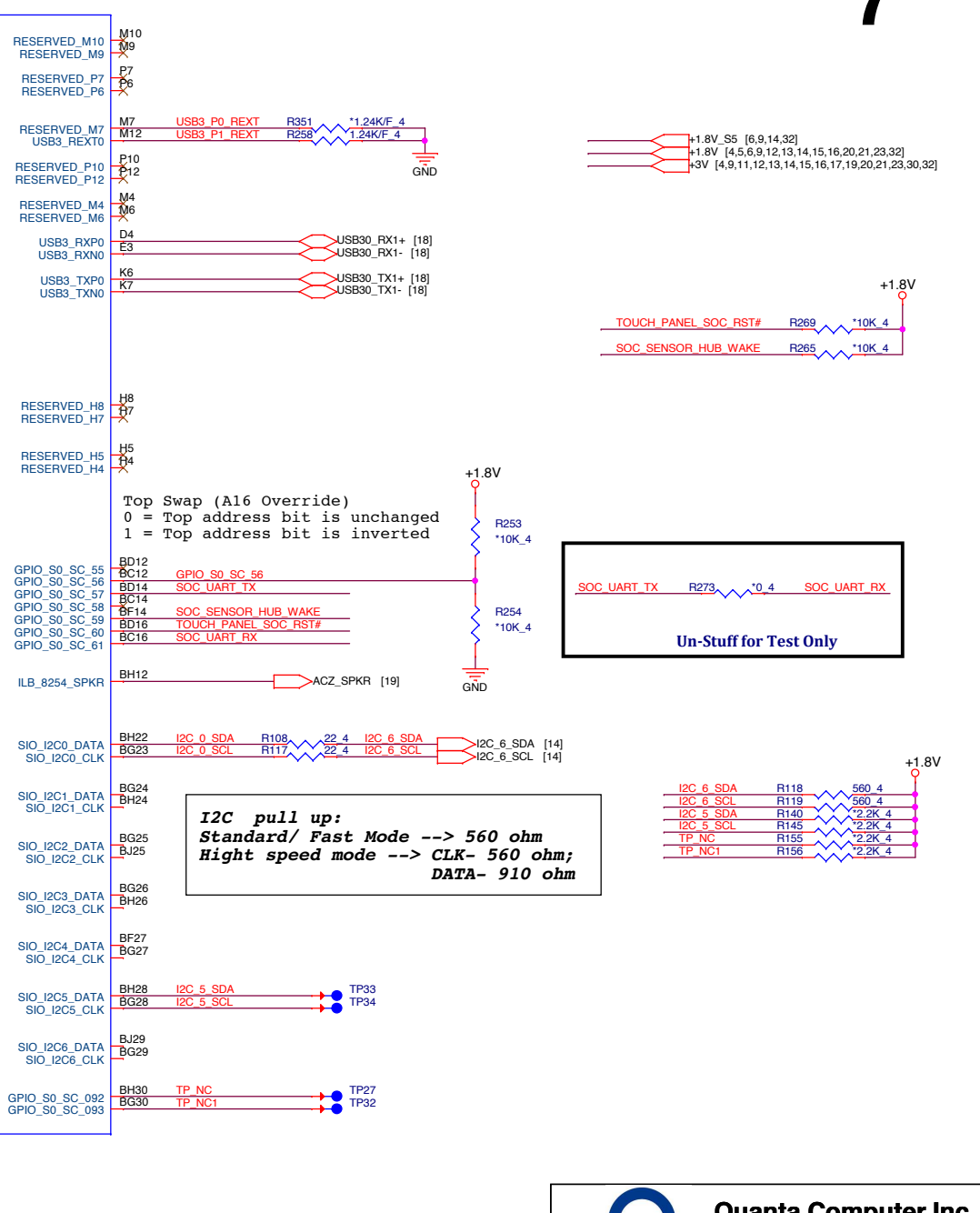
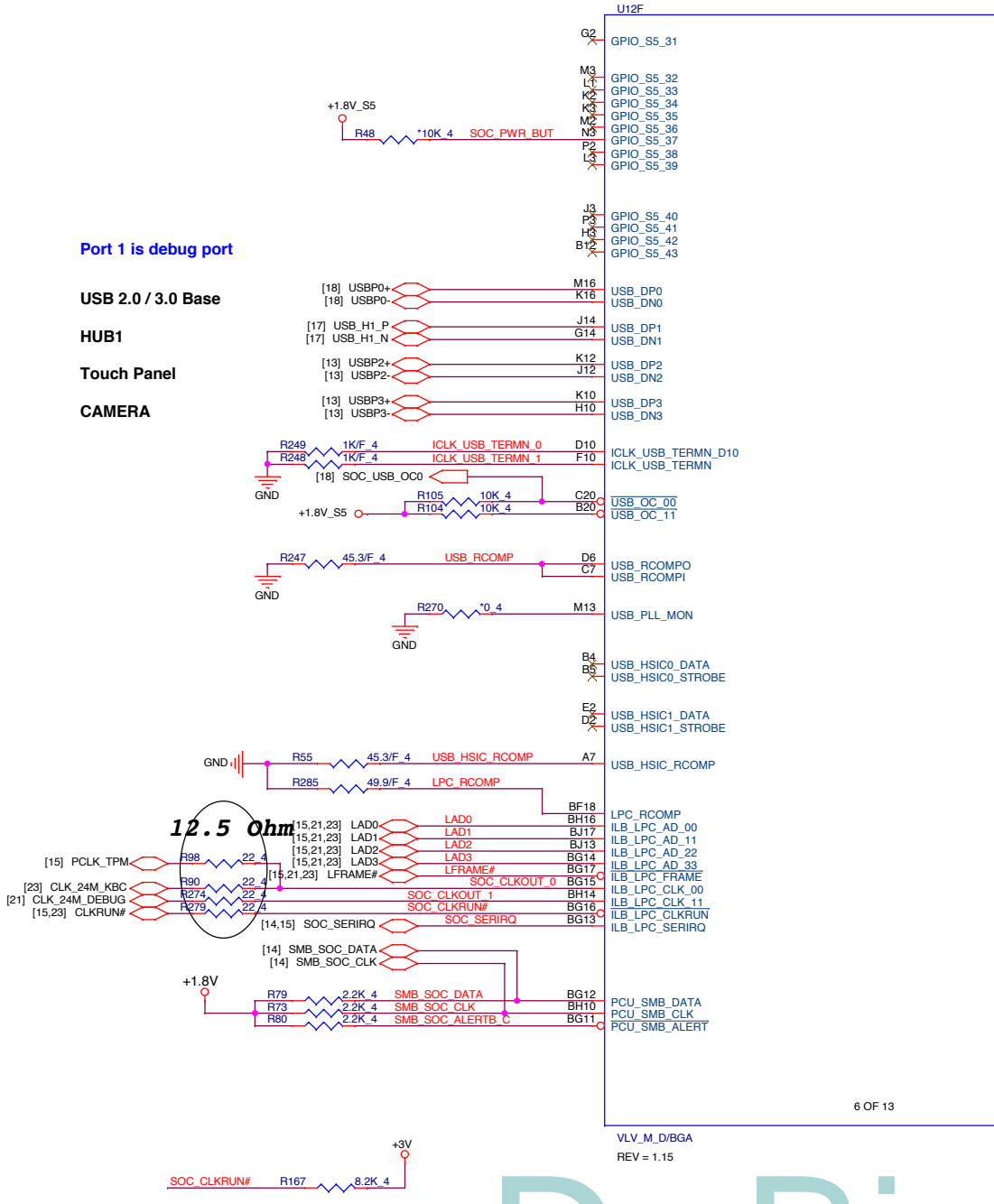
Port 1 is debug port

USB 2.0 / 3.0 Base

HUB1

Touch Panel

CAMERA



+1.8V\_S5 [6,9,14,32]  
 +1.8V [4,5,6,9,12,13,14,15,16,20,21,23,32]  
 +3V [4,9,11,12,13,14,15,16,17,19,20,21,23,30,32]

+1.8V  
 TOUCH PANEL SOC\_RST# R269 \*10K 4  
 SOC\_SENSOR\_HUB\_WAKE R265 \*10K 4

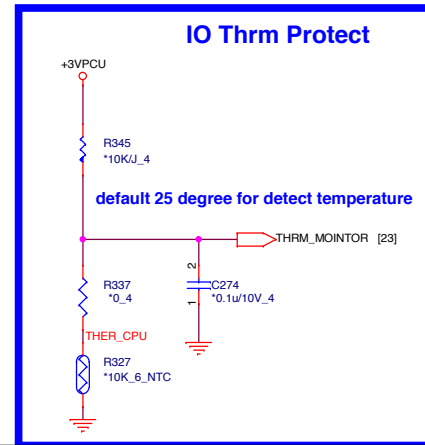
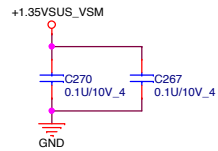
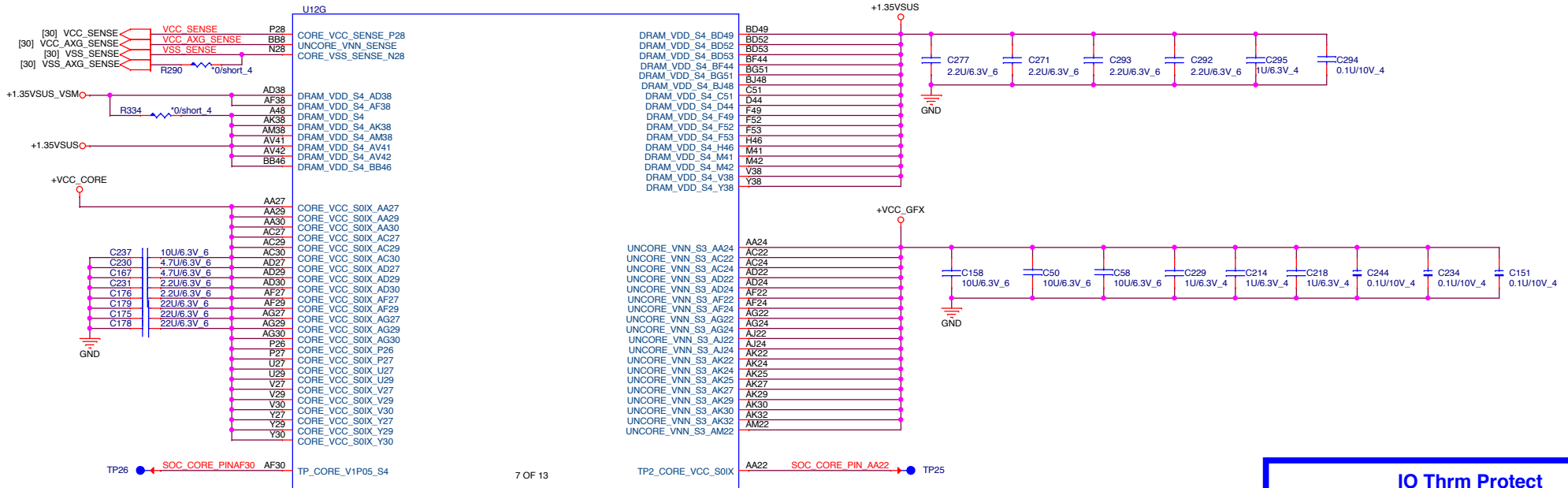
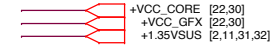
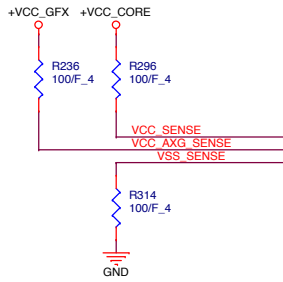
Top Swap (A16 Override)  
 0 = Top address bit is unchanged  
 1 = Top address bit is inverted



I2C pull up:  
 Standard/ Fast Mode --> 560 ohm  
 High speed mode --> CLK- 560 ohm;  
 DATA- 910 ohm

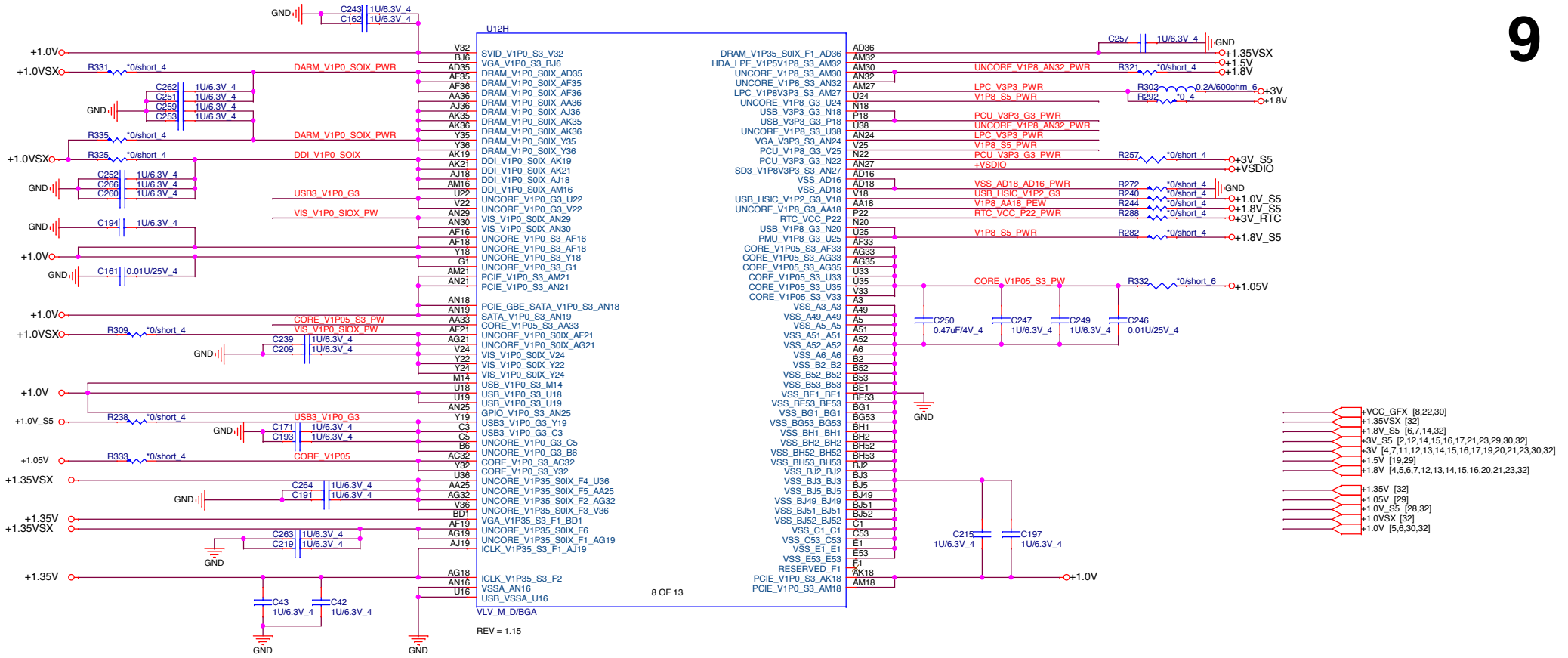
+1.8V  
 I2C 6\_SDA R118 560 4  
 I2C 6\_SCL R119 560 4  
 I2C 5\_SDA R140 \*2.2K 4  
 I2C 5\_SCL R145 \*2.2K 4  
 TP\_NC R155 \*2.2K 4  
 TP\_NC1 R156 \*2.2K 4



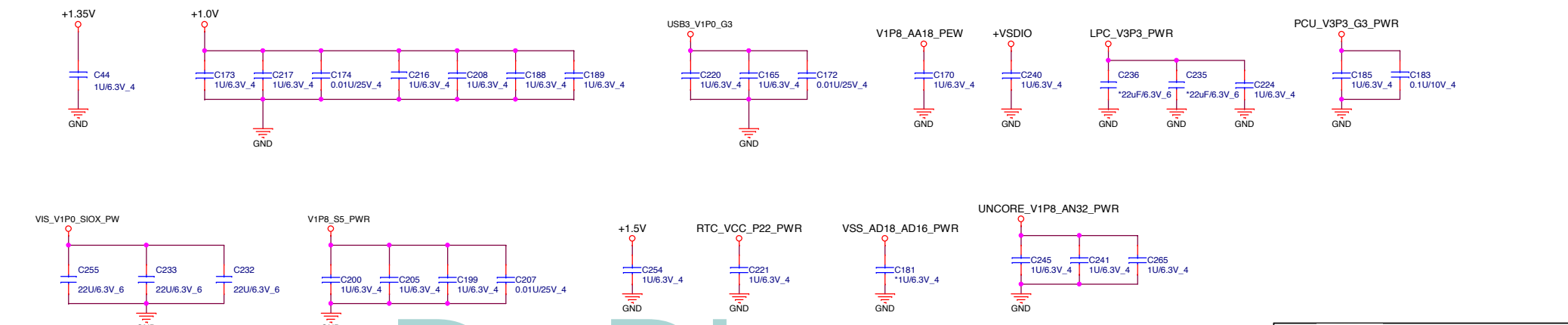


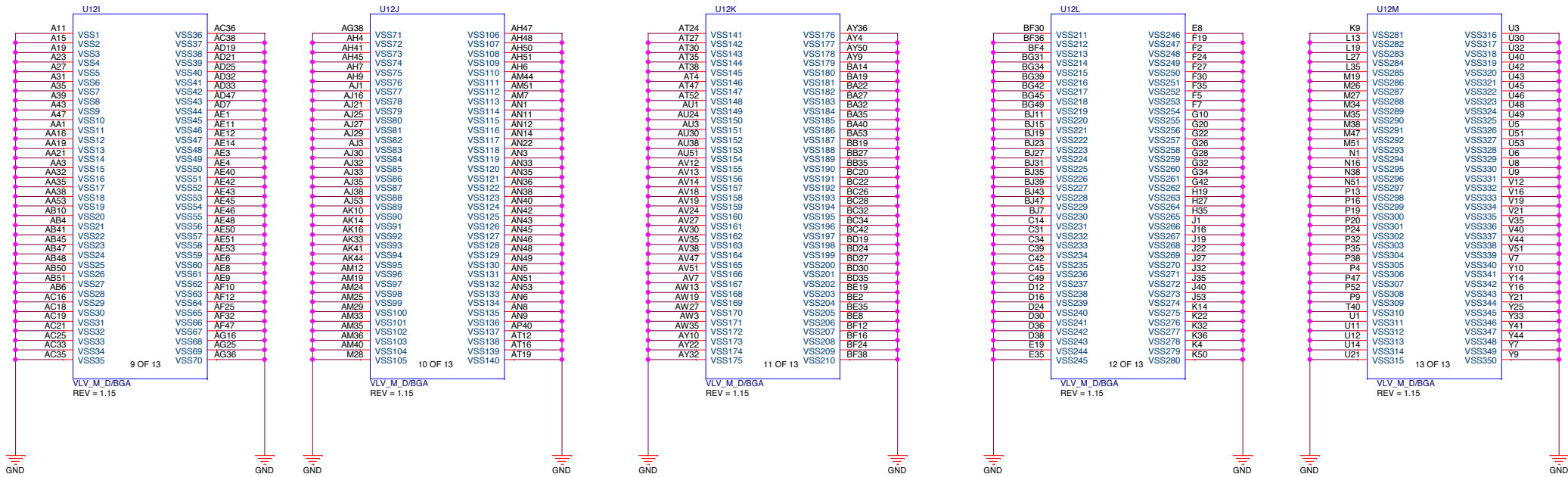
Dr-Bios.com

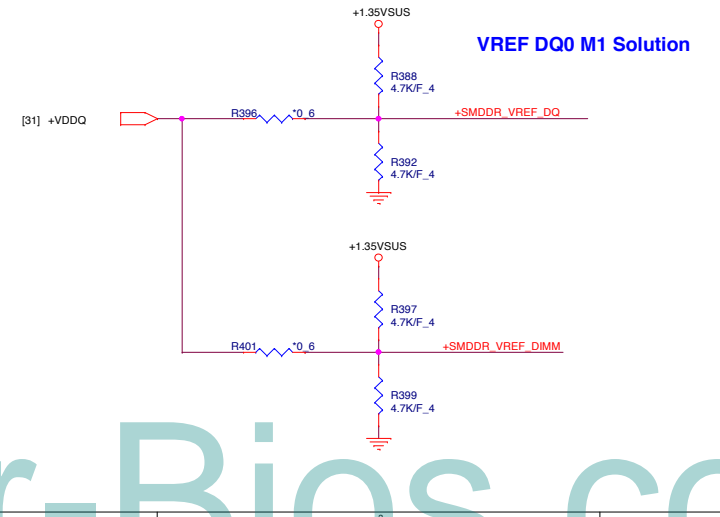
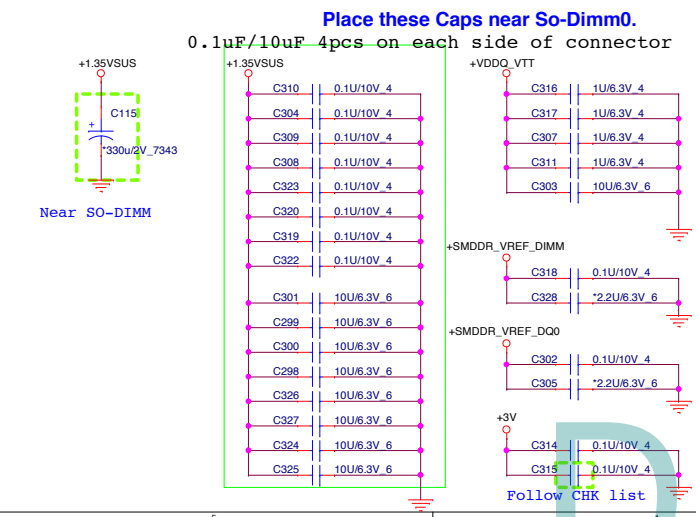
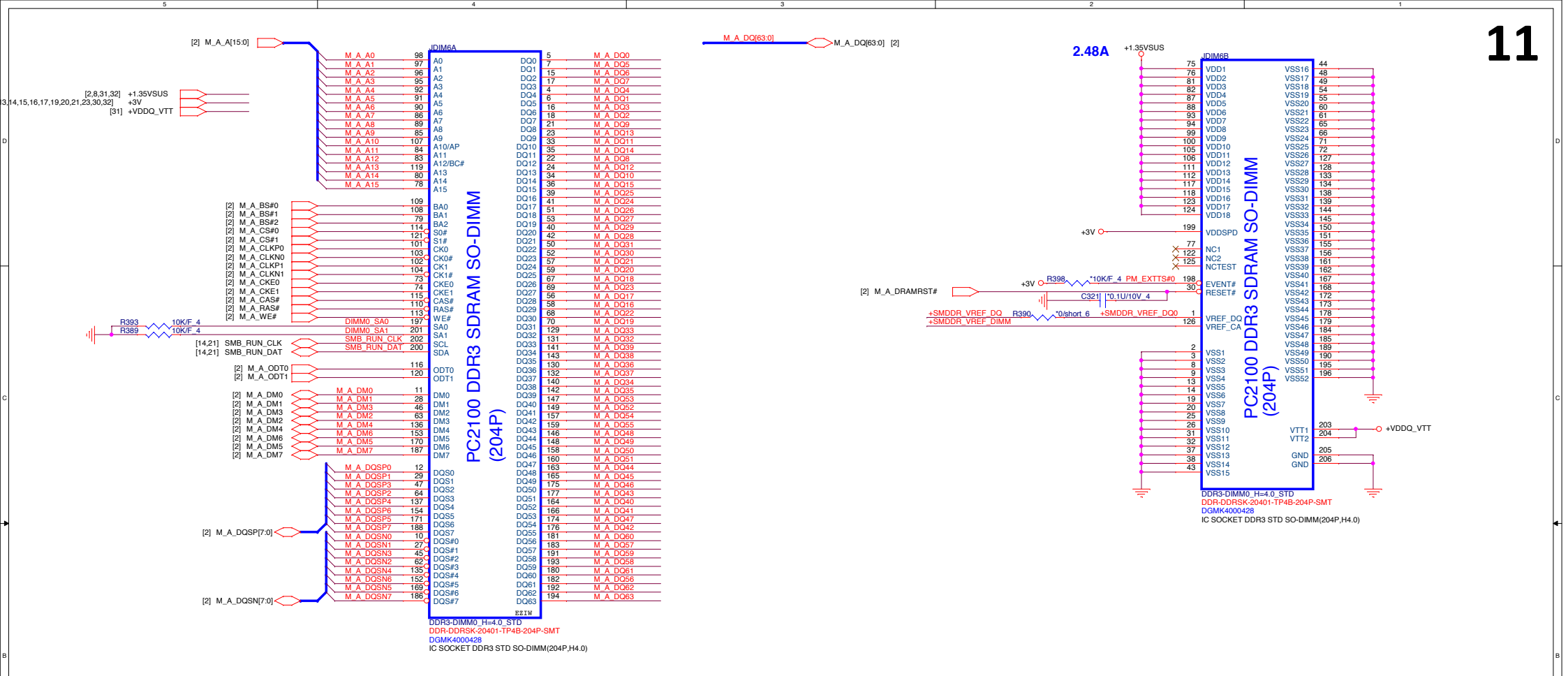




- +VCC GFX [8,22,30]
- +1.35V\_S5 [32]
- +1.8V\_S5 [6,7,14,32]
- +3V\_S5 [2,12,14,15,16,17,21,23,29,30,32]
- +3V [4,7,11,12,13,14,15,16,17,19,20,21,23,30,32]
- +1.5V [19,29]
- +1.8V [4,5,6,7,12,13,14,15,16,20,21,23,32]
- +1.35V [32]
- +1.05V [29]
- +1.0V\_S5 [28,32]
- +1.0V\_SX [32]
- +1.0V [5,6,30,32]



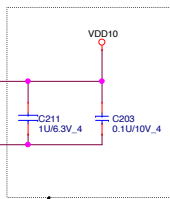
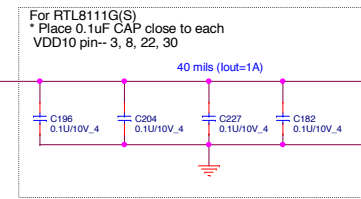
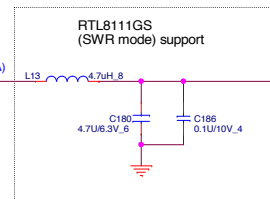
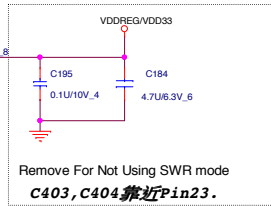
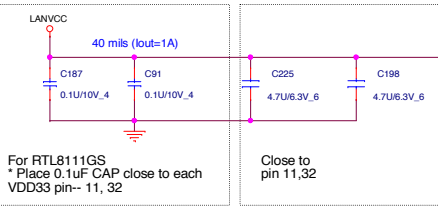
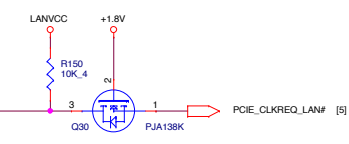
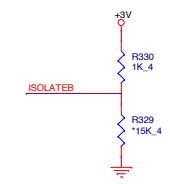
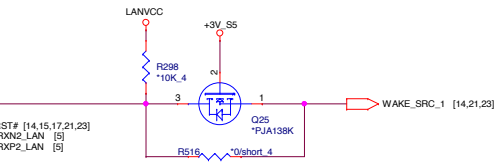
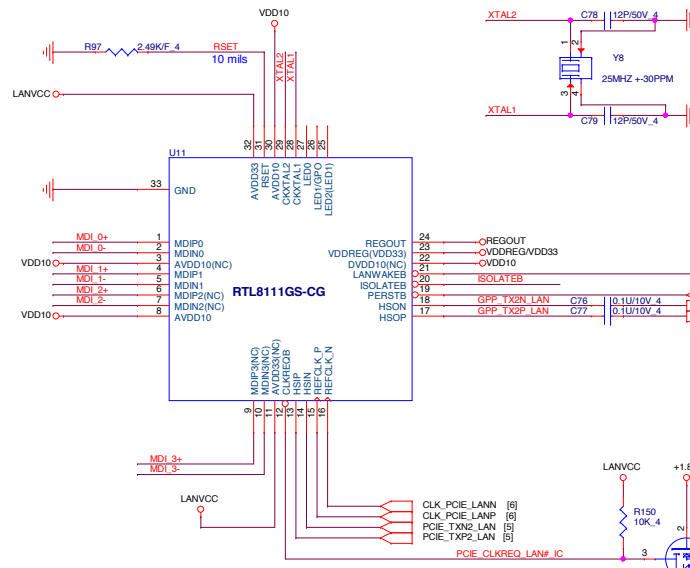
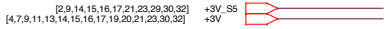
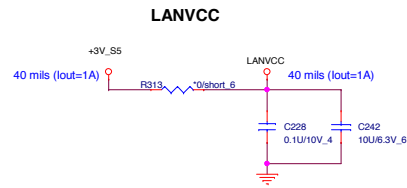




**Quanta Computer Inc.**  
PROJECT : ZHK

Size	Document Number	Rev
	<b>DDR3 DIMM0-STD(4.0H)</b>	1A
Date:	Monday, June 30, 2014	Sheet 11 of 33

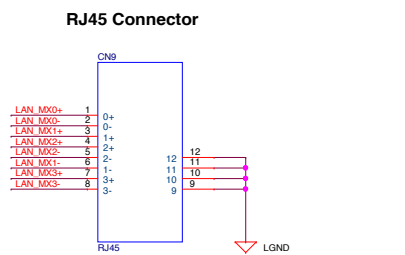
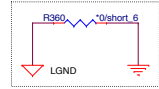
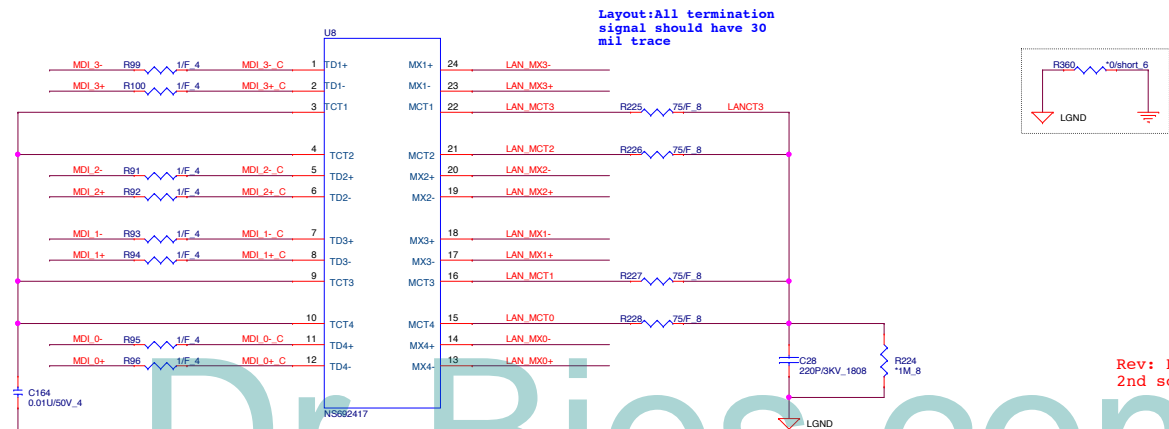
Dr-Bios.com



Rev: B (C-test) L13 Remove CV-4708MN00 for SMT request, change P/N to CV-4710T201

For RTL8111G(S)  
\* Place 1uF CAP close to each VDD10 pin-- 22 (reserve)

Transformer



Rev: B (C-test) CN9 change main source P/N to DFTJ08FR414  
2nd source : DFTJ08FR417

Dr-Bios.com

**Quanta Computer Inc.**

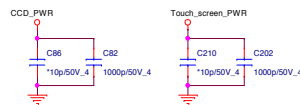
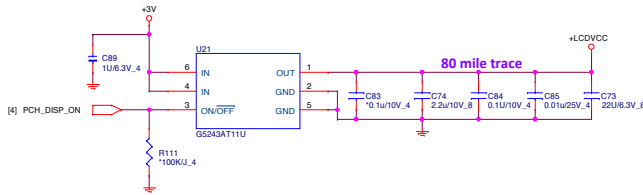
**PROJECT : ZHK**

**LAN (RTL8111GS)**

Size    Document Number    Rev

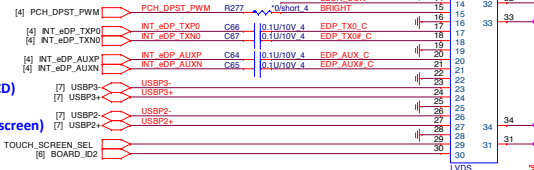
Date: Monday, June 30, 2014    Sheet 12 of 33

**LVDS Conn.**



**Touch screen ON/OFF**

For ANGEL panel



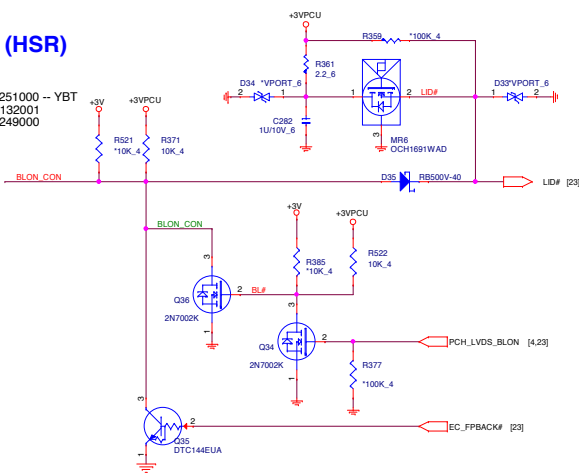
**USB to Connector (CCD)**

**USB to Connector (Touch screen)**

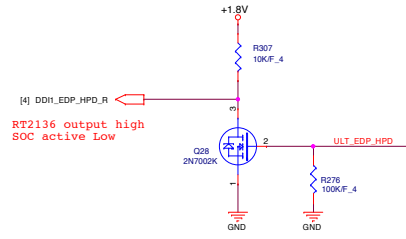
Touch screen SEL.  
Auto enable/disable touch panel USB port

**HALL IC (HSR)**

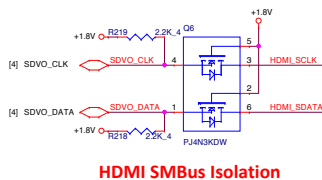
1st source : EOD  
2nd source : AL008251000 - YBT  
3rd source : AL009132001  
4th source : AL009249000



[4,7,9,11,12,14,15,16,17,19,20,21,23,30,32] +3V  
[4,5,6,7,9,12,14,15,16,20,21,23,32] +1.8V  
[16,19,32] +5V

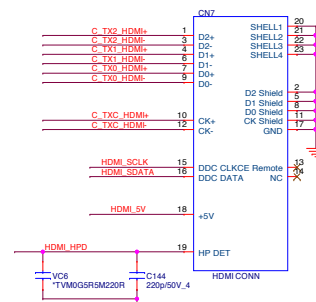
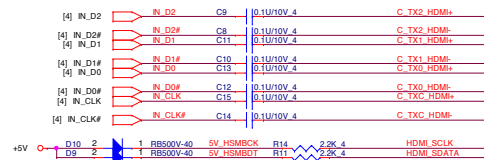
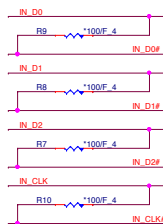


**HDMI Conn.**

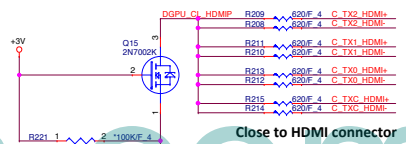
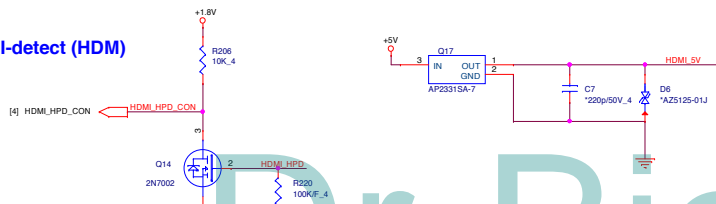


**HDMI SMBus Isolation**

**EMI (EMC)**



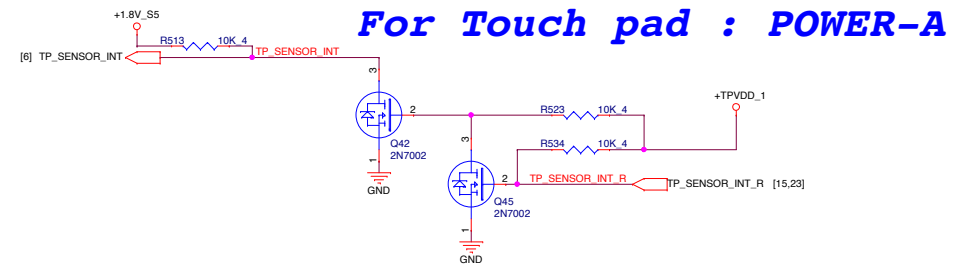
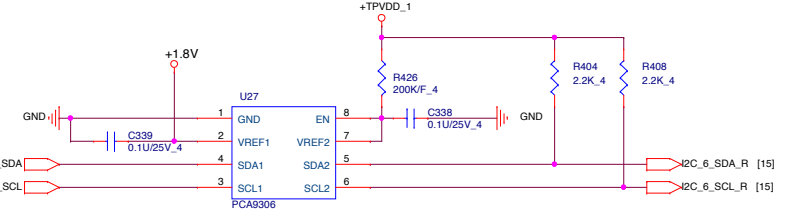
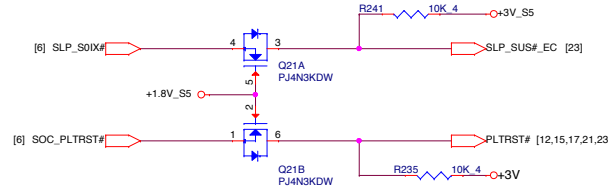
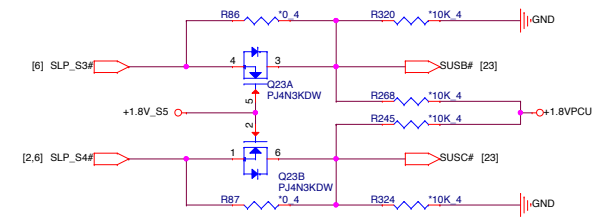
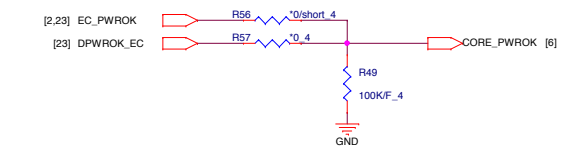
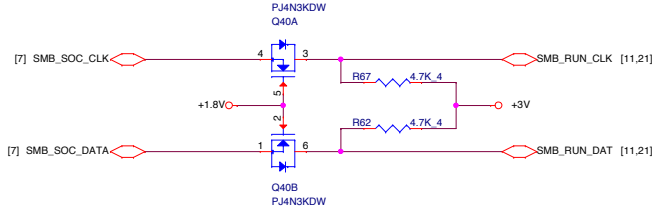
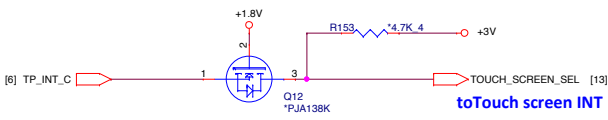
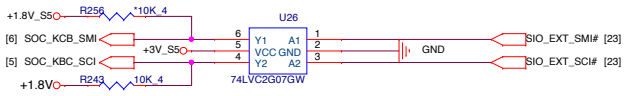
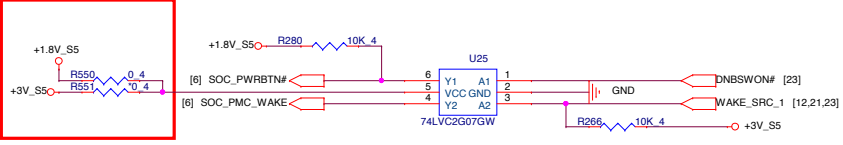
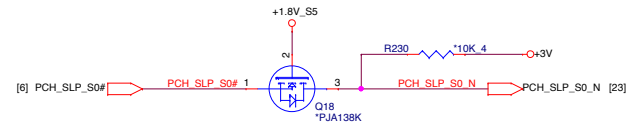
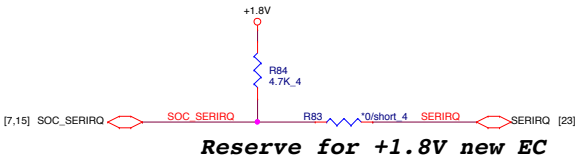
**HDMI-detect (HDM)**



Close to HDMI connector

Dr-Bios.com

[6,7,9,32]	+1.8V_S5
[2,9,12,15,16,17,21,23,29,30,32]	+3V_S5
[4,5,6,7,9,12,13,15,16,20,21,23,32]	+1.8V
[4,7,9,11,12,13,15,16,17,19,20,21,23,30,32]	+3V



**Quanta Computer Inc.**  
PROJECT : ZHK

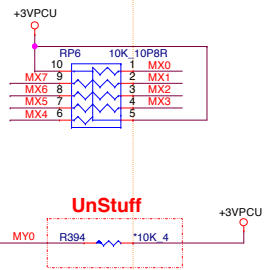
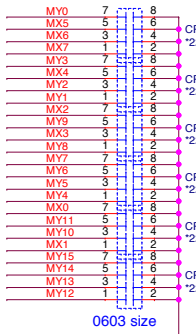
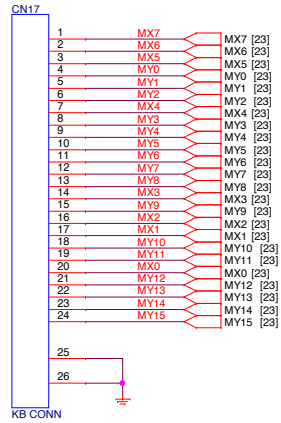
Size	Document Number	Rev
	Level Shifter	1A
Date:	Monday, June 30, 2014	Sheet 14 of 33

# KEYBOARD (KBC)

<20110214(E1A)>  
Change CP1-CP6 footprint from 8p4r-0402-smt to 8P4R, for SMT open issue.

<EMI>

## INTERNAL KEYBOARD STRIP SET (KBC)



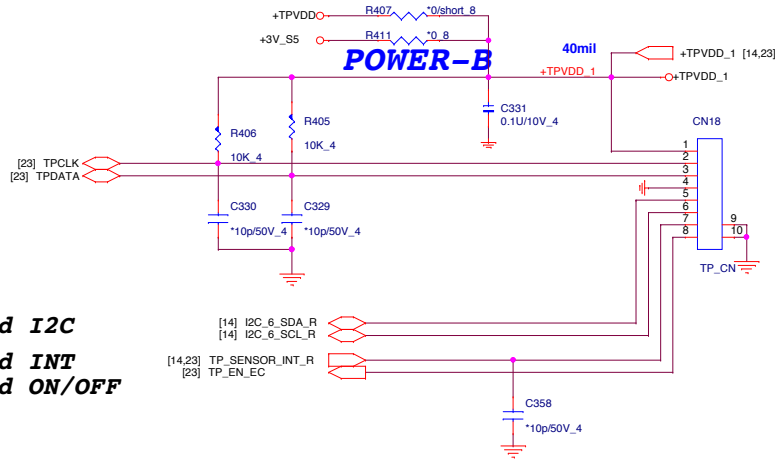
UnStuff

Rev: B (C-test) CN17 change ACS P/N from DFFC24FR000 to DFFC24FR110

# TOUCH PAD (TPD)

## POWER-A

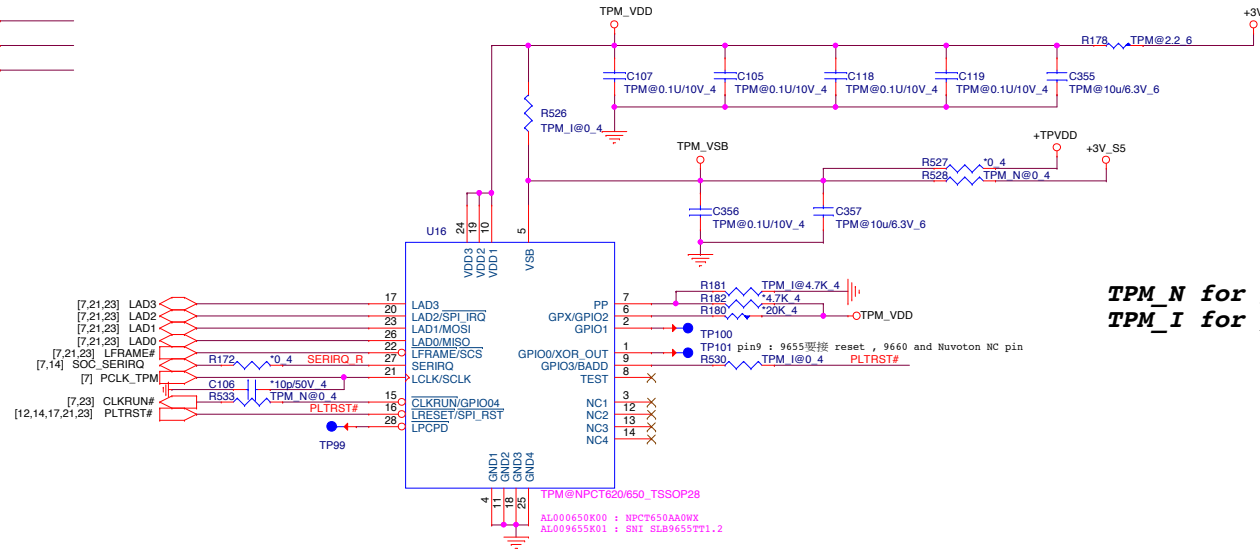
## POWER-B



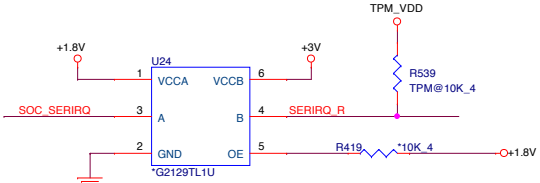
Touch pad I2C  
Touch pad INT  
Touch pad ON/OFF

ACER DEFINE  
VDD  
PS2-CLK  
PS2-DATA  
GND  
I2C-CLK  
I2C-DATA  
ATTN (INT)  
SER-OFF.

# TPM (TPM)



TPM\_N for 新唐  
TPM\_I for 英飛凌---- default



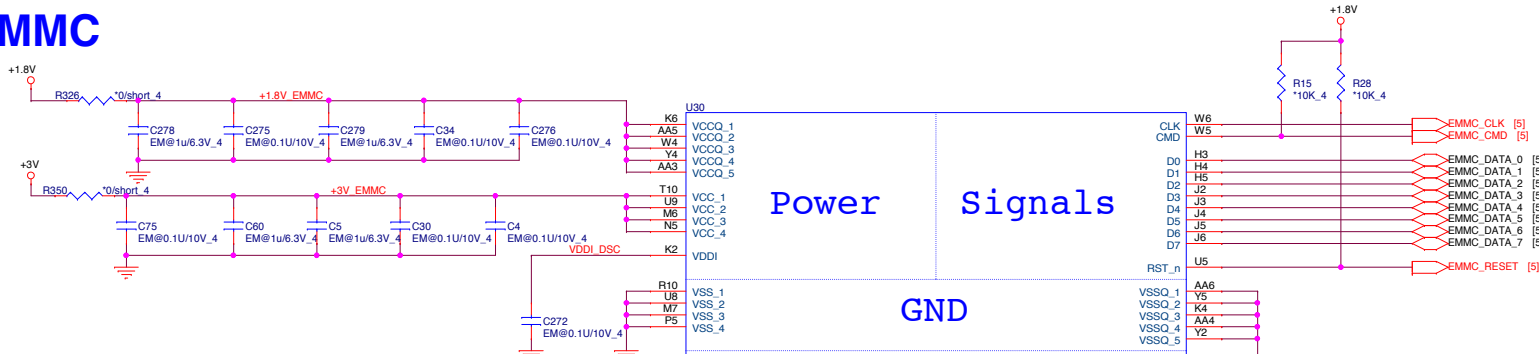
note: serie need to add level shift

**Quanta Computer Inc.**  
PROJECT : ZHK

Size	Document Number	Rev
	KB/BT/TP	1A
Date:	Monday, June 30, 2014	Sheet 15 of 33



# eMMC

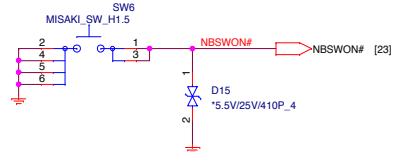


Vendor	P/N
SAMSUNG 64G	AKE3TZPT506
Samsung 32G	AKE5SZ0T501
HYNIX 64G	AKE34GPTW00
HYNIX 32G	AKE34ZPTW00
Sandisk 32G	AKE3SZ-T105

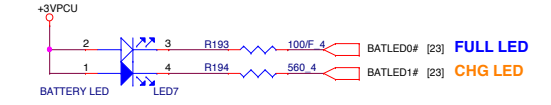
- Power**
- VCC0\_1
  - VCC0\_2
  - VCC0\_3
  - VCC0\_4
  - VCC0\_5
  - VCC\_1
  - VCC\_2
  - VCC\_3
  - VCC\_4
  - VDDI
  - VSS\_1
  - VSS\_2
  - VSS\_3
  - VSS\_4
- Signals**
- CLK
  - CMD
  - DATA\_0
  - DATA\_1
  - DATA\_2
  - DATA\_3
  - DATA\_4
  - DATA\_5
  - DATA\_6
  - DATA\_7
  - RESET
- GND**
- VSSQ\_1
  - VSSQ\_2
  - VSSQ\_3
  - VSSQ\_4
  - VSSQ\_5
- NC**
- NC\_1 to NC\_98
  - NC\_99 to NC\_138

- +5V [13,19,32]
- +3VPCU [6,8,13,15,19,23,25,26,27,32]
- +3V [4,7,9,11,12,13,14,15,17,19,20,21,23,30,32]
- +3V\_SS [2,9,12,14,15,17,21,23,29,30,32]

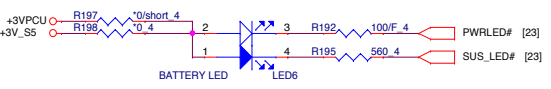
## PWR button



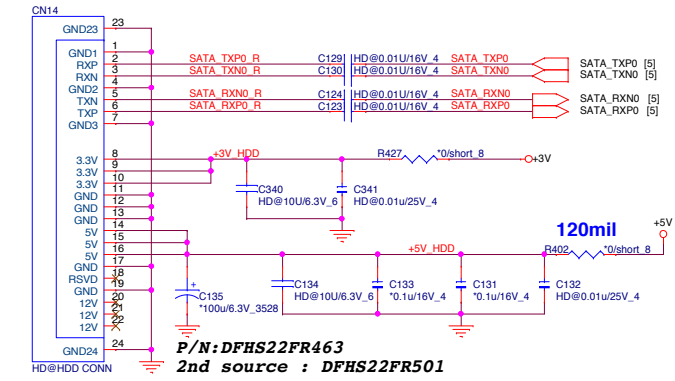
## Battery indicator



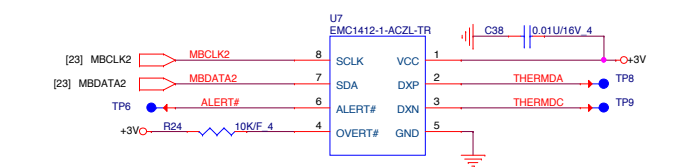
## PWR indicator



## SATA HDD

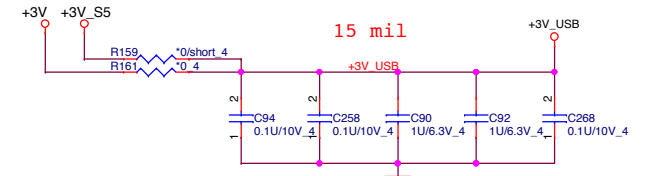
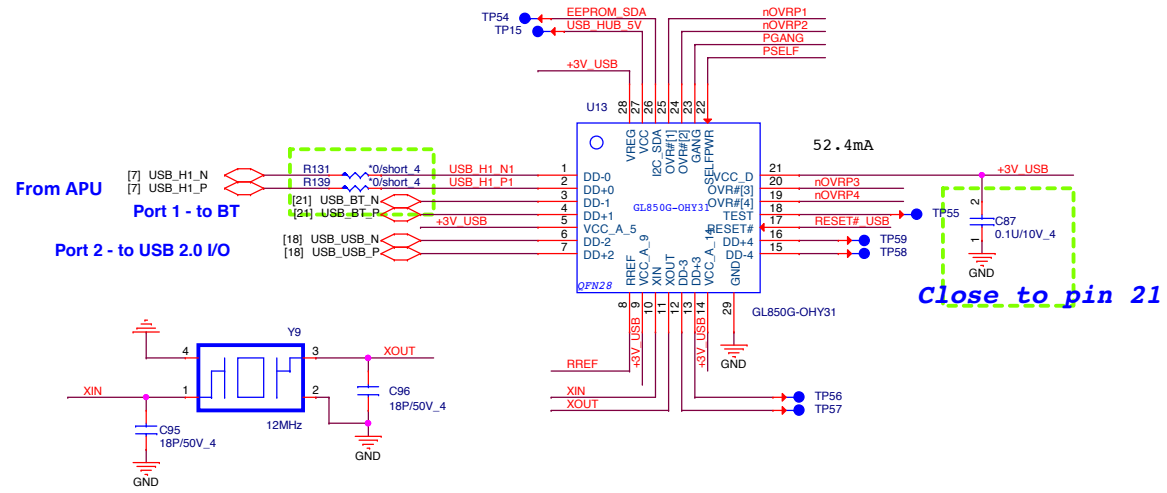


## CPU Thermal sensor(THS) / MB Local TEMP

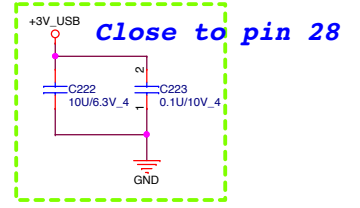


Main:AL001412003 EMC1412-1-ACZL-TR(98h)  
 2nd:AL000431014 TMP431ADGKR(98h)

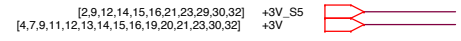
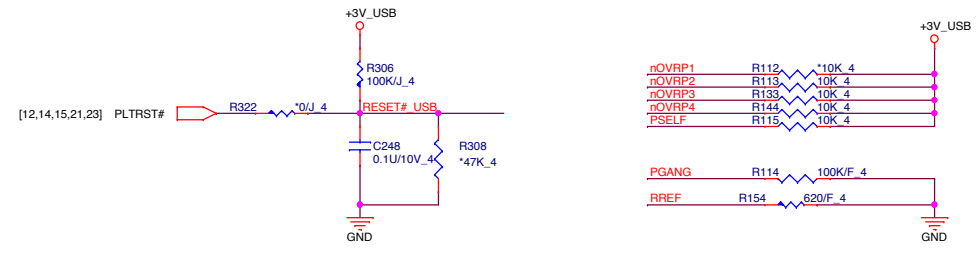





Close to GL850G-31 IC

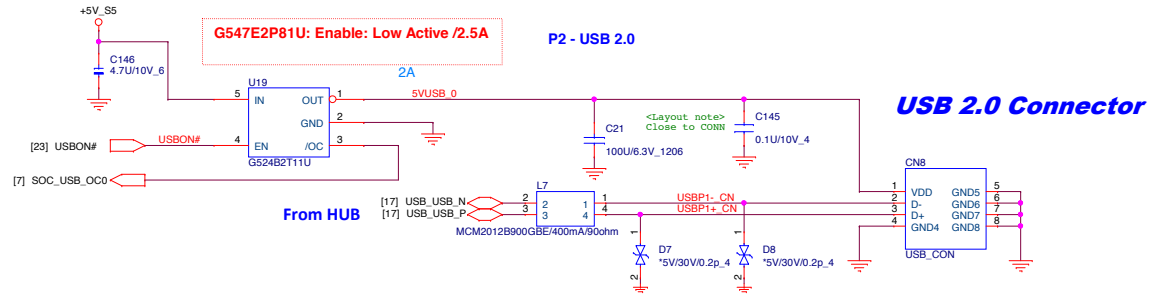
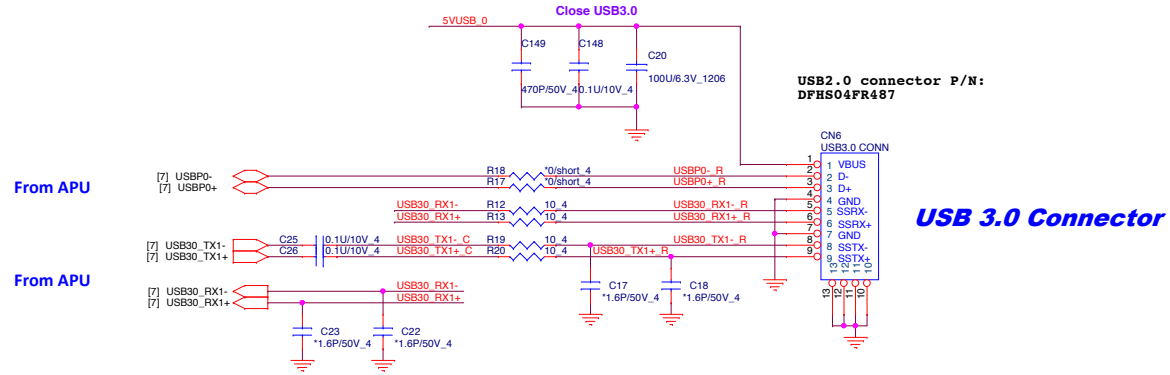


Close to pin 28



 <b>Quanta Computer Inc.</b> <b>PROJECT : ZHK</b>			
			Size
		<b>USB HUB -1</b>	1A
Date:	Monday, June 30, 2014	Sheet	17 of 33

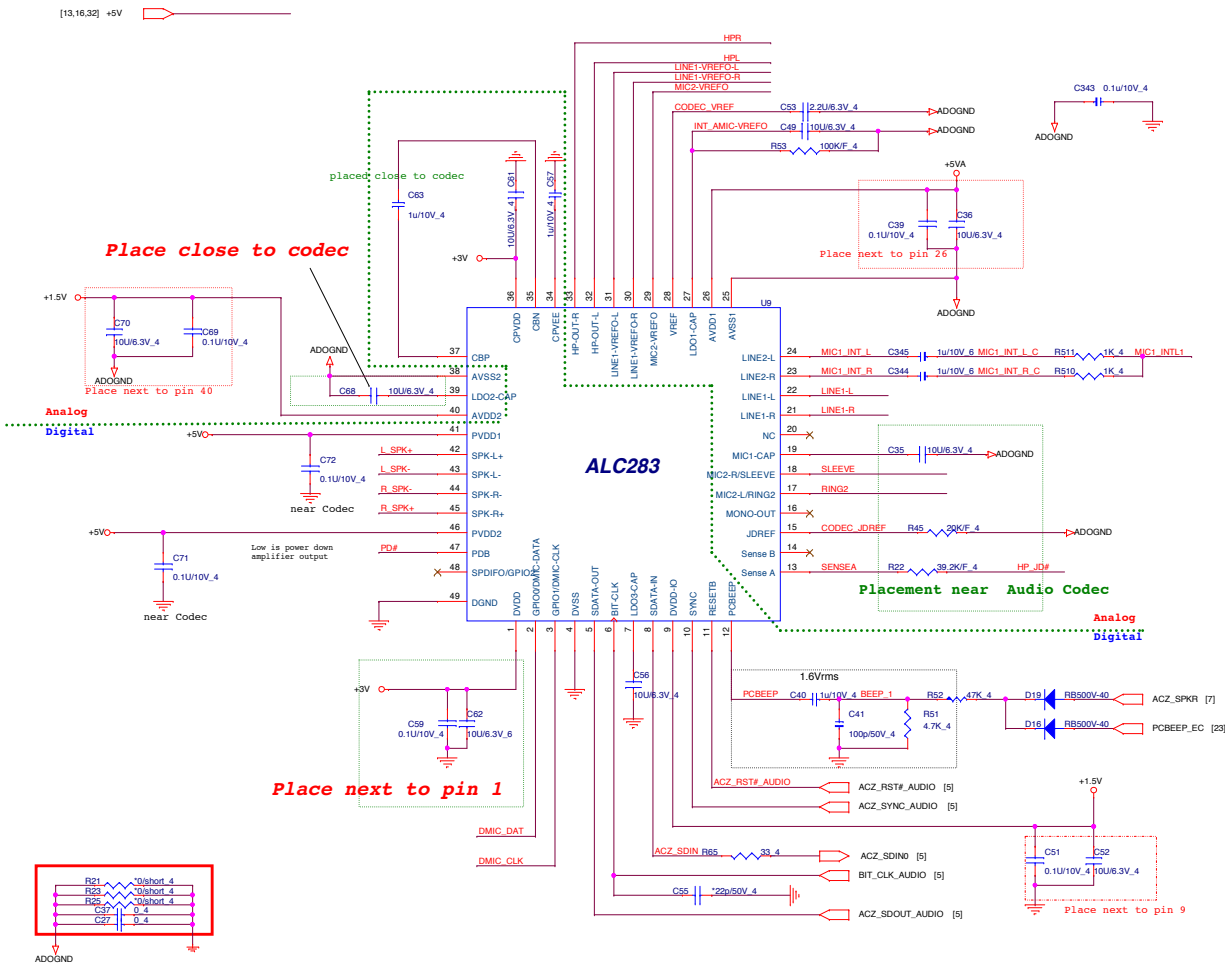
[6,26,28,29,30,31,32] +5V\_S5



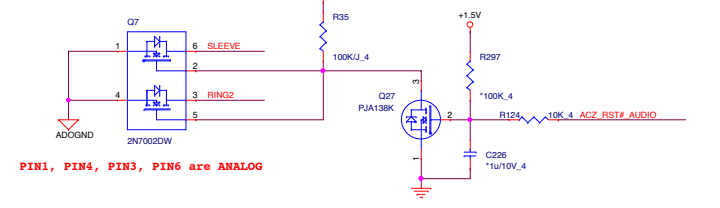
**Quanta Computer Inc.**  
PROJECT : ZHK

Size	Document Number	Rev
	<b>USB / eMMC CONN</b>	1A
Date:	Monday, June 30, 2014	Sheet 18 of 33

### Codec(ADO)



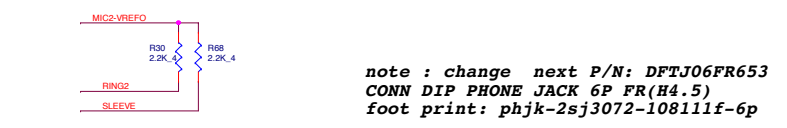
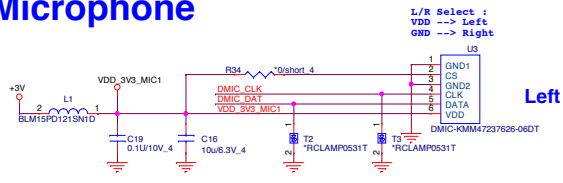
### Grounding circuit(ADO)



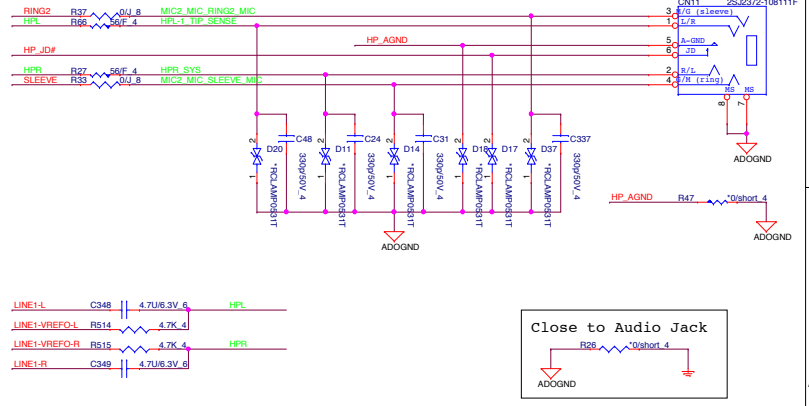
### INT MIC array



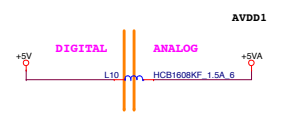
### Microphone



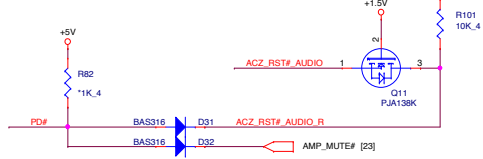
### HP\_MIC 上/下/左/右包覆AGND



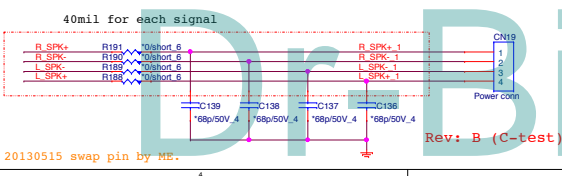
### Codec PWR 5V(ADO)



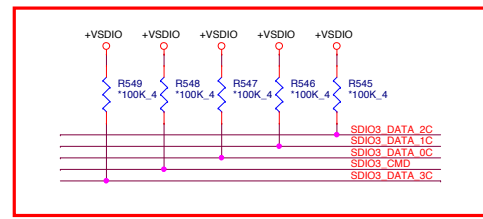
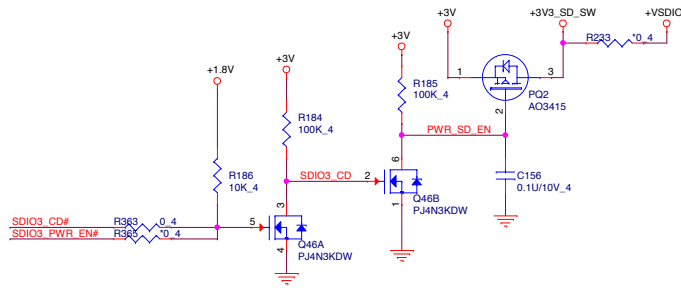
### Mute(ADO)



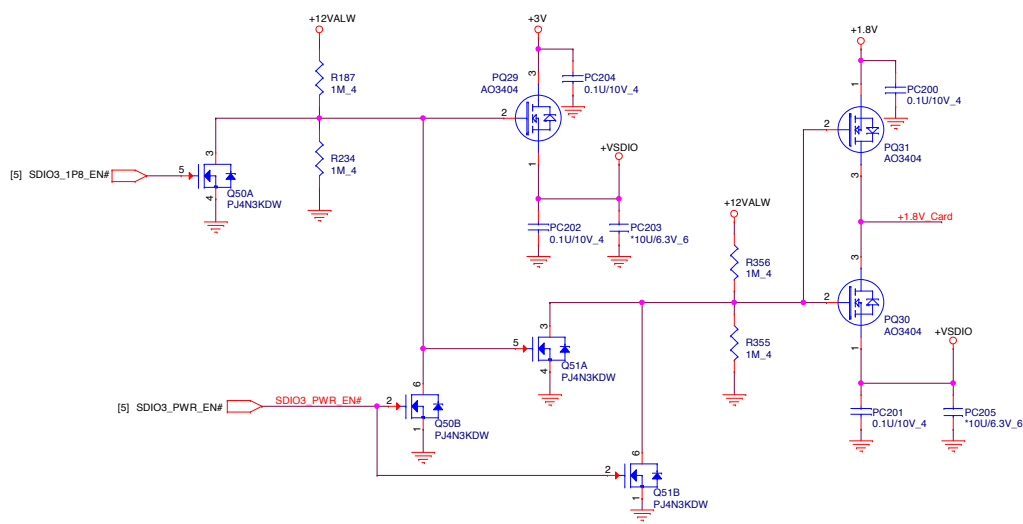
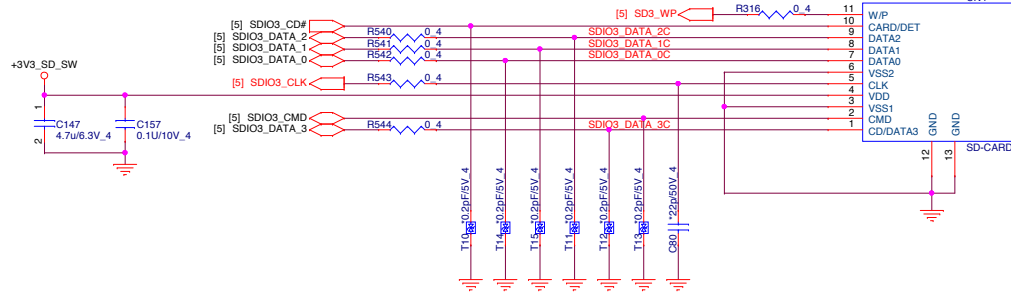
### Internal Speaker



Rev: B (C-test) CN19 change ACS P/N from DFHD04MR75 to DFHD04MR211

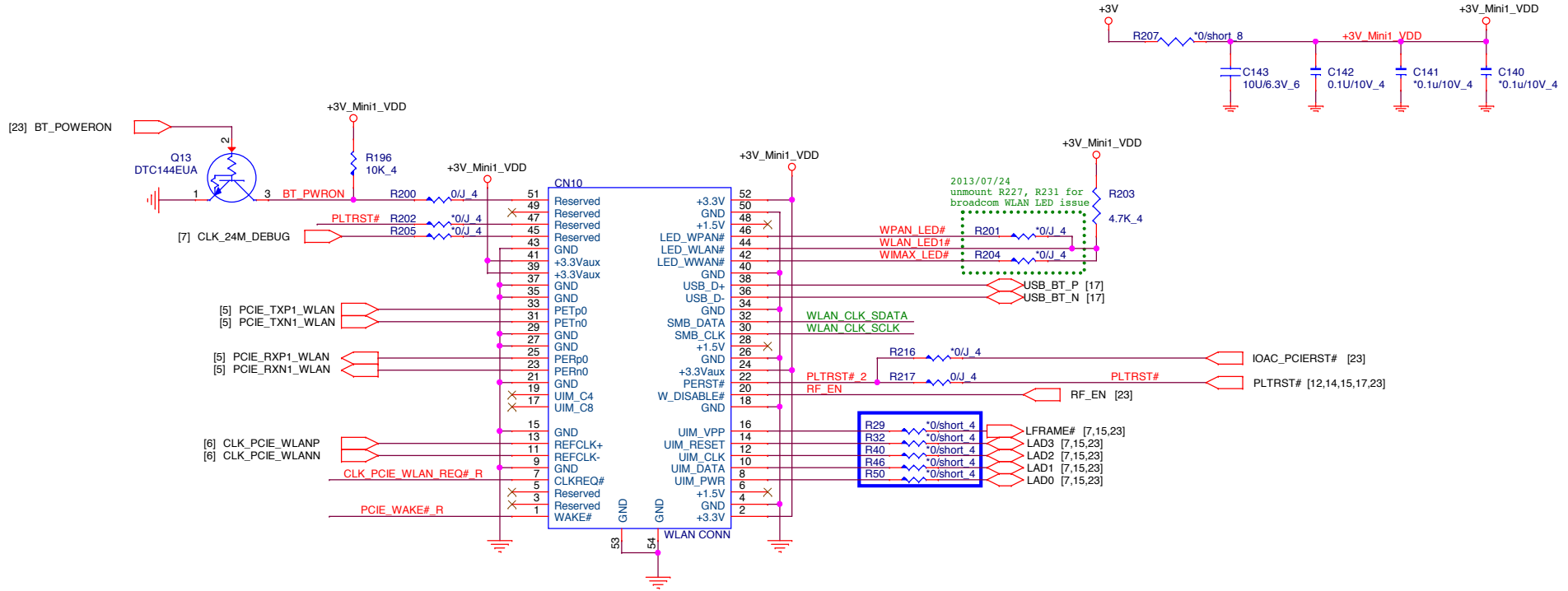


SD\_CARD\_DET\_N  
 L: Card inserted  
 H: Card remove



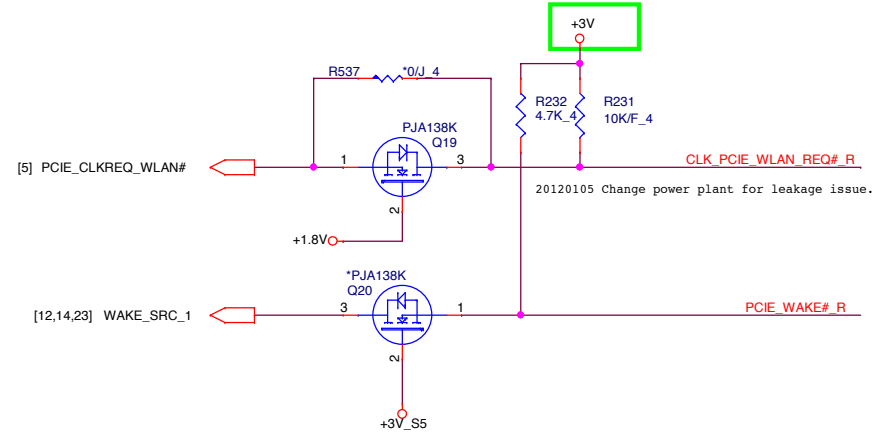
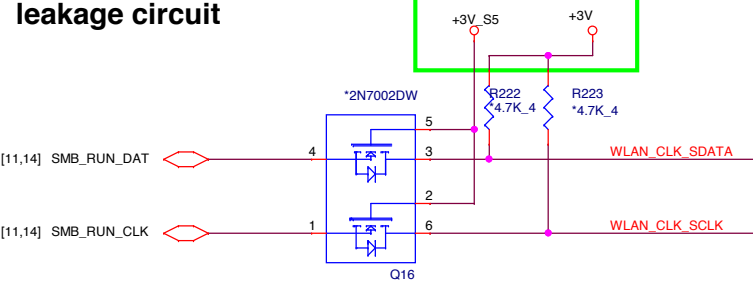
**Quanta Computer Inc.**  
**PROJECT : ZHK**

Size	Document Number	Rev
	Cardreader GL834L	1A
Date: Monday, June 30, 2014	Sheet 20 of 33	



leakage circuit

20120105 Change power plant for leakage issue.



- [4,5,6,7,9,12,13,14,15,16,20,23,32] +1.8V
- [4,7,9,11,12,13,14,15,16,17,19,20,23,30,32] +3V
- [2,9,12,14,15,16,17,23,29,30,32] +3V\_S5

Dr-Bios.com

Quanta Computer Inc.  
PROJECT : ZHK

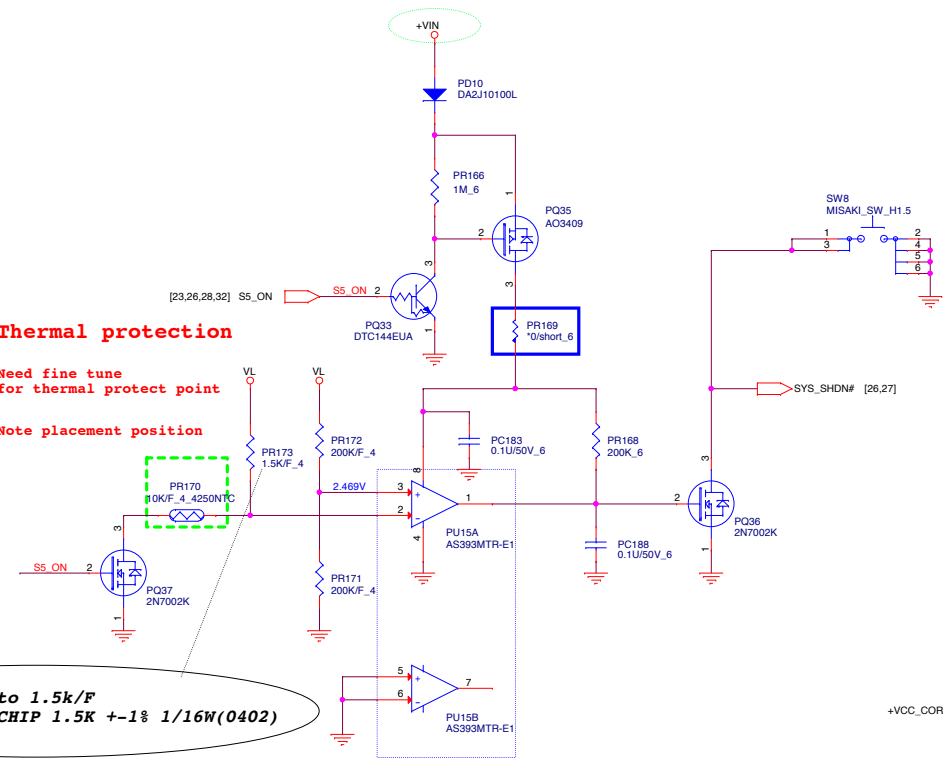
Size	Document Number	Rev
	WiFi & BT	1A
Date:	Monday, June 30, 2014	Sheet 21 of 33

HG-TC256BC354D118P2

Thermal protection

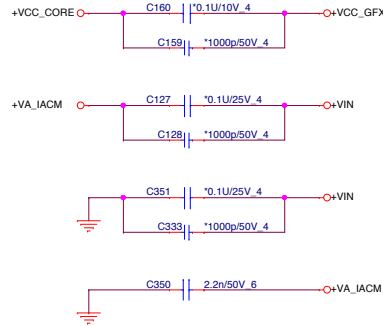
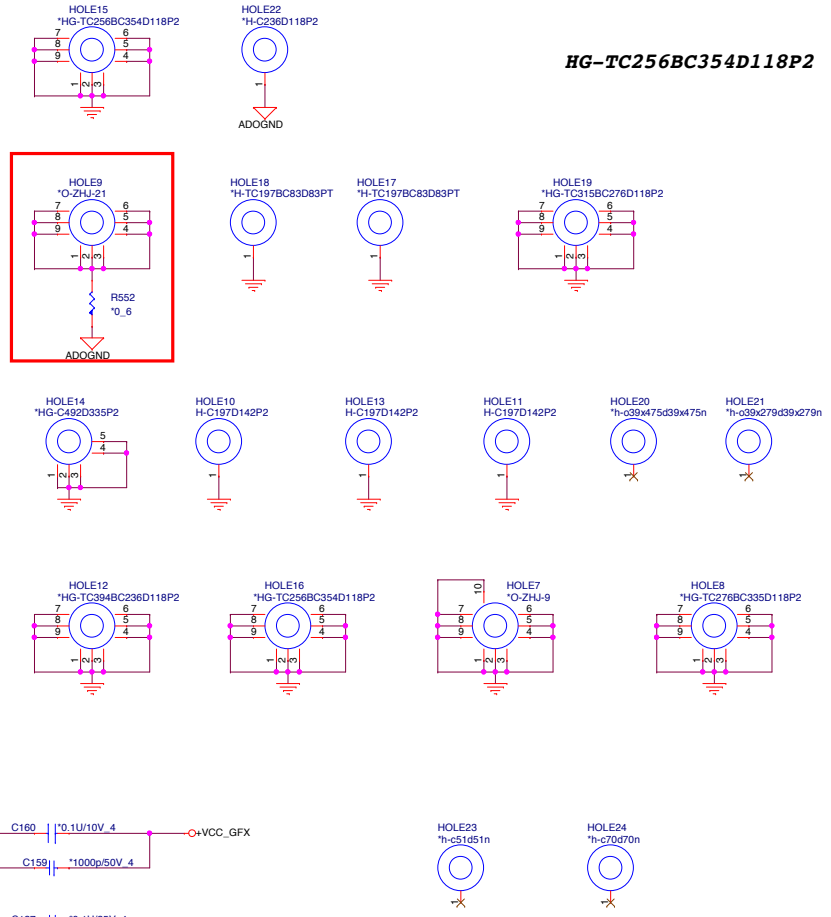
Need fine tune for thermal protect point

Note placement position



note: PR173 change to 1.5k/F  
 CS21502FB14 RES CHIP 1.5K +-1% 1/16W(0402)

For EC control thermal protection (output 3.3V)



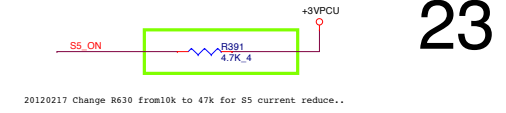
**Quanta Computer Inc.**  
 PROJECT : ZHJ

Size	Document Number	Rev
	Thermal / Hole	2A
Date:	Monday, July 07, 2014	Sheet 22 of 33



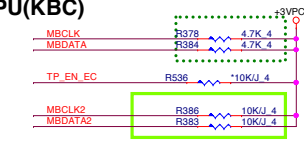
1.8V p/n: AJ009850F02  
Discription: IC CONTROLLER (128P) NPCE985LB1DX (LQFP)

Note:  
GPIO75 EMU\_LiD Touch panel enable/disable#Follow ZEA -->ZHJ None  
GPIO70 TP\_EN\_EC Touch panel enable/disable# -->ok  
GPIO27 TP\_INT\_EC# Touch pad interrupt

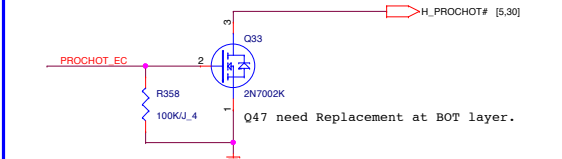


20120217 Change R630 from 10k to 47k for S5 current reduce..

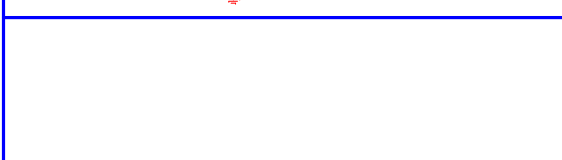
SM BUS PU(KBC)



2013/07/31  
SMBus Tr fail (spec 1000 ns max, result 1046 ns)  
Change PU resistor (R424, R428) from 10K to 4.7K



Q47 need Replacement at BOT layer.

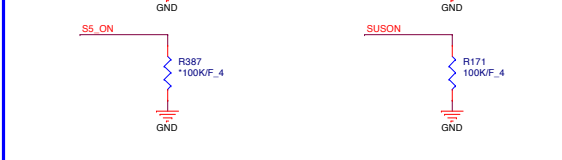
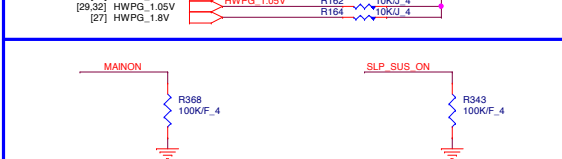
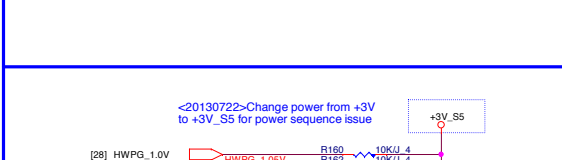
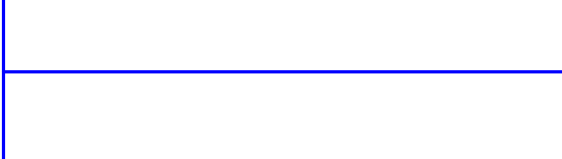


HWPG(KBC)

Note: GPIO75 (pin82) for TOUCHPANEL\_ON  
pin91 in 985L is 1.8V only



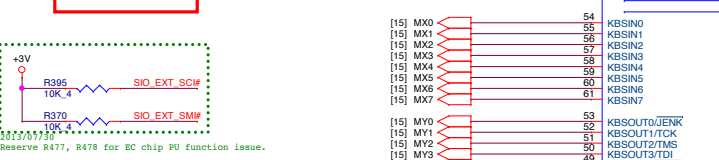
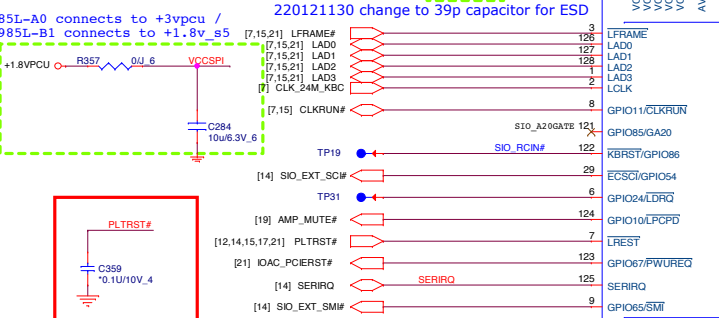
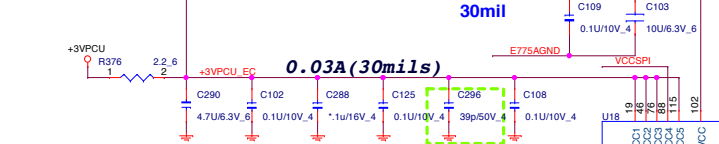
<20090721\_FAE suggestion>  
Stuff 100k and close to EC side for improving power consumption



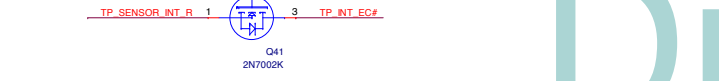
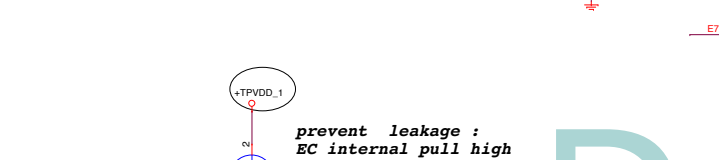
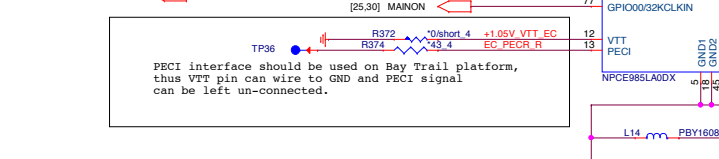
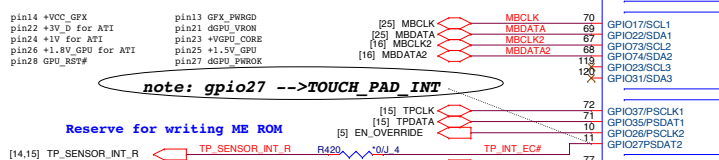
SM BUS ARRANGEMENT TABLE

SM Bus 1	Battery
SM Bus 2	PCH
SM Bus 3	GPU

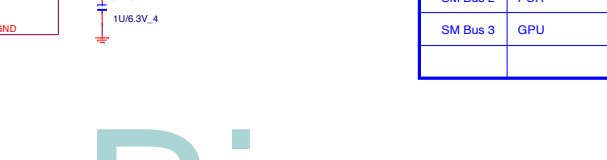
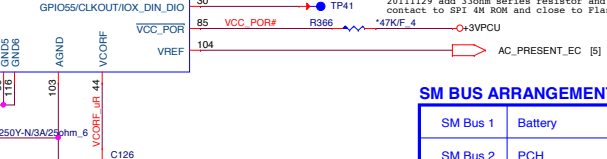
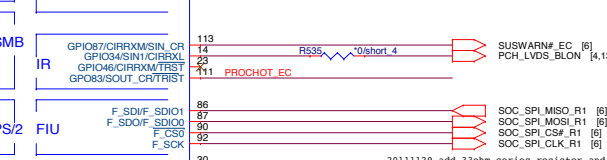
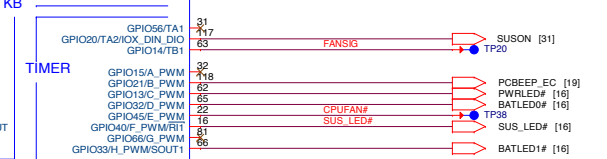
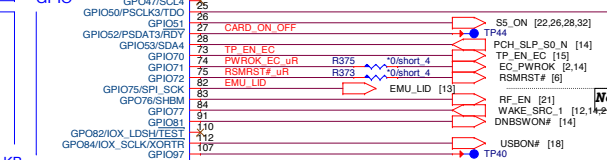
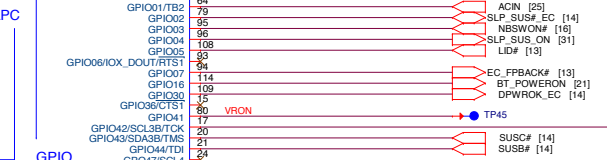
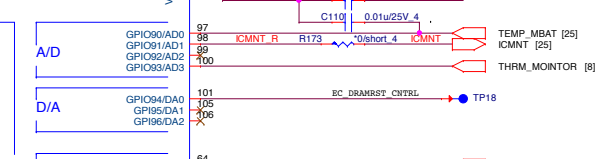
EC(KBC)



Reserve R477, R478 for EC chip PU function issue.




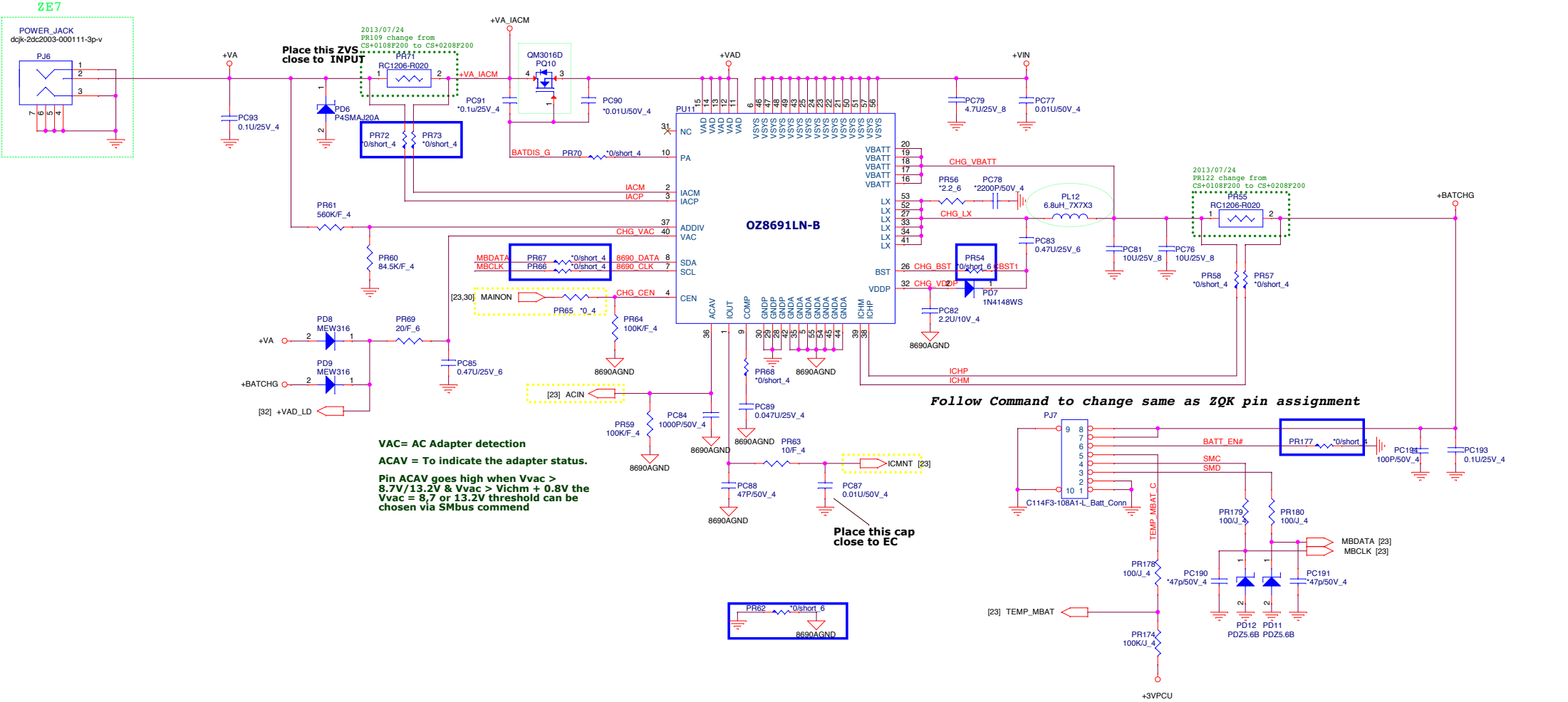
prevent leakage :  
EC internal pull high



COMPLETED

IMAGE

		<b>Quanta Computer Inc.</b>
		<b>PROJECT : ZHK</b>
Size	Document Number	Rev
	<b>Power sequence</b>	1A
Date:	Monday, June 30, 2014	Sheet 24 of 33

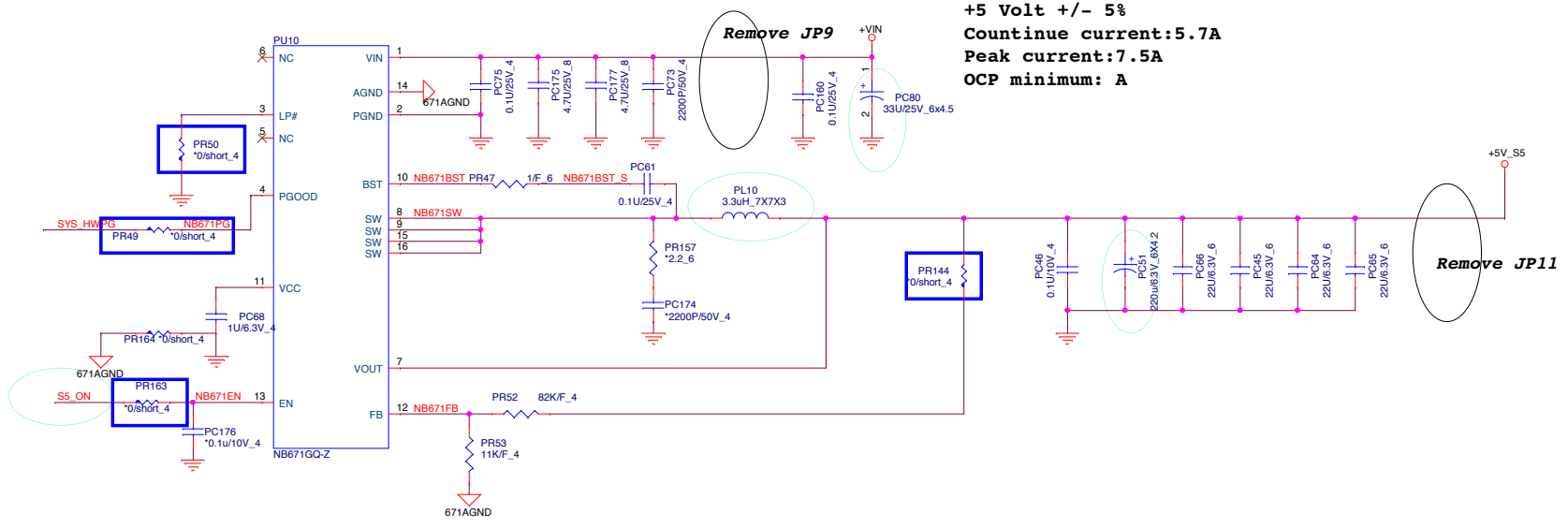
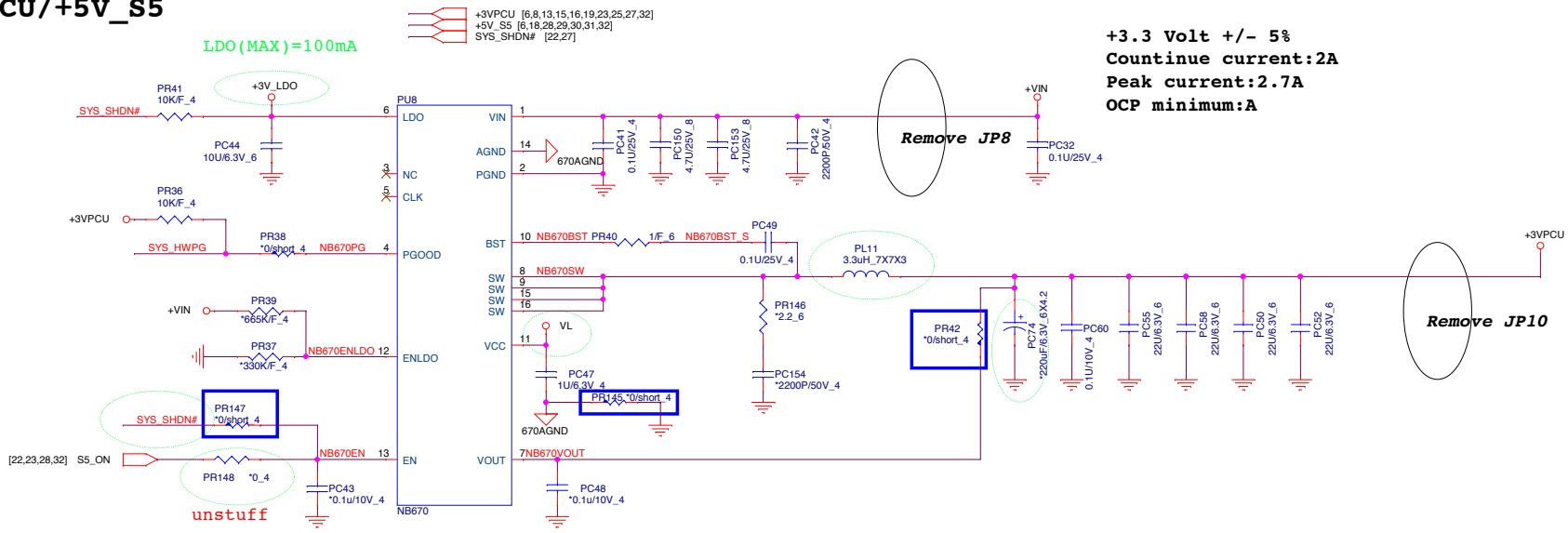


**VAC= AC Adapter detection**  
**ACAV = To indicate the adapter status.**  
**Pin ACAV goes high when Vvac > 8.7V/13.2V & Vvac > Vichm + 0.8V the Vvac = 8,7 or 13.2V threshold can be chosen via SMBus command**

*Follow Command to change same as ZQK pin assignment*

**Place this cap close to EC**

DC/DC +3VPCU/+5V\_S5



Dr-Bios.com

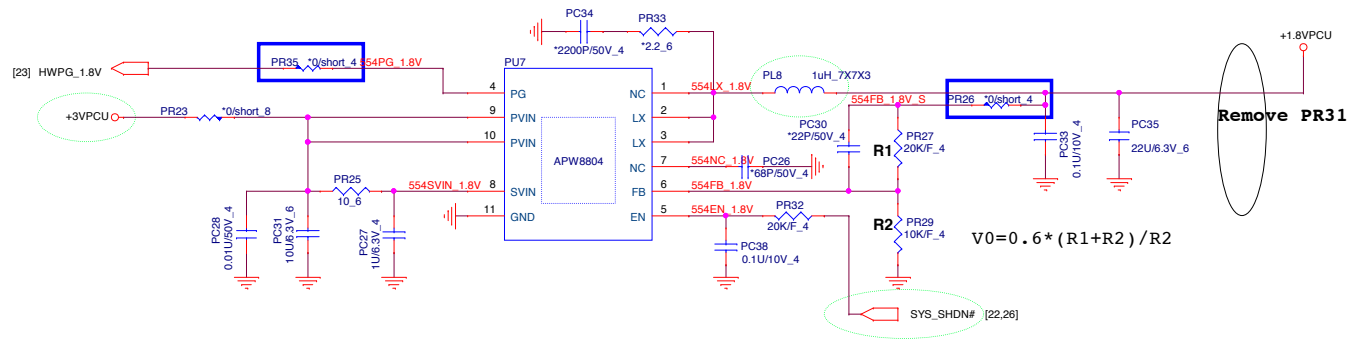
**Quanta Computer Inc.**  
**PROJECT : ZHJ**

Size	Document Number	Rev.
	<b>3/5VS5 (NB670/NB669)</b>	<b>1A</b>
Date:	Monday, June 30, 2014	Sheet 26 of 33


[14,23,32] +1.8VPCU

[6,8,13,15,16,19,23,25,26,32] +3VPCU

**+1.8V Volt +/- 5%**  
**Countinue current:0.08A**  
**Peak current:0.11A**  
**OCP minimum:A**

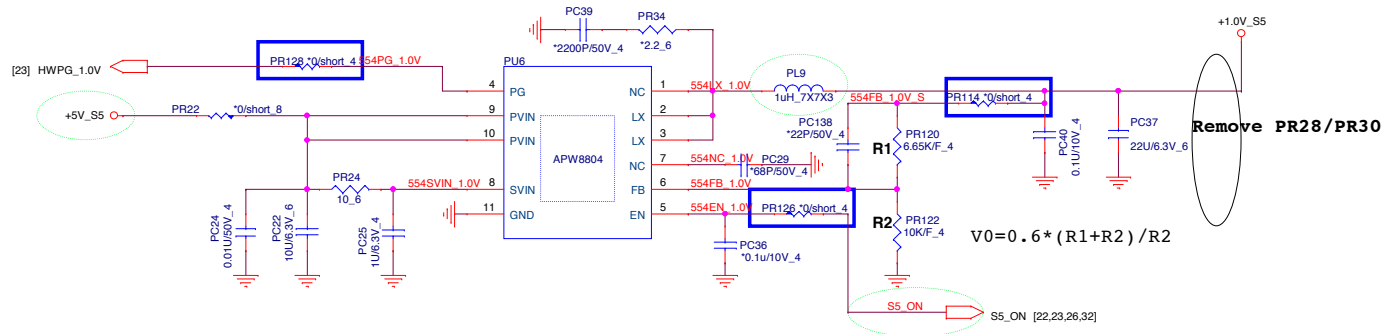


Dr-Bios.com


 <b>Quanta Computer Inc.</b> <b>PROJECT : ZHJ</b>		Rev:
		1A
Size:	Document Number:	
	<b>+1.8VPCU</b>	
Date:	Monday, June 30, 2014	Sheet 27 of 33

+1.0V Volt +/- 5%  
Countinue current:2.4A  
Peak current:3.2A  
OCP minimum:A

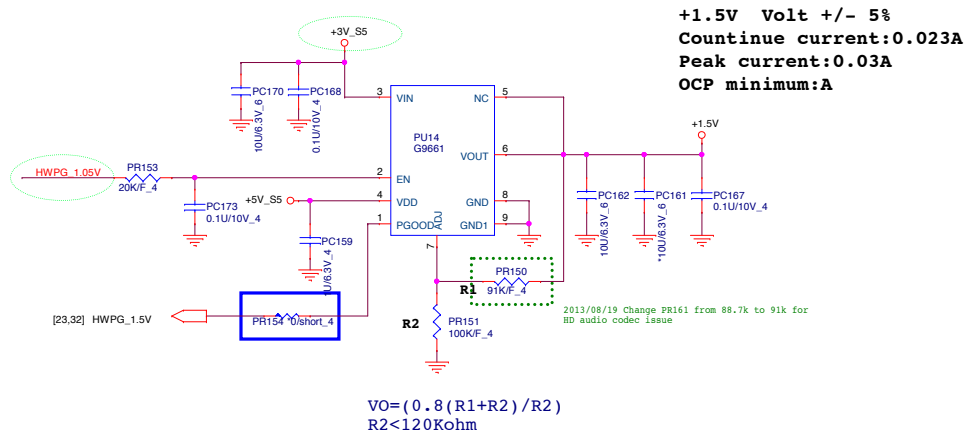
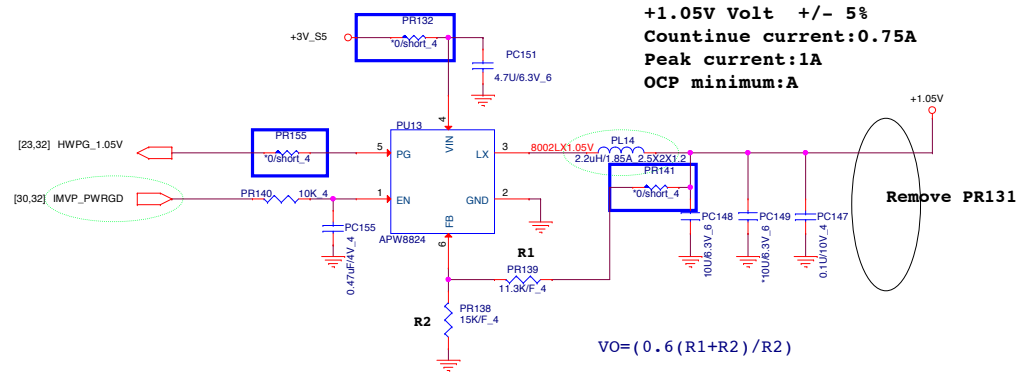
[9,32] +1.0V\_S5  
[6,18,26,29,30,31,32] +5V\_S5



Dr-Bios.com

 <b>Quanta Computer Inc.</b> <b>PROJECT : ZHJ</b>		Rev:
		1A
Size:	Document Number	
	<b>+1.0V</b>	
Date:	Monday, June 30, 2014	Sheet 28 of 33

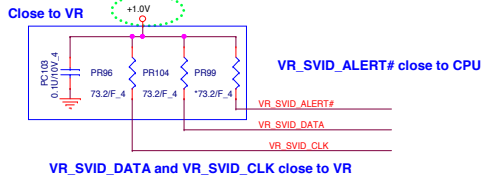
- [2,9,12,14,15,16,17,21,23,30,32] +3V\_S5
- [9] +1.05V
- [9,19] +1.5V



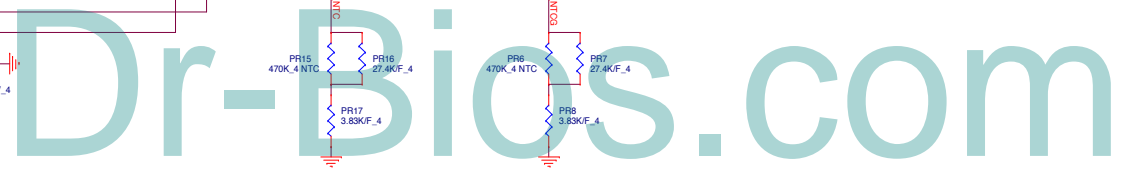
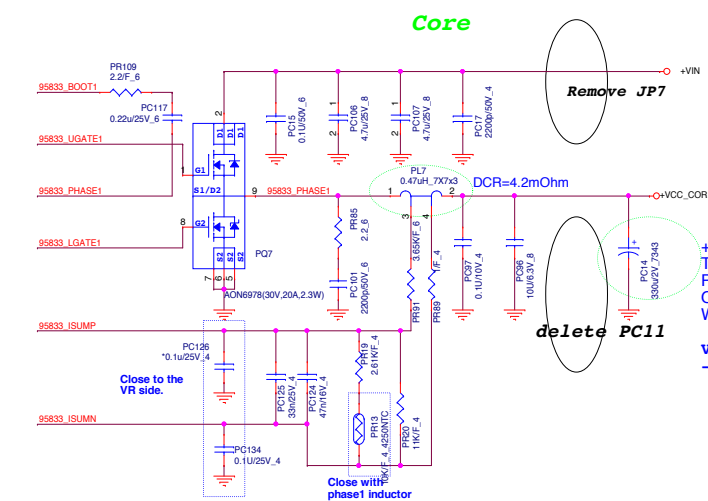
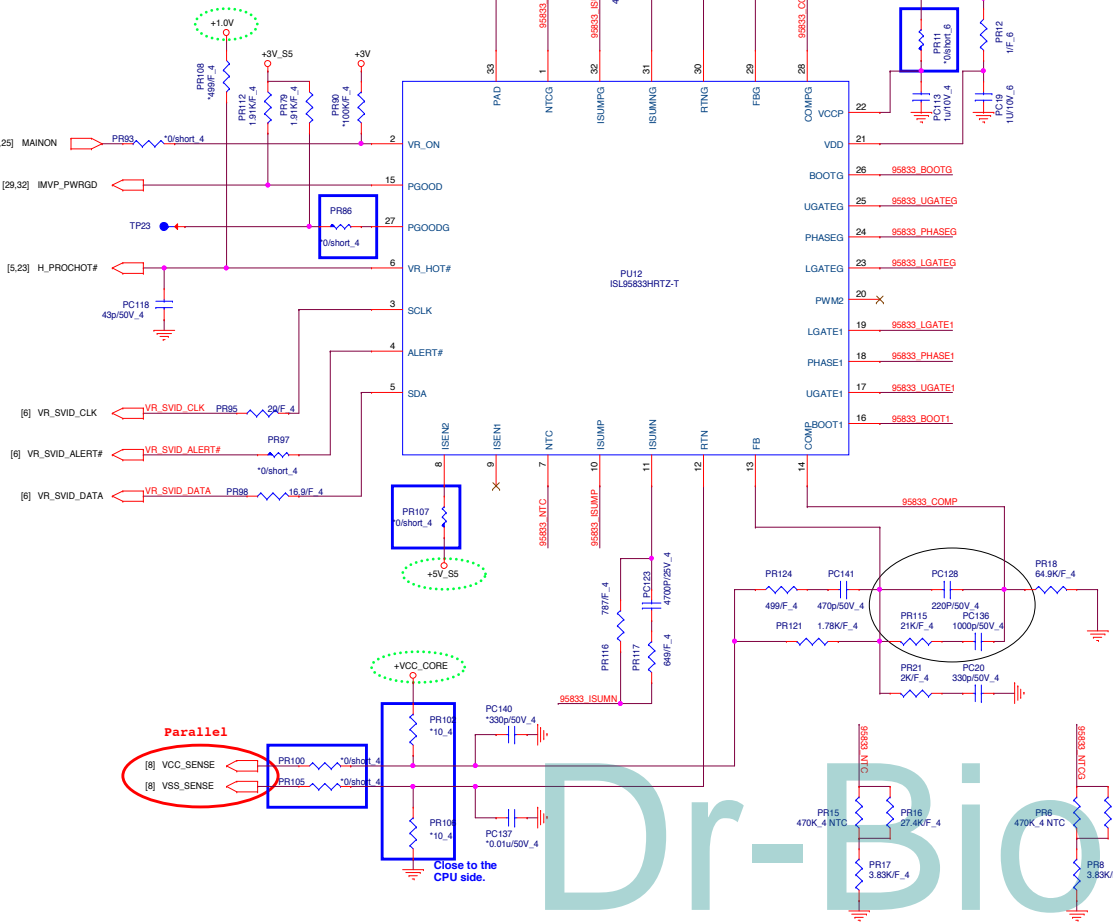
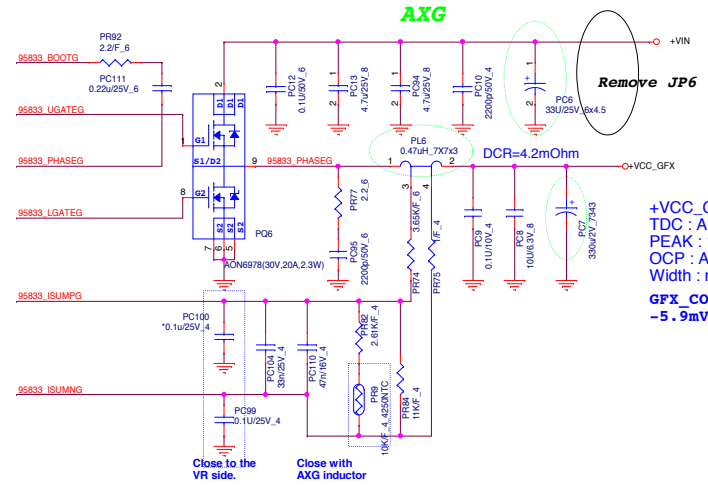
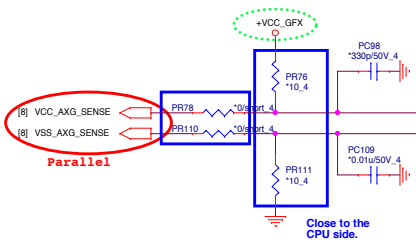
		<b>Quanta Computer Inc.</b> <b>PROJECT : ZHJ</b>	
		<b>+1.05V/1.5V</b>	
Date:	Monday, June 30, 2014	Sheet	29 of 33



20130617 Change +1.05V to +1.0V



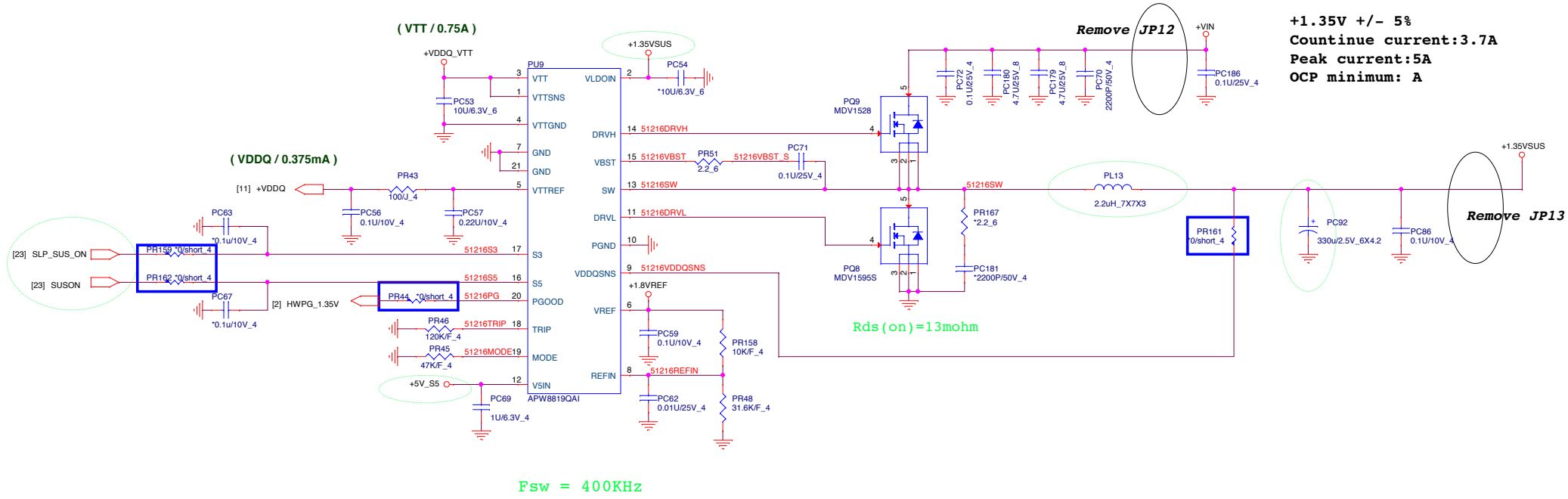
- [5,6,9,32] +1.0V
- [8,22] +VCC\_GFX
- [13,22,25,26,31,32] +VIN
- [6,18,26,28,29,31,32] +5V\_S5



[2,8,11,32] +1.35VSUS

[11] +VDDQ\_VTT

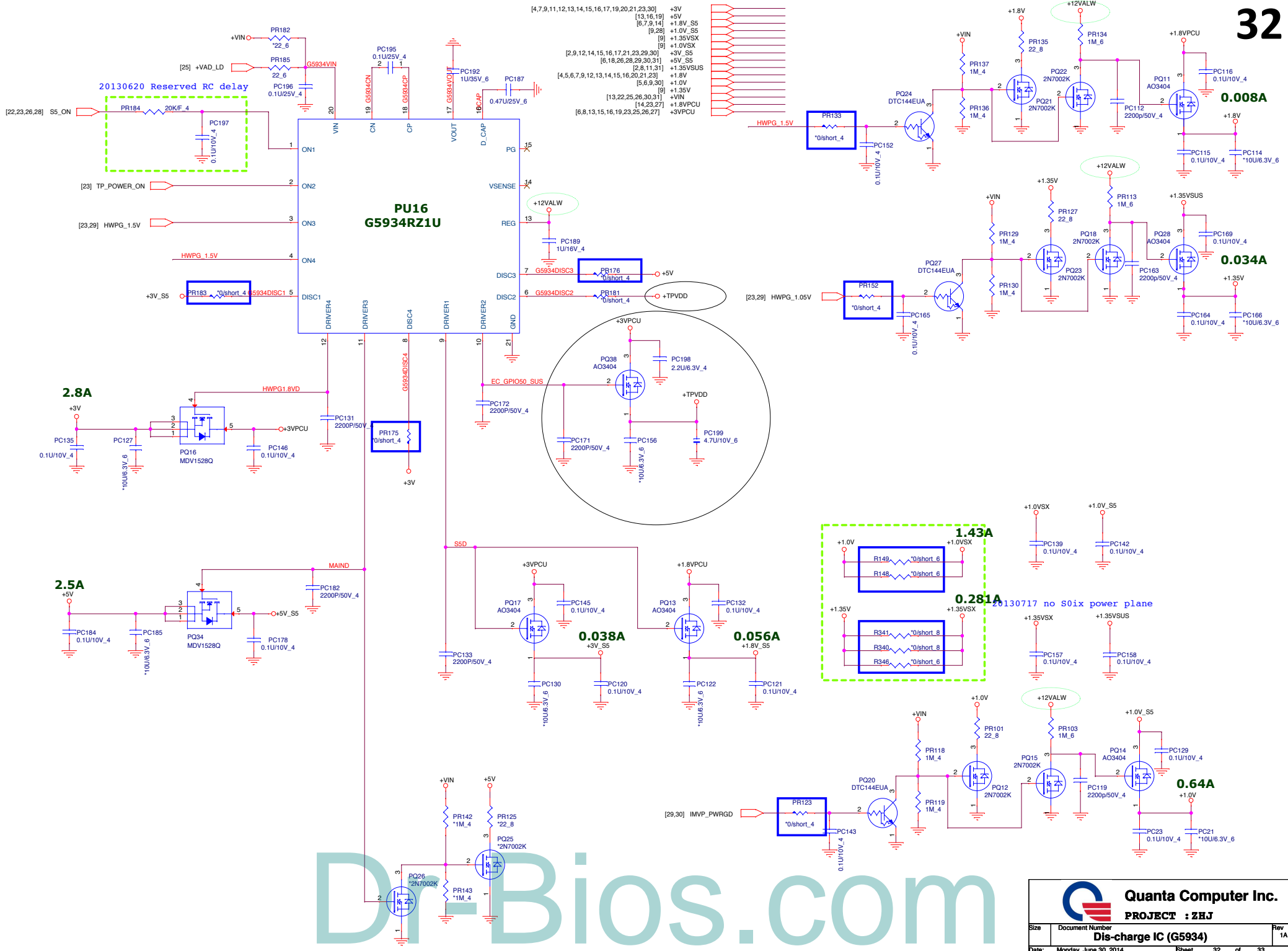
[6,18,26,28,29,30,32] +5V\_S5



Dr-Bios.com

**Quanta Computer Inc.**  
**PROJECT : ZHJ**

Size	Document Number	Rev
	<b>DDR3 (APW8819)</b>	1A
Date:	Monday, June 30, 2014	Sheet 31 of 33



Dr-Bios.com

**Quanta Computer Inc.**  
**PROJECT : ZHJ**

Size	Document Number	Rev
	<b>Dis-charge IC (G5934)</b>	<b>1A</b>
Date:	Monday, June 30, 2014	Sheet 32 of 33

CHANGE LIST

Model


Version

ZHK

3A

1. Stuff C27 & C37 for ESD. (page19)
2. Reserve C289 for EMI (page23)
3. R361 Change 0 OHM to 2.2 ohm for ESD(page13)
4. Reserve R545 R546 R547 R548 & R549 for Cardread function(page20)
5. Reserve R552 for ESD (page21)
6. Reserve R70 & R71 for eDP AUX (page13)
7. Add PQ31 for cardread function.

DOC NO.	PROJECT MODEL	ZHK	APPROVED BY:	DATE:
	PART NUMBER:		DRAWING BY:	REVISION:



Quanta Computer Inc.  
 PROJECT: ZHK  
 Drawing Title: Change list

Dr-Bios.com