


QUANTA COMPUTER INC.

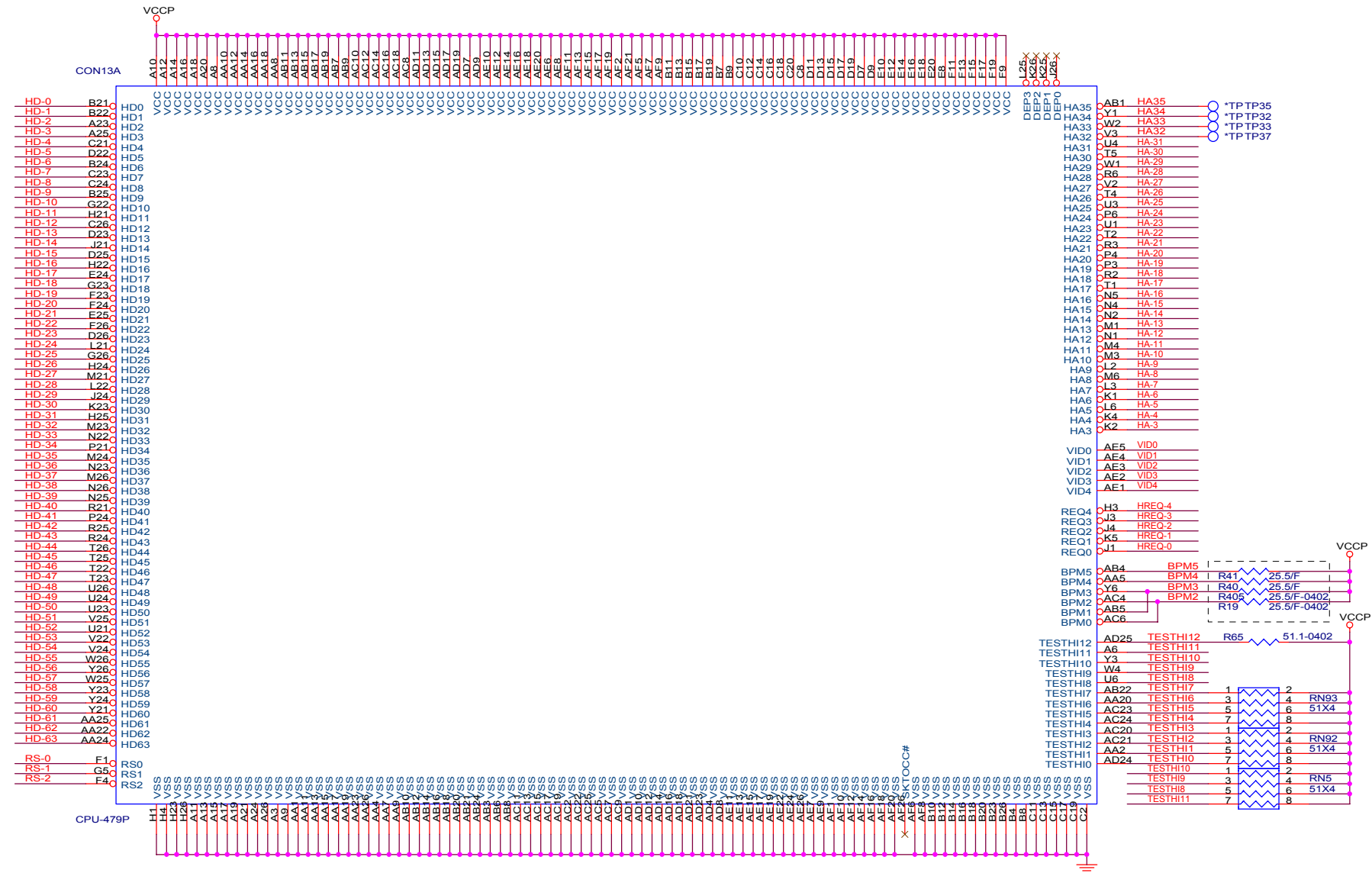
(March/31/2003) Rev : E

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Custom	COVER PAGE	E
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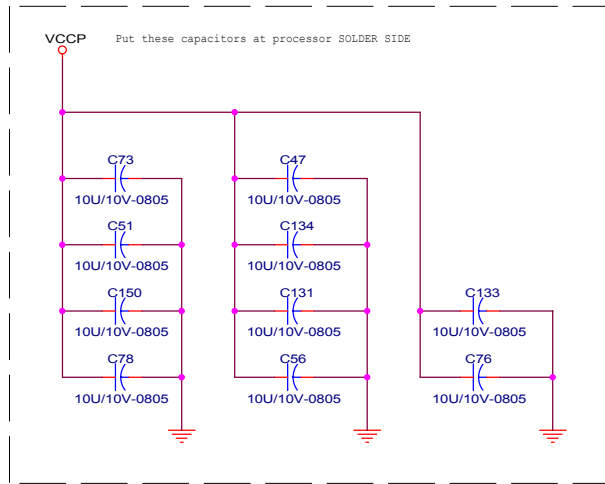
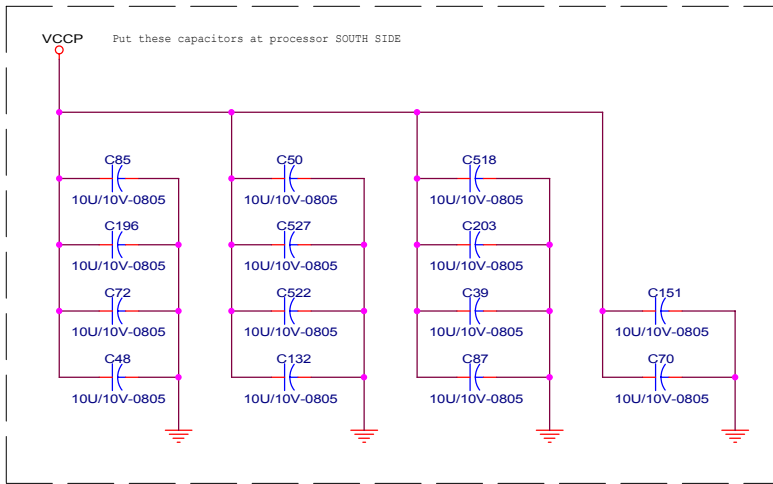
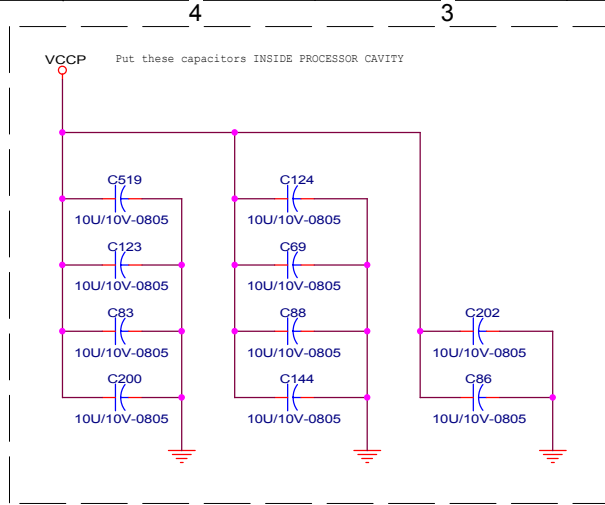
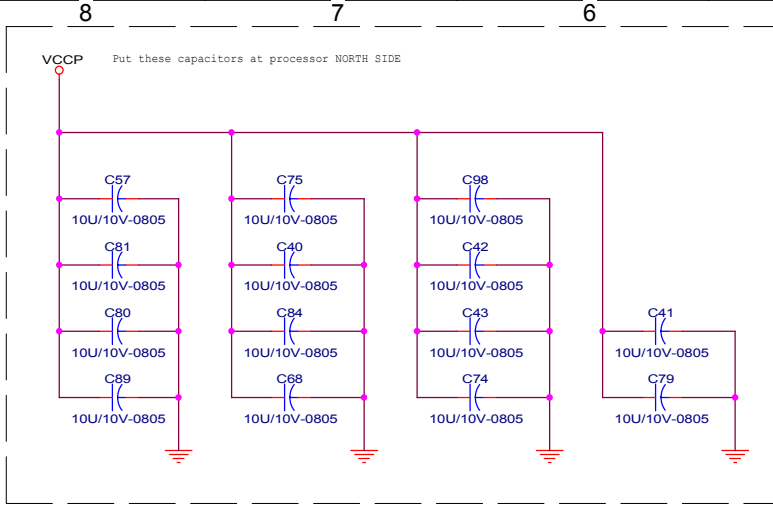


- [6] RS-[0..2] RS-10..21
- [6] HD-[0..63] HD-10..631
- [6] HA-[3..31] HA-3..311
- [6] HREQ-[0..4] HREQ-0..41
- [35] VID[0..4] VID0..41

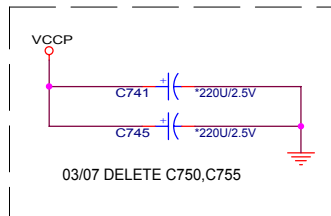
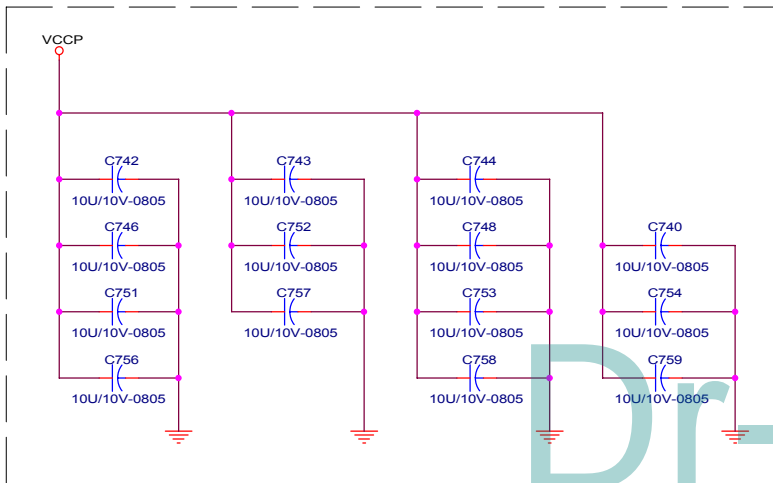
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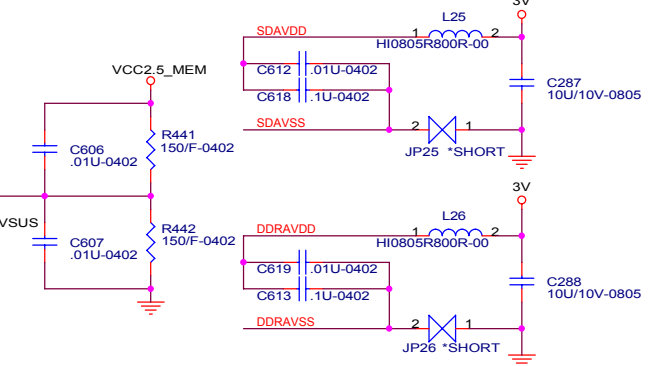
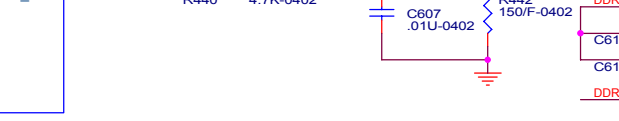
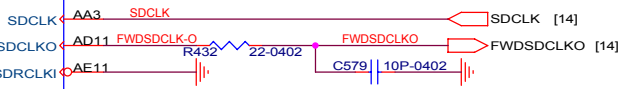
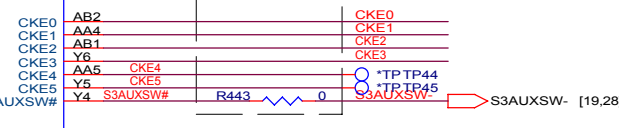
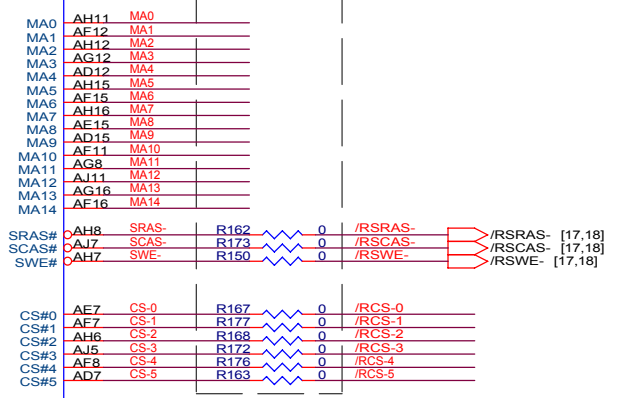
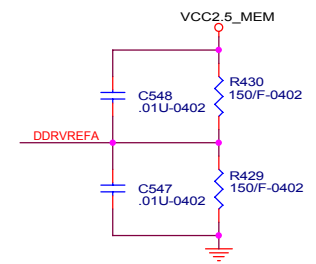
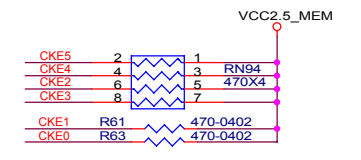
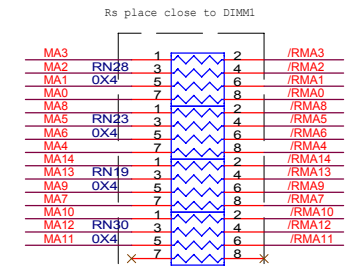
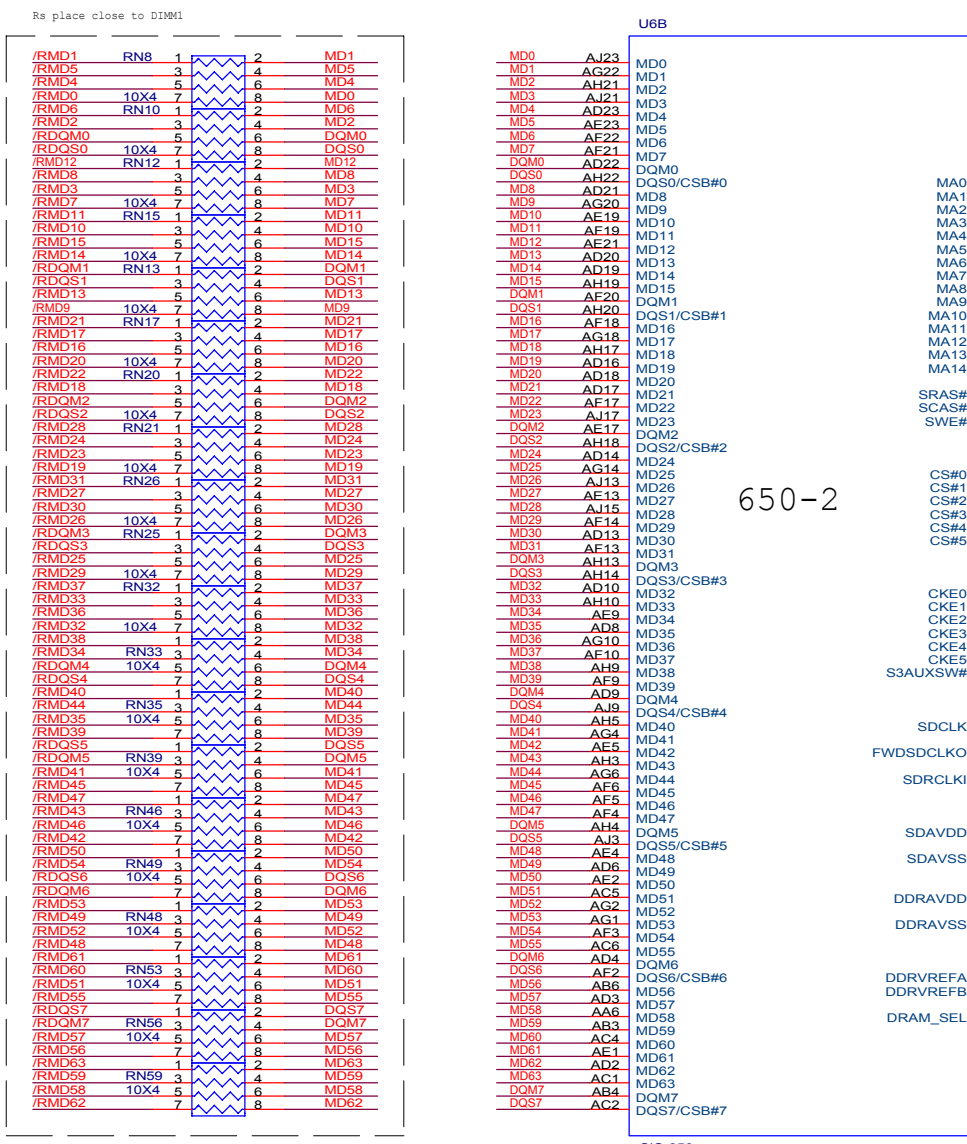
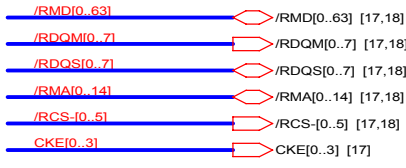
P.S. Choose X7R/X5R components instead of Y5V for all 10uF_1206 capacitors on this page.



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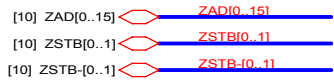


SIS-650

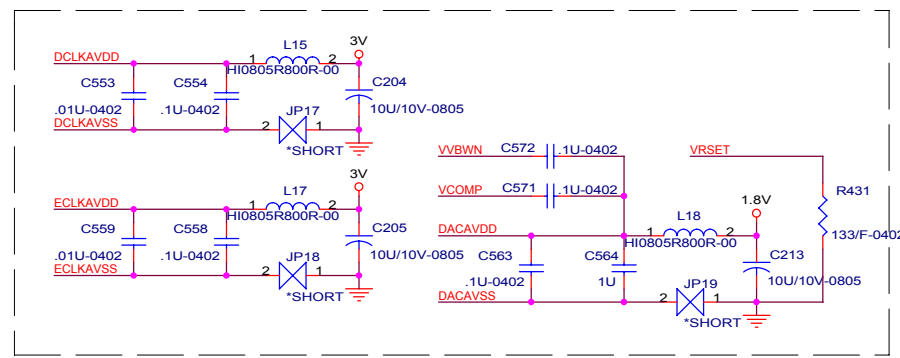
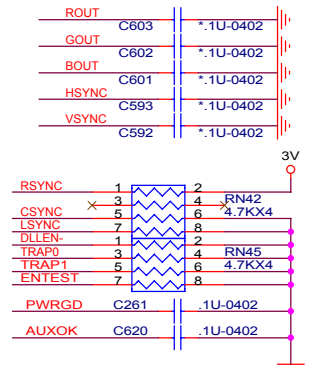
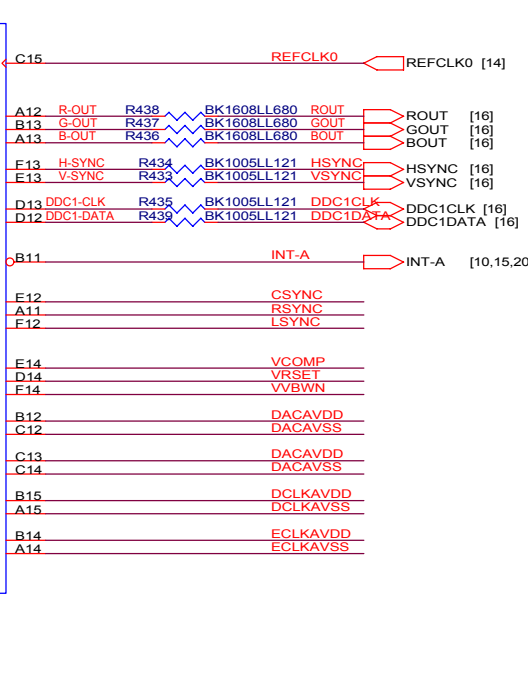
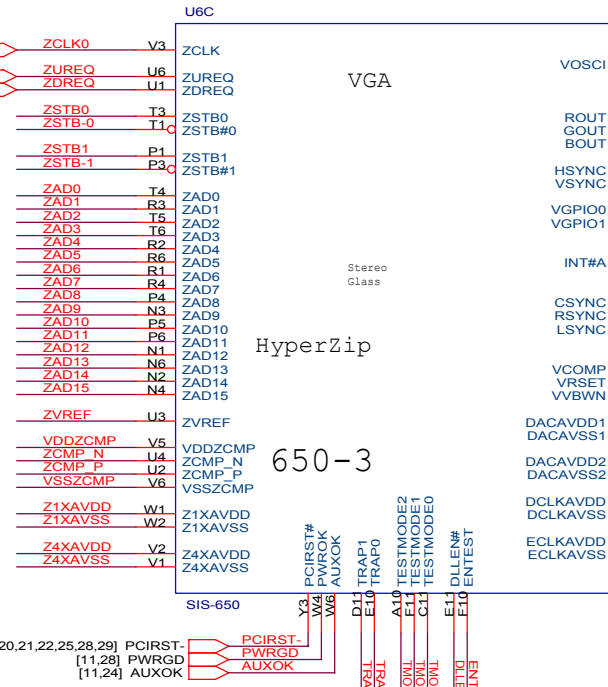
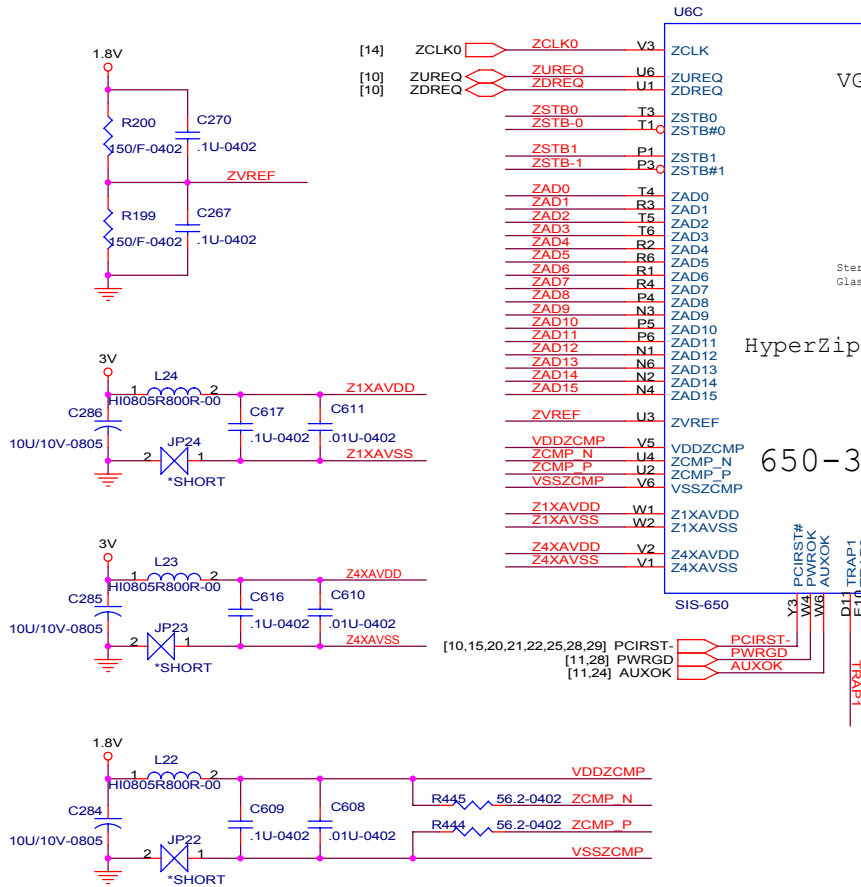


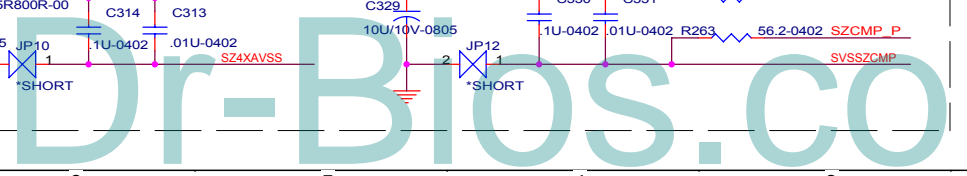
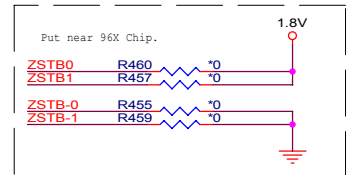
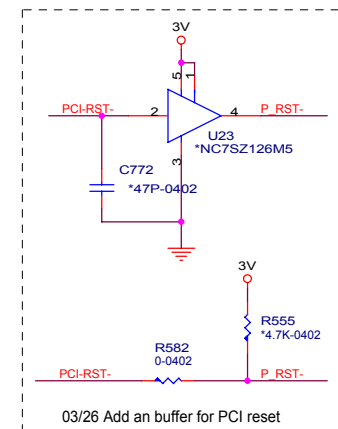
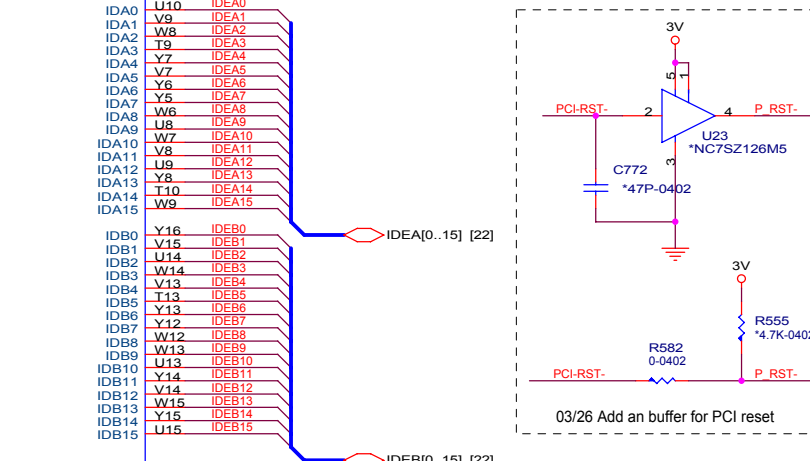
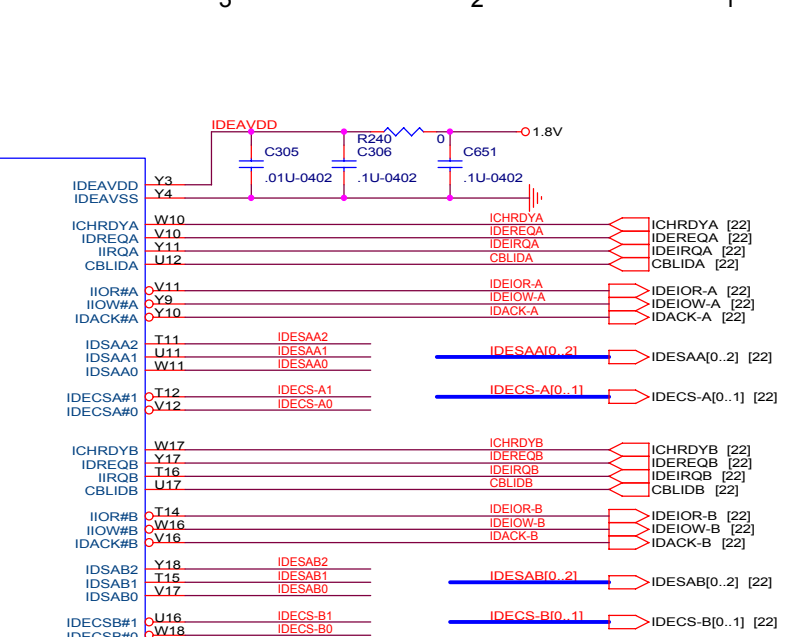
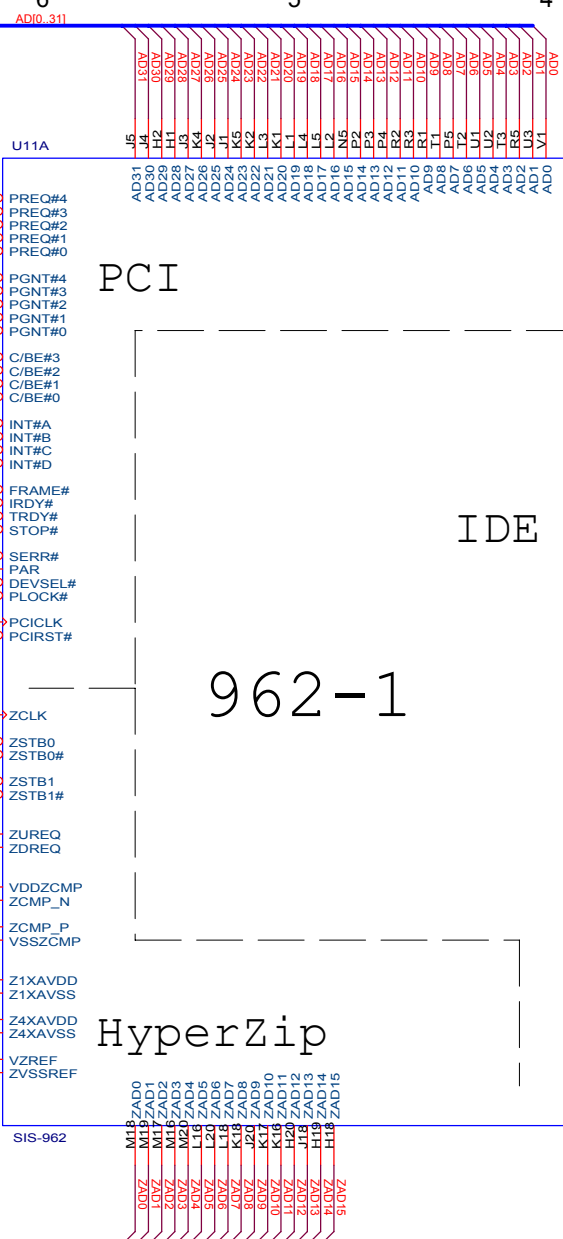
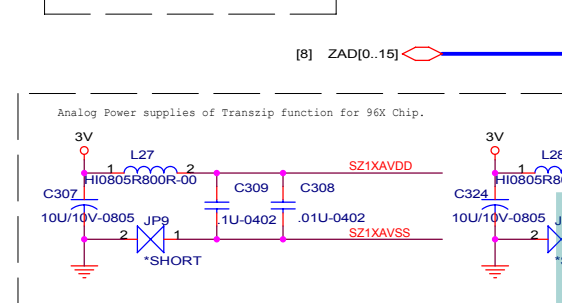
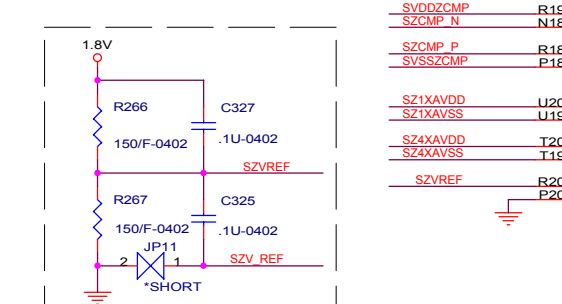
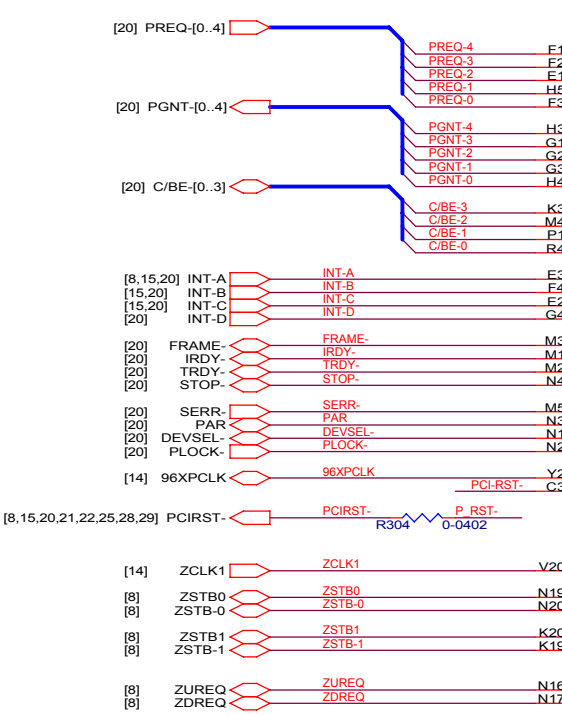
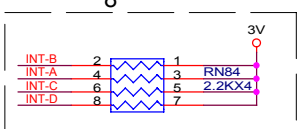
PROJECT : DT1
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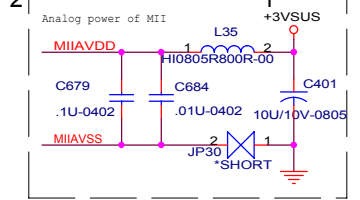
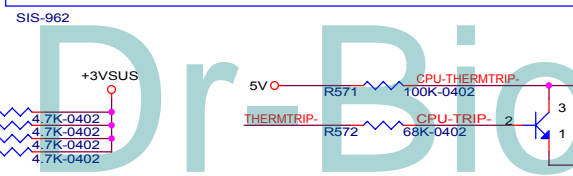
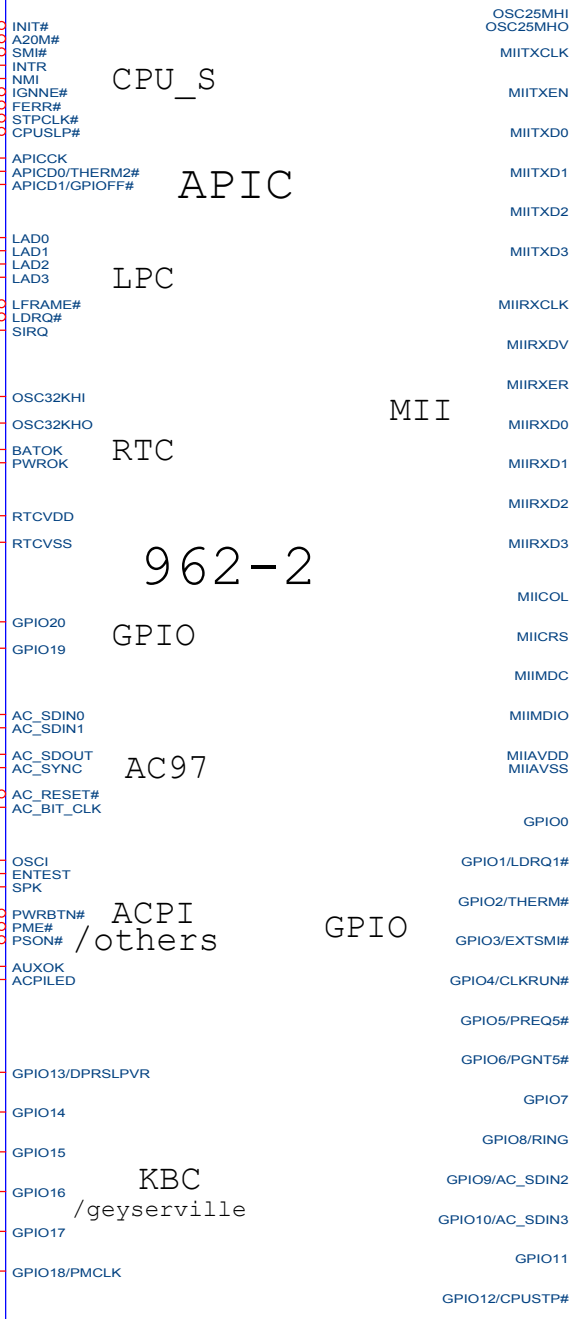
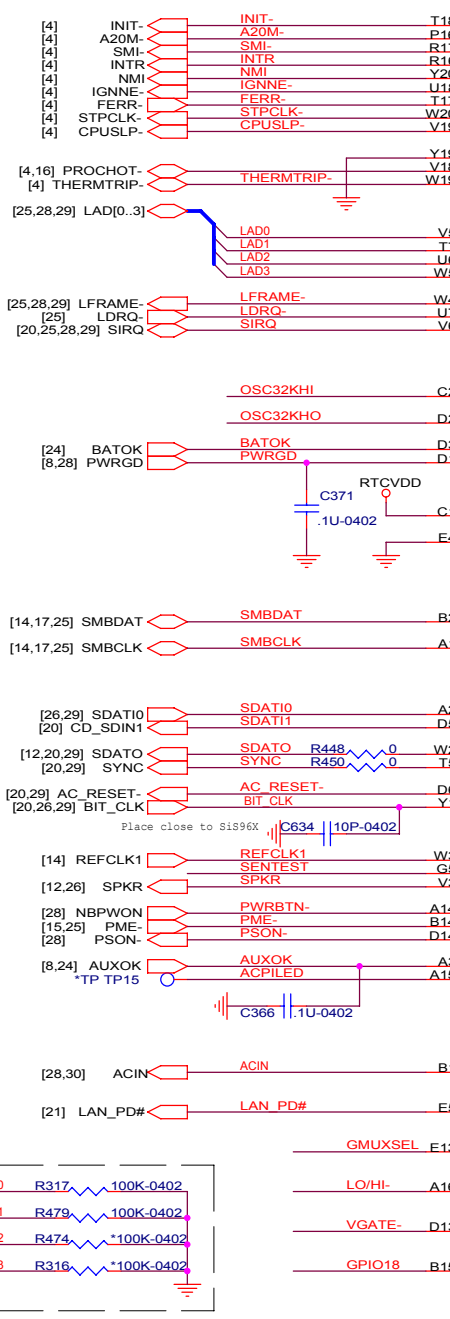
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Custom	NB1-MEMORY FOR DDR	E
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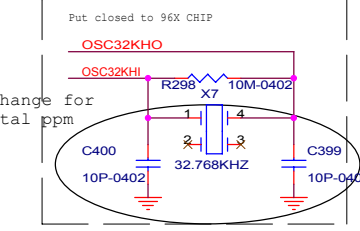
NB Hardware Trap Table			
			embedded pull-low (30-50K Ohm)
DLLEN#	enable PLL	disable PLL	0
DRAM_SEL	SDR	DDR	1 (DDR)
TRAP0	normal	NB debug mode	0
TRAP1	TV selection, NTSC/FAL(0/1)		0
CSYNC	enable VB		0
RSYNC	enable VGA interrupt		1
LSYNC	enable panel link		0



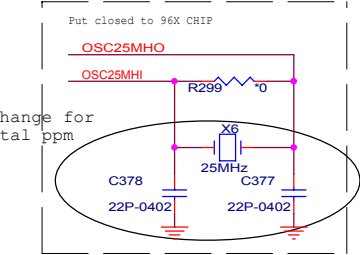




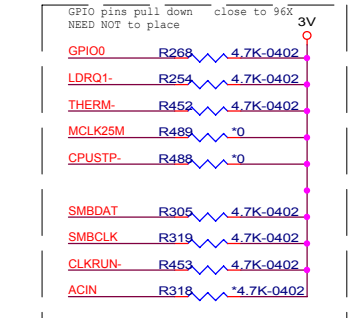
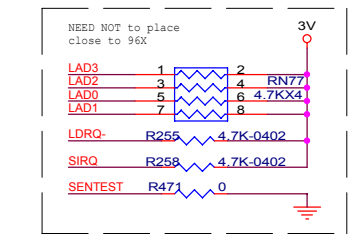
02/28 X7 COMPONENT FROM BG332768721 TO BG33768216



02/28 C399 FROM 20P TO 10P, C400 FROM 15P TO 10P

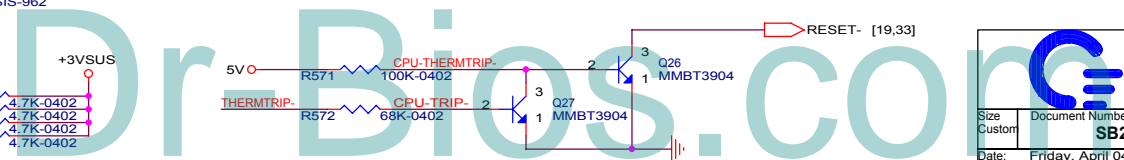
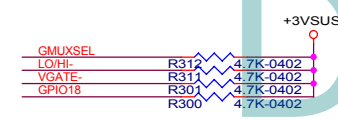
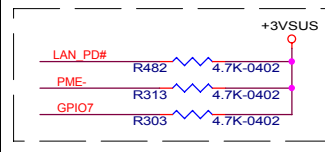


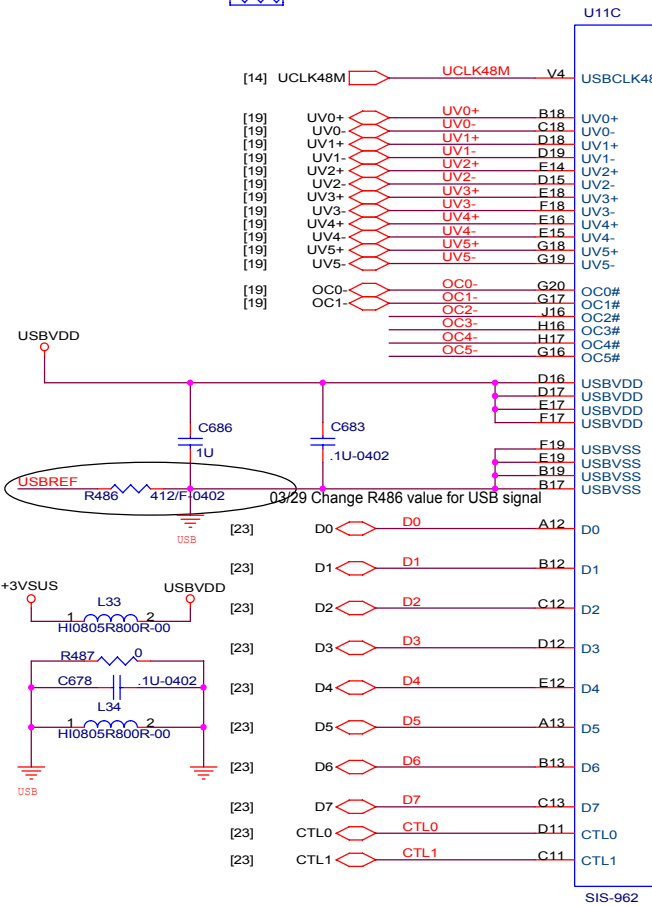
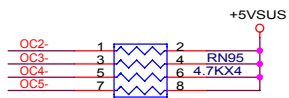
Change for Xtal ppm



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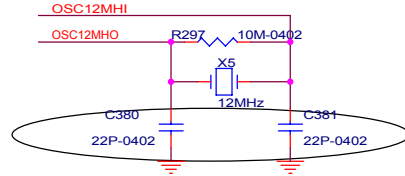
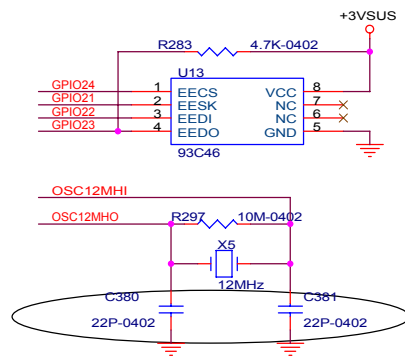
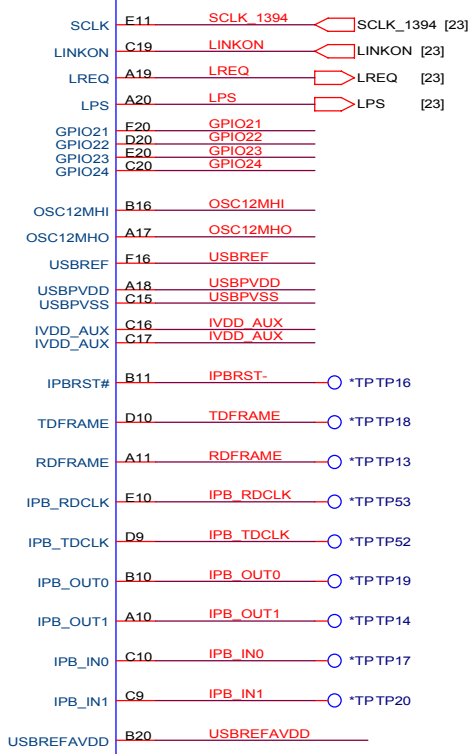


OTHERS

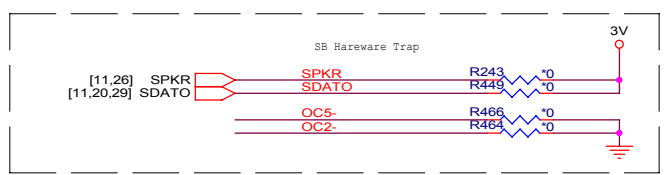
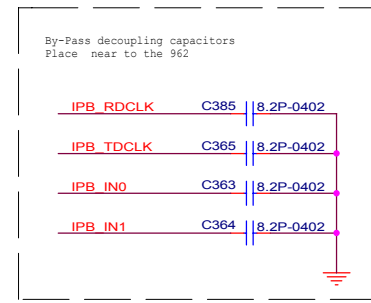
962-3

ACR-IPB /ADSL
(For 962,963)

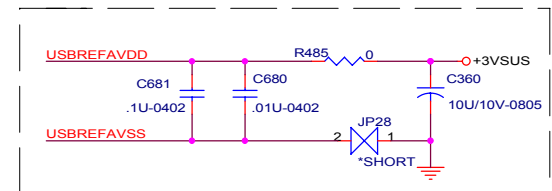
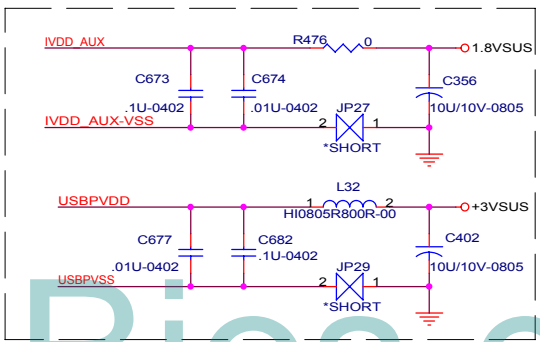
ACR-WIRELESS /BLUETOOTH
(ONLY FOR 963)



3/29 Modify for Xtal



	0	1	Default
SPKR (LPC addr mapping)	disable	enable	0
SDATO (Trap mode)	ROM	PCI_AD	0
OC2-	enable	disable	1
OC5-	enable	disable	1

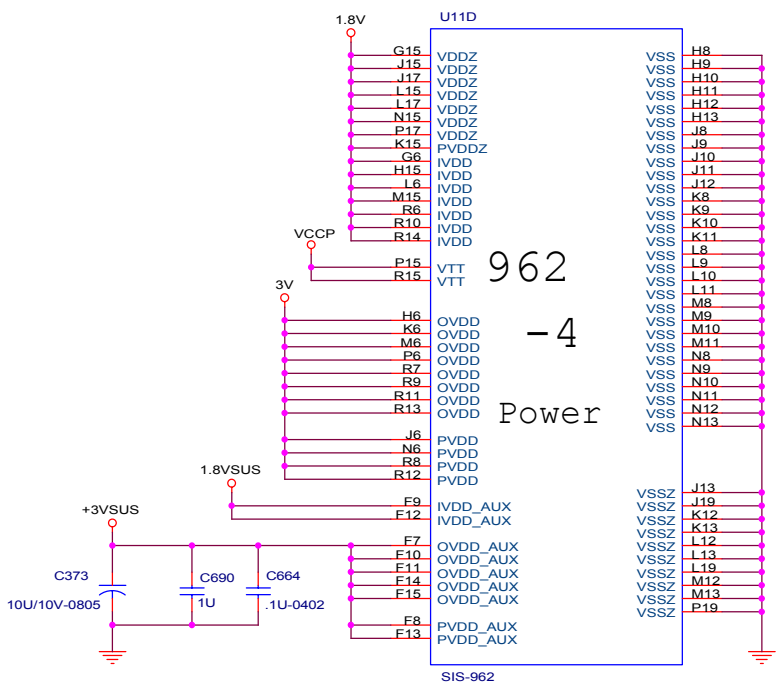
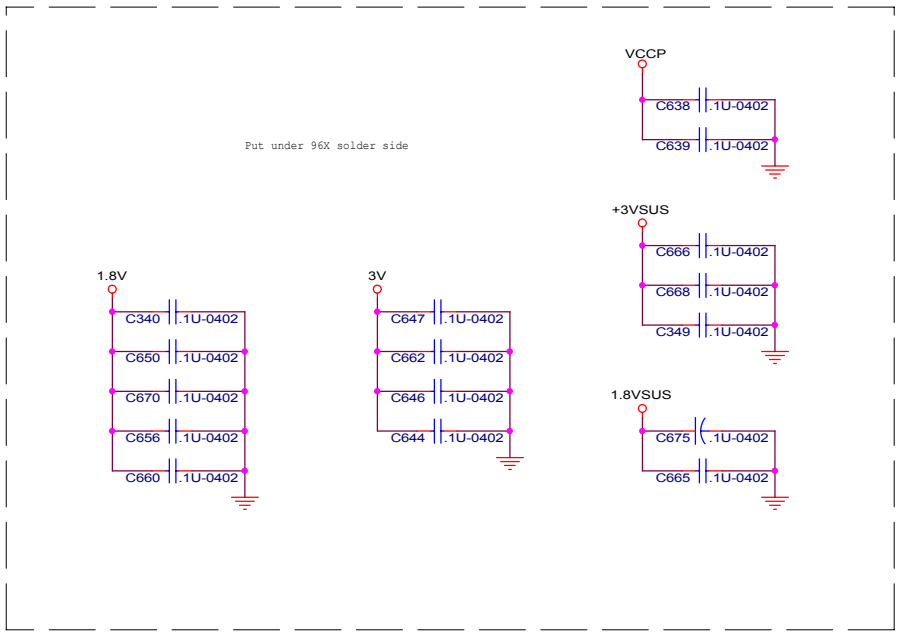
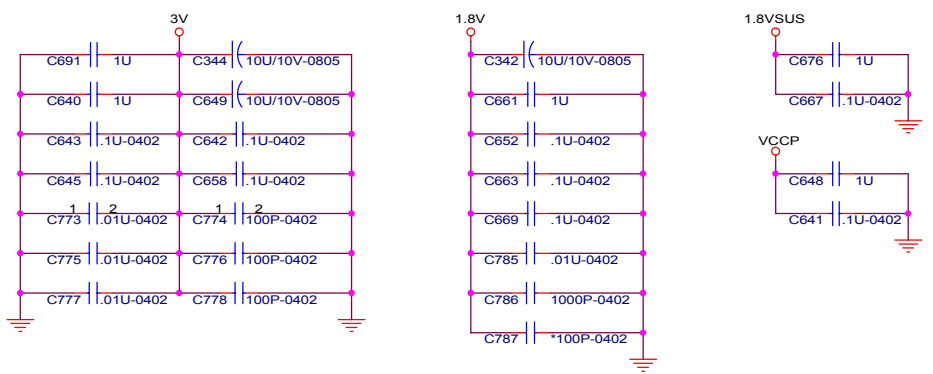


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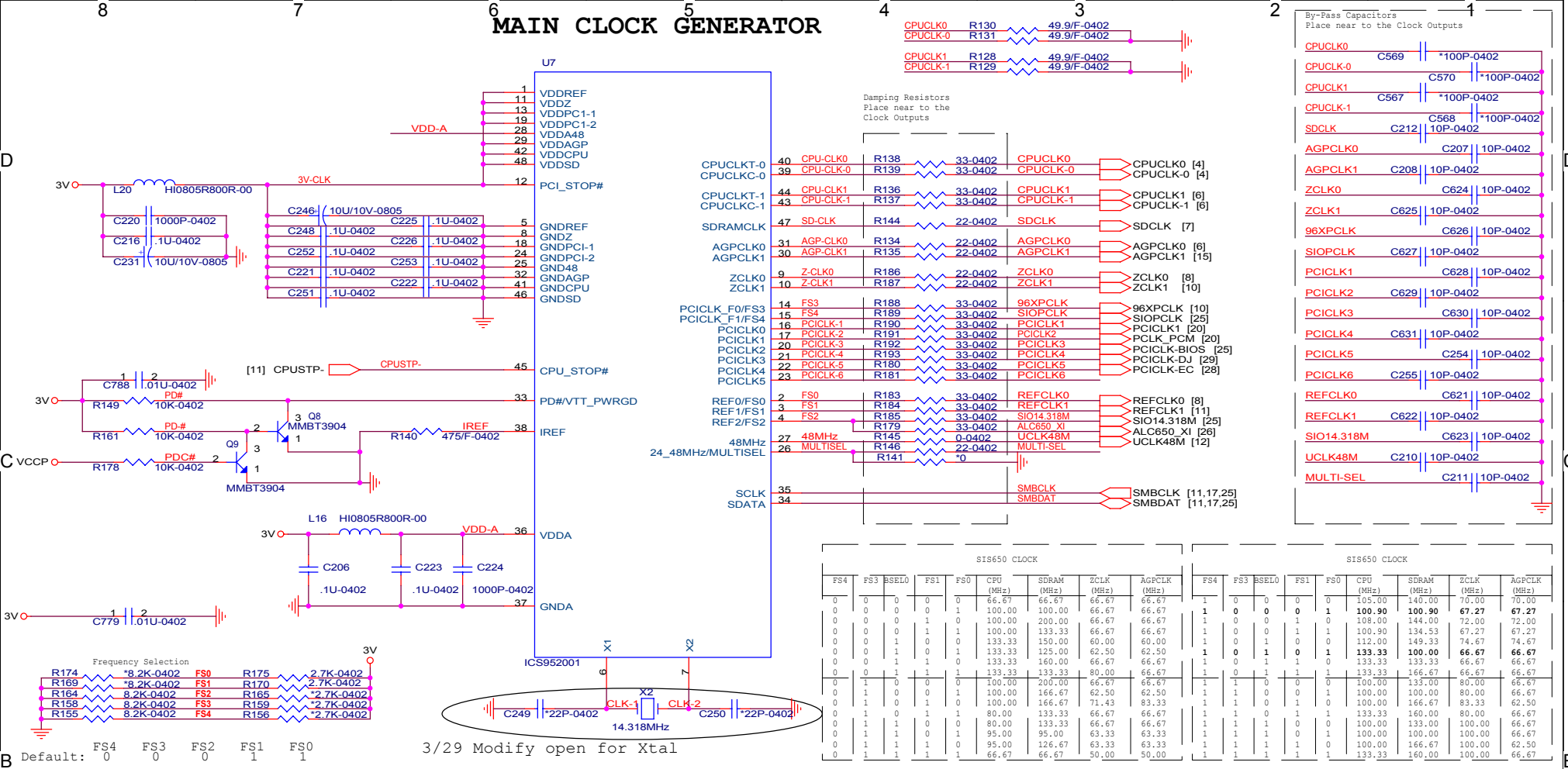
PROJECT : DT1
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Size Custom Document Number SB3-USB/IEEE 1394/Others Rev E

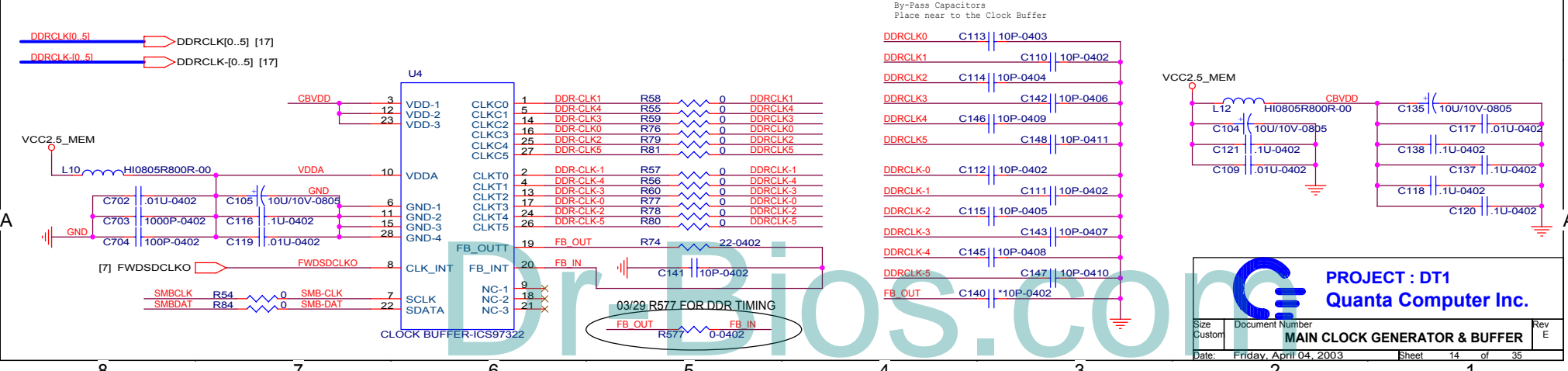
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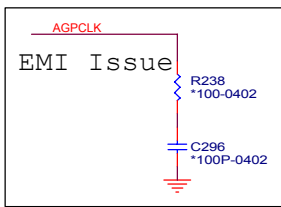


MAIN CLOCK GENERATOR



CLOCK BUFFER

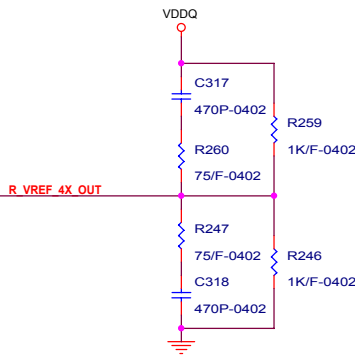
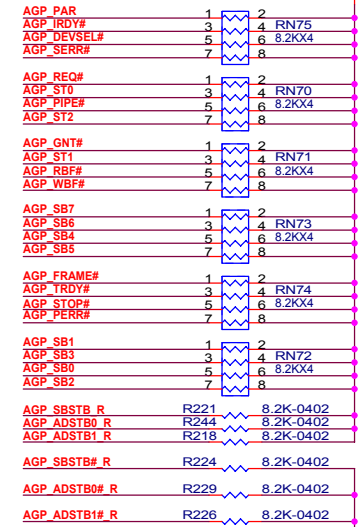




AGP 4X OUT INTERFACE

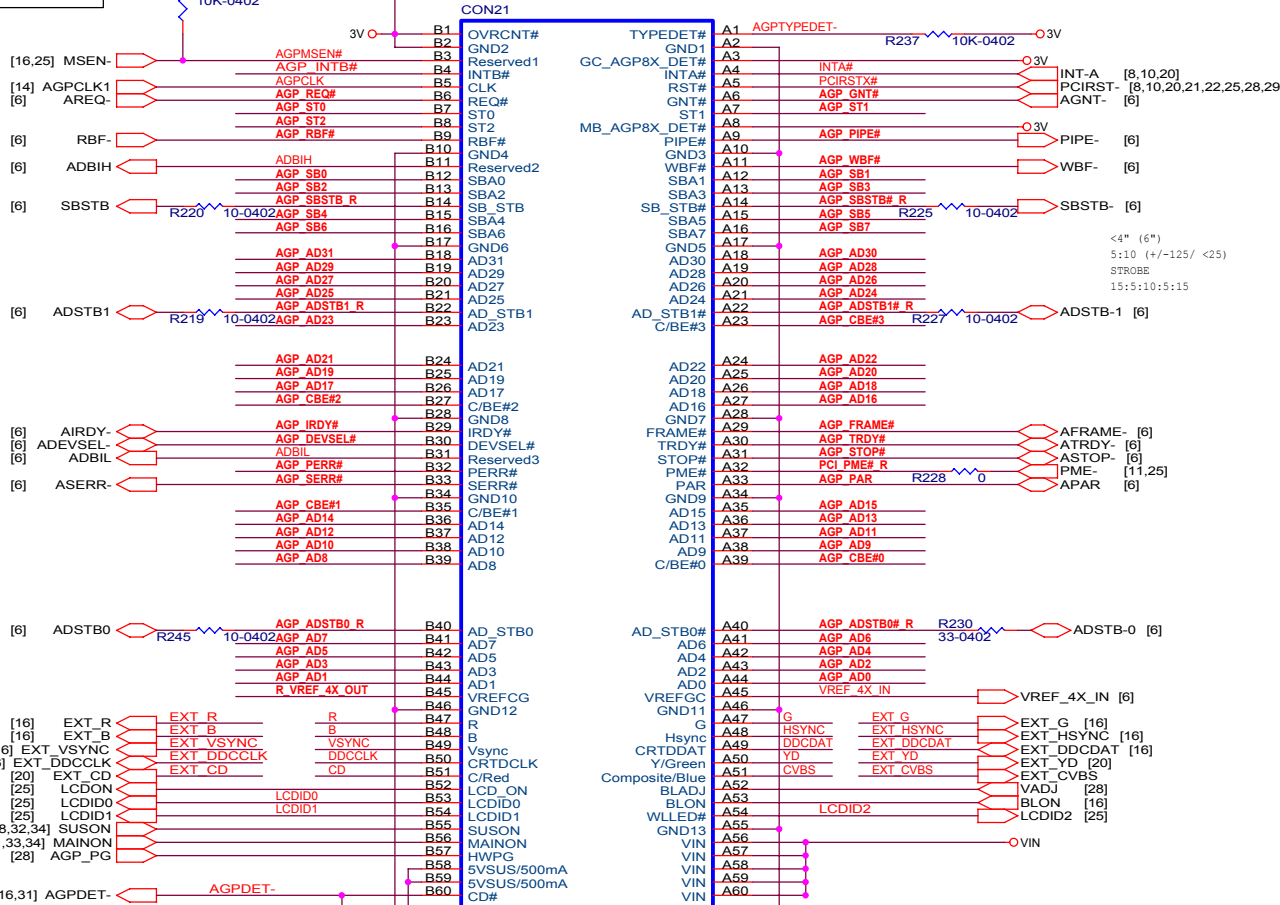
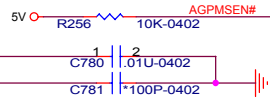
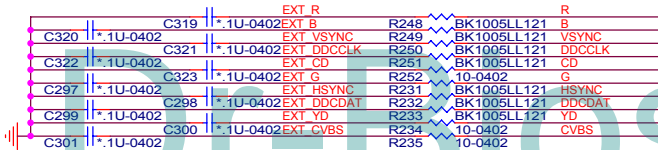
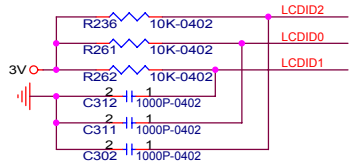
AGP_PLUG:
HI: USE AGP GRAPHIC
LO: USE ATI GRAPHIC

AGP PULL UPS



PLACE CLOSE TO CONNECTOR

02/26 +5VUS CHANGE TO 5V

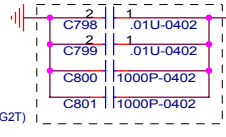
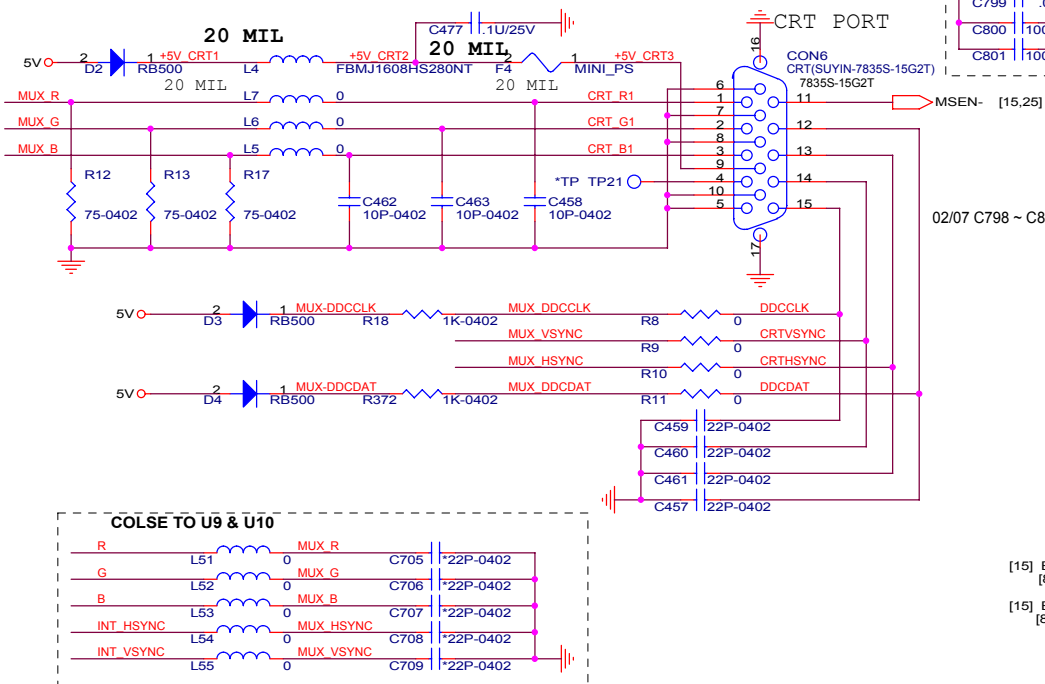


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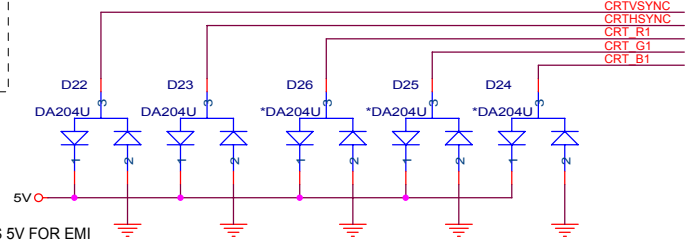
Size: Custom Document Number: **AGP SLOT** Rev: E

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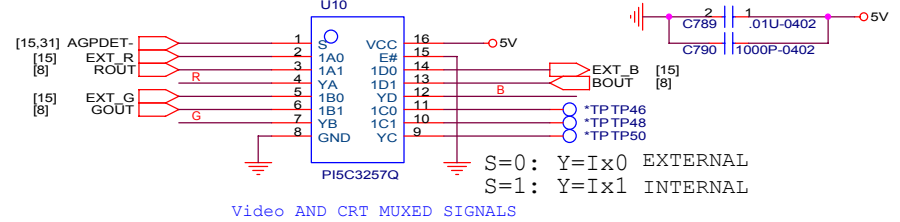
CRT



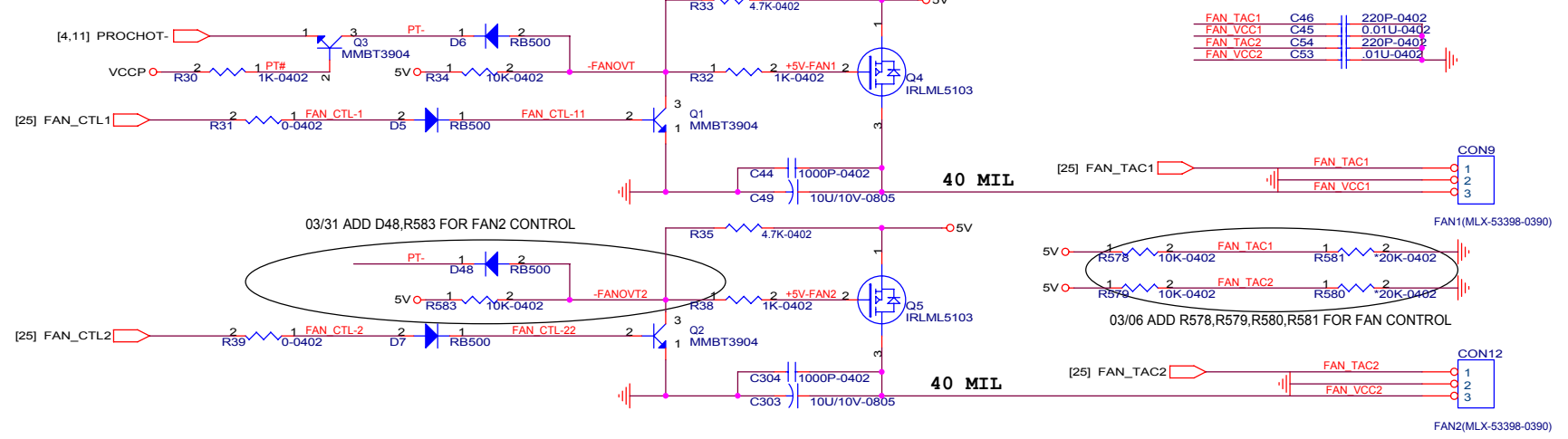
PSOT05LC



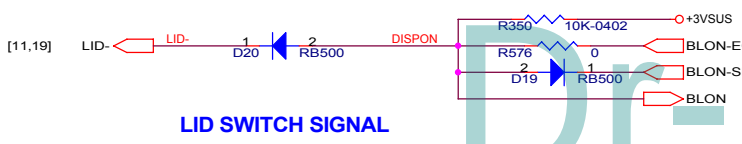
Add 2 multiplexer for CRT and TV swap



FAN Control



LCD-LID

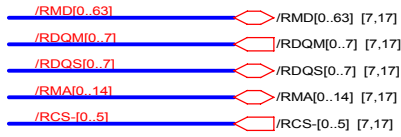


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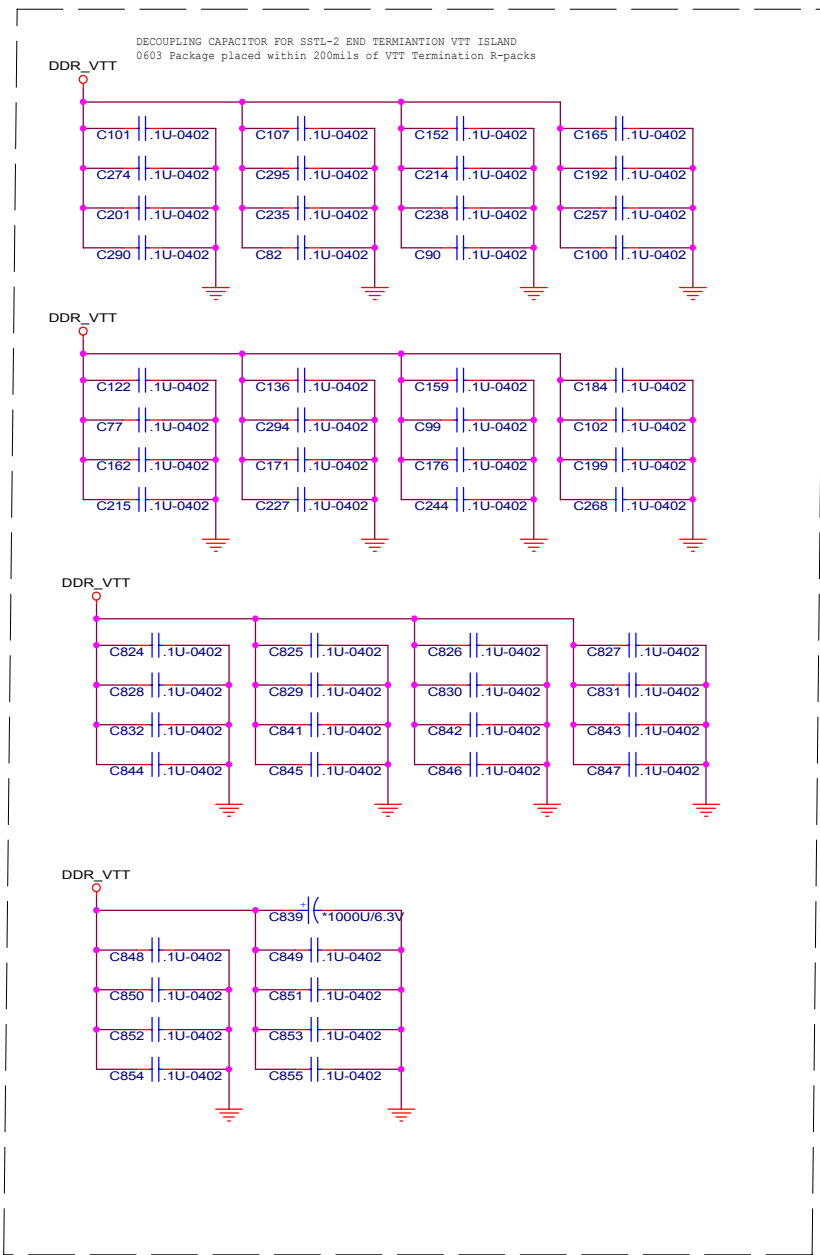
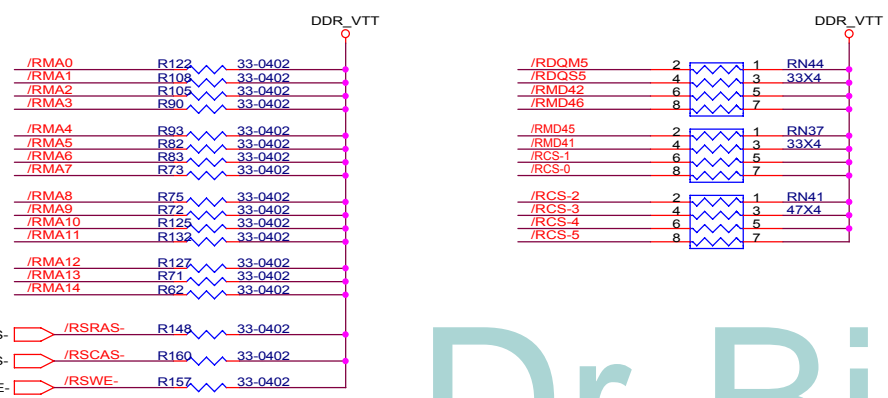
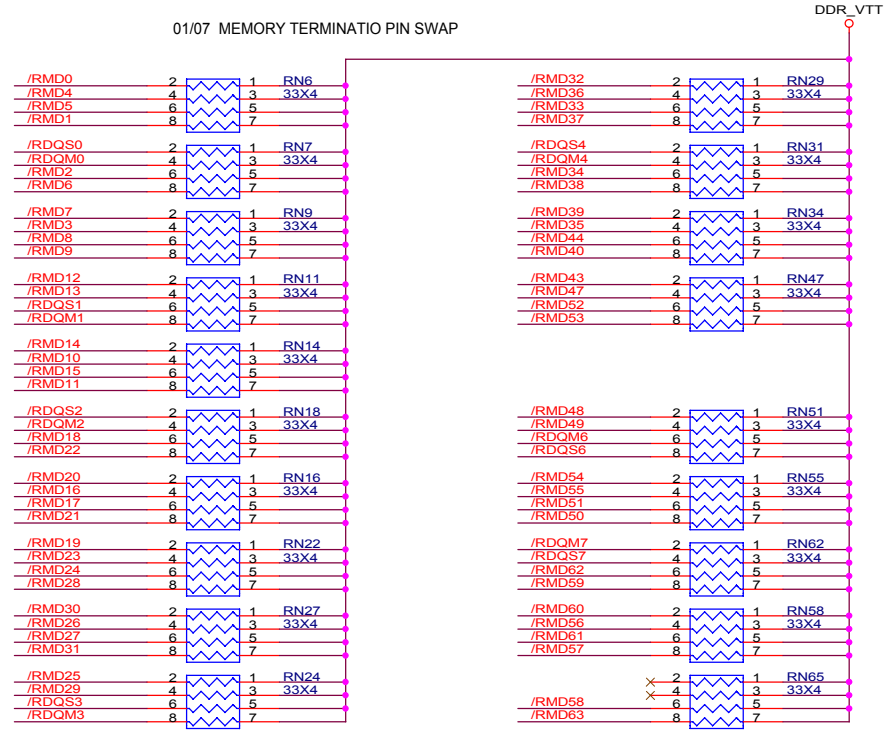
Size: Document Number
Customer: CRT, LCD LID, BLON & FAN CONTROL
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SSTL-2 Termination Resistors

SDR		Rs	DDR		Rs	Rtt
MD/DQM (/DQS)	LV-CMOS	0/10/-	SSTL-2		10	33
MA/Control	LV-CMOS	10	SSTL-2		0	33
CS	LV-CMOS	0	SSTL-2		0	47
CKE	OD 3.3V		SSTL-2			
			OD 2.5V			



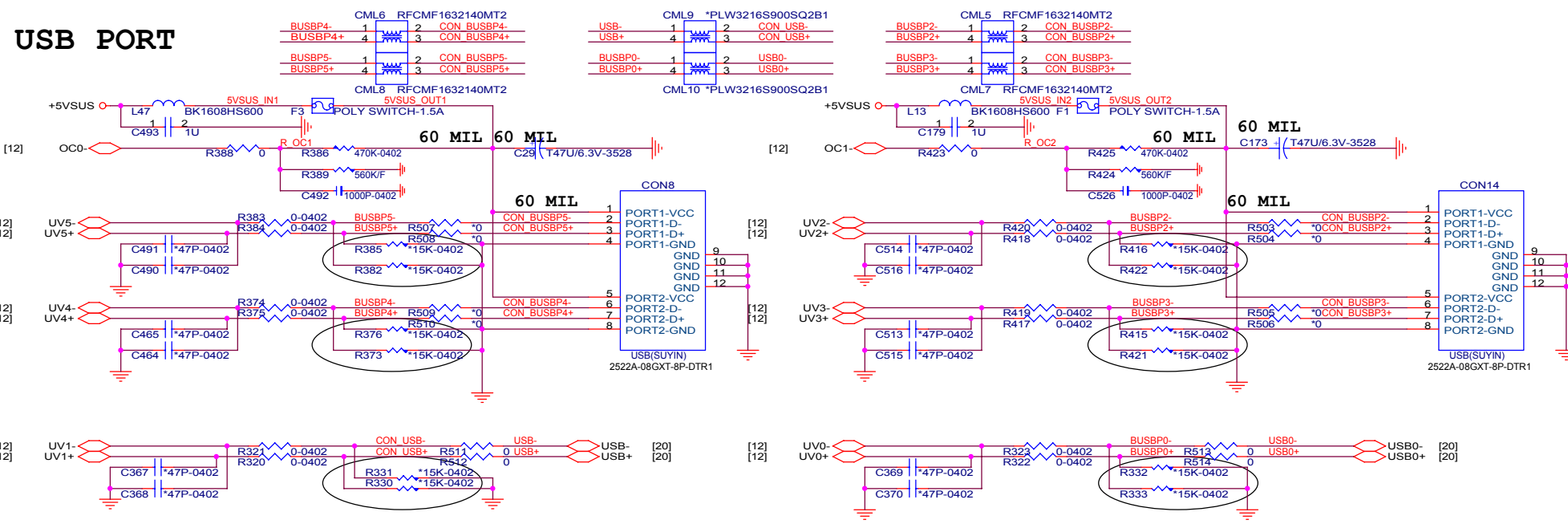
01/07 MEMORY TERMINATIO PIN SWAP



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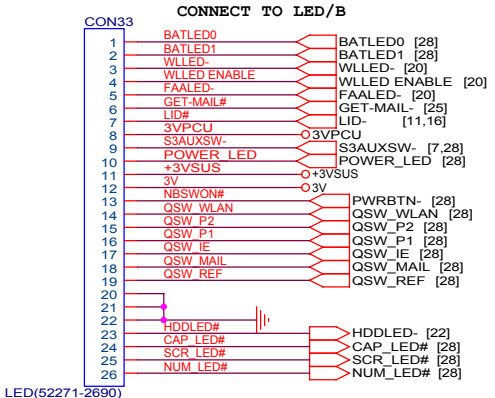
Size	Document Number	Rev
Custom	SSTL-2 TERMINATION RESISTORS	E
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USB PORT

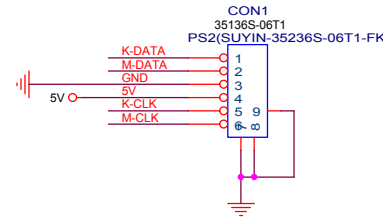


03/07 R330,R331,R332,R333,R373,R376,R382,R385,R416,R422 UNSTAFF

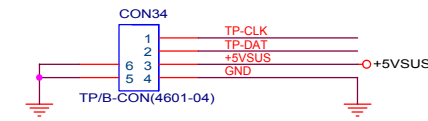
LED/B CONNECTOR



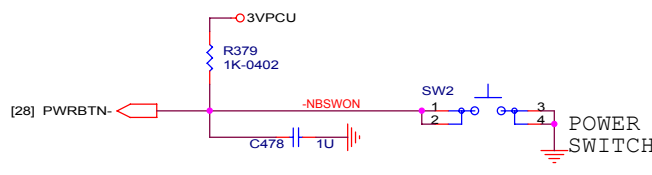
PS/2



TP/B CONNECTOR



POWER SW



RESET SW

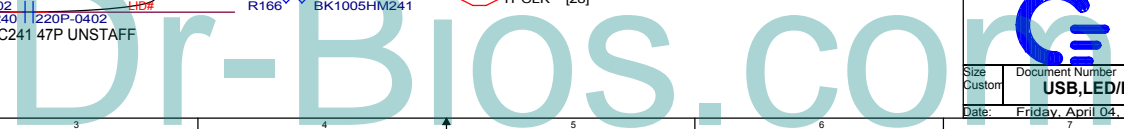


02/28 C45,C455,C456,C457 FROM 100P CHANGE TO 270P FOR EMI

QSW P1	C262	*.1U-0402	C452	.1U-0402	3V
QSW P2	C262	*.1U-0402	C452	.1U-0402	+5VSUS
NBSWON#	C266	*.1U-0402	C482	.1U-0402	3VPCU
WLLED ENABLE	C233	220P-0402	C245	.1U-0402	+3VSUS
FAILED-	C233	220P-0402	C269	*.1U-0402	QSW_REF
QSW WLAN	C247	220P-0402	C228	*.1U-0402	BATLED0
QSW MAIL	C247	220P-0402	C228	*.1U-0402	BATLED1
QSW IE	C258	*.1U-0402	C230	*.1U-0402	WLLED-
HDDLED#	C260	*.1U-0402	C232	*.1U-0402	GET-MAIL#
CAP_LED#	C273	220P-0402	C239	220P-0402	TP-DAT
SCR_LED#	C275	220P-0402	C242	*47P-0402	TP-CLK
NUM_LED#	C280	220P-0402	C244	*47P-0402	LID#
	C281	220P-0402	C240	220P-0402	



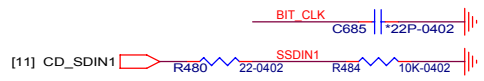
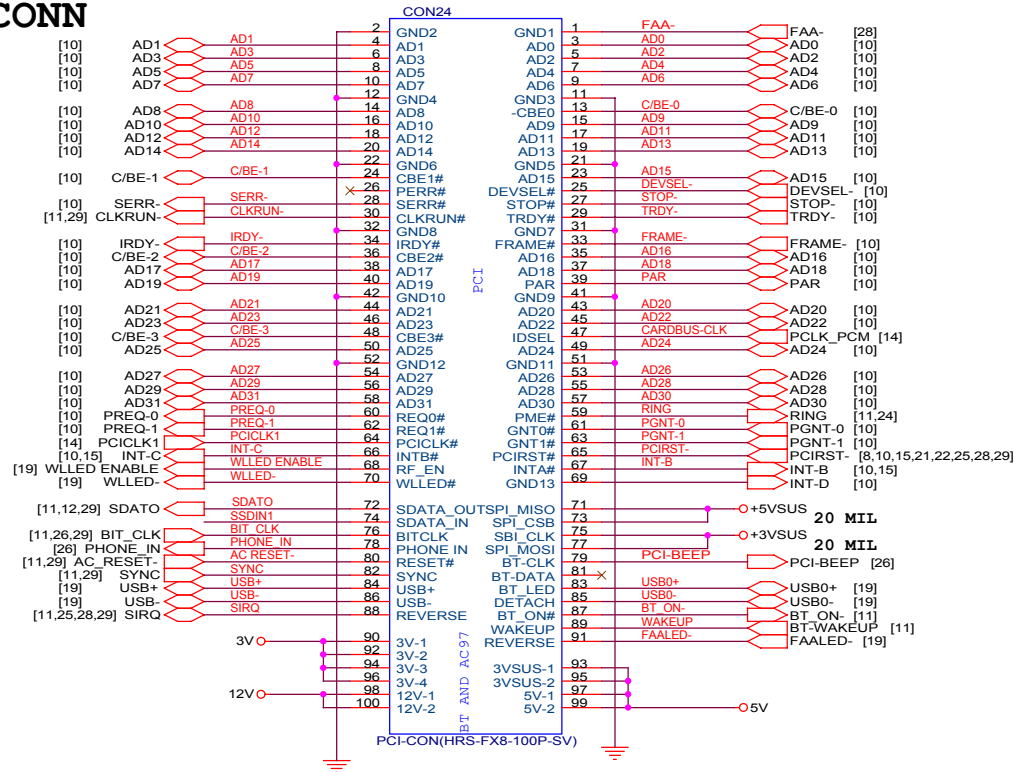
03/06 C453,C454,C455,C456 UNSTAFF



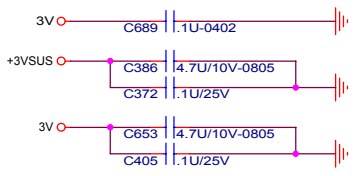
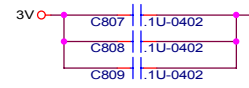
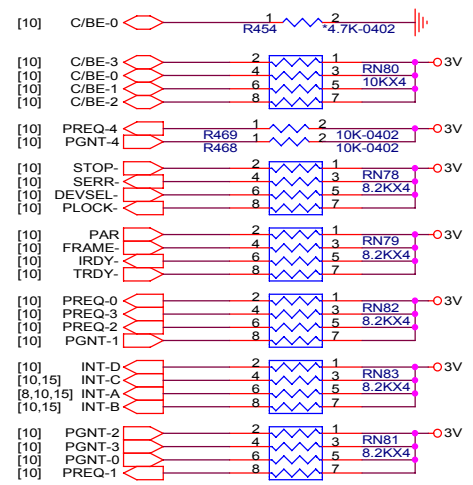
PROJECT : DT1
Quanta Computer Inc.

Size	Document Number	Rev
Customer	USB,LED/B,PS2,PW & RST CONN	E
Date:	Friday, April 04, 2003	Sheet 19 of 35

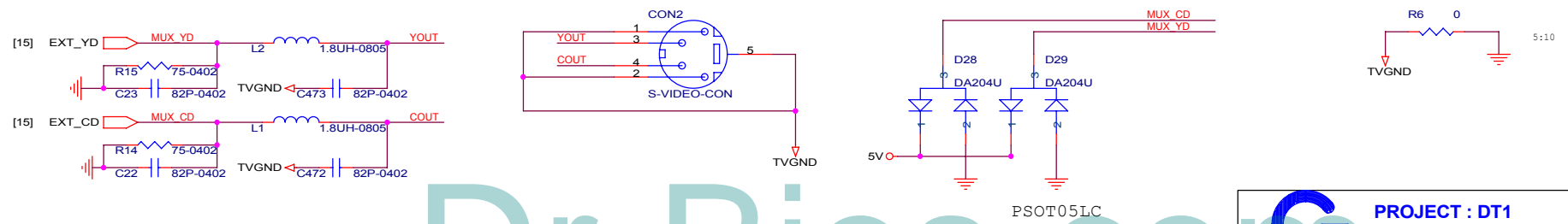
PCI-CONN



PCI PULL UP



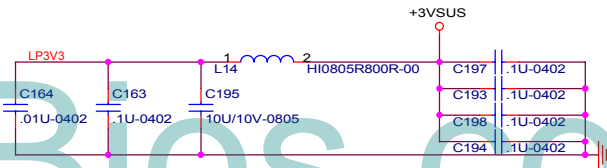
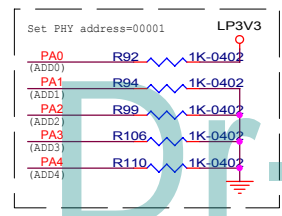
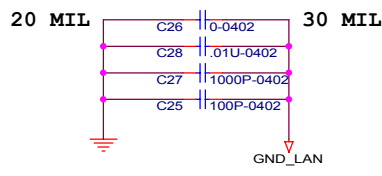
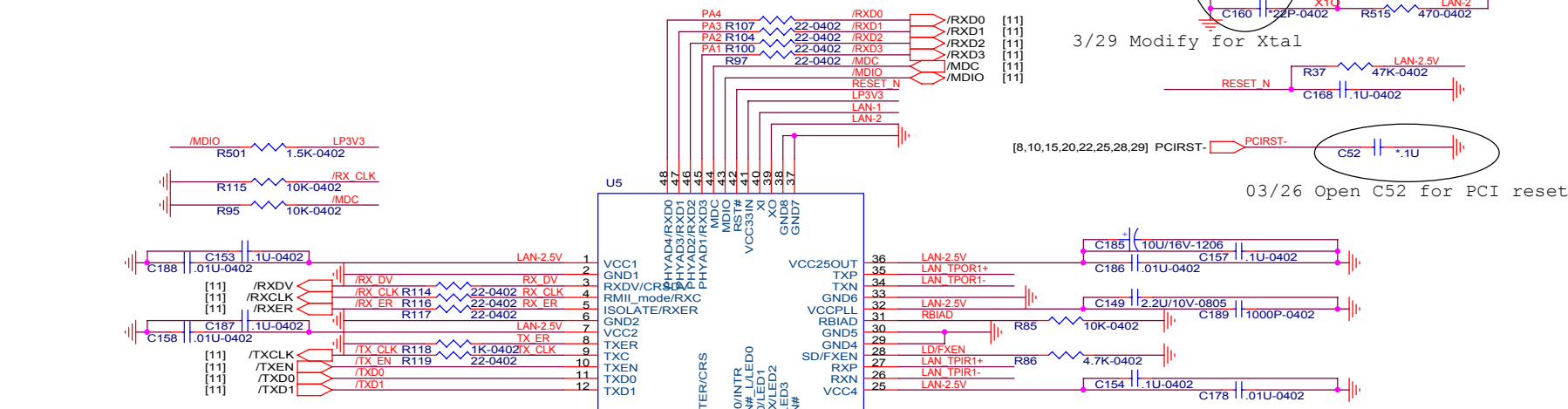
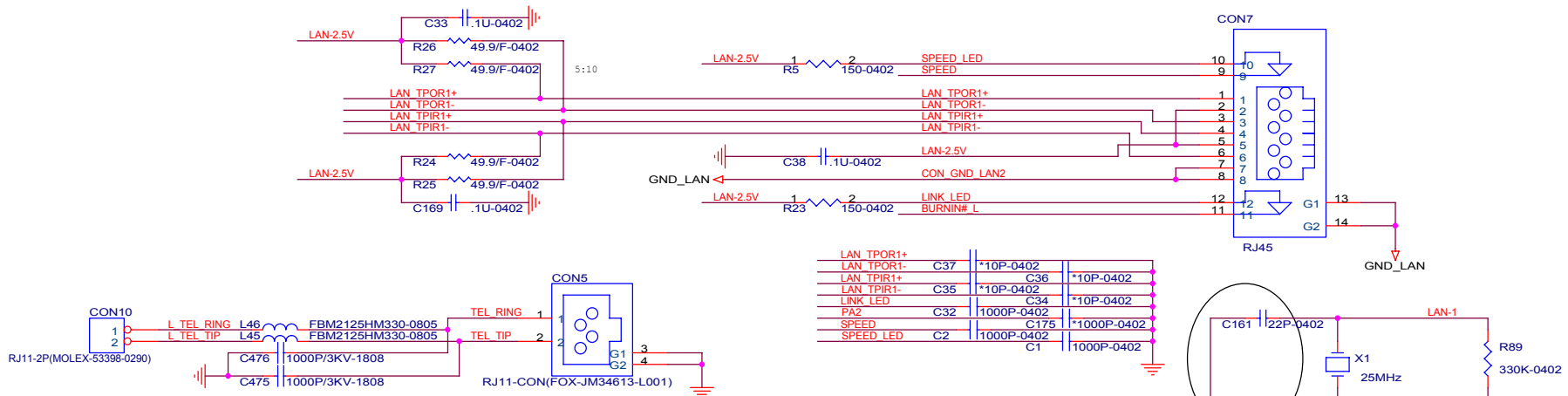
S-VIDEO (TV OUT)



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PROJECT : DT1
Quanta Computer Inc.

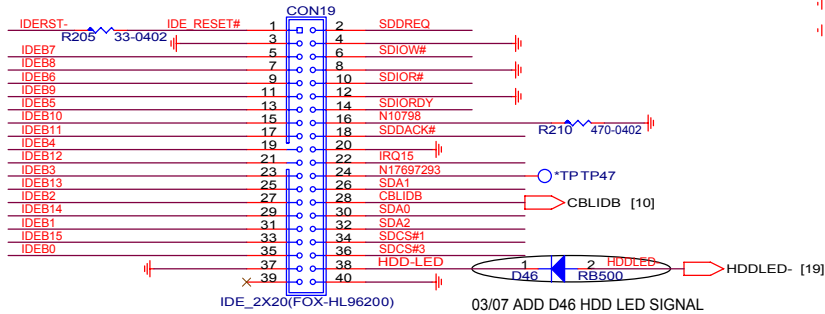
Size	Document Number	Rev
Custom	PCI CONN & PULL UP	E
Date:	Friday, April 04, 2003	Sheet 20 of 35



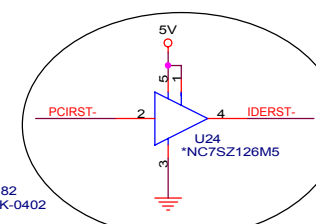
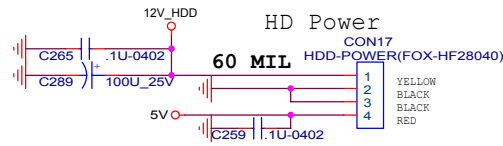
PROJECT : DT1
Quanta Computer Inc.

HDD, CD-ROM CONNECTOR

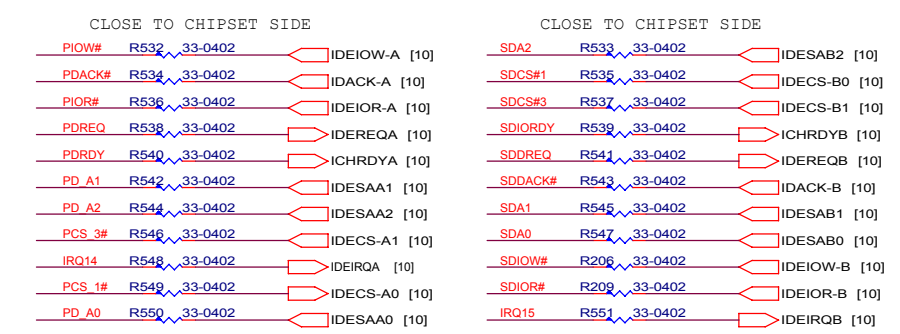
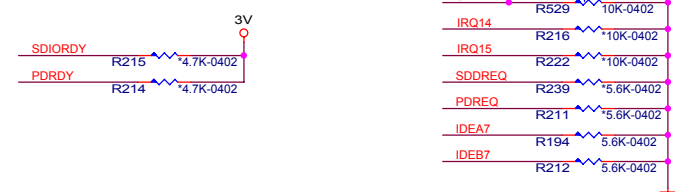
12/18 CON19 CHANGE COMPONENT



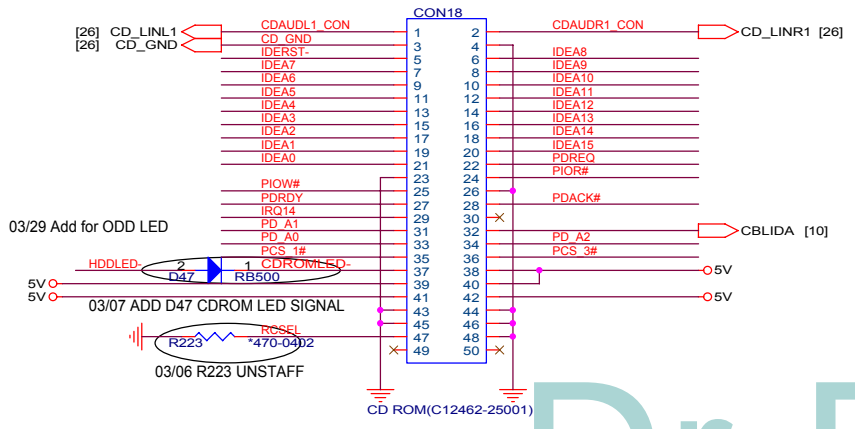
HDD POWER CONNECTOR



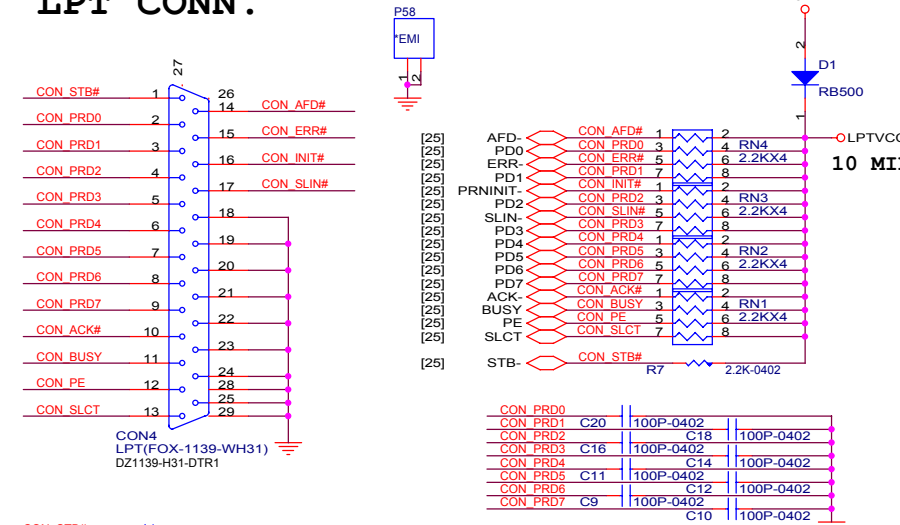
PLACEMENT ON BOT SIDE



CD-ROM connector



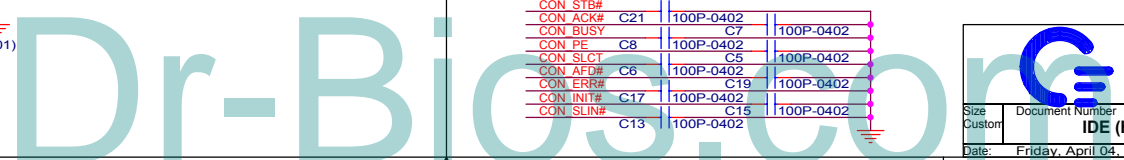
LPT CONN.

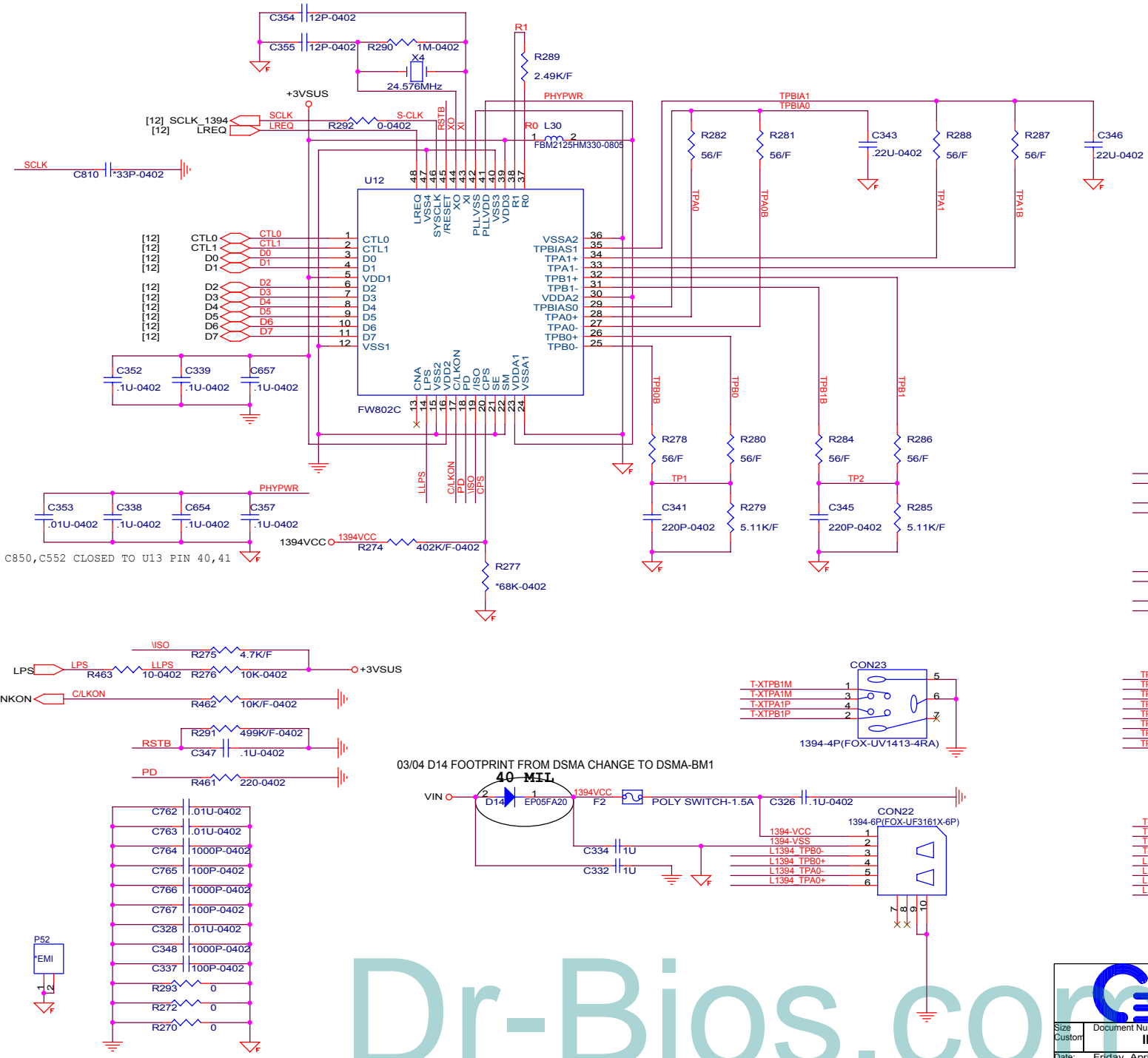


CON STB#	C21	100P-0402	C7	100P-0402
CON ACK#	C8	100P-0402	C7	100P-0402
CON BUSY	C8	100P-0402	C7	100P-0402
CON PE	C8	100P-0402	C7	100P-0402
CON SLCT	C6	100P-0402	C5	100P-0402
CON AFD#	C6	100P-0402	C19	100P-0402
CON ERR#	C17	100P-0402	C15	100P-0402
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CON SLIN#	C13	100P-0402	C13	100P-0402

PROJECT : DT1
Quanta Computer Inc.

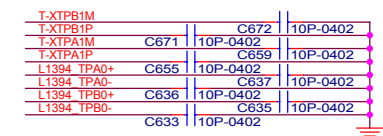
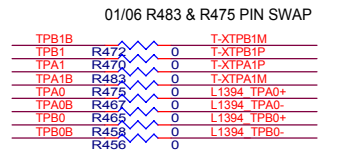
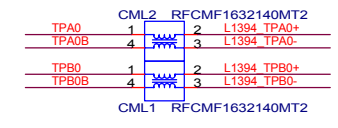
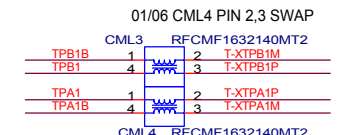
Size: Document Number: Rev E
Customer: IDE (HDD & CDROM) & LPT
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C850, C552 CLOSED TO U13 PIN 40, 41

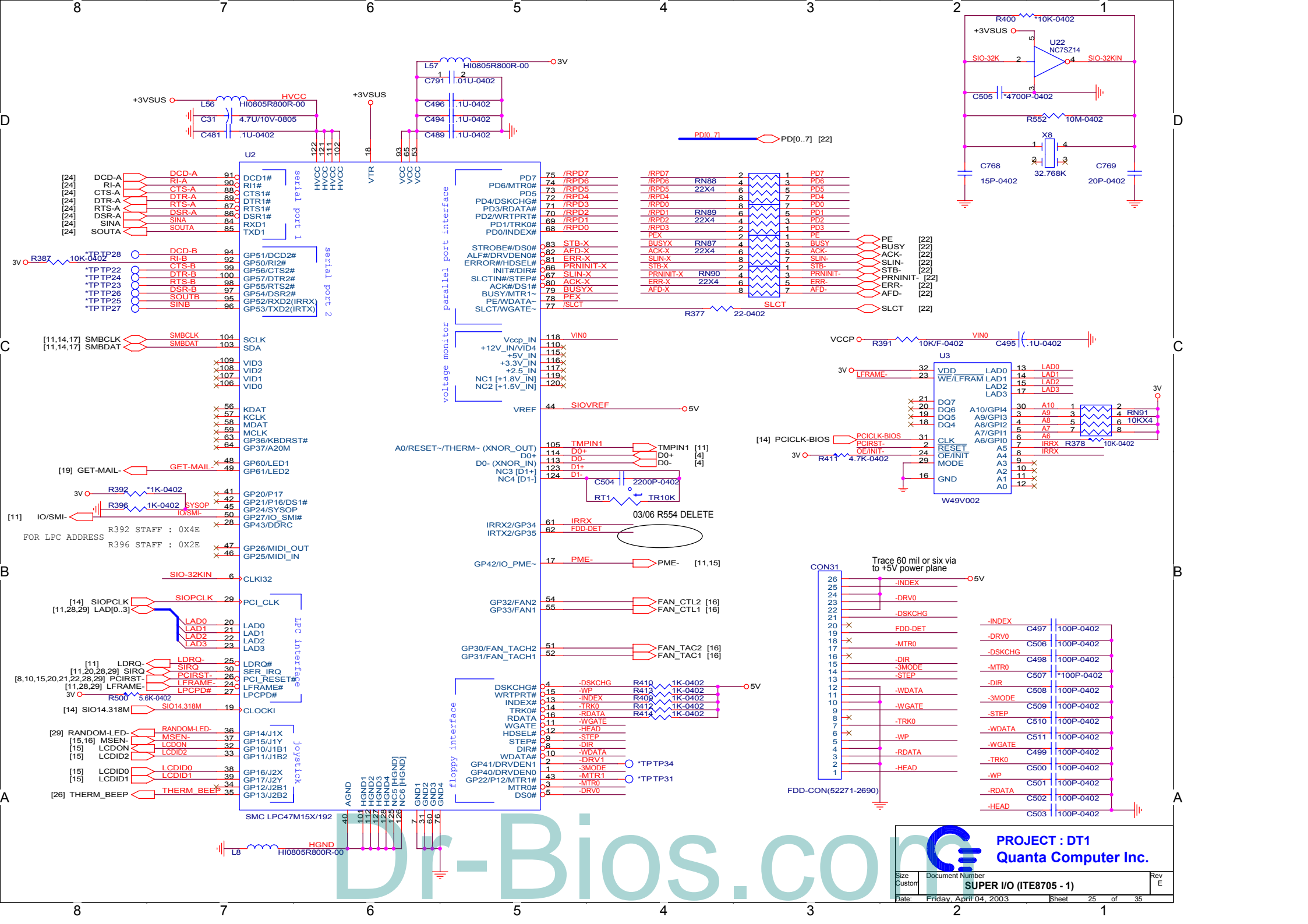
03/04 D14 FOOTPRINT FROM DSMA CHANGE TO DSMA-BM1

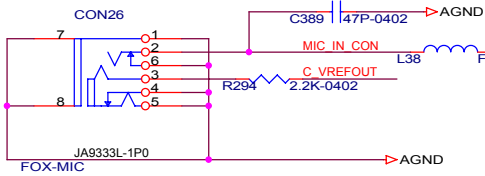
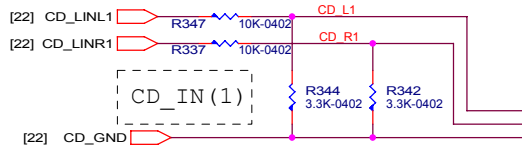
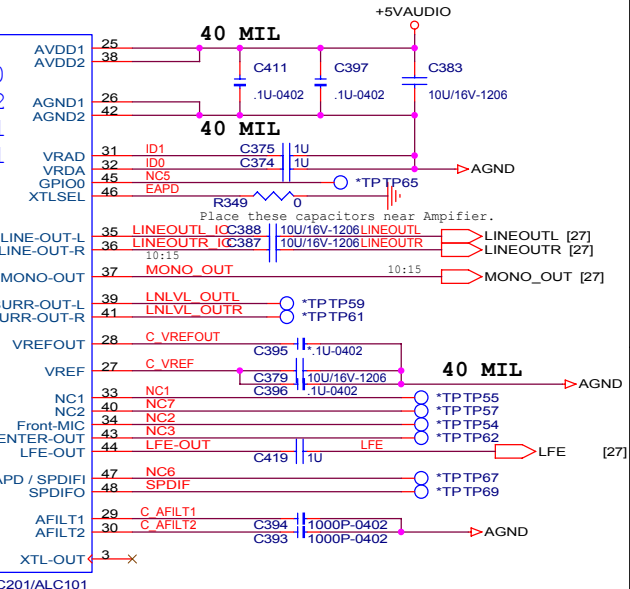
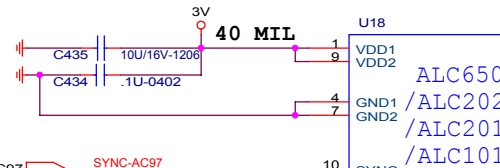
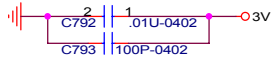


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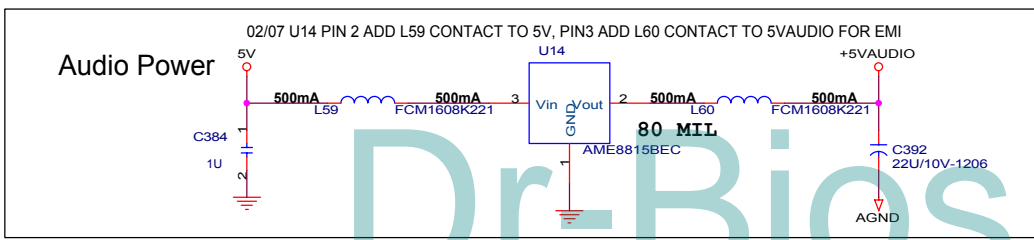
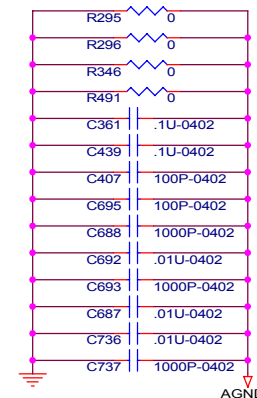
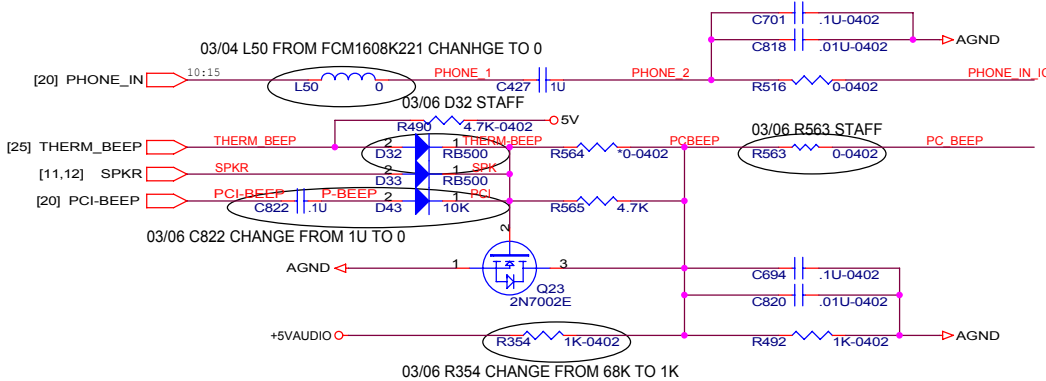
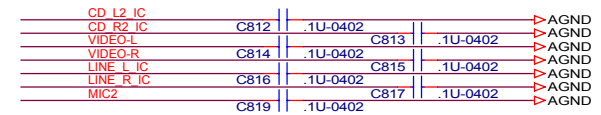
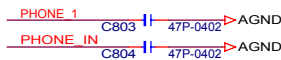
PROJECT : DT1
Quanta Computer Inc.

Size	Document Number	Rev
Custom	IEEE 1394-FW802C	E
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MIC_IN (PINK)



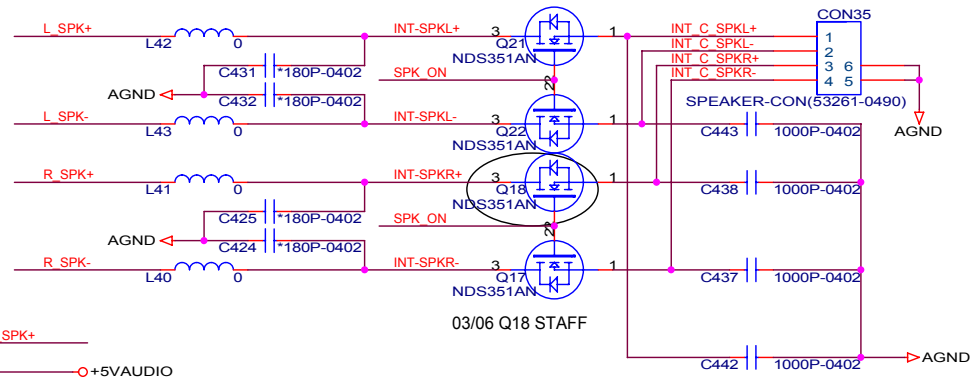
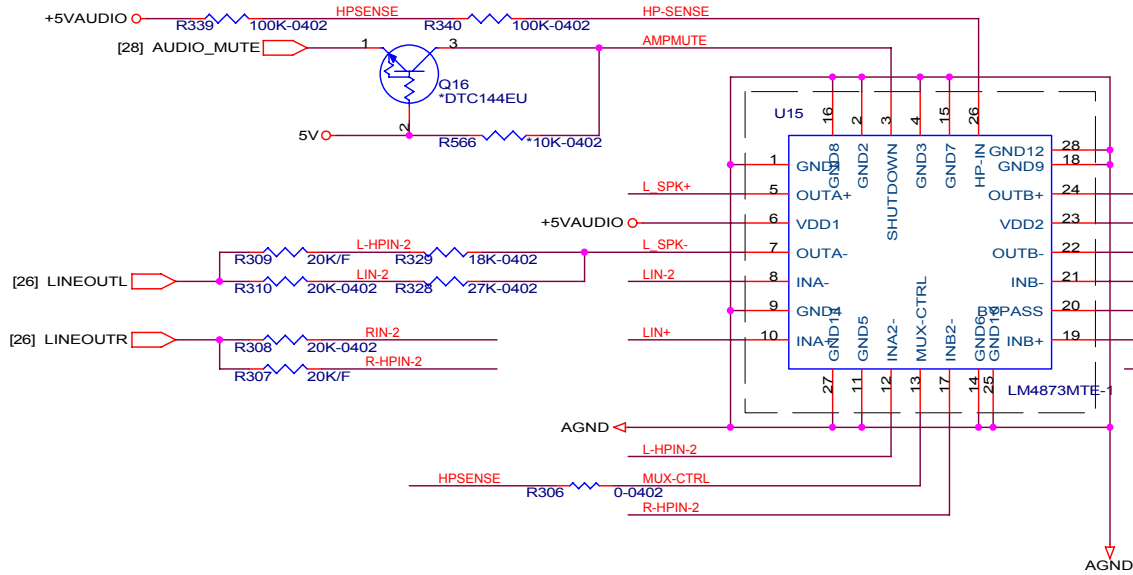
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PROJECT : DT1
Quanta Computer Inc.

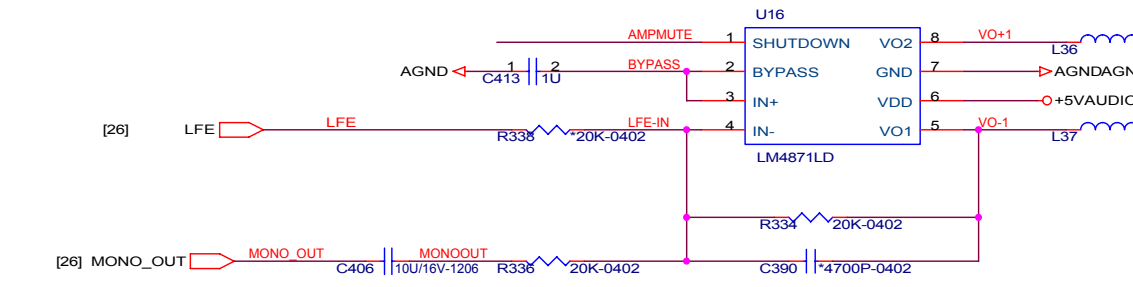
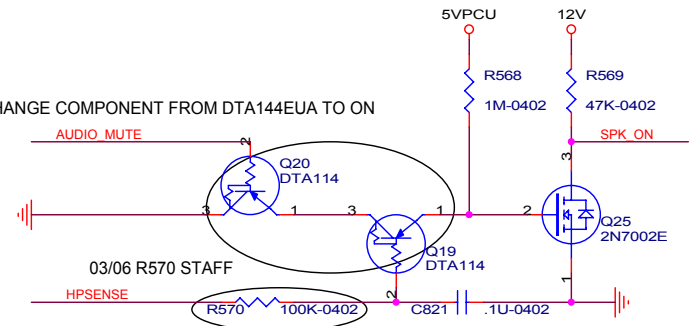
Size	Document Number	Rev
Custom	AUDIO CODEC(ALC650)	E
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Audio amplifier

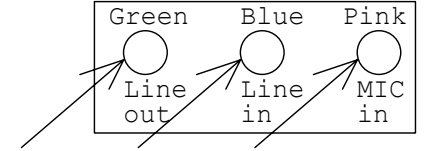
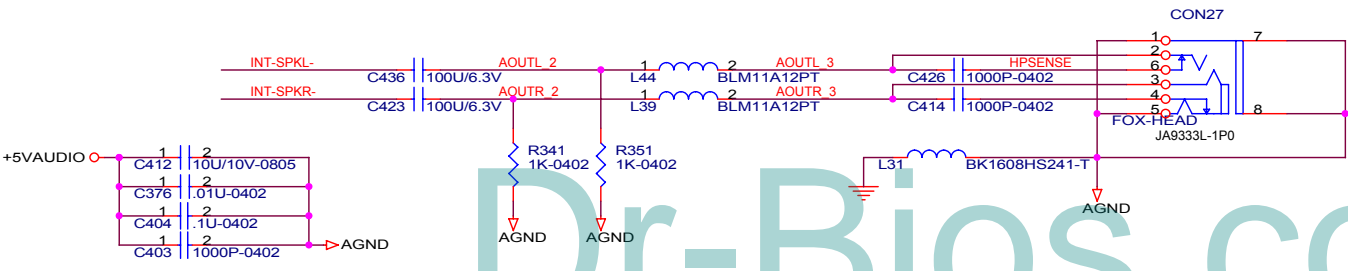
03/06 U15 CHANGE COMPONENT FROM LM4873LQ TO LM4873MTE-1



03/06 Q19, Q20 CHANGE COMPONENT FROM DTA144EUA TO ON



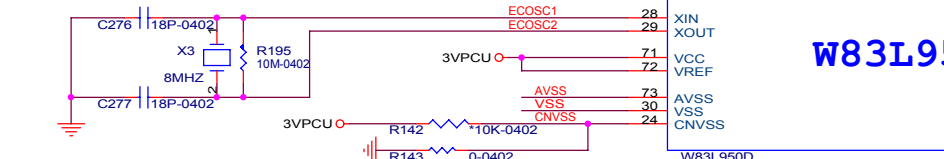
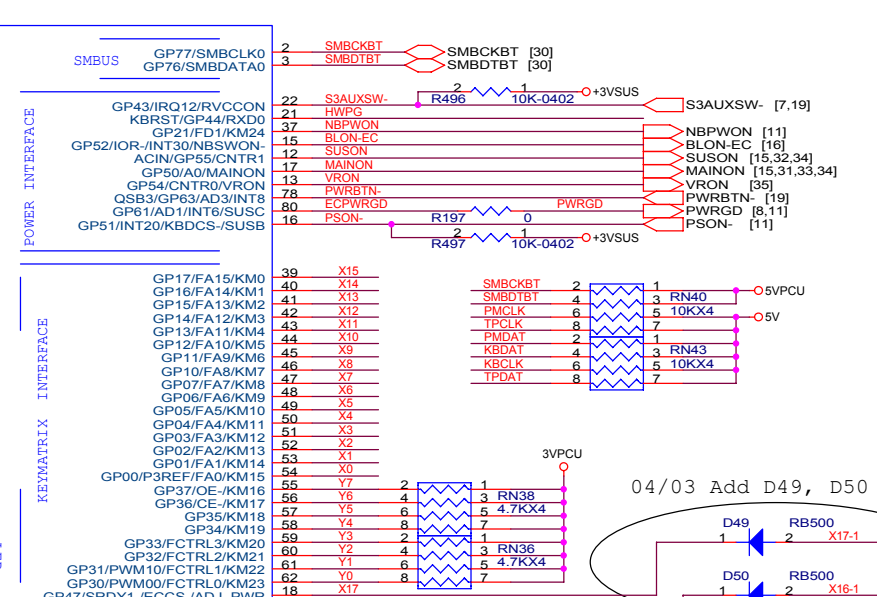
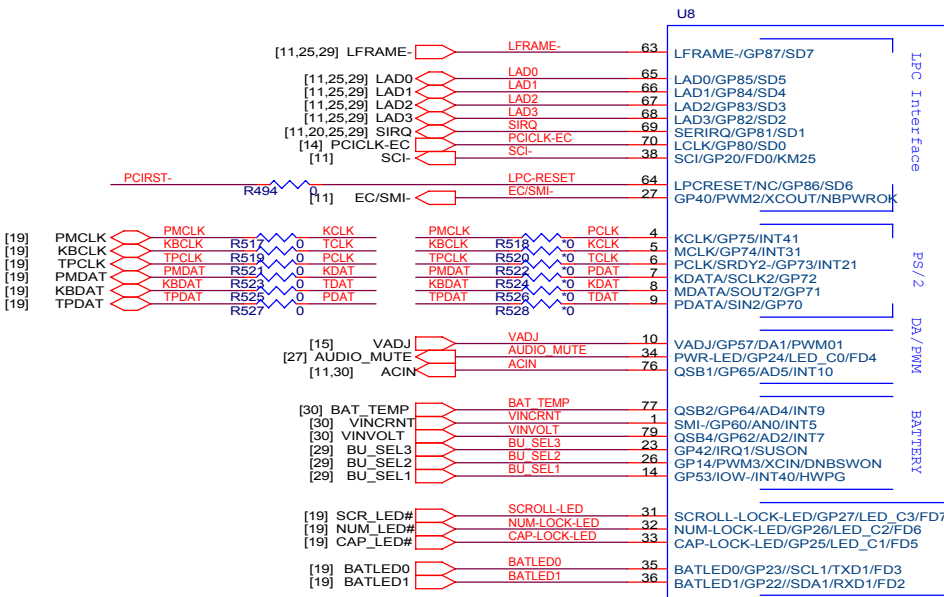
HEADPHONE OUT



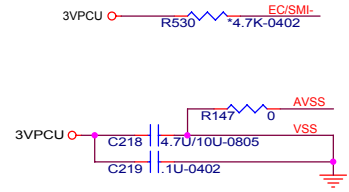
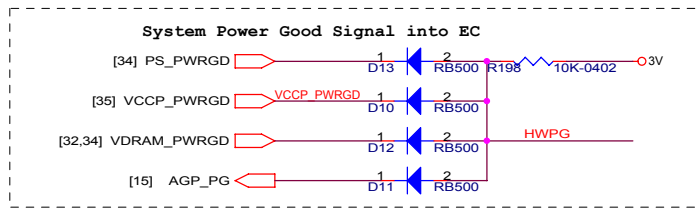
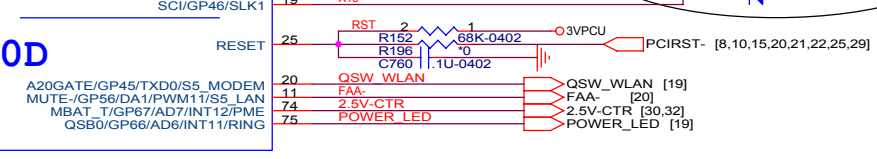
PROJECT : DT1
Quanta Computer Inc.

Size	Document Number	Rev	E
Custom	AUDIO AMPLIFIER(LM4873 & 4871)		
Date:	Friday, April 04, 2003	Sheet	27 of 35

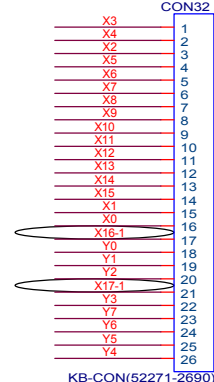
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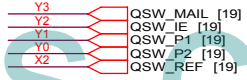
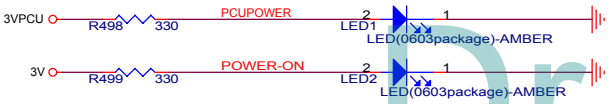
PIN 24 CNVSS PULL-HI EC PROGRAMMABLE
PIN 24 CNVSS PULL-LO EC UNPROGRAMMABLE



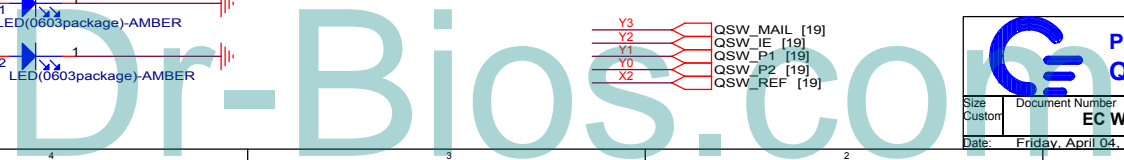
X2 & Y3: E-MAIL
X2 & Y2: EXPLORER
X2 & Y1: P1
X2 & Y0: P2

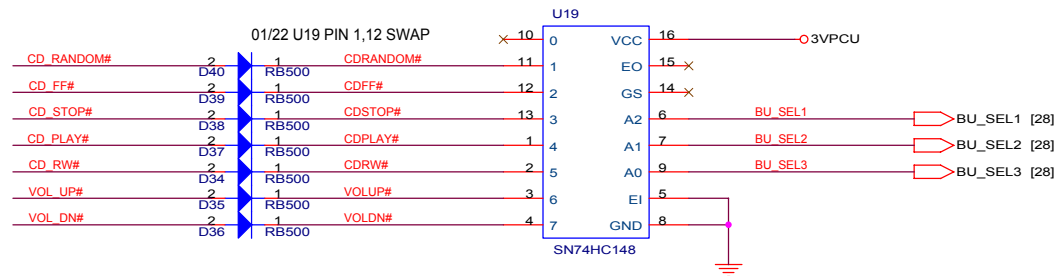
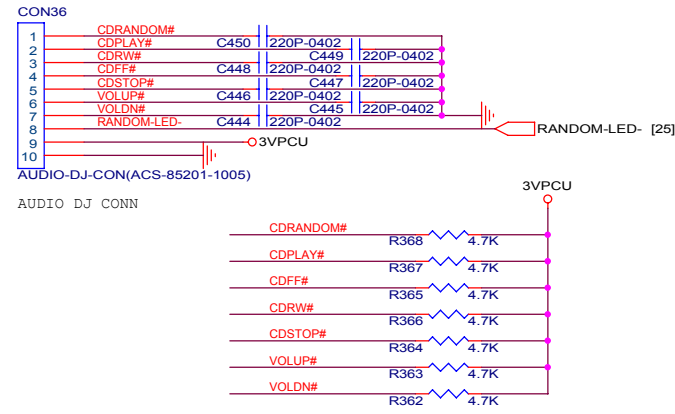
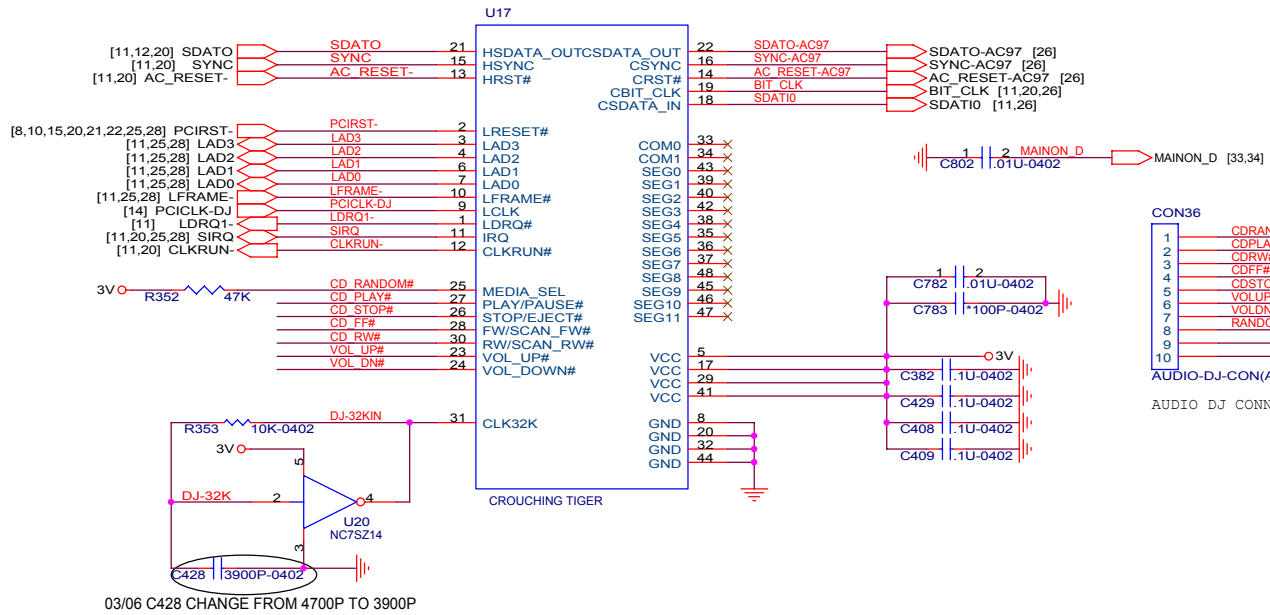


04/03 Add D49 D50 for KBC



PROJECT : DT1
Quanta Computer Inc.





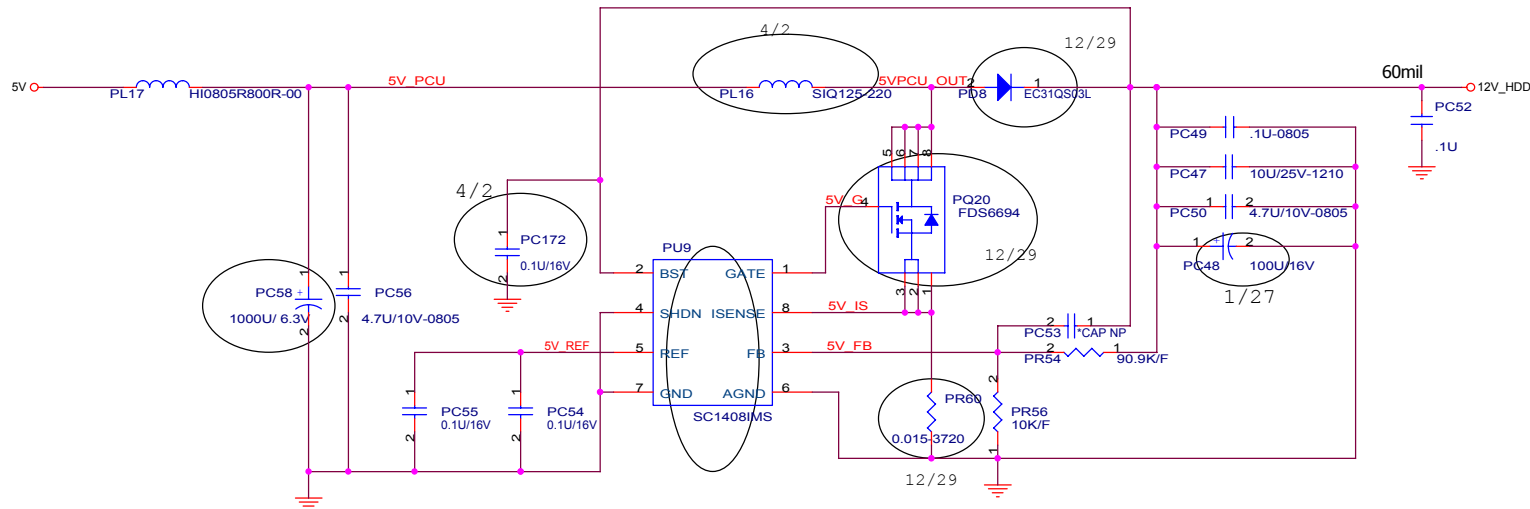
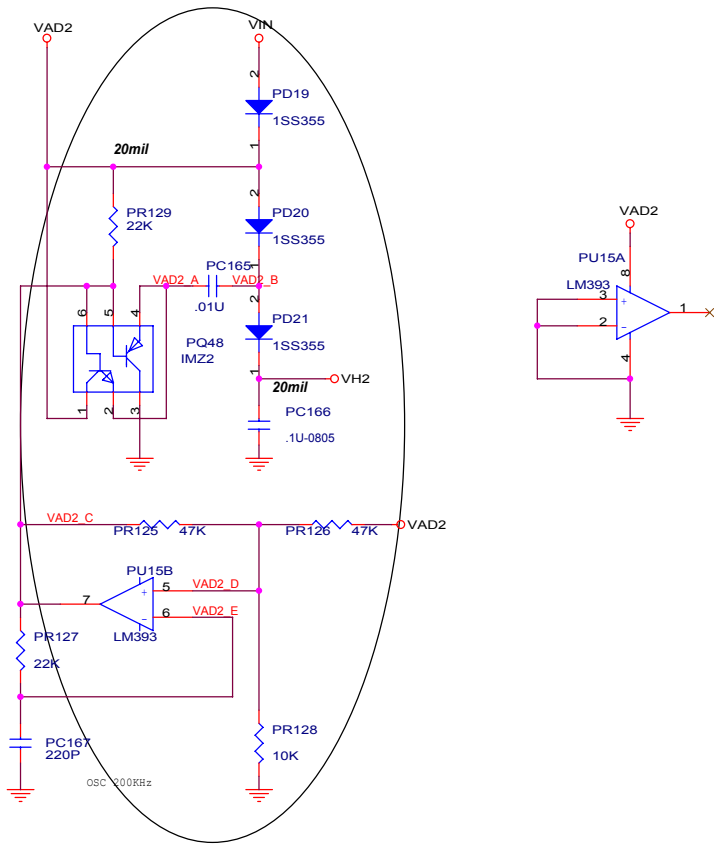
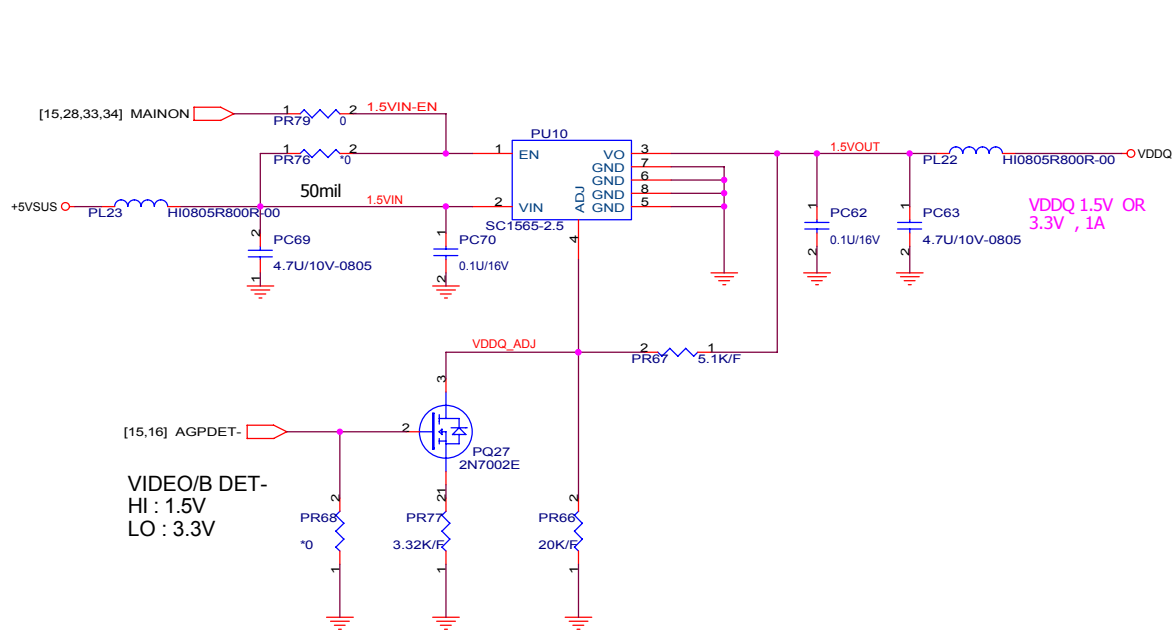
SN74HC148 TABLE

INPUTS								OUTPUTS					
EI	0	1	2	3	4	5	6	7	A2	A1	A0	GS	EO
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L	H	H	H	H	H	H	H	H	H	H	H	H	L
L	X	X	X	X	X	X	X	L	L	L	L	L	H
L	X	X	X	X	X	L	H	H	L	H	L	L	H
L	X	X	X	X	L	H	H	H	L	H	H	L	H
L	X	X	X	L	H	H	H	H	H	L	L	L	H
L	X	L	H	H	H	H	H	H	H	H	L	L	H
L	L	H	H	H	H	H	H	H	H	H	H	L	H

PROJECT : DT1
Quanta Computer Inc.

Size: Custom Document Number: OZ263 AUDIO-DJ Rev: E

Date: Friday, April 04, 2003 Sheet: 29 of 35

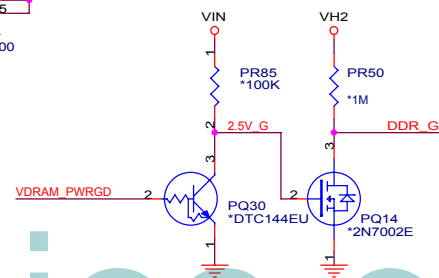
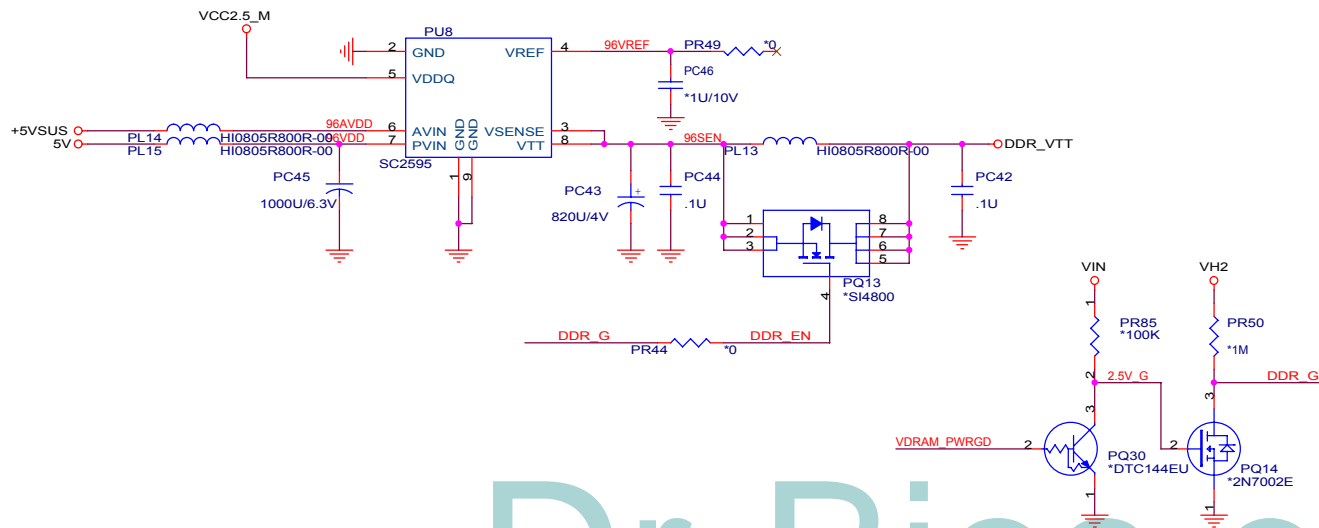
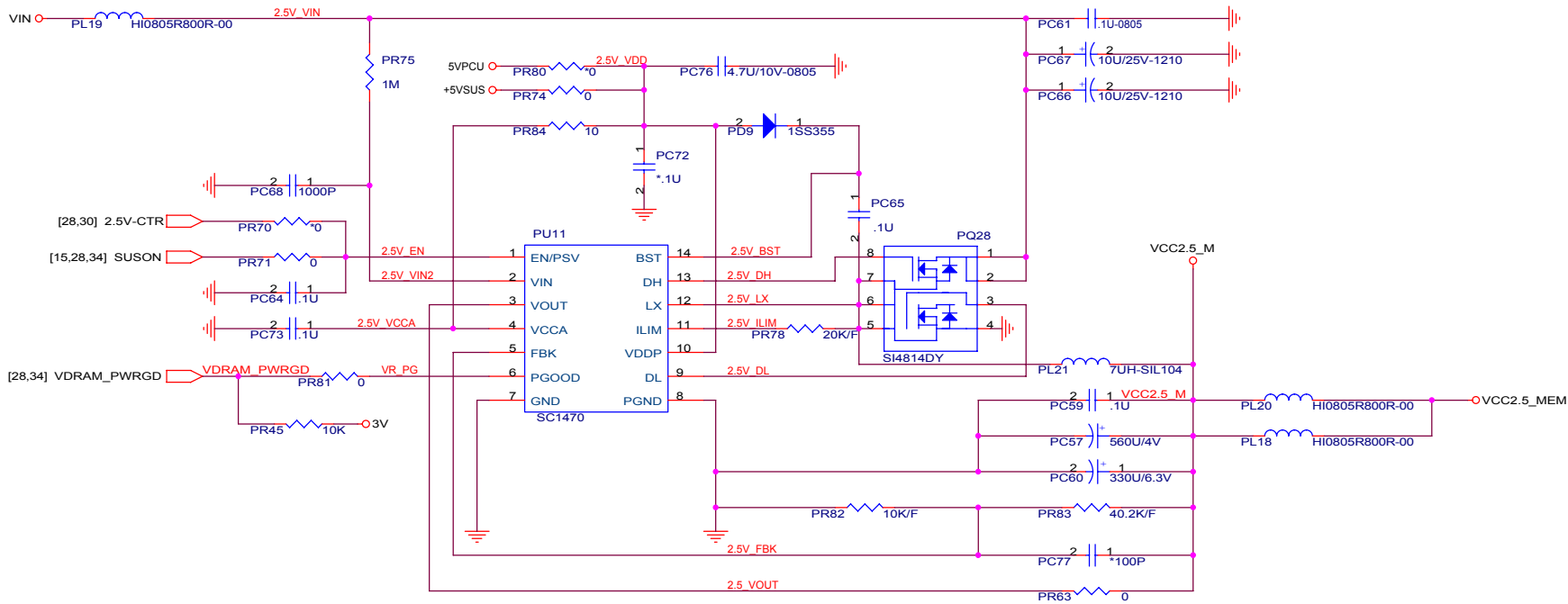


03/06 PC58 CHANGE COMPONENT TO 100U/16V

03/06 PR60 CHANGE FROM 10mohm TO 15mohm



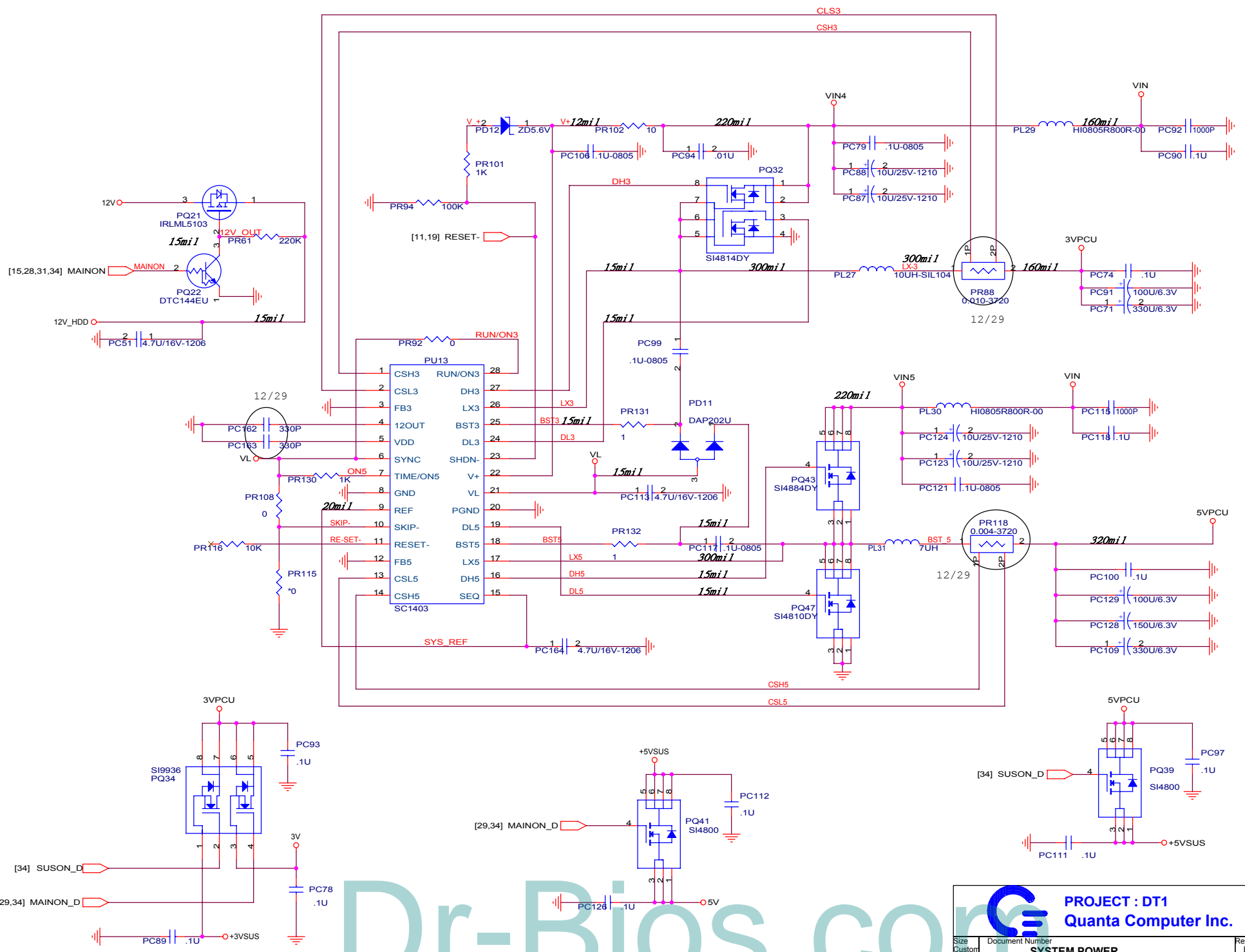
		PROJECT : DT1	
		Quanta Computer Inc.	
Size	Document Number	POWER 12V_HDD & AGP(VDDQ)	
Custom		Rev E	
Date:	Friday, April 04, 2003	Sheet	31 of 35



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Quanta Computer Inc.

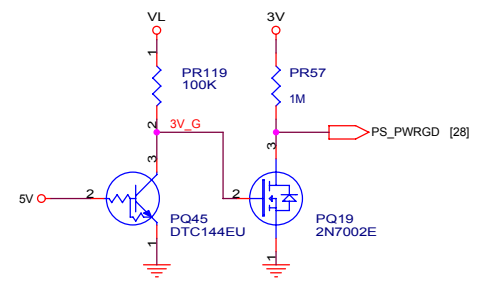
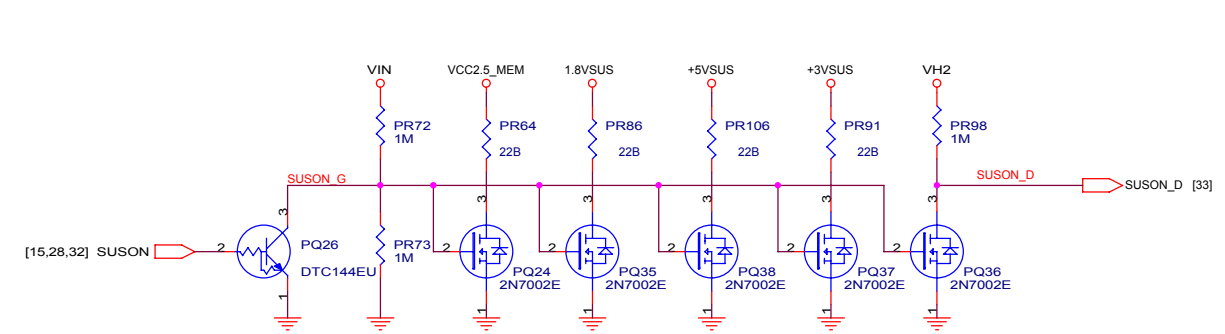
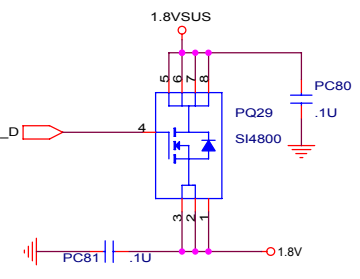
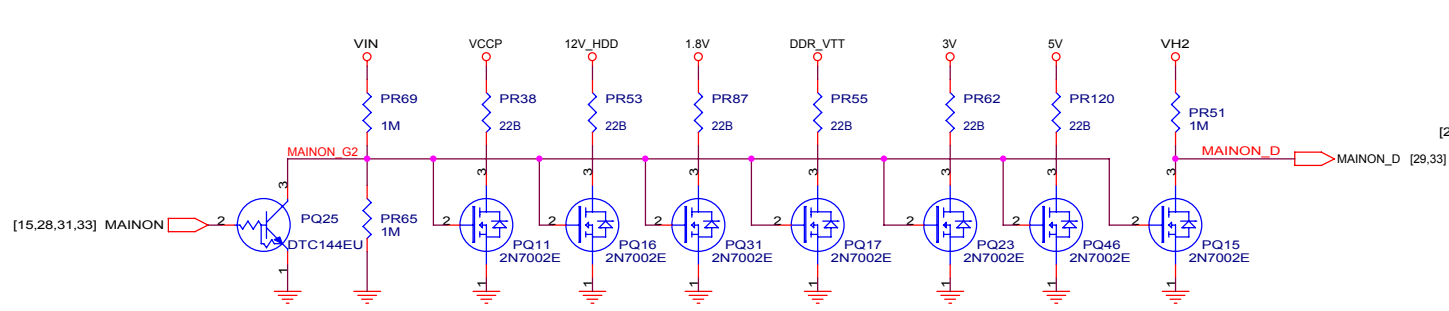
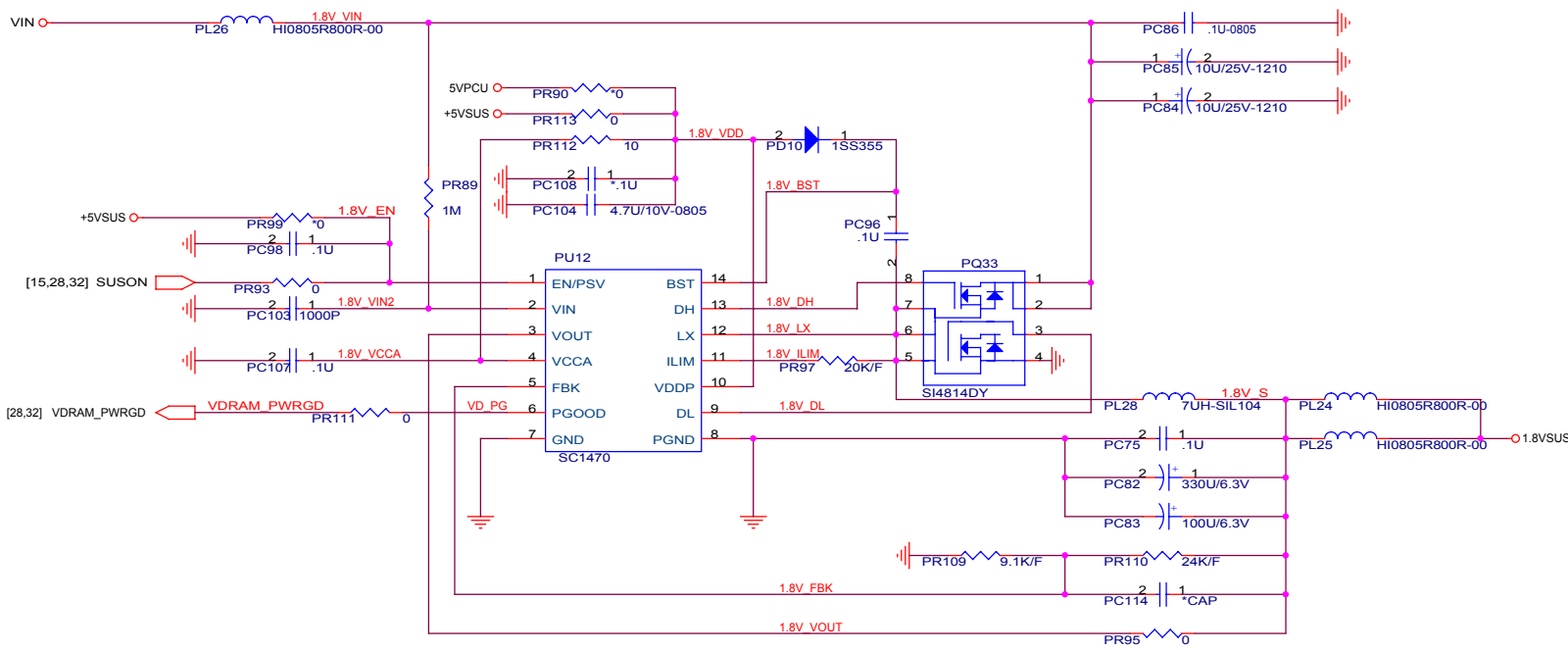
Size	Document Number	Rev
Custom	POWER - DDR & 2.5V	E
Date:	Friday, April 04, 2003	Sheet 32 of 35



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PROJECT : DT1
Quanta Computer Inc.

Size	Document Number	Rev
Custom	SYSTEM POWER	E
Date: Friday, April 04, 2003	Sheet 33 of 35	

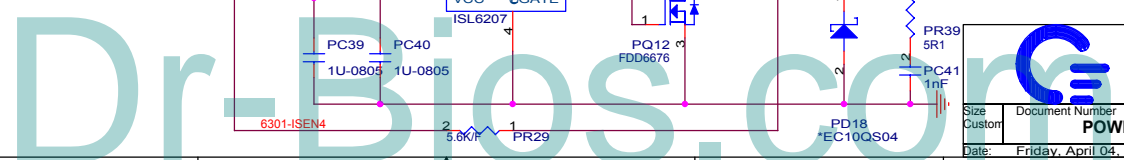
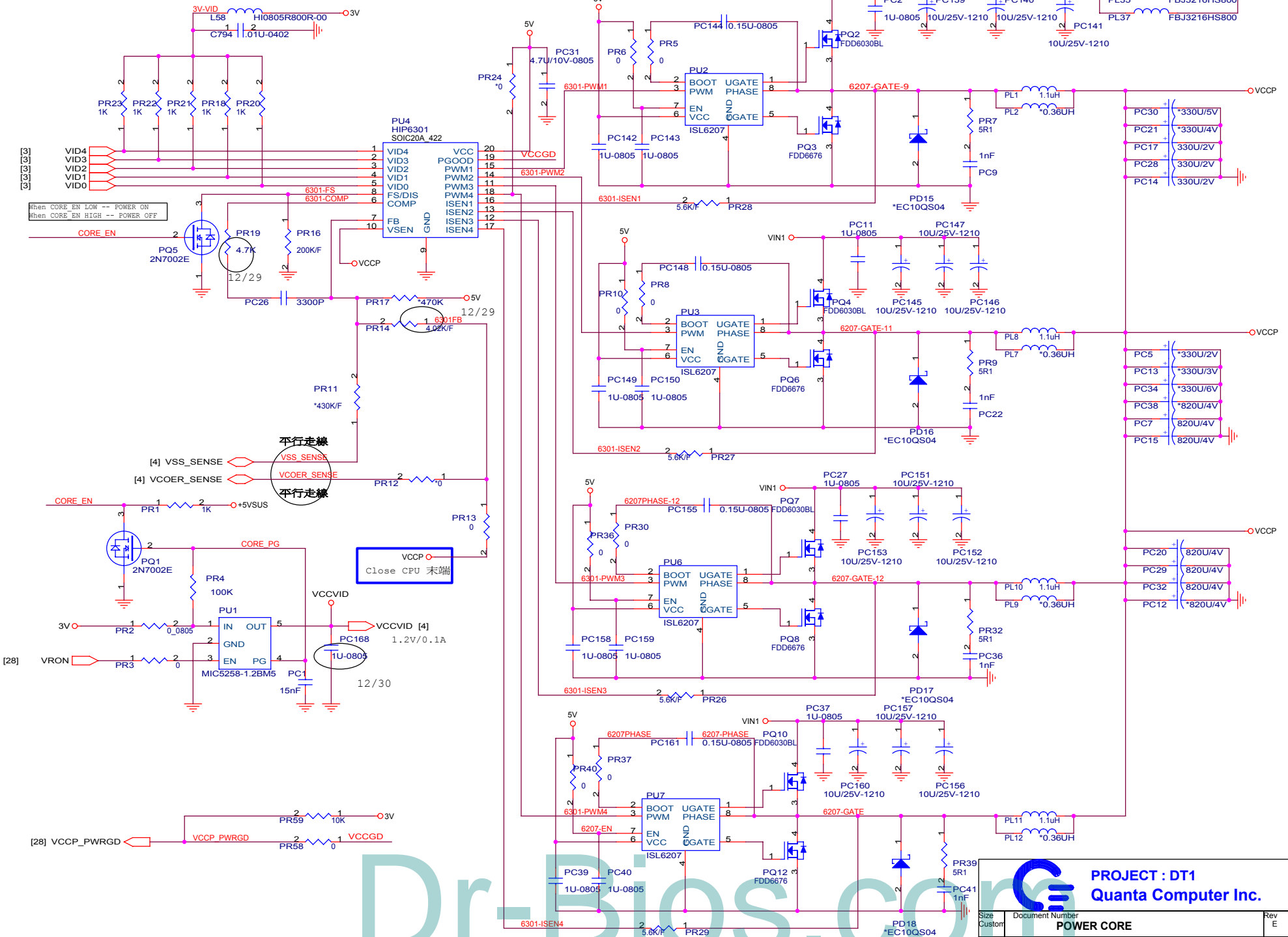


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PROJECT : DT1
Quanta Computer Inc.

Size	Document Number	Rev
Custom	POWER DISCHARGE	E
Date:	Friday, April 04, 2003	Sheet 34 of 35

HIP6301 FOR INTEL Brookdale POWER CKT-DDR



PROJECT : DT1
Quanta Computer Inc.

Size	Document Number	Rev
Custom	POWER CORE	E
Date:	Friday, April 04, 2003	Sheet 35 of 35