

# ZL8

**CPU CORE**  
SENTECH  
SC4511TSTR  
Page: 23

**SYSTEM 3V/5V**  
MAXIM  
MAX1999  
Page: 24

+3VPCU  
+3V\_S5/+3VSUS  
+3V  
+5VPCU  
+5VSUS  
+5V  
+15V

+1.8VSUS  
+1.8V ON  
NCP5214

+0.9VSUS  
+0.9V ON  
NCP5214

+1.5V SENTECH  
SC1470

+1.5V\_S5  
SI9183-AD

+2.5V SENTECH  
SC1565

+1.05V SENTECH  
SC4215  
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**BATTERY CHARGER**  
MAXIM  
MAX8724  
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(Option)

**MINI-PCIE slot**  
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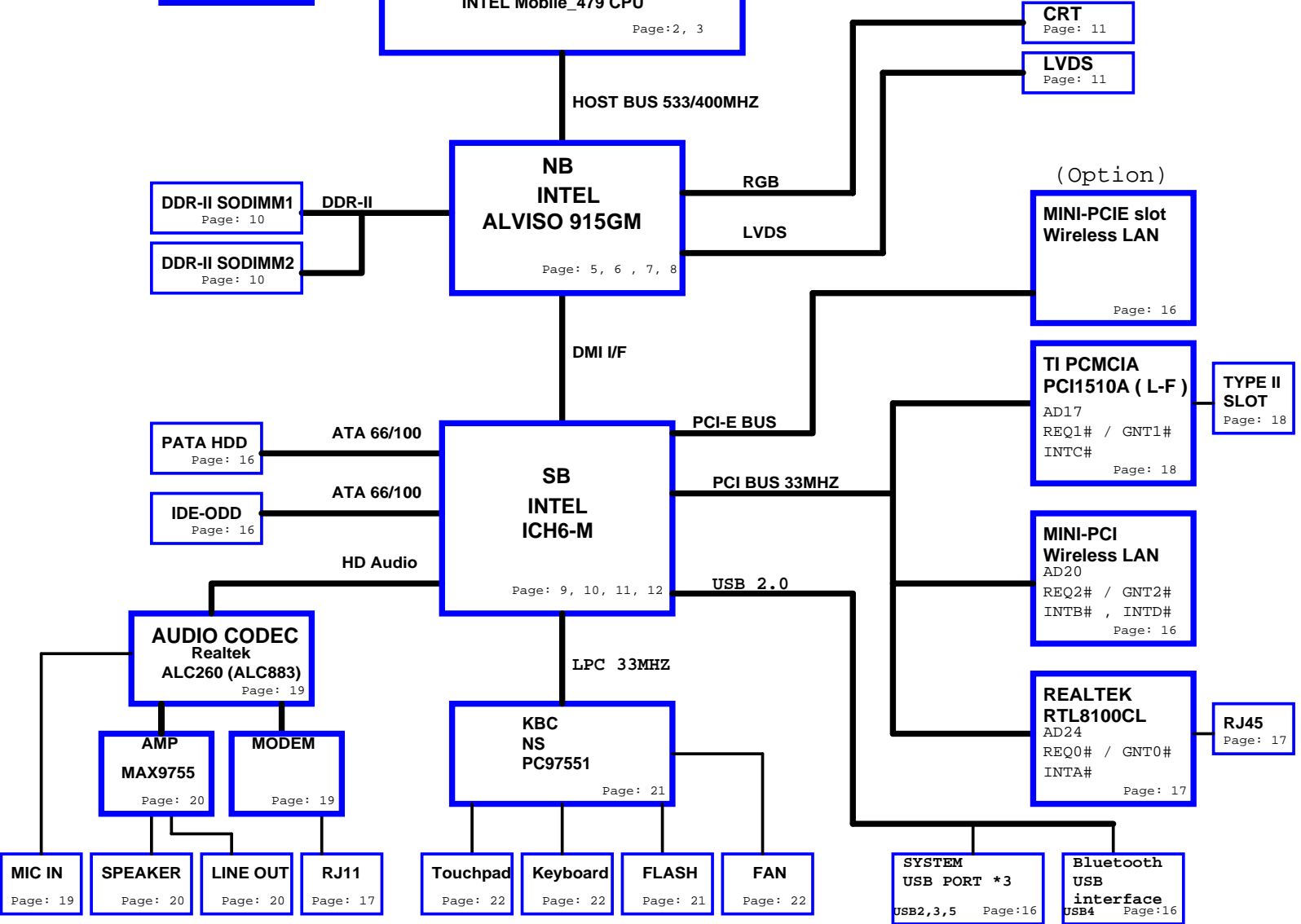
**TI PCMCIA**  
PCI1510A (L-F)  
AD17  
REQ1# / GNT1#  
INTC#  
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**TYPE II SLOT**  
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**MINI-PCI**  
Wireless LAN  
AD20  
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**REALTEK**  
RTL8100CL  
AD24  
REQ0# / GNT0#  
INTA#  
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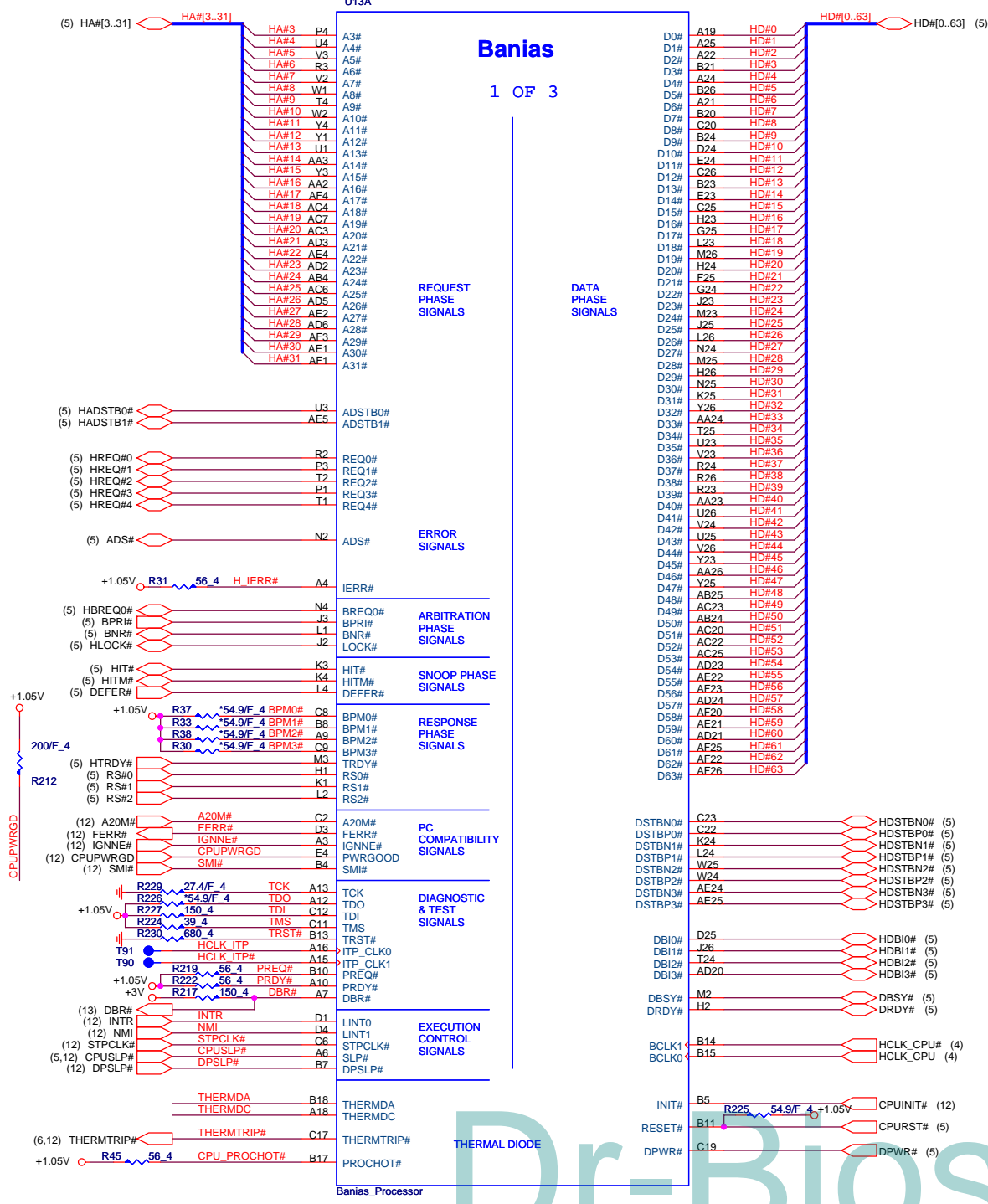
**RJ45**  
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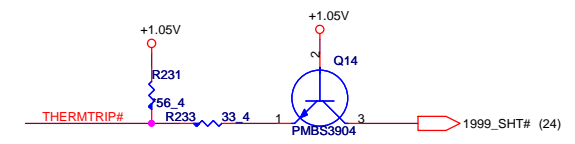
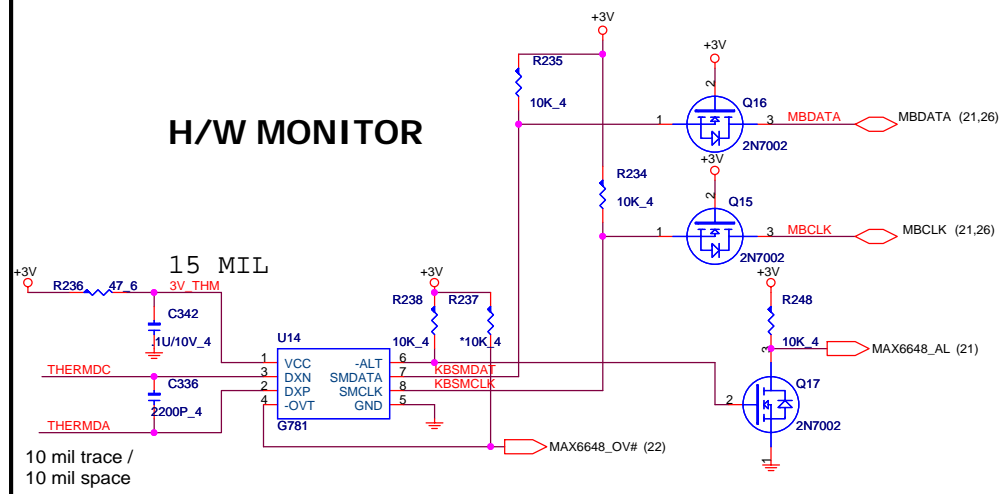
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Quanta Computer Inc.

Size	Document Number	Rev
	<b>BLOCK DIAGRAM</b>	1A
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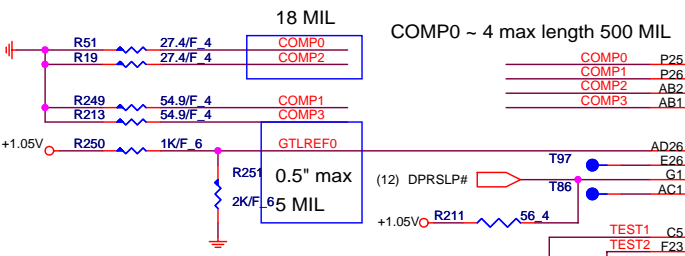
### H/W MONITOR





**PROJECT : ZL8**  
**Quanta Computer Inc.**

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	<b>CPU (HOST BUS)-1</b>	1A	
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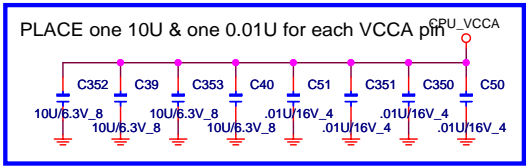
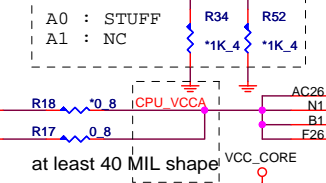


COMP0 ~ 4 max length 500 MIL

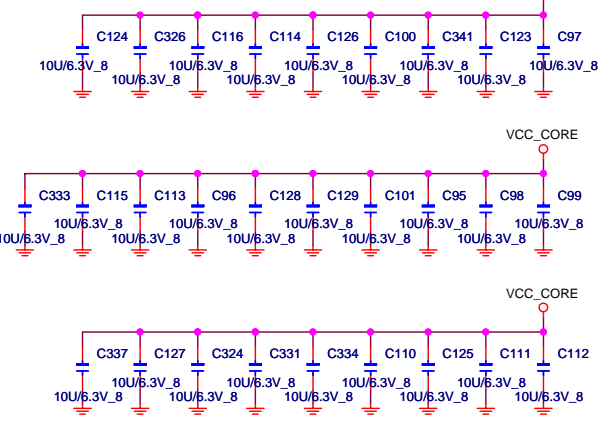
COMP0 P25  
COMP1 P26  
COMP2 AB2  
COMP3 AB1

**Banias**  
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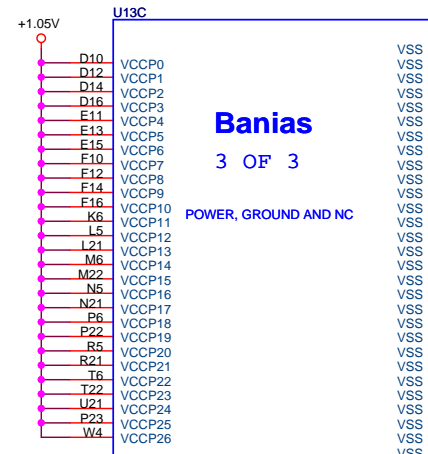
POWER, GROUND, RESERVED SIGNALS



**10U/6.3V/X5R(CC0805) \*30**

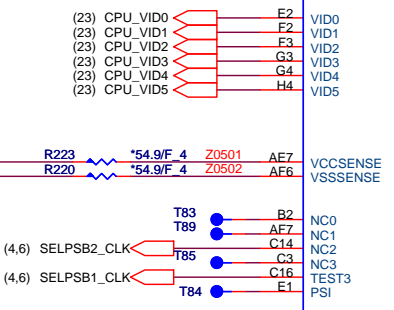


VSS	A2	VSS	W23
VSS	A5	VSS	W26
VSS	A8	VSS	Y2
VSS	A11	VSS	Y5
VSS	A14	VSS	Y21
VSS	A17	VSS	Y24
VSS	A20	VSS	AA1
VSS	A23	VSS	AA4
VSS	A26	VSS	AA6
VSS	B3	VSS	AA8
VSS	B6	VSS	AA10
VSS	B9	VSS	AA12
VSS	B12	VSS	AA14
VSS	B16	VSS	AA16
VSS	B19	VSS	AA18
VSS	B22	VSS	AA20
VSS	B25	VSS	AA22
VSS	C1	VSS	AA25
VSS	C4	VSS	AB3
VSS	C7	VSS	AB5
VSS	C10	VSS	AB7
VSS	C13	VSS	AB9
VSS	C16	VSS	AB11
VSS	C18	VSS	AB13
VSS	C21	VSS	AB15
VSS	C24	VSS	AB17
VSS	D2	VSS	AB19
VSS	D5	VSS	AB21
VSS	D7	VSS	AB23
VSS	D9	VSS	AB26
VSS	D11	VSS	AC2
VSS	D13	VSS	AC5
VSS	D15	VSS	AC8
VSS	D17	VSS	AC10
VSS	D19	VSS	AC12
VSS	D21	VSS	AC14
VSS	D23	VSS	AC16
VSS	D26	VSS	AC18
VSS	E3	VSS	AC21
VSS	E6	VSS	AC24
VSS	E8	VSS	AD1
VSS	E10	VSS	AD4
VSS	E12	VSS	AD7
VSS	E14	VSS	AD9
VSS	E16	VSS	AD11
VSS	E18	VSS	AD13
VSS	E20	VSS	AD15
VSS	E22	VSS	AD17
VSS	E25	VSS	AD19
VSS	F1	VSS	AD22
VSS	F4	VSS	AD25
VSS	F5	VSS	AE3
VSS	F7	VSS	AE6
VSS	F9	VSS	AE8
VSS	F11	VSS	AE10
VSS	F13	VSS	AE12
VSS	F15	VSS	AE14
VSS	F17	VSS	AE16
VSS	F19	VSS	AE18
VSS	F21	VSS	AE20
VSS	F24	VSS	AE23
VSS	G2	VSS	AE26
VSS	G6	VSS	AF2
VSS	G22	VSS	AF5
VSS	G23	VSS	AF9
VSS	G26	VSS	AF11
VSS	H3	VSS	AF13
VSS	H5	VSS	AF15
VSS	H21	VSS	AF17
VSS	H25	VSS	AF19
VSS	J1	VSS	AF21
VSS	J4	VSS	AF24
VSS	J6	VSS	
VSS	J22	VSS	
VSS	J24	VSS	
VSS	K2	VSS	
VSS	K5	VSS	
VSS	K21	VSS	
VSS	K23	VSS	
VSS	K26	VSS	
VSS	L3	VSS	
VSS	L6	VSS	
VSS	L22	VSS	
VSS	L25	VSS	
VSS	M1	VSS	
VSS	M4	VSS	
VSS	M5	VSS	
VSS	M21	VSS	
VSS	M24	VSS	
VSS	N3	VSS	
VSS	N6	VSS	
VSS	N22	VSS	
VSS	N23	VSS	
VSS	N26	VSS	
VSS	P2	VSS	
VSS	P5	VSS	
VSS	P21	VSS	
VSS	P24	VSS	
VSS	R1	VSS	
VSS	R4	VSS	

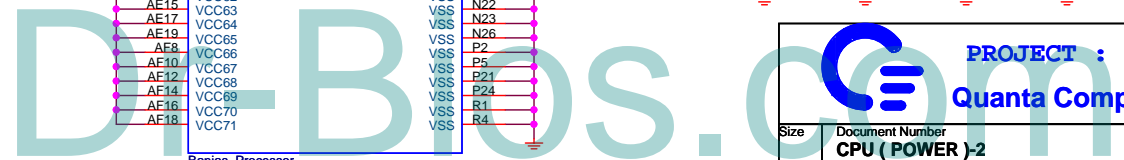
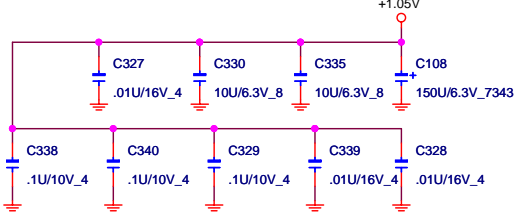
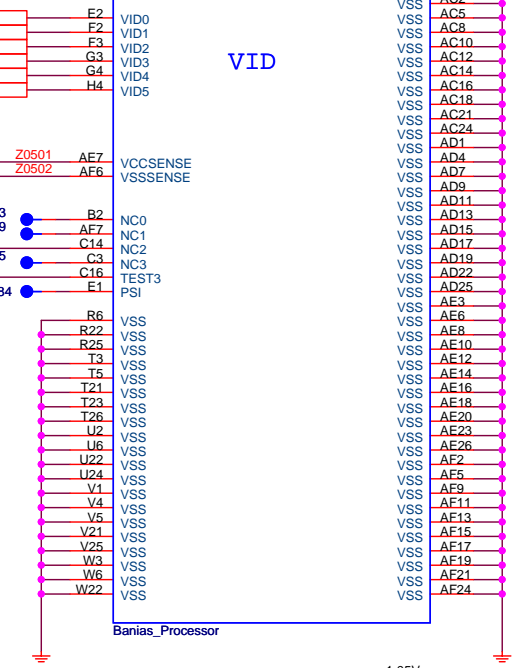


**Banias**  
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POWER, GROUND AND NC

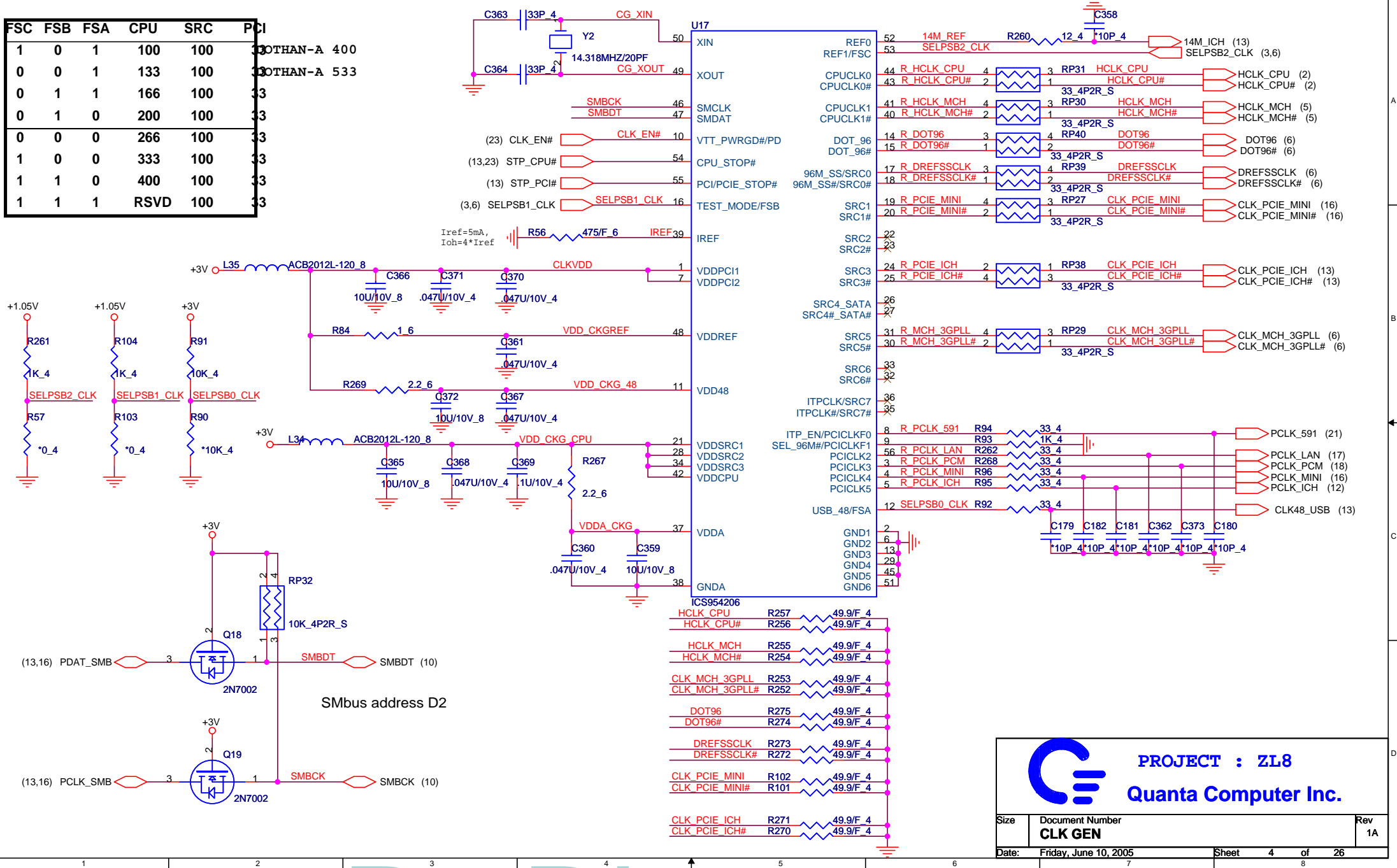


**VID**



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FSC	FSB	FSA	CPU	SRC	PCI
1	0	1	100	100	DOOTHAN-A 400
0	0	1	133	100	DOOTHAN-A 533
0	1	1	166	100	33
0	1	0	200	100	33
0	0	0	266	100	33
1	0	0	333	100	33
1	1	0	400	100	33
1	1	1	RSVD	100	33

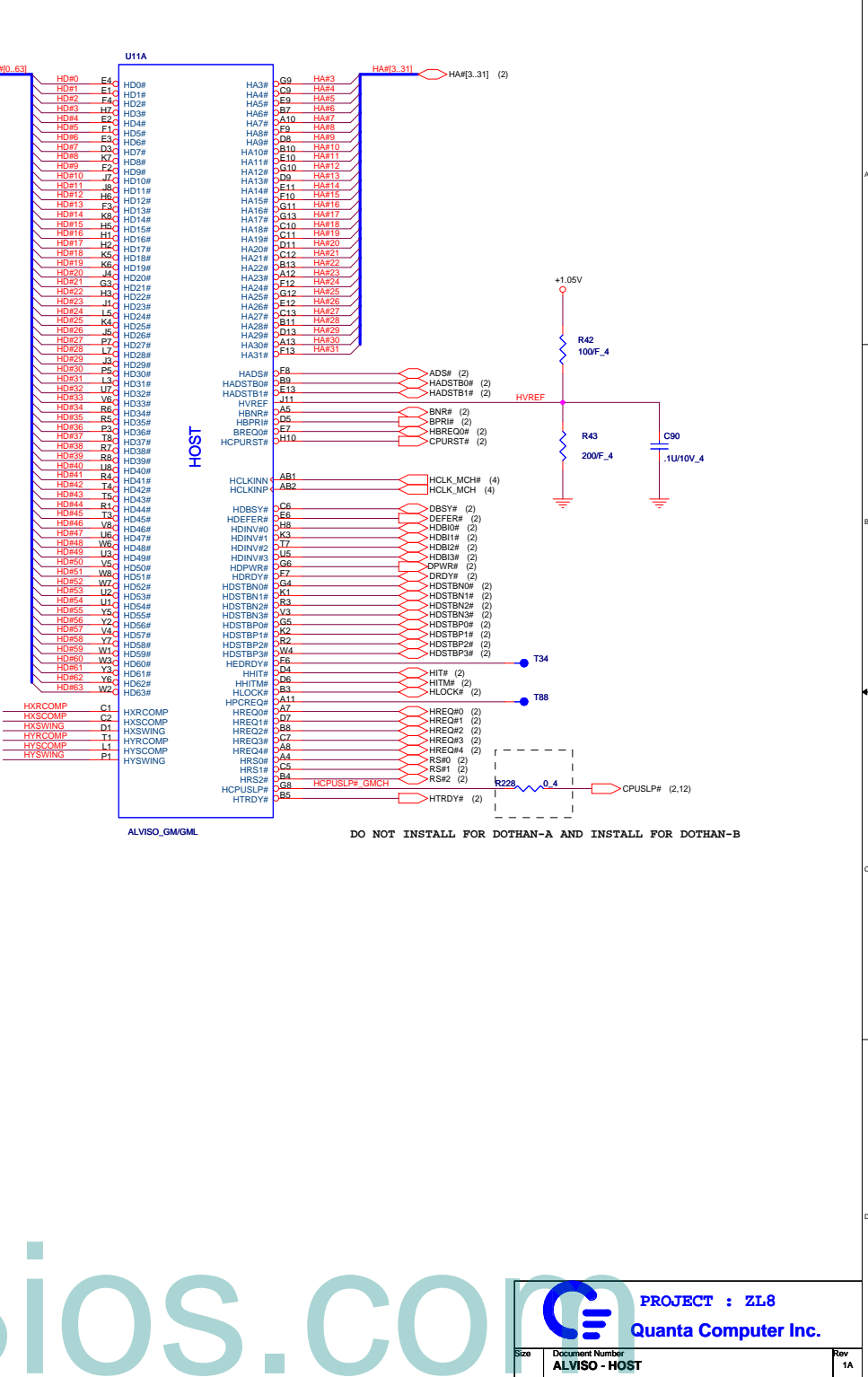


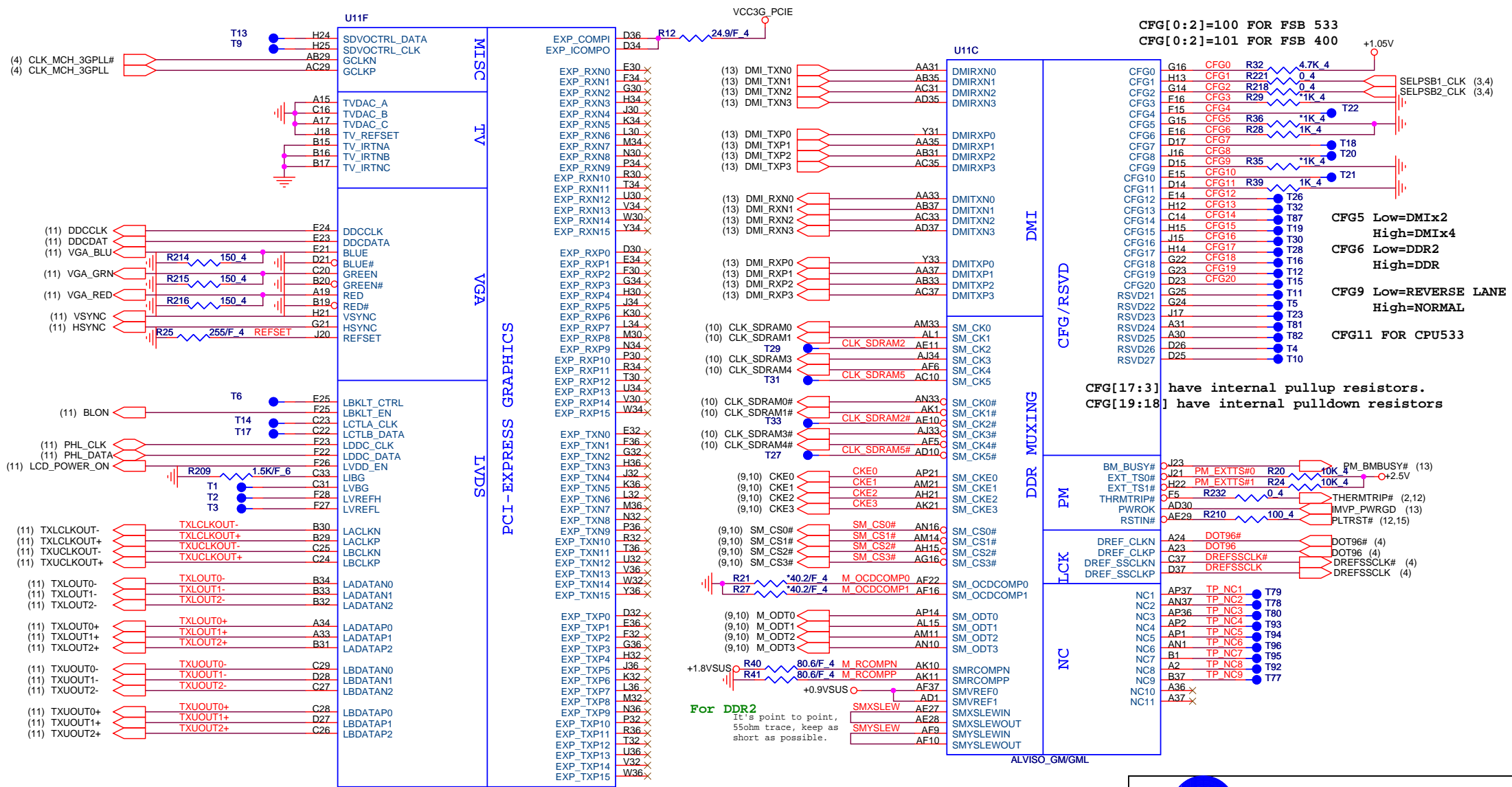
**PROJECT : ZL8**  
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Size	Document Number	Rev
	<b>CLK GEN</b>	1A
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U11E		
AF23	VSS136	VSS0
AP23	VSS137	VSS1
AL22	VSS138	VSS2
AH22	VSS139	VSS3
J22	VSS140	VSS4
VSS141	VSS5	M37
D22	VSS142	K37
AN21	VSS143	H37
AF21	VSS144	VSS8
F21	VSS145	VSS9
C21	VSS146	AN36
AK20	VSS147	AL36
VSS148	VSS12	AF36
VSS149	VSS13	VSS10
F20	VSS150	VSS14
Q20	VSS151	VSS15
F20	VSS152	VSS16
D20	VSS153	VSS17
A20	VSS154	VSS18
AN19	VSS155	VSS19
AG19	VSS156	VSS20
W19	VSS157	VSS21
T19	VSS158	VSS22
H19	VSS159	VSS23
VSS160	VSS24	T35
C19	VSS161	VSS25
AL18	VSS162	VSS26
U18	VSS163	VSS27
B18	VSS164	VSS28
A18	VSS165	VSS29
AN17	VSS166	VSS30
AF17	VSS167	VSS31
C17	VSS168	VSS32
VSS169	VSS33	H35
AL16	VSS170	VSS34
K16	VSS171	VSS35
H16	VSS172	VSS36
D16	VSS173	VSS37
A16	VSS174	VSS38
VSS175	VSS39	AN34
C15	VSS176	AD34
AN14	VSS177	AC34
AL14	VSS178	AA34
AG14	VSS179	C34
J14	VSS180	VSS44
F14	VSS181	VSS45
B14	VSS182	VSS46
A14	VSS183	VSS47
J12	VSS184	VSS48
D12	VSS185	VSS49
B12	VSS186	VSS50
AL11	VSS187	VSS51
AL11	VSS188	VSS52
A11	VSS189	VSS53
AG11	VSS190	VSS54
AF11	VSS191	VSS55
AA11	VSS192	VSS56
Y11	VSS193	VSS57
F11	VSS194	VSS58
AL10	VSS195	VSS59
L10	VSS196	VSS60
VSS200	VSS61	VSS62
D10	VSS201	VSS63
AH9	VSS202	VSS64
AC9	VSS203	VSS65
AE9	VSS204	VSS66
AA9	VSS205	VSS67
V9	VSS206	VSS68
TV9	VSS207	VSS69
K9	VSS208	VSS70
H9	VSS209	VSS71
AL9	VSS210	VSS72
VSS211	VSS73	AG31
AL8	VSS212	AD31
PK8	VSS213	VSS75
L8	VSS214	VSS76
E8	VSS215	VSS77
C8	VSS216	VSS78
AN7	VSS217	VSS79
AK7	VSS218	VSS80
AG7	VSS219	VSS81
AV7	VSS220	VSS82
V7	VSS221	VSS83
G7	VSS222	VSS84
AE6	VSS223	VSS85
AC6	VSS224	VSS86
AE6	VSS225	VSS87
AC6	VSS226	VSS88
TE6	VSS227	VSS89
PK6	VSS228	VSS90
L6	VSS229	VSS91
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BE6	VSS231	VSS93
AL6	VSS232	VSS94
V5	VSS233	VSS95
E5	VSS234	VSS96
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AF4	VSS236	VSS98
Y4	VSS237	VSS99
U4	VSS238	VSS100
P4	VSS239	VSS101
H4	VSS240	VSS102
C4	VSS241	VSS103
A3	VSS242	VSS104
AC3	VSS243	VSS105
AB3	VSS244	VSS106
AC3	VSS245	VSS107
AE3	VSS246	VSS108
AD3	VSS247	VSS109
V2	VSS248	VSS110
T2	VSS249	VSS111
L2	VSS250	VSS112
B2	VSS251	VSS113
G2	VSS252	VSS114
E2	VSS253	VSS115
AN2	VSS254	VSS116
AE2	VSS255	VSS117
V2	VSS256	VSS118
T2	VSS257	VSS119
L2	VSS258	VSS120
B2	VSS259	VSS121
G2	VSS260	VSS122
E2	VSS261	VSS123
AN2	VSS262	VSS124
AL2	VSS263	VSS125
V2	VSS264	VSS126
E2	VSS265	VSS127
AN2	VSS266	VSS128
AL2	VSS267	VSS129
G2	VSS268	VSS130
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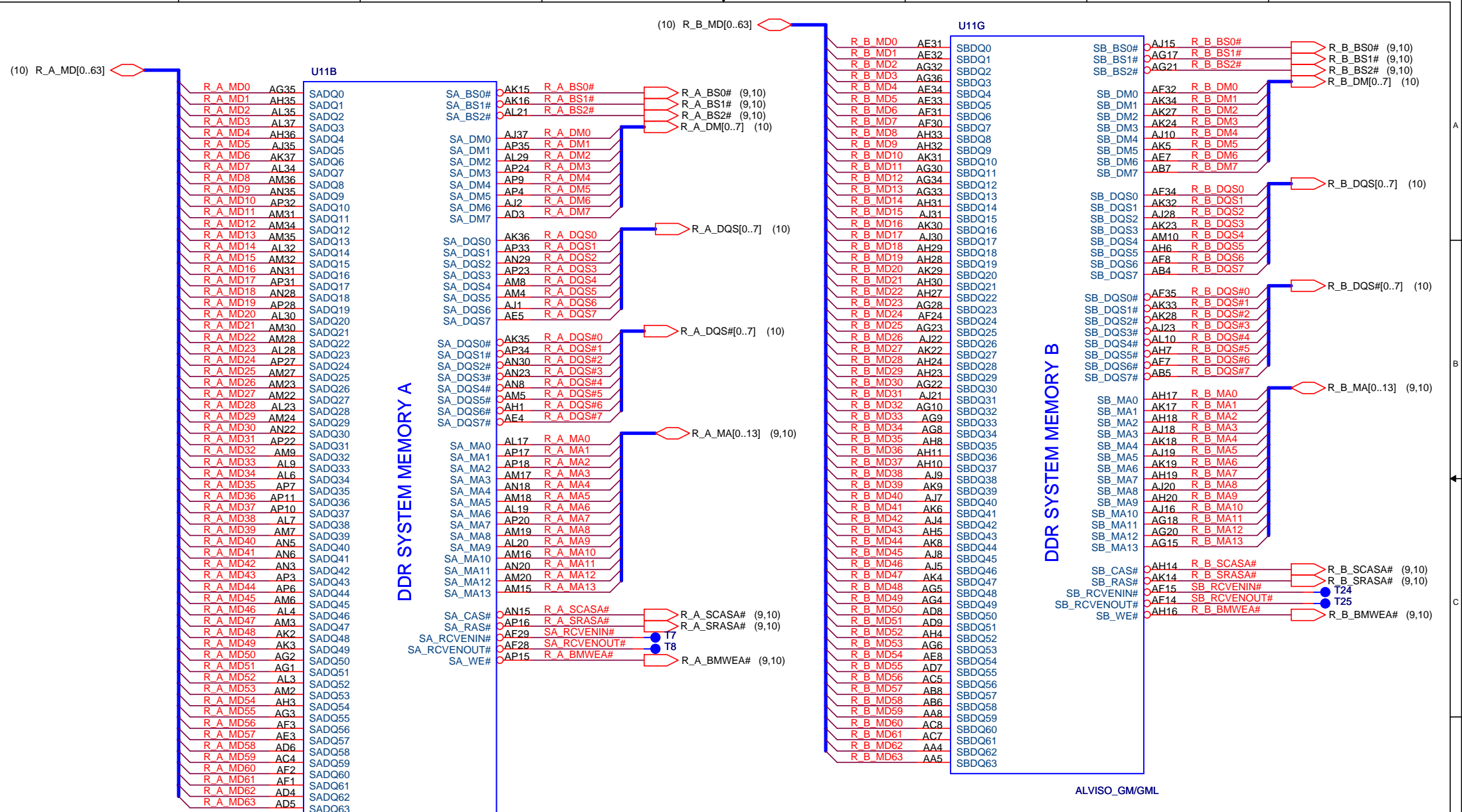
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VSS8	VSS7	
VSS9	VSS8	
AN36	VSS9	
AL36	VSS10	
AF36	VSS11	
VSS12	VSS12	
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VSS131	VSS131	
VSS132	VSS132	
VSS133	VSS133	
VSS134	VSS134	
VSS135	VSS135	





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**Quanta Computer Inc.**


Size	Document Number	Rev
	<b>ALVISO - DMI / VGA</b>	1A
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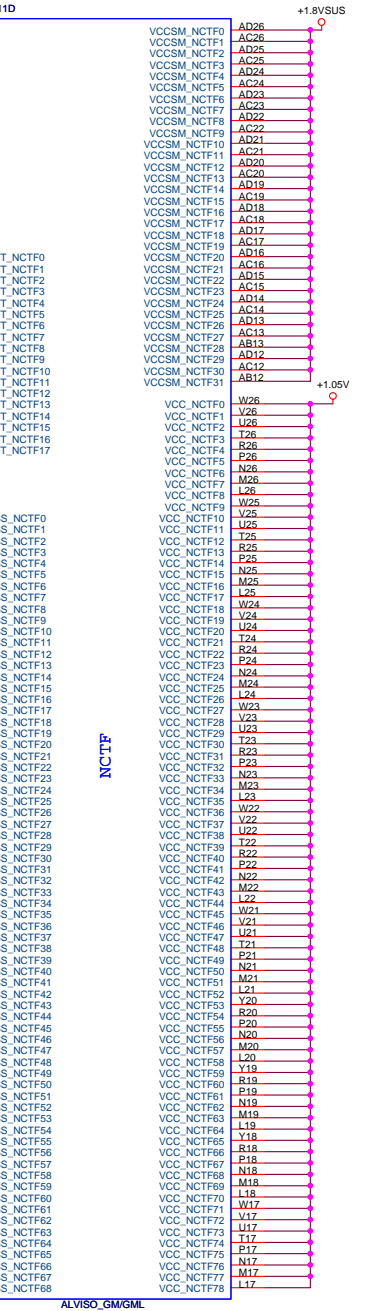
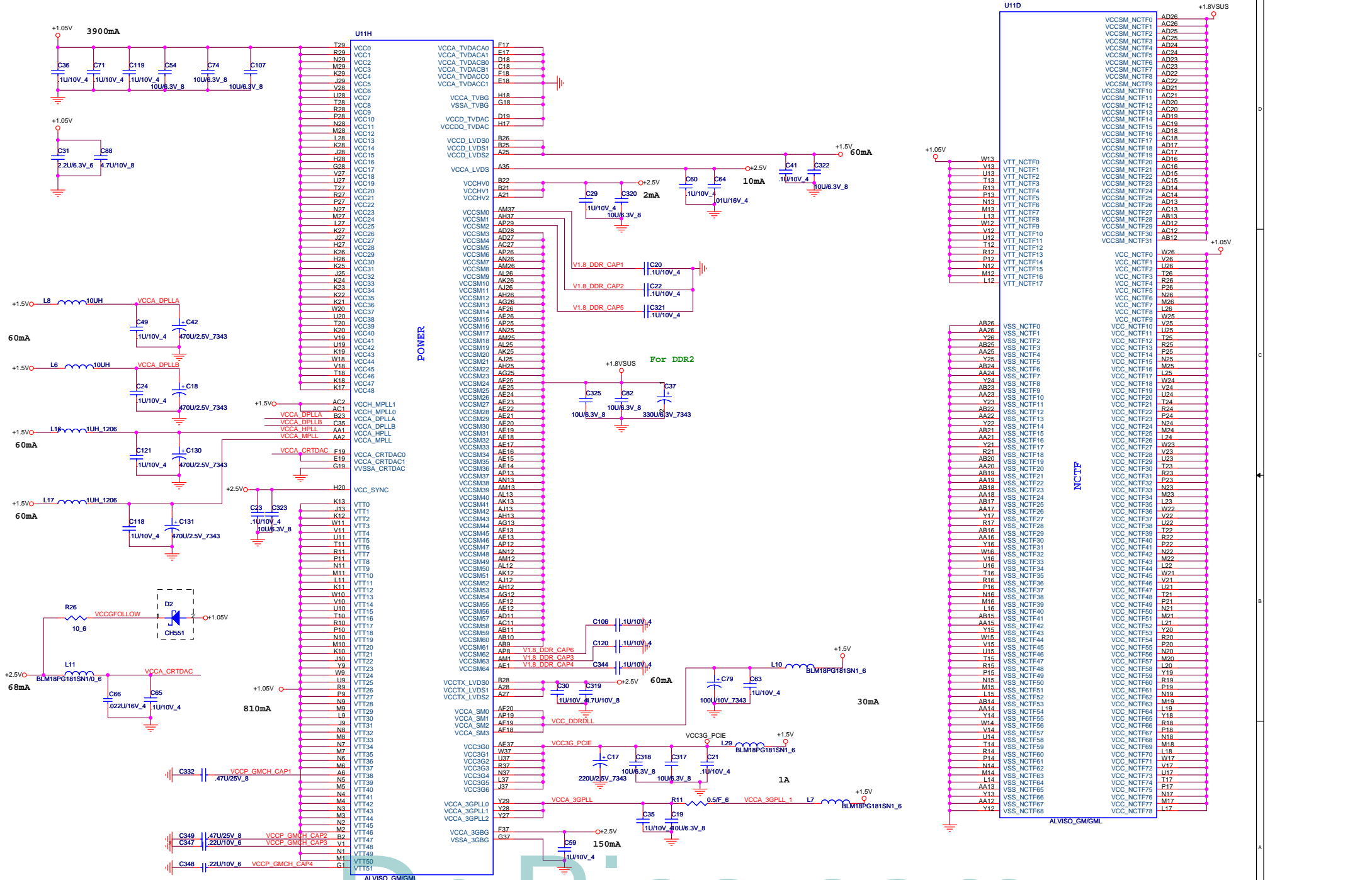
DDR SYSTEM MEMORY A

DDR SYSTEM MEMORY B

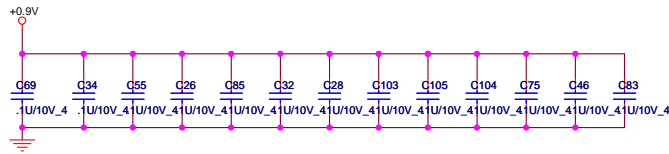
ALVISO\_GM/GML

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		Quanta Computer Inc.	
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	ALVISO - DDRII	1A	
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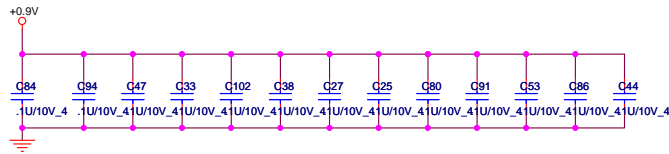
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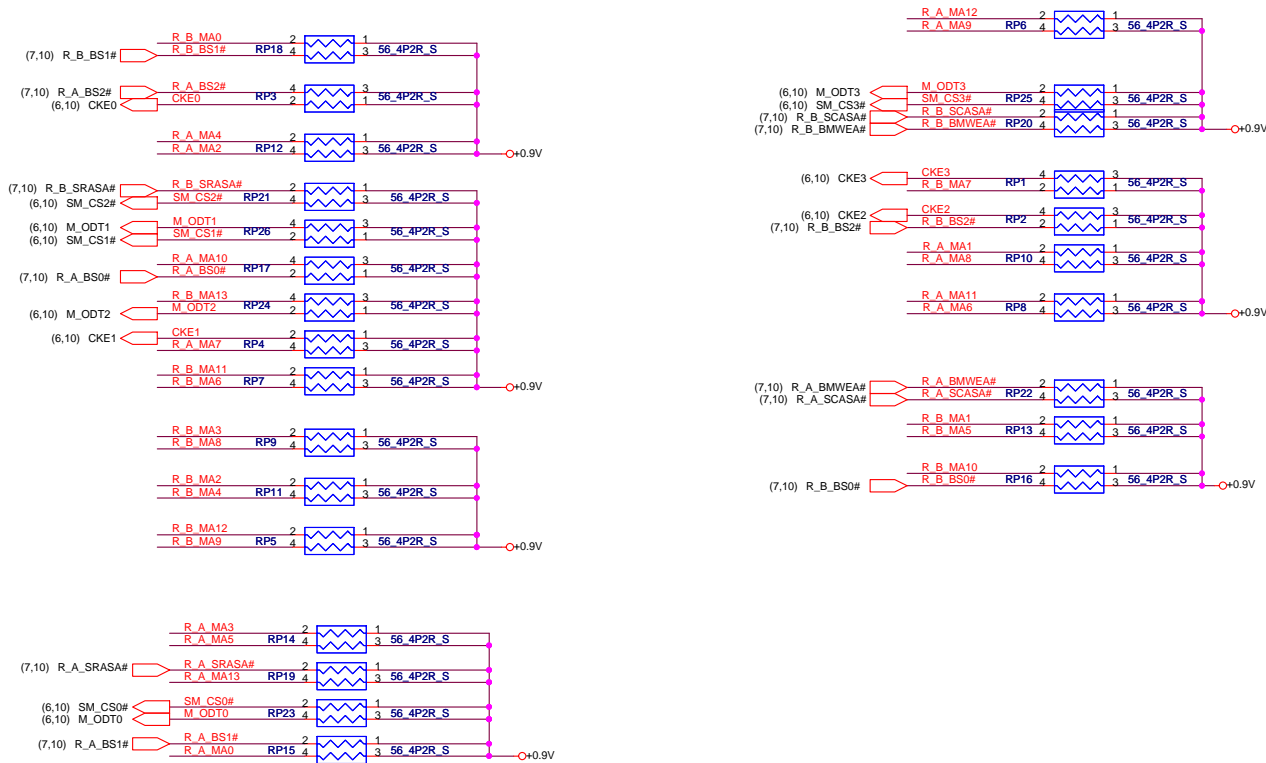





Layout note: Place one cap close to every 2 pullup resistors terminated to +0.9V



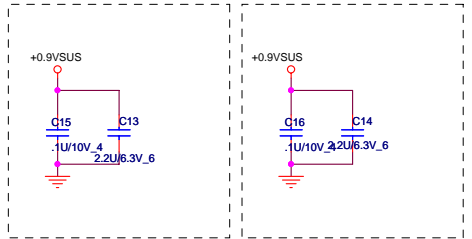
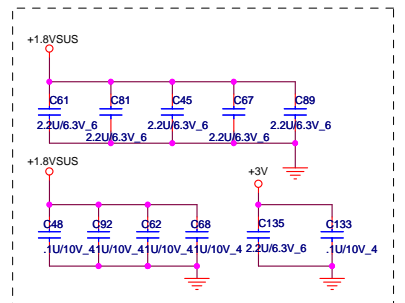
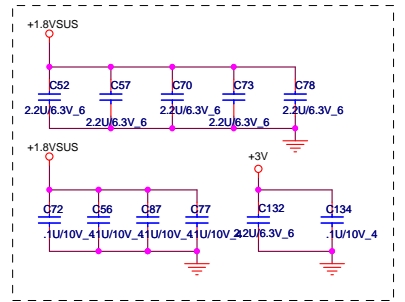
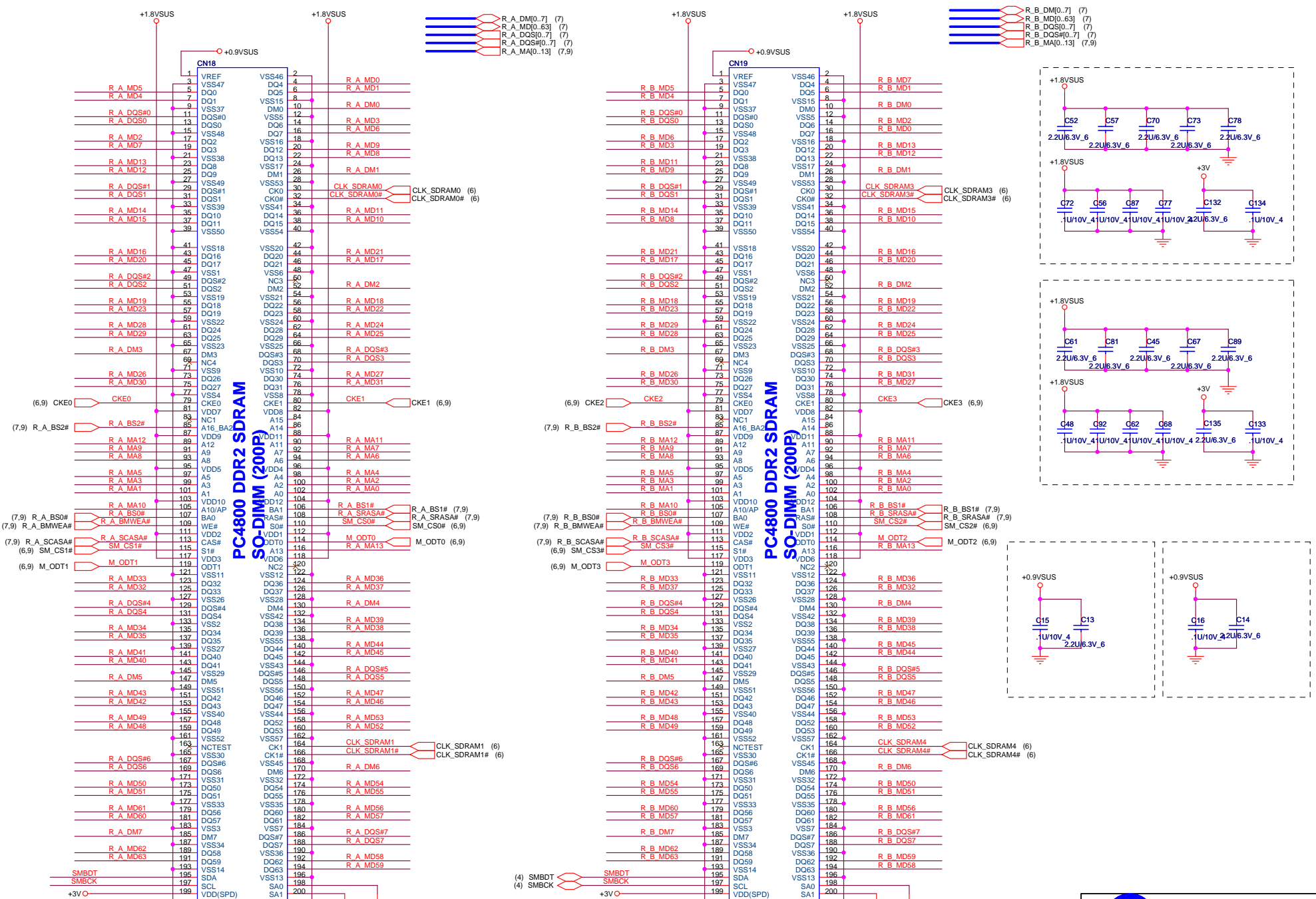
Layout note: Place one cap close to every 2 pullup resistors terminated to +0.9V



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**PROJECT : ZL8**  
**Quanta Computer Inc.**

Size	Document Number	Rev
	<b>DDR2 TERMINATION</b>	1A
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**PC4800 DDR2 SDRAM  
SO-DIMM (200P)**

**PC4800 DDR2 SDRAM  
SO-DIMM (200P)**

CLOCK 0,1,2  
CKE 0,1

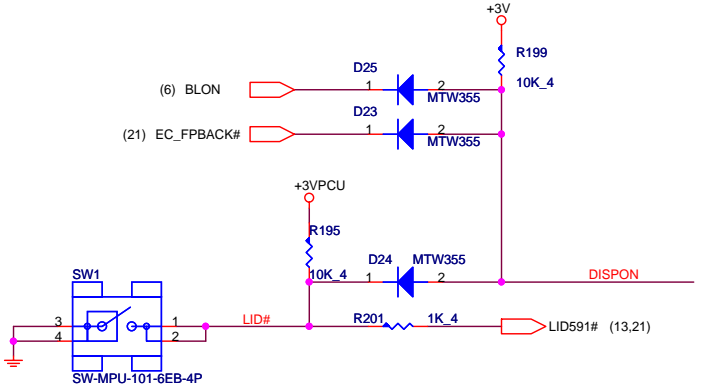
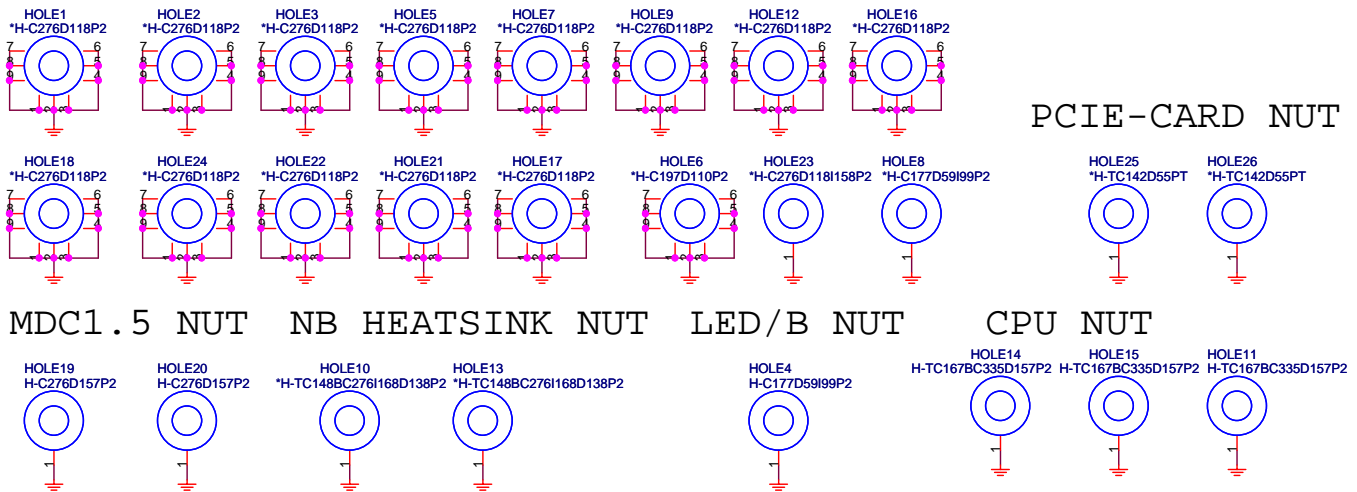
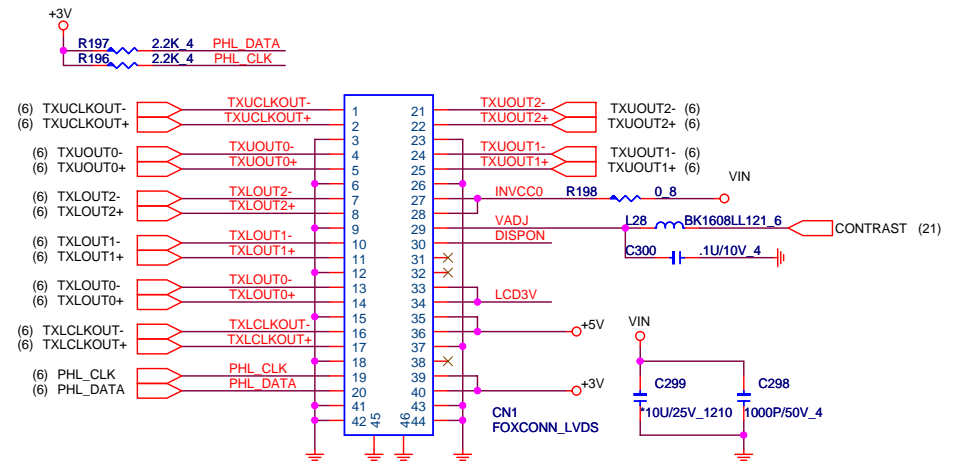
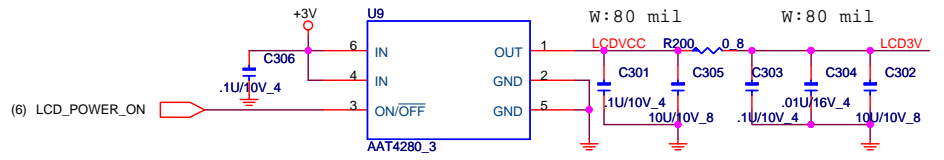
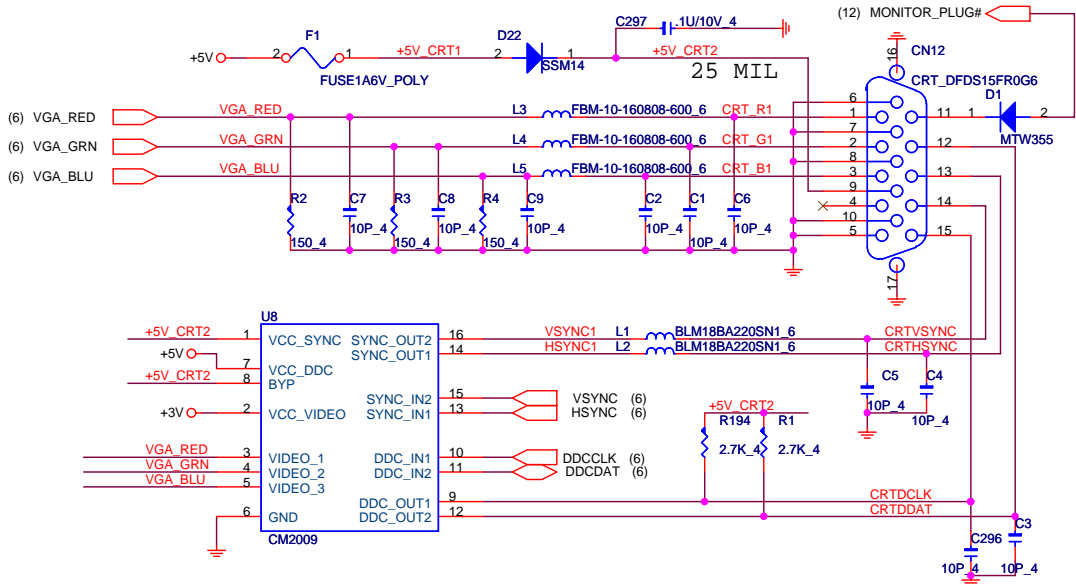
CLOCK 3,4,5  
CKE 2,3

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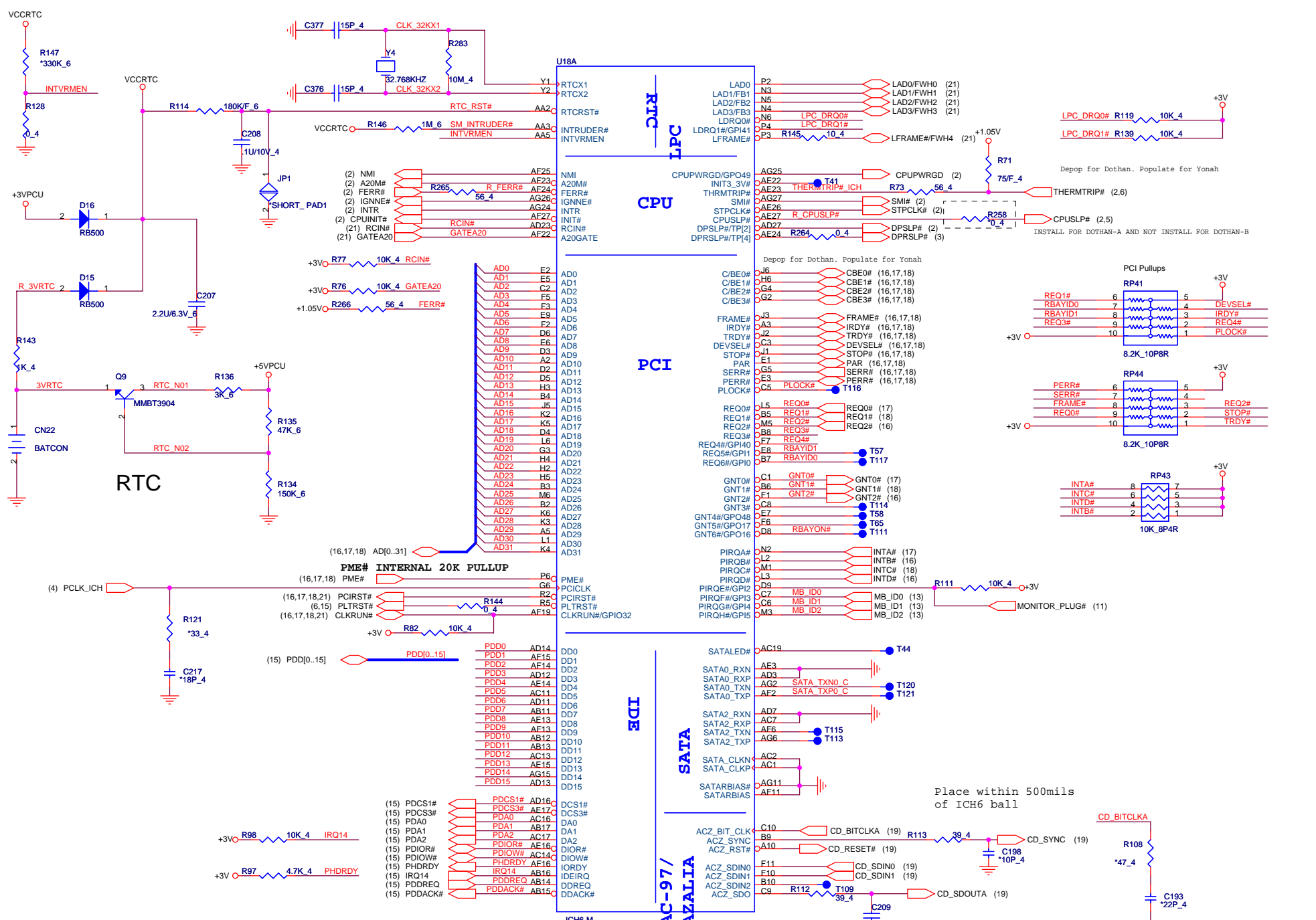
Size	Document Number	Rev
	<b>DDR2 SO-DIMM ( 200P )</b>	1A
Date:	Friday, June 10, 2005	Sheet 10 of 26

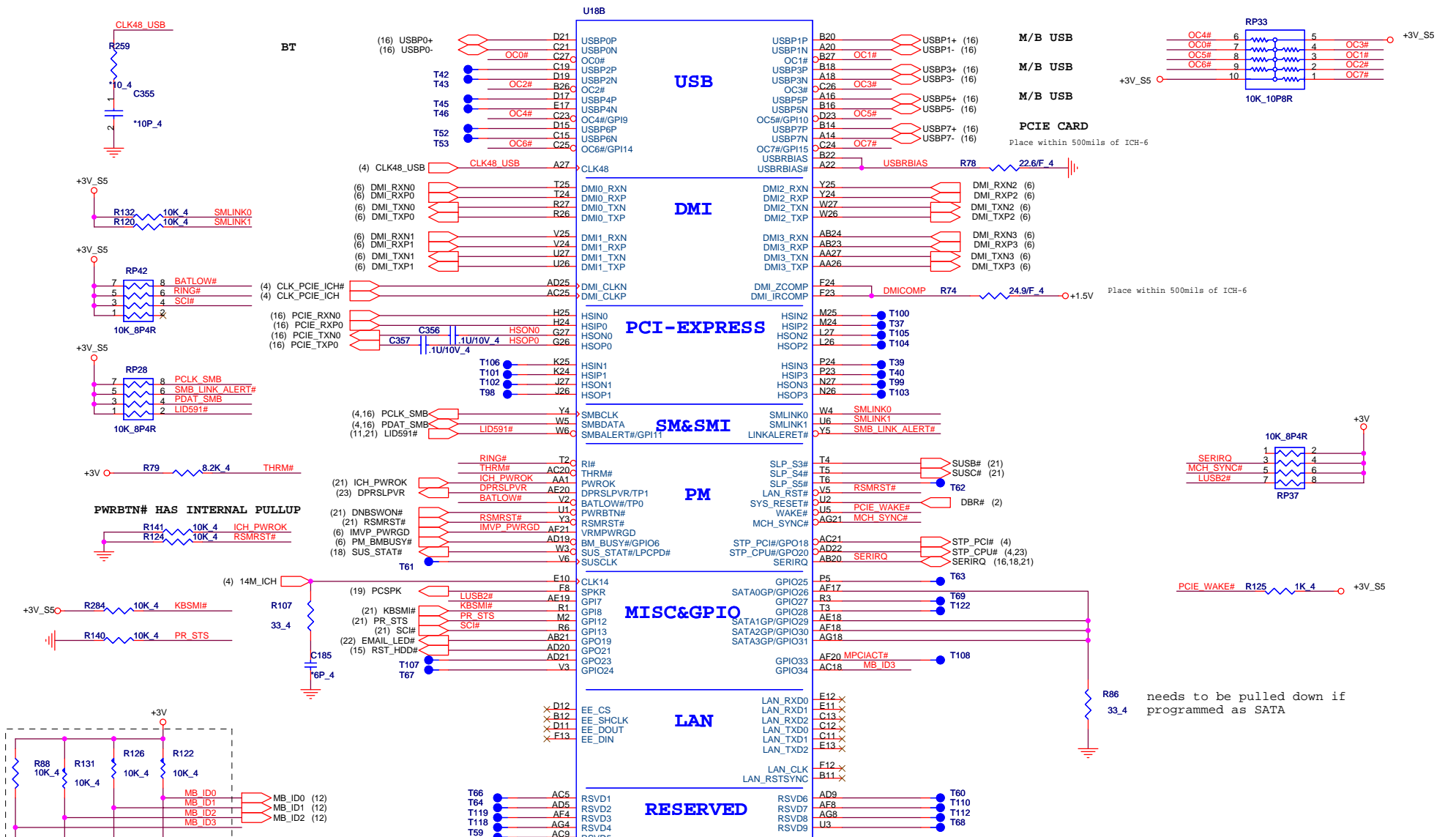
# CRT PORT



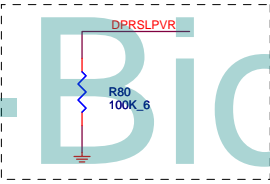
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Size	Document Number	Rev
	<b>LVDS,VGA Ports, LID, &amp; HOLES</b>	1A
Date: Friday, June 10, 2005		Sheet 11 of 26

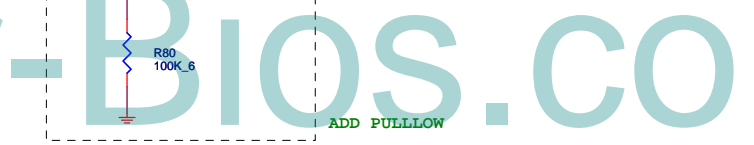




	ID3	ID2	ID1	ID0
	0	0	0	0
	0	0	1	0
	0	1	0	0
	0	1	1	0
	1	0	1	1

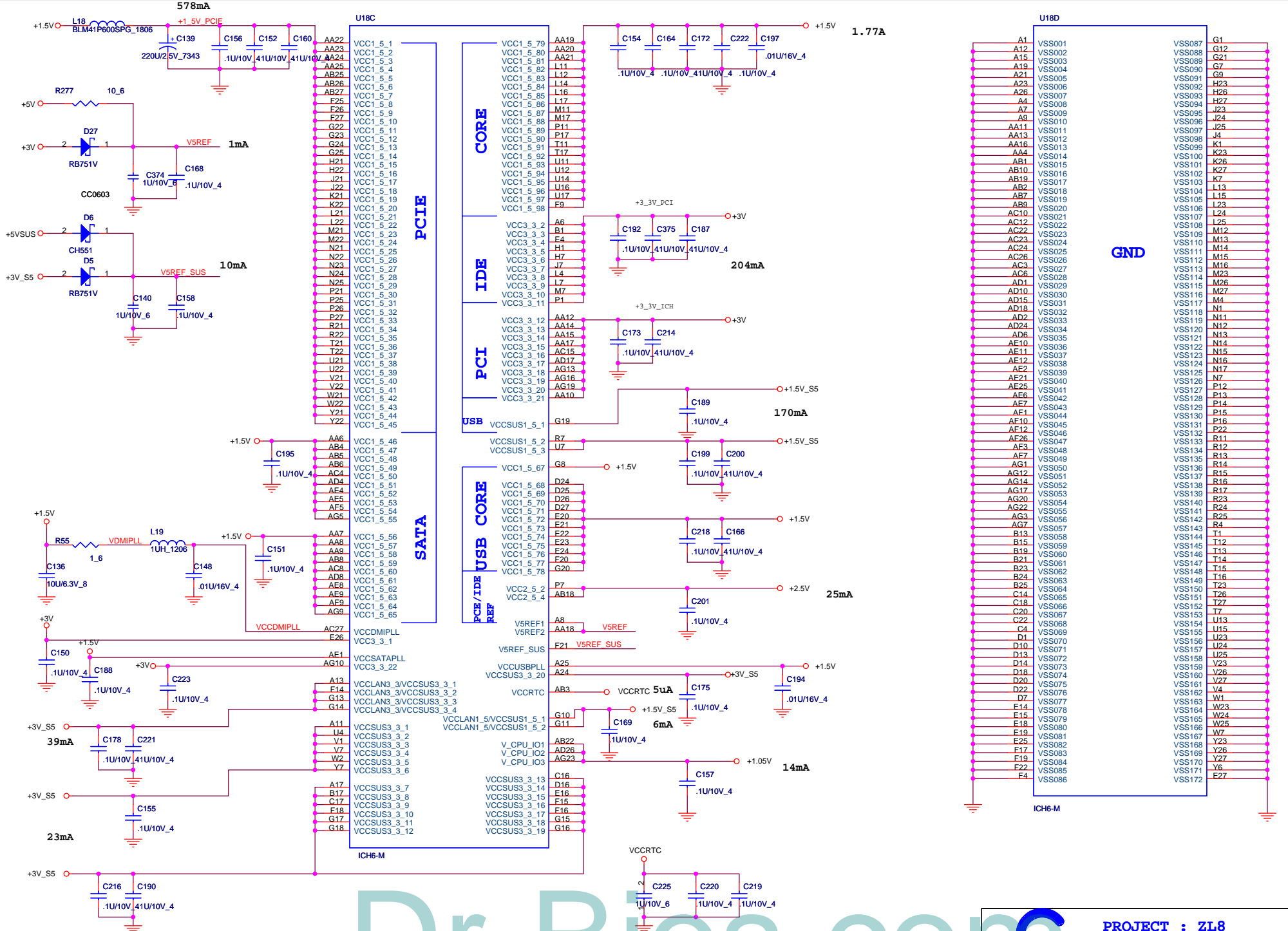


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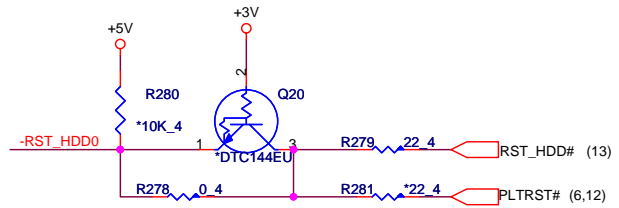
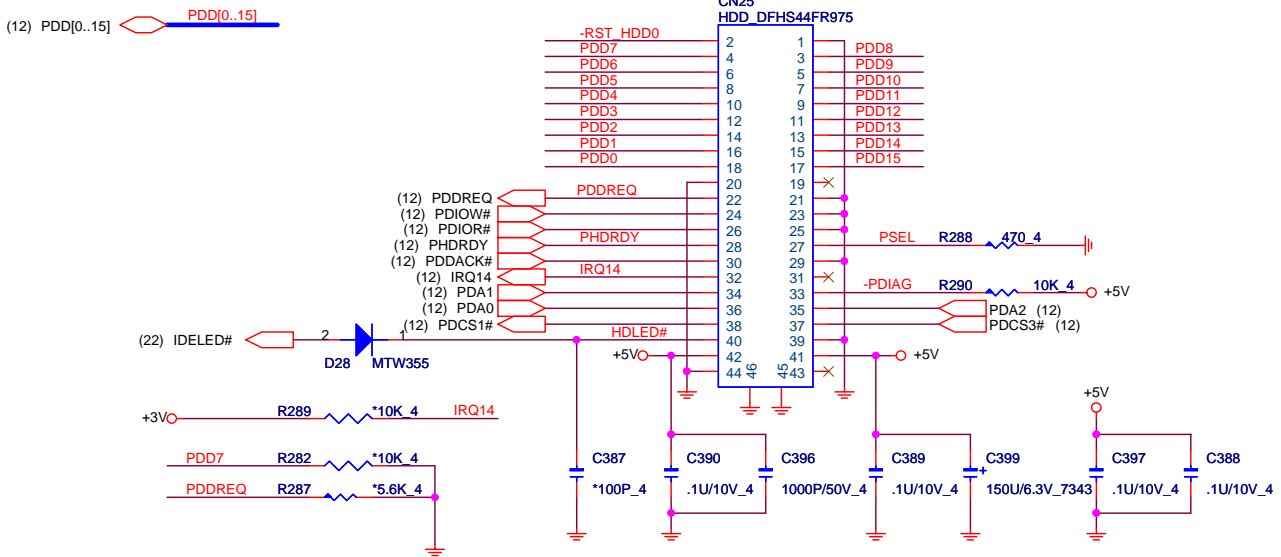
R86 needs to be pulled down if programmed as SATA

ADD PULLUP

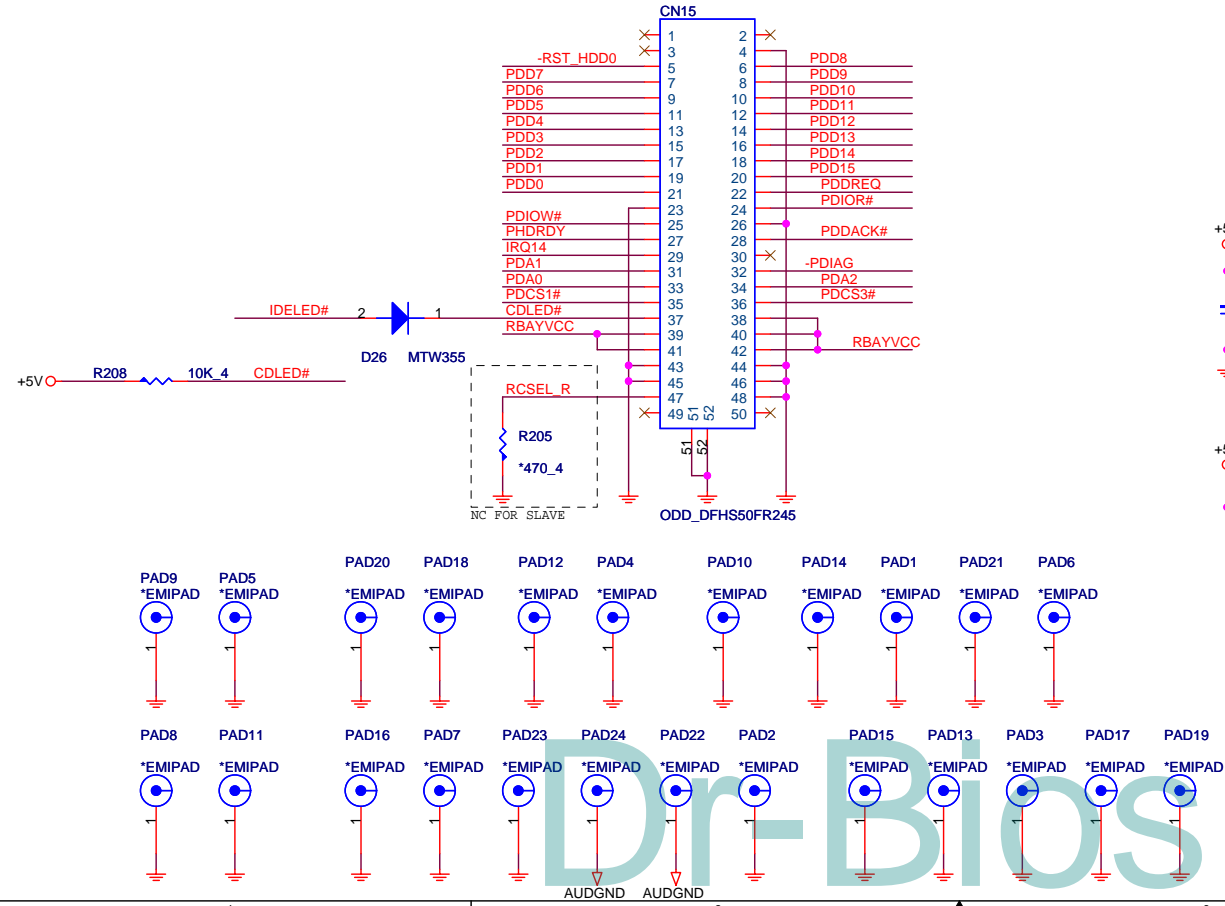


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ODD Connector

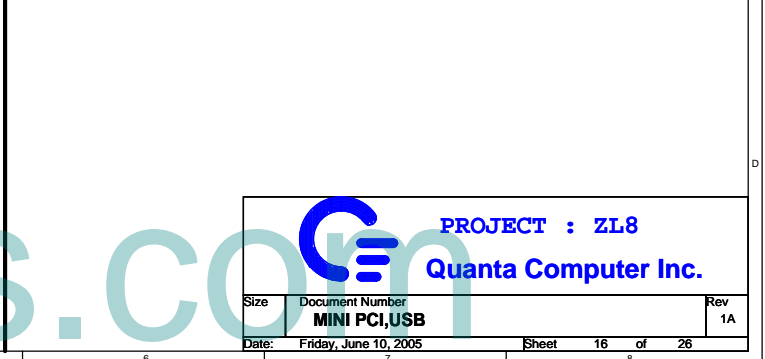
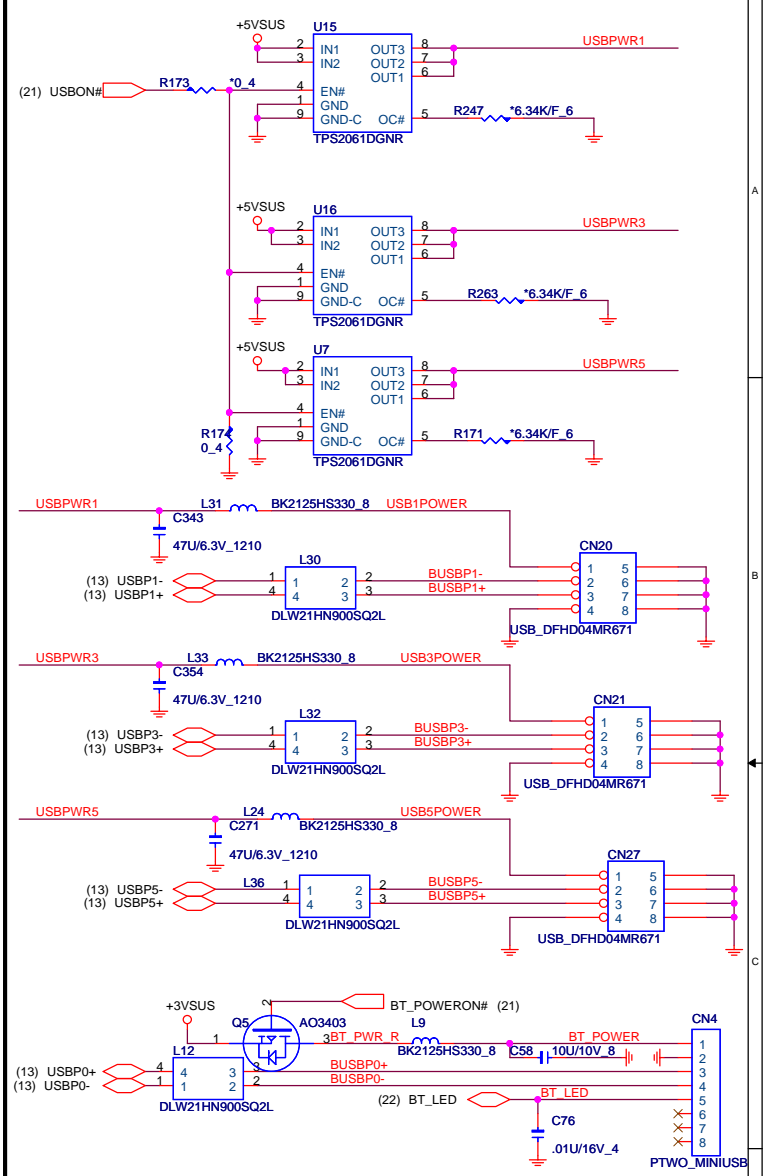
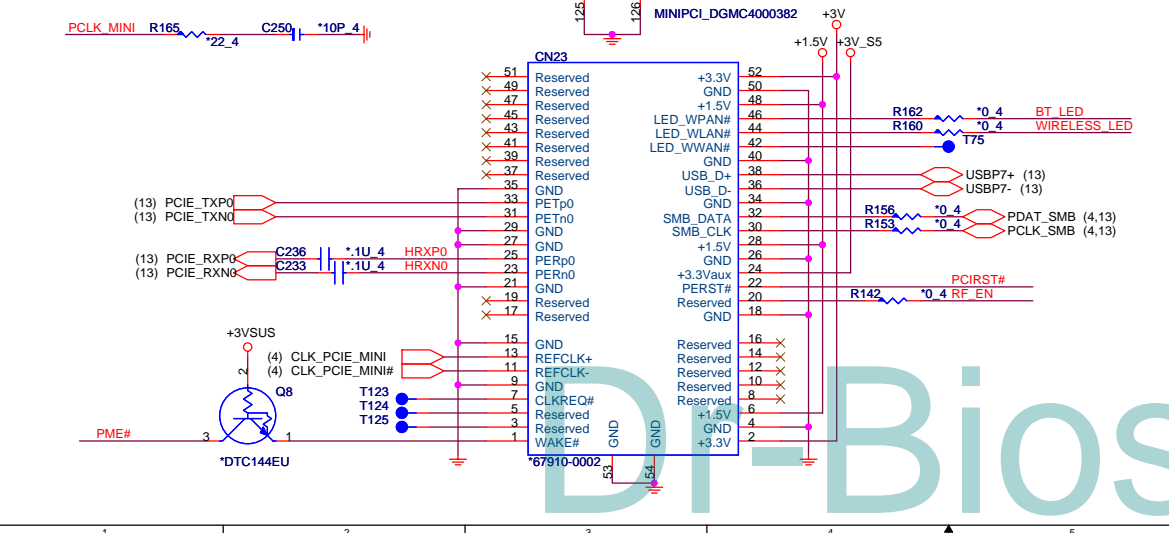
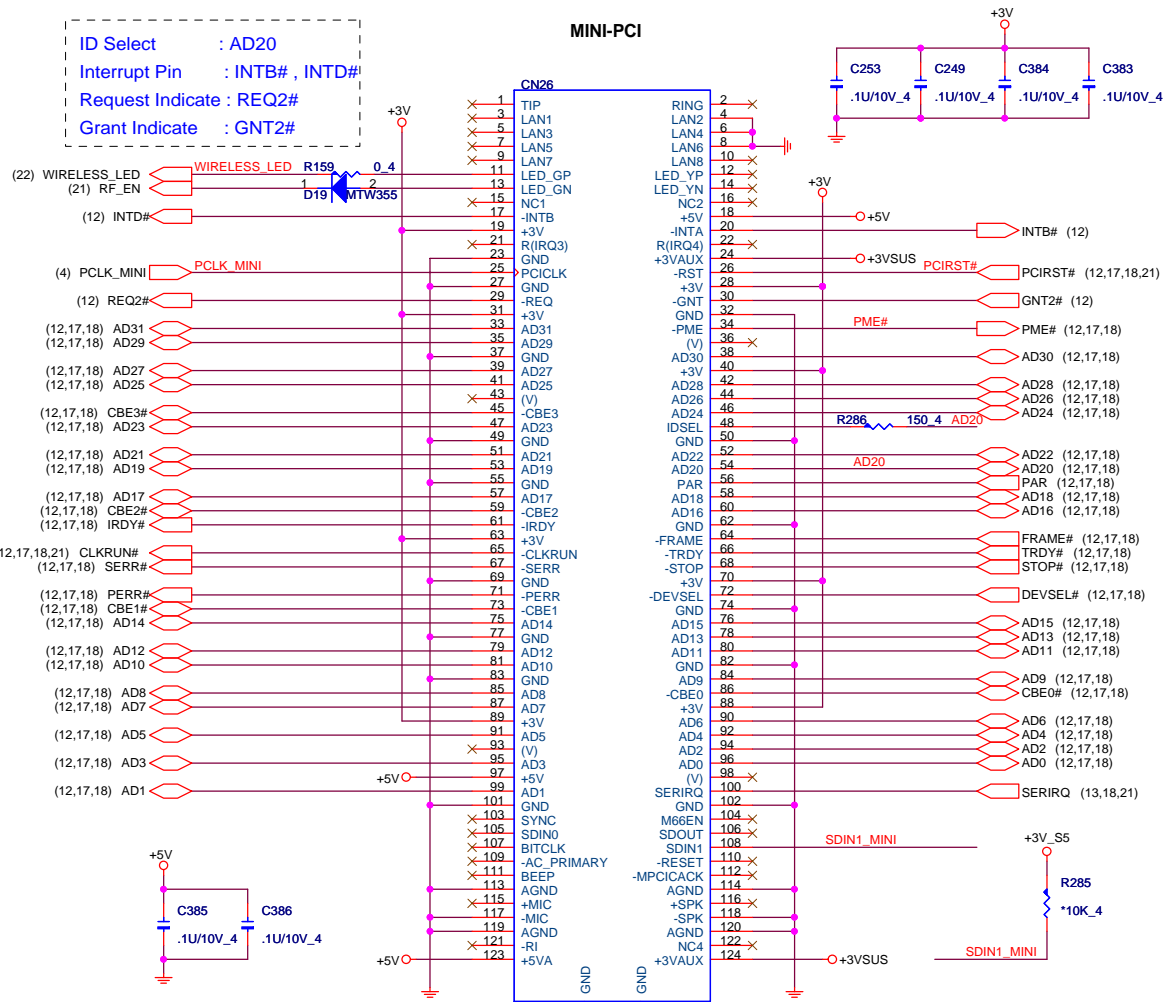


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Size	Document Number	Rev
	<b>HDD &amp; CDROM</b>	1A
Date:	Friday, June 10, 2005	Sheet 15 of 26

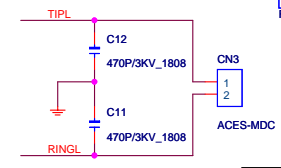
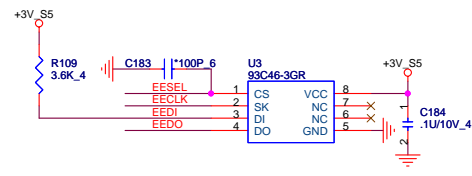
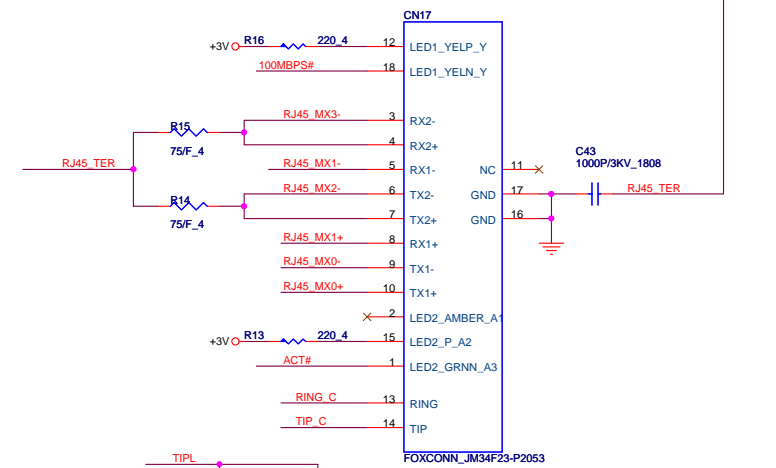
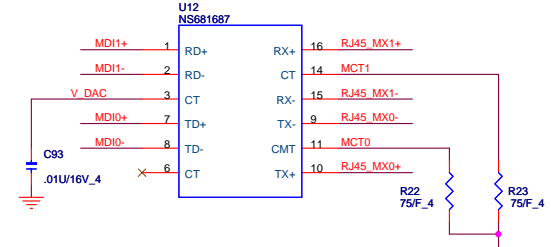
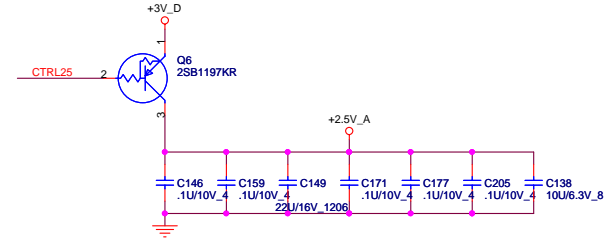
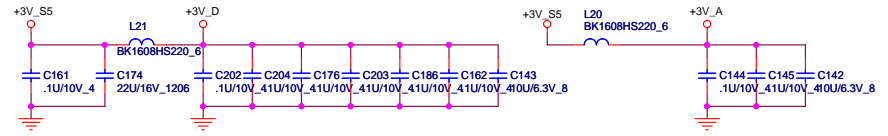
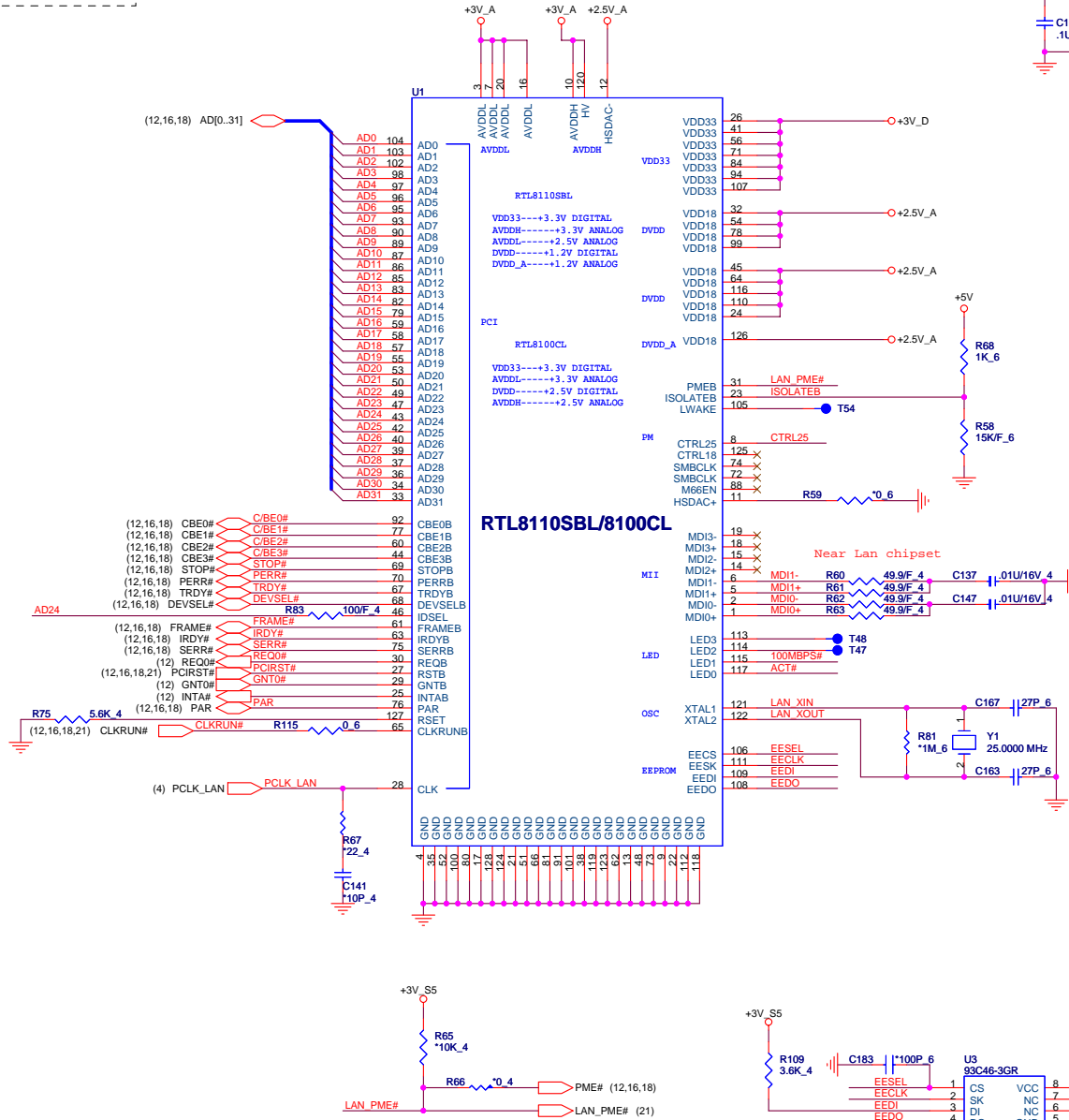
ID Select : AD20  
 Interrupt Pin : INTB# , INTD#  
 Request Indicate : REQ2#  
 Grant Indicate : GNT2#

MINI-PCI





ID Select : AD24  
 Interrupt Pin : INTA#  
 Request Indicate : REQ0#  
 Grant Indicate : GNT0#

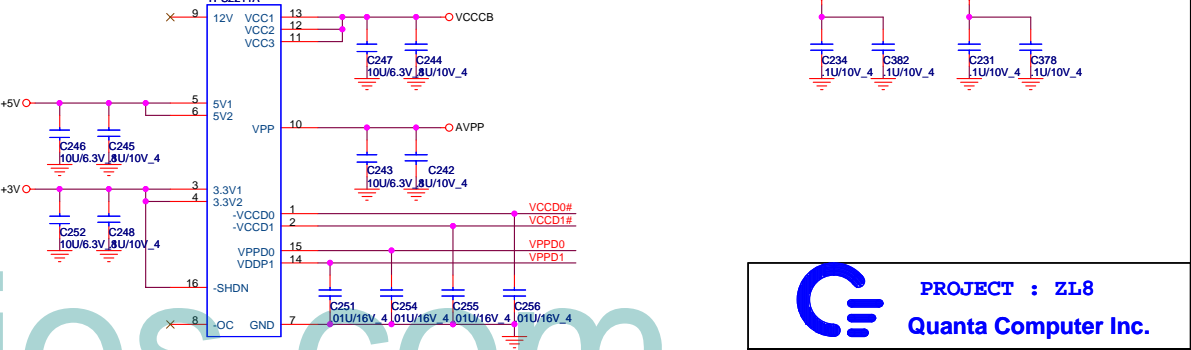
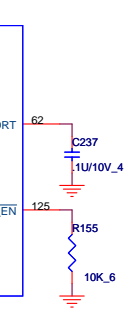
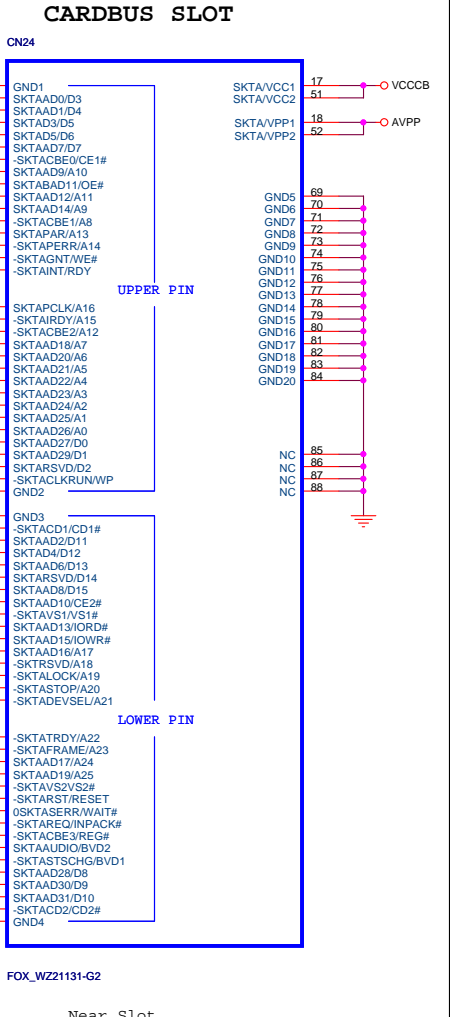
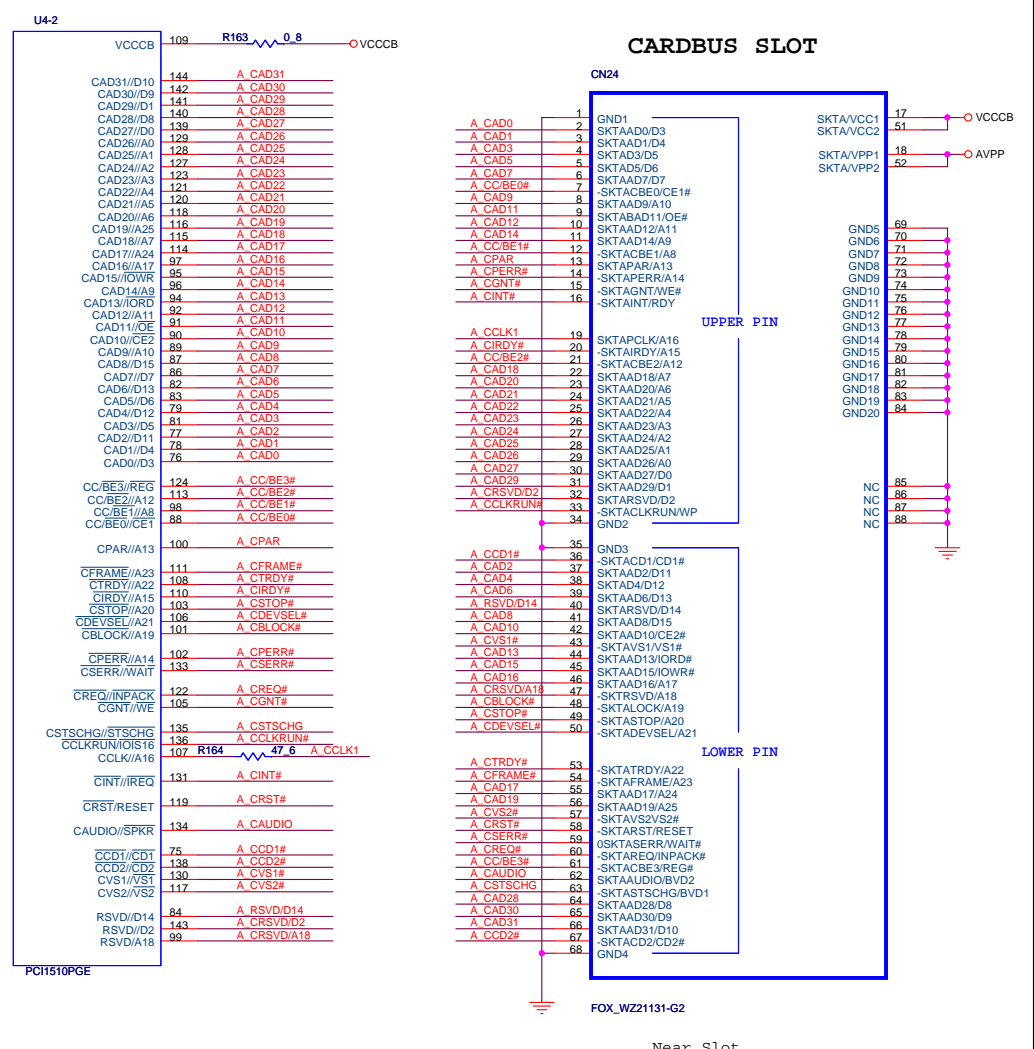
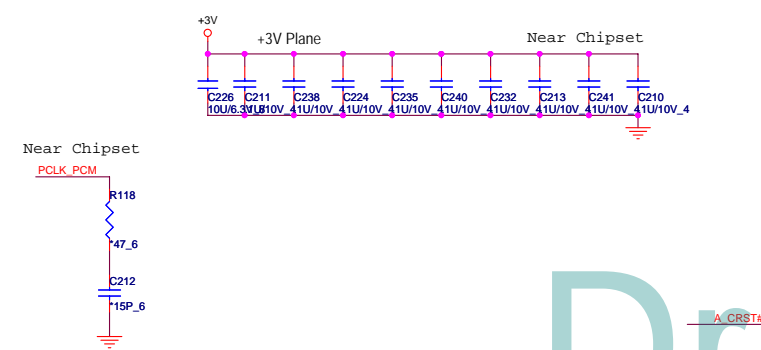
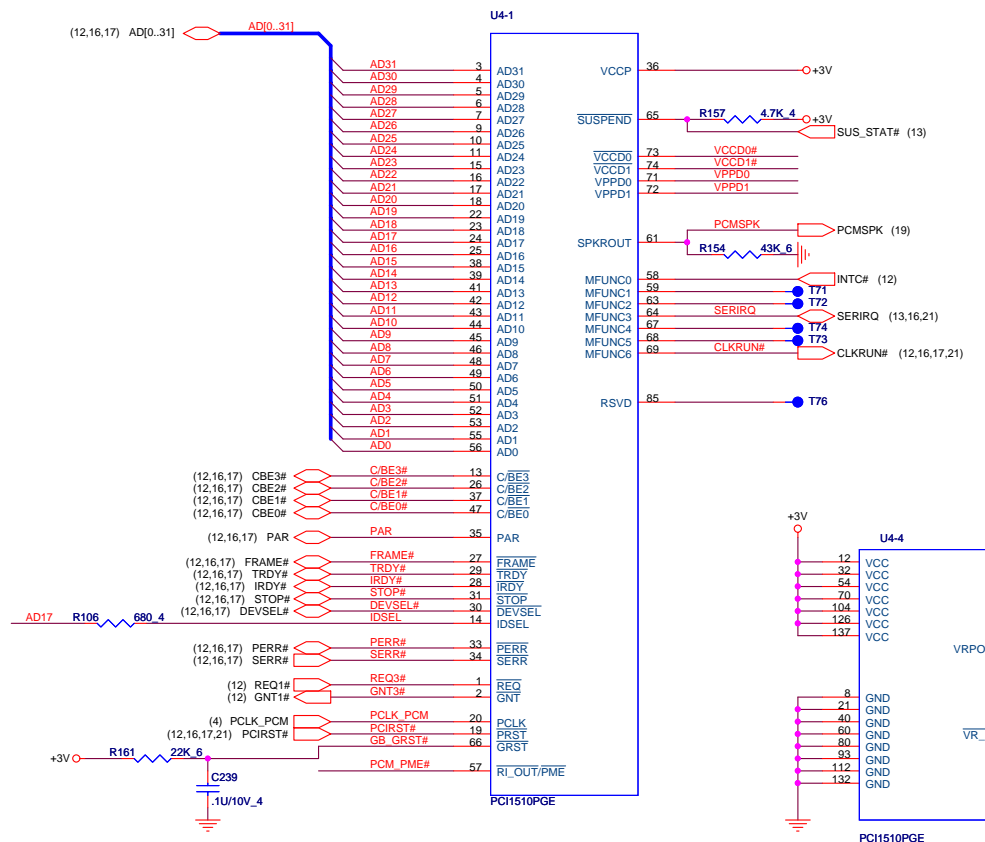


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**PROJECT : ZL8**  
**Quanta Computer Inc.**

Size	Document Number	Rev
	<b>LAN RTL8110SBL/8100CL</b>	1A
Date:	Monday, June 13, 2005	Sheet 17 of 26

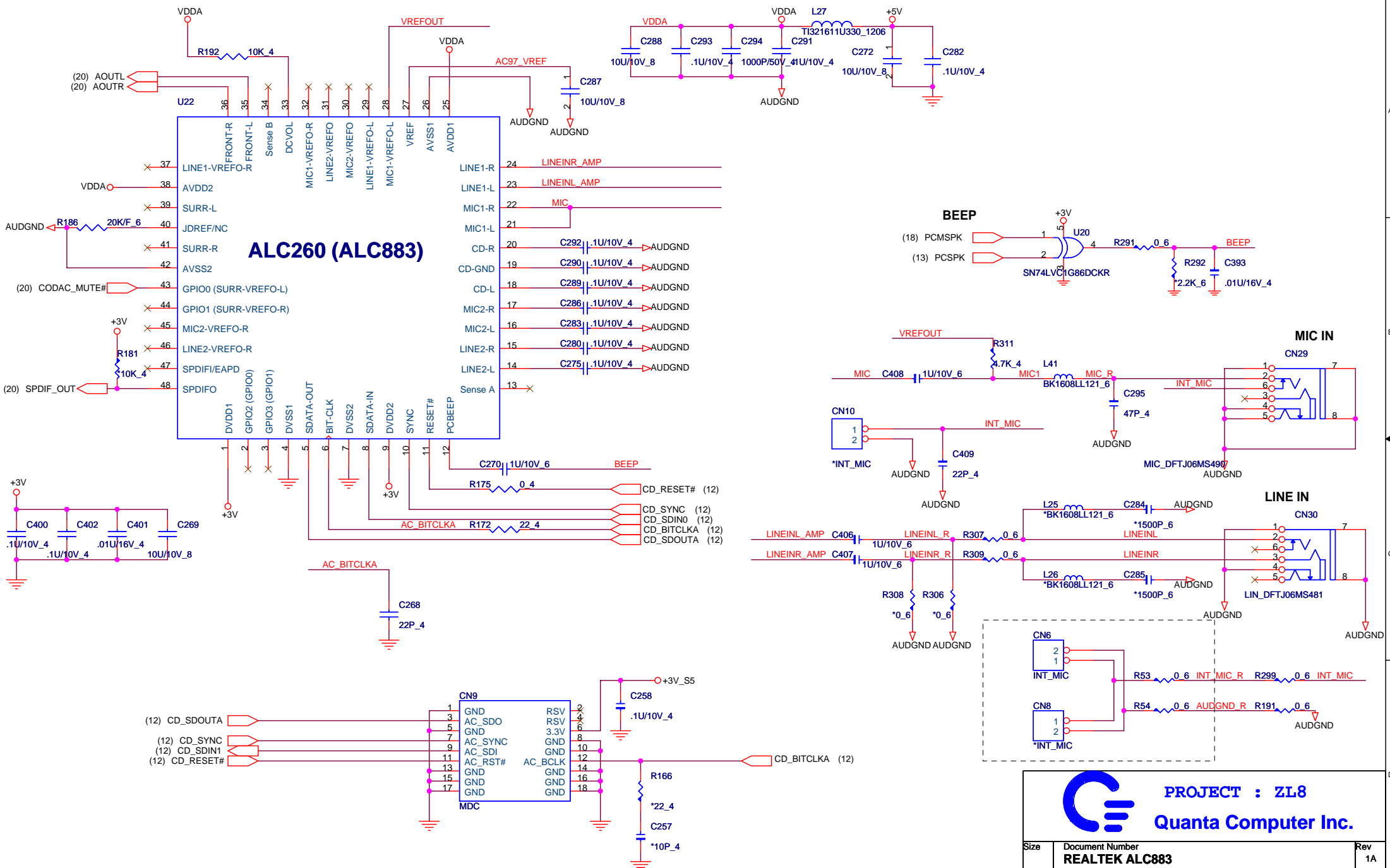
ID Select : AD17  
 Interrupt Pin : INTC#  
 Request Indicate : REQ1#  
 Grant Indicate : GNT1#




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PROJECT : ZL8  
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Size	Document Number	Rev
	PCMCIA CONTROLLER	1A
Date:	Friday, June 10, 2005	Sheet 18 of 26

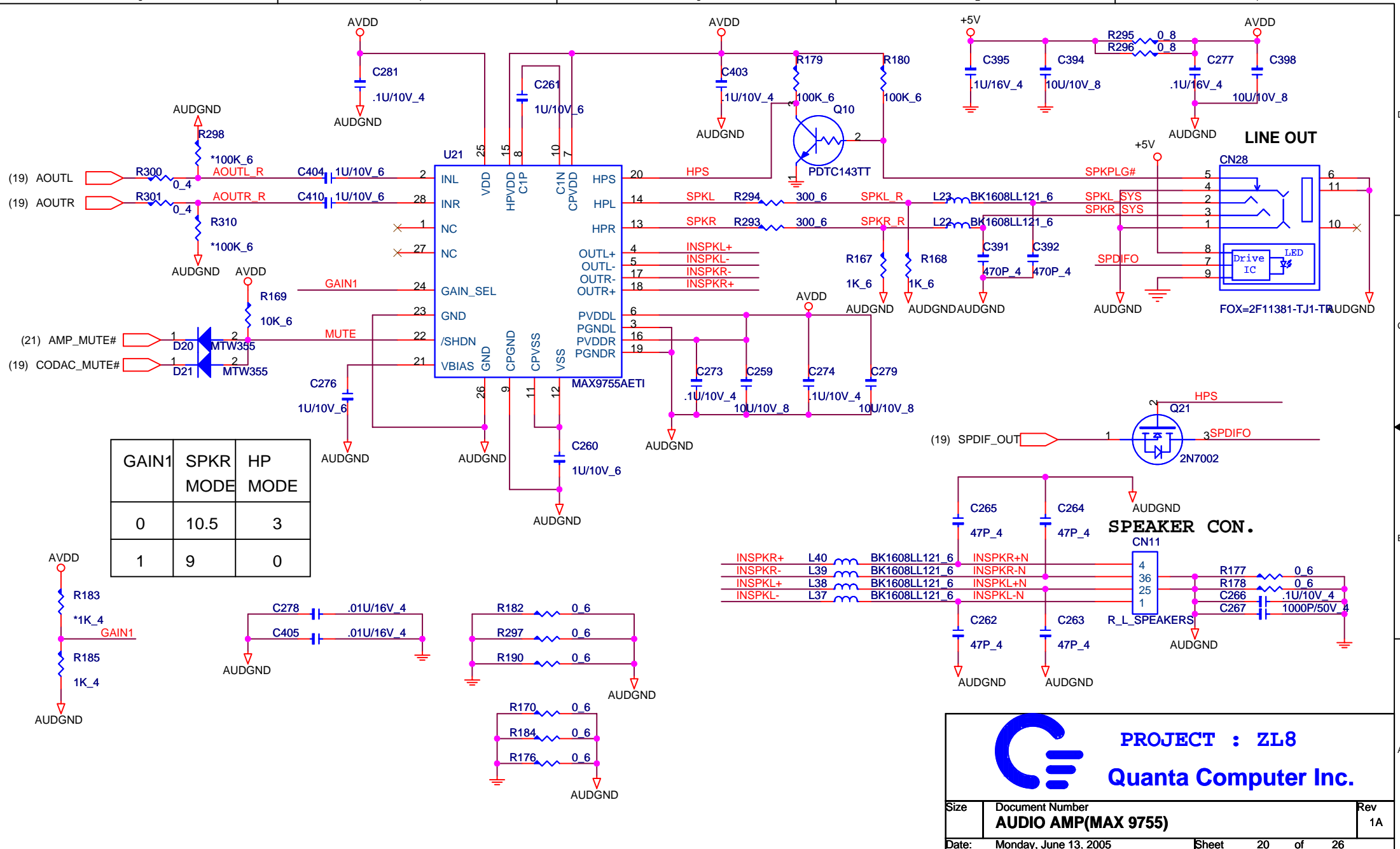





**PROJECT : ZL8**  
**Quanta Computer Inc.**

Size	Document Number <b>REALTEK ALC883</b>	Rev 1A
Date:	Friday, June 10, 2005	Sheet 19 of 26

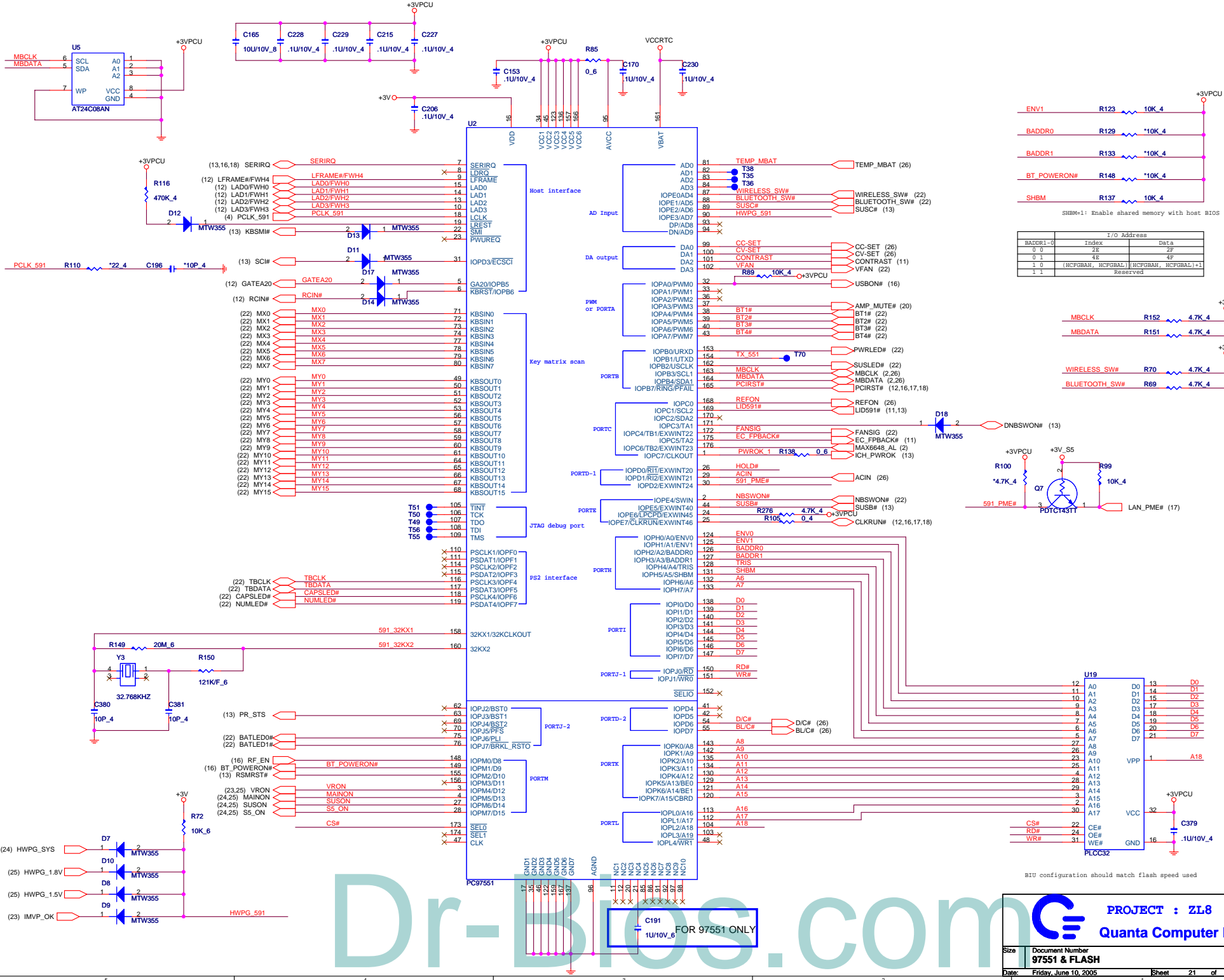
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**PROJECT : ZL8**  
**Quanta Computer Inc.**

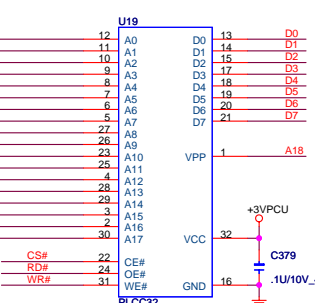
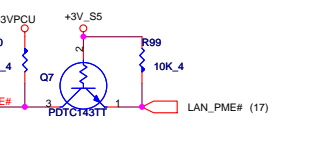
Size	Document Number <b>AUDIO AMP(MAX 9755)</b>	Rev 1A
Date:	Monday, June 13, 2005	Sheet 20 of 26



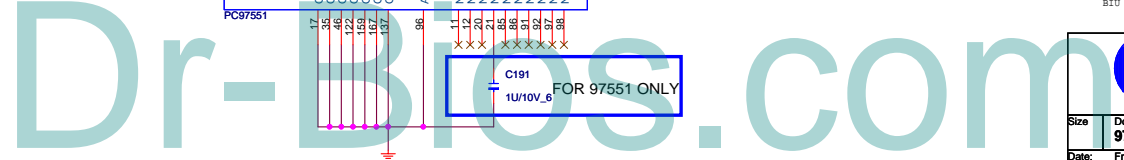
SHBM=1: Enable shared memory with host BIOS

BADDR1-0	Index	Data
0 0	2E	2F
0 1	4E	4F
1 0	(HCFGBAH, HCFGBAL) (HCFGBAL)+1	
1 1	Reserved	

Signal	Value
MCLK	4.7K_4
MBDATA	4.7K_4
WIRELESS_SW#	4.7K_4
BLUETOOTH_SW#	4.7K_4



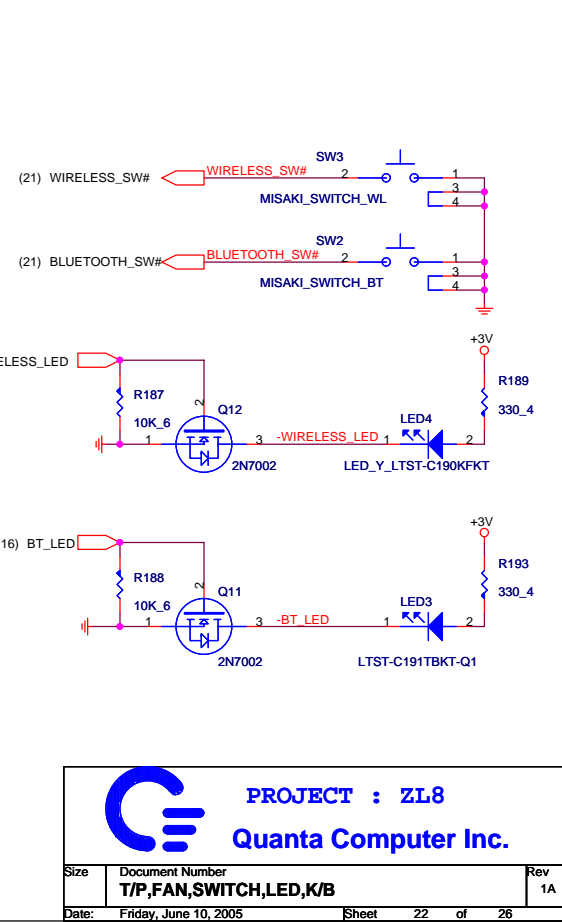
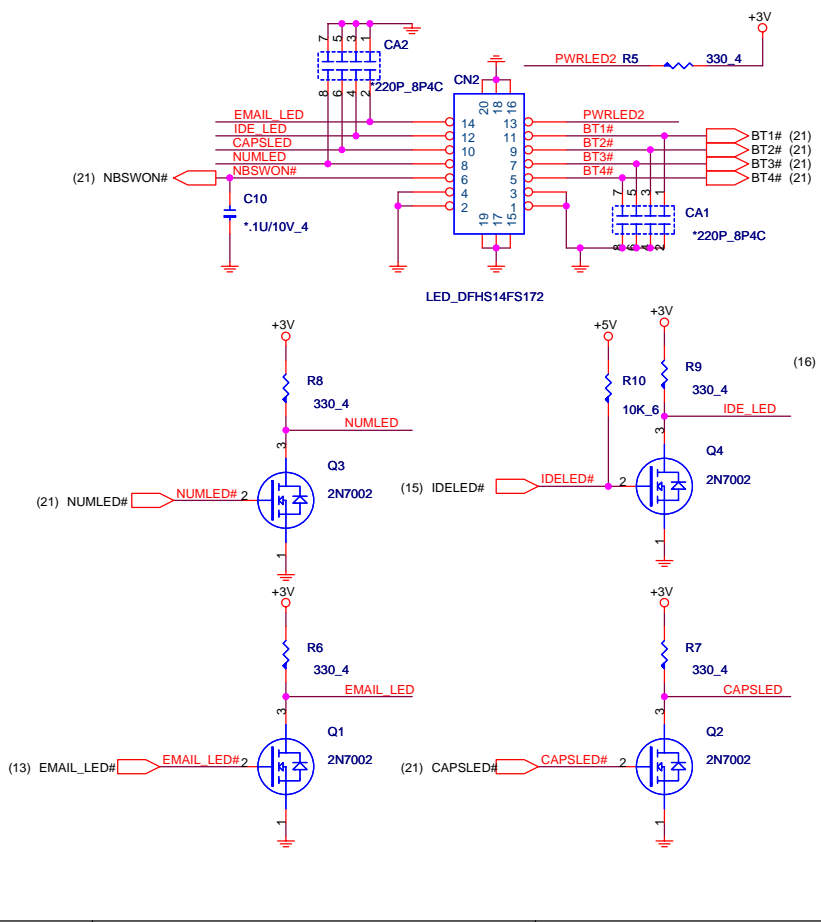
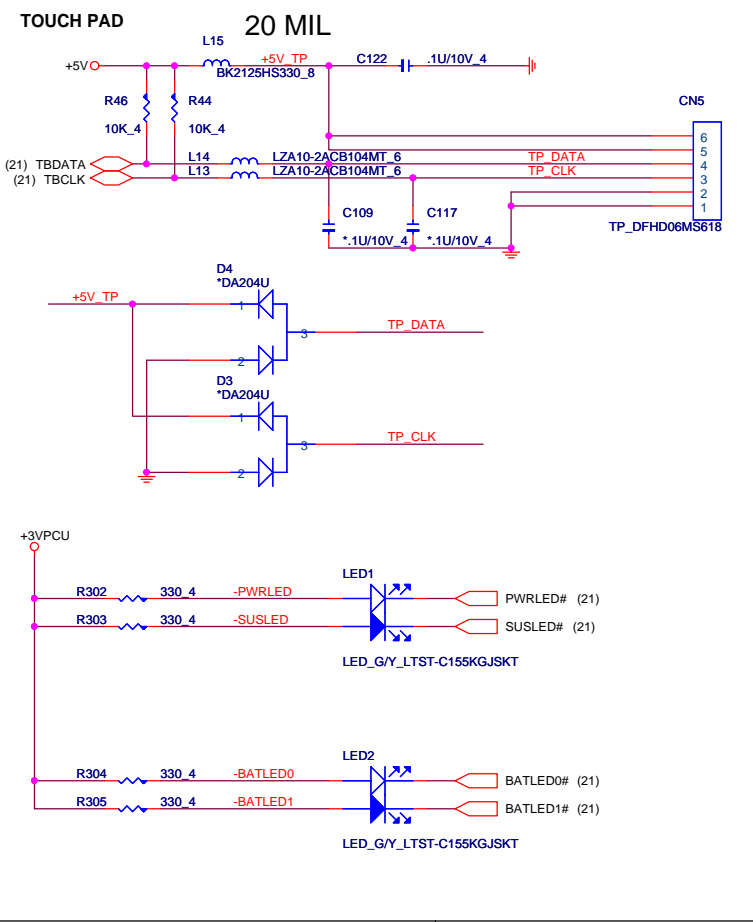
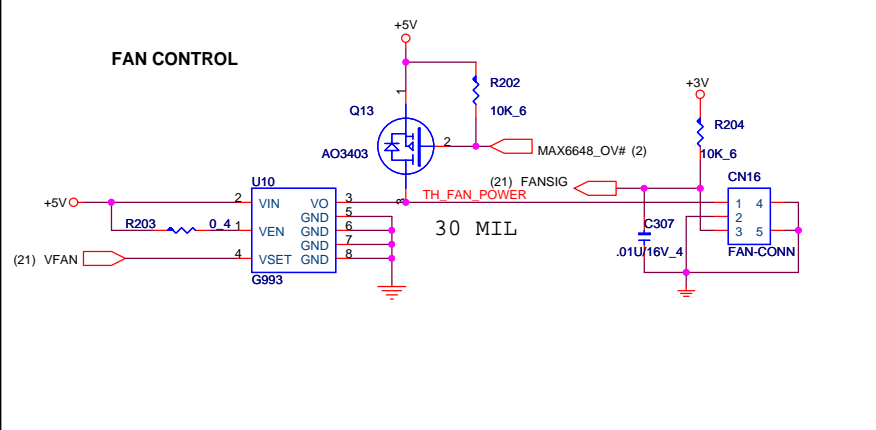
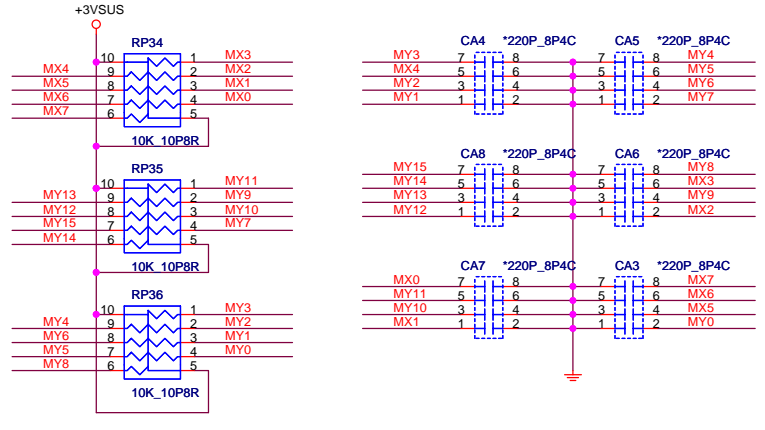
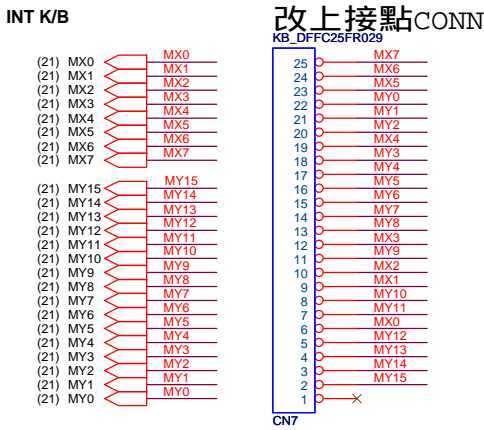
BIU configuration should match flash speed used



**PROJECT : ZL8**  
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Size	Document Number	Rev
	<b>97551 &amp; FLASH</b>	1A
Date	Friday, June 10, 2005	Sheet 21 of 26

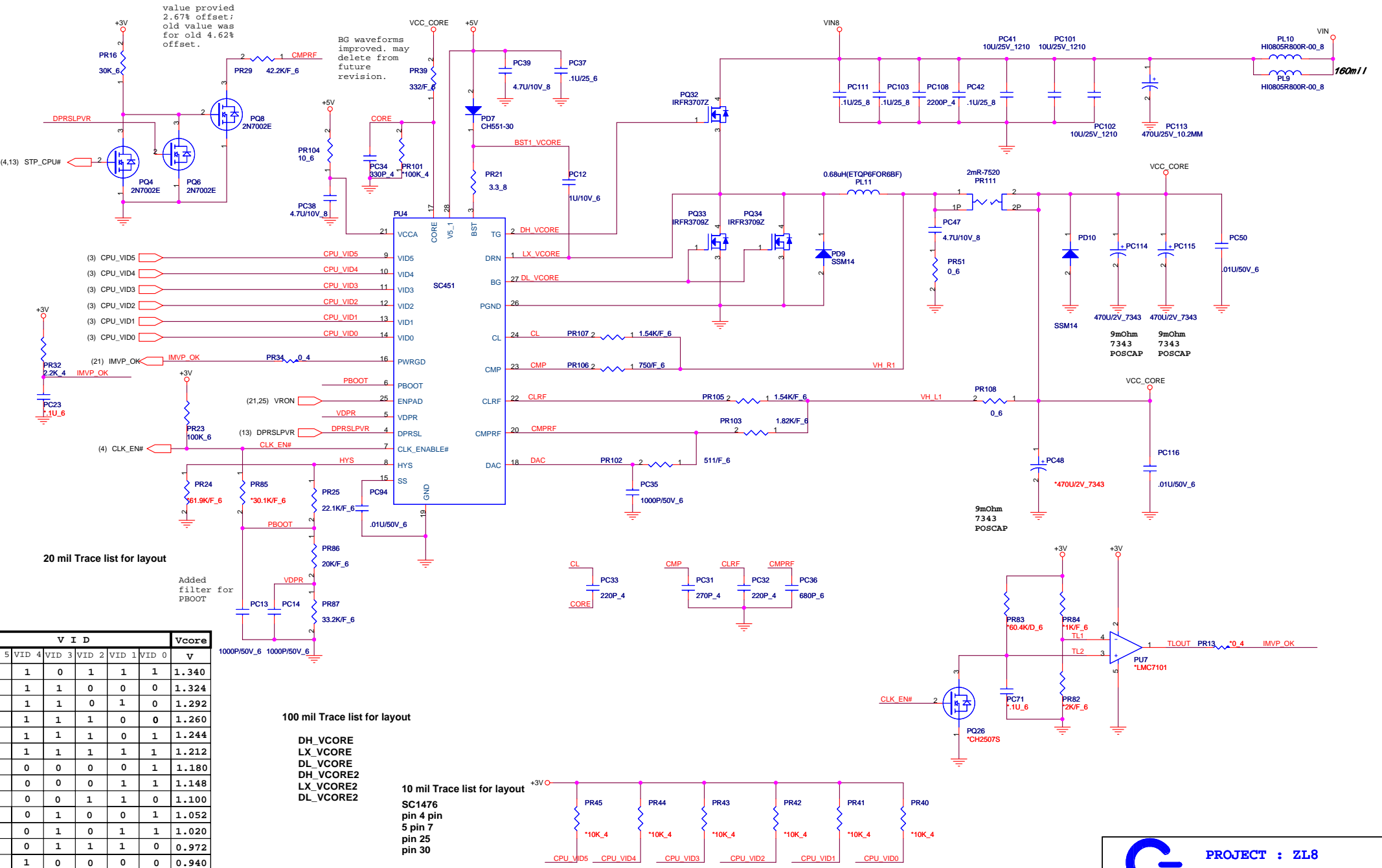
C191  
1U/10V\_6  
FOR 97551 ONLY



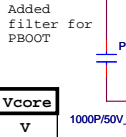
**PROJECT : ZL8**  
**Quanta Computer Inc.**

Size	Document Number	Rev
Date: Friday, June 10, 2005	<b>T/P,FAN,SWITCH,LED,K/B</b>	1A
Sheet	22 of 26	

value provided  
2.67% offset;  
old value was  
for old 4.62%  
offset.



**20 mil Trace list for layout**



V I D							Vcore
VID 5	VID 4	VID 3	VID 2	VID 1	VID 0		v
0	1	0	1	1	1	1	1.340
0	1	1	0	0	0	0	1.324
0	1	1	0	1	0	0	1.292
0	1	1	1	0	0	0	1.260
0	1	1	1	1	0	1	1.244
0	1	1	1	1	1	1	1.212
1	0	0	0	0	1	1	1.180
1	0	0	1	1	0	1	1.100
1	0	1	0	0	1	1	1.052
1	0	1	0	1	1	1	1.020
1	0	1	1	1	0	0	0.972
1	1	0	0	0	0	0	0.940

**100 mil Trace list for layout**

- DH\_VCORE
- LX\_VCORE
- DL\_VCORE
- DH\_VCORE2
- LX\_VCORE2
- DL\_VCORE2

**10 mil Trace list for layout**

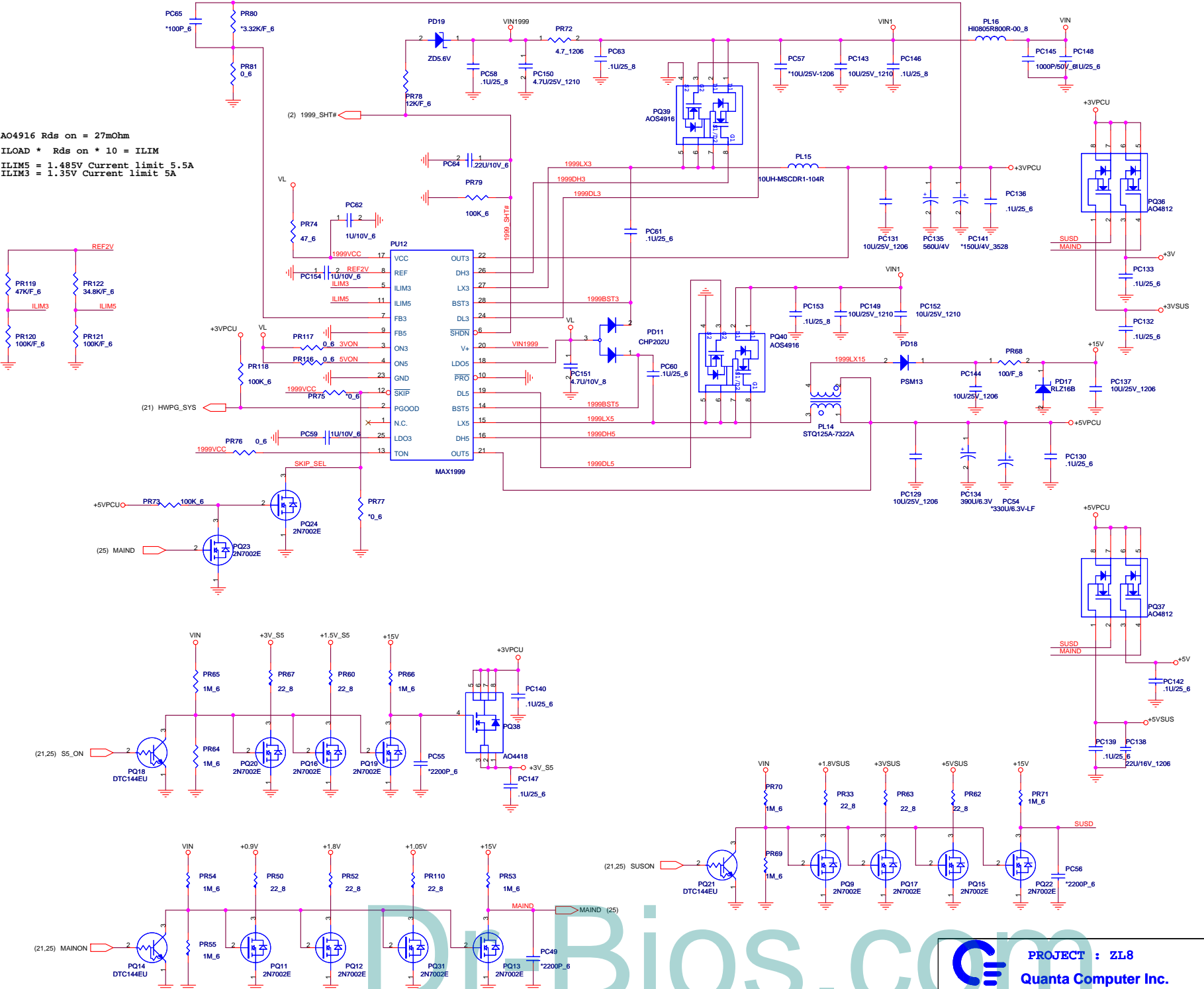
- SC1476
- pin 4 pin
- 5 pin 7
- pin 25
- pin 30

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Size	Document Number	Rev
	<b>CPU CORE (SC451)</b>	1A
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AO4916 Rds on = 27mOhm  
 ILOAD \* Rds on \* 10 = ILIM  
 ILIM5 = 1.485V Current limit 5.5A  
 ILIM3 = 1.35V Current limit 5A

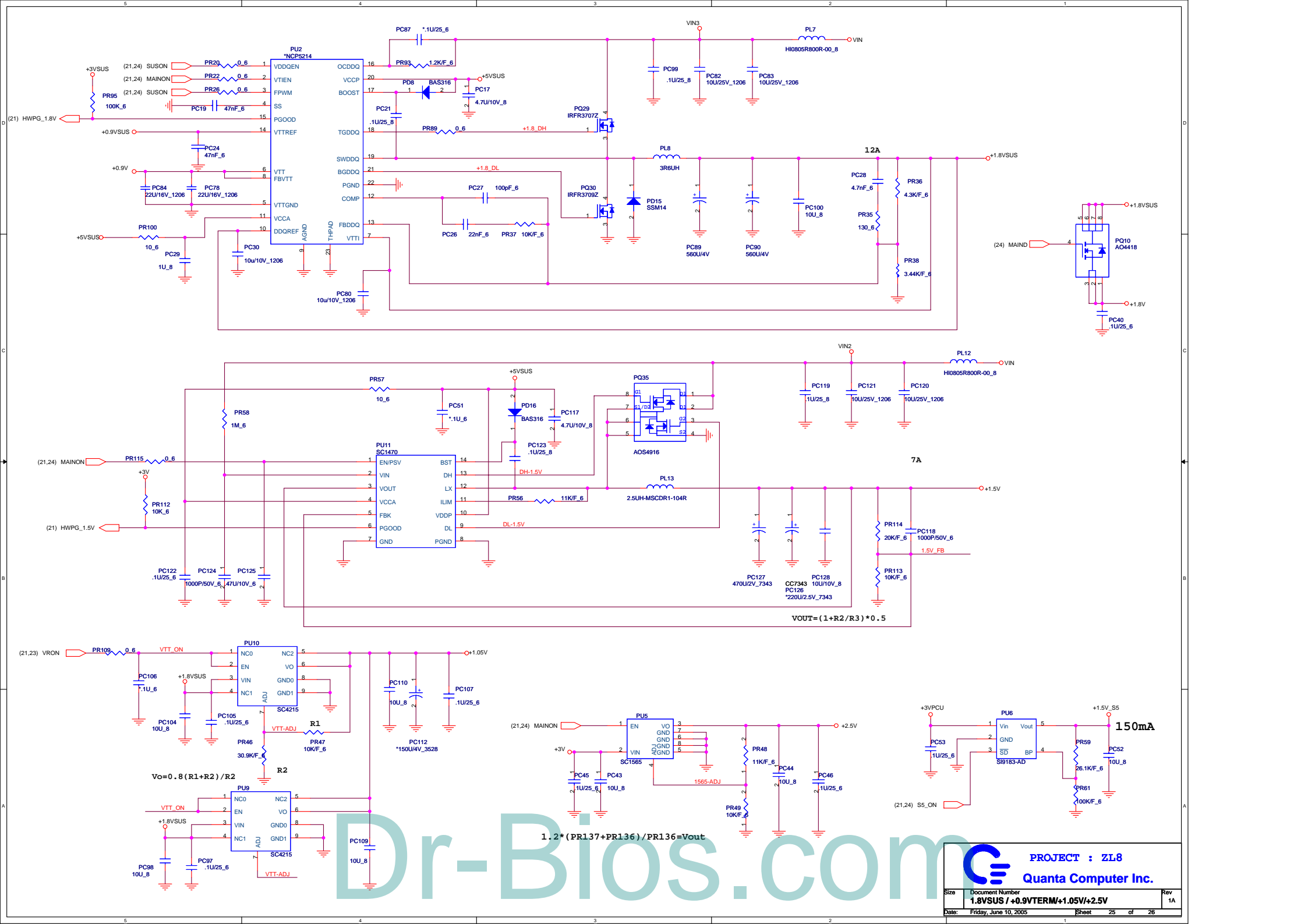


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Size	Document Number	Rev
	5V/3.3V (MAX1999)	1A
Date:	Monday, June 13, 2005	Sheet 24 of 26





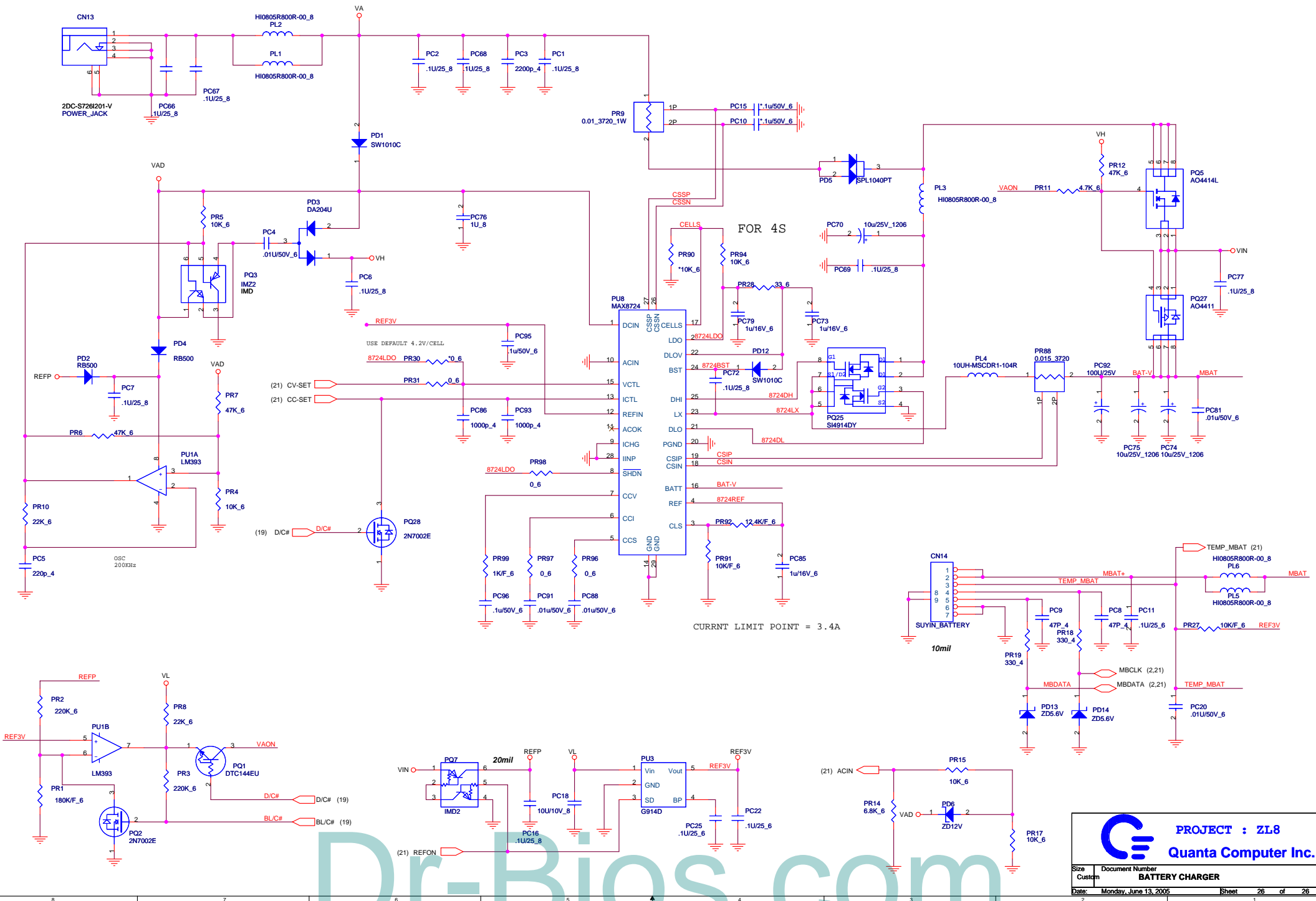
$$V_o = 0.8 \cdot (R1 + R2) / R2$$

$$1.2 \cdot (PR137 + PR136) / PR136 = V_{out}$$


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**PROJECT : ZL8**  
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Size	Document Number	Rev
	1.8VSUS / +0.9VTERM/+1.05V/+2.5V	1A
Date:	Friday, June 10, 2005	Sheet 25 of 26



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Size	Document Number	Rev
Custom	<b>BATTERY CHARGER</b>	1A
Date:	Monday, June 13, 2005	Sheet 26 of 26